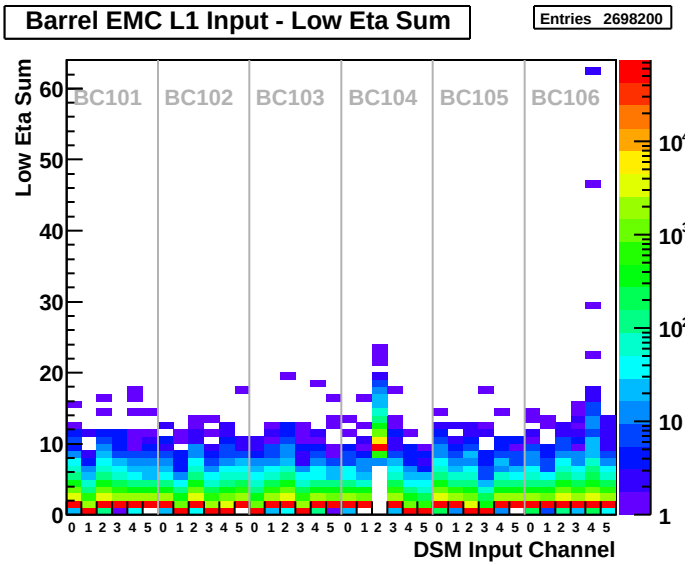
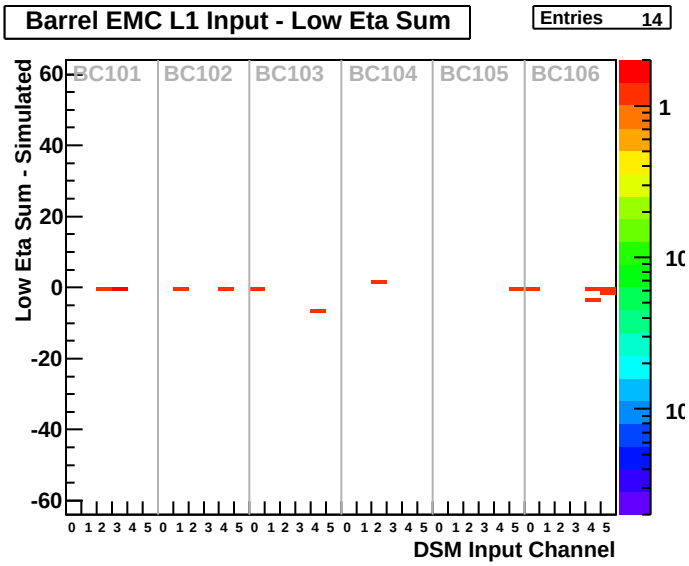
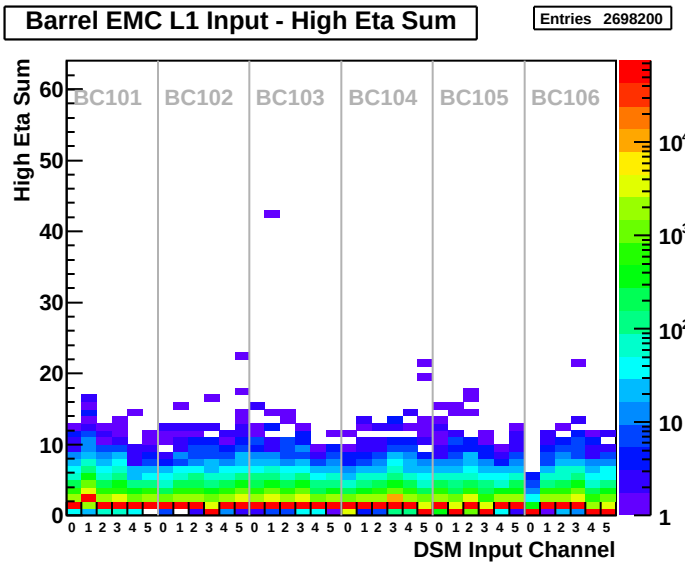
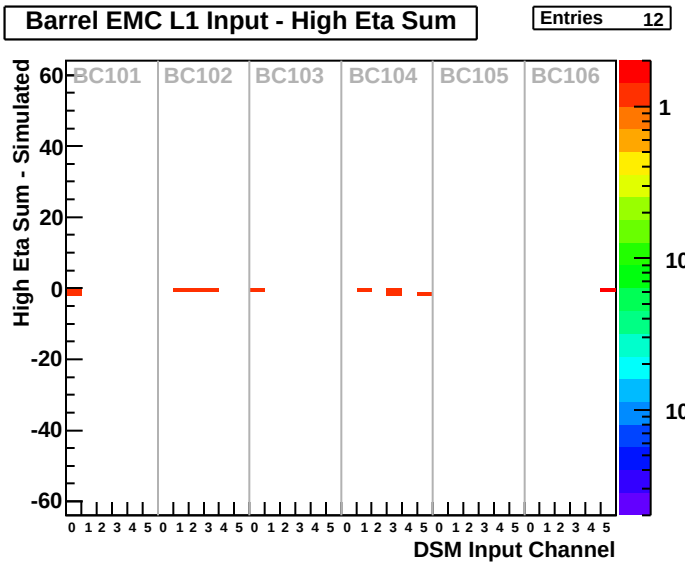
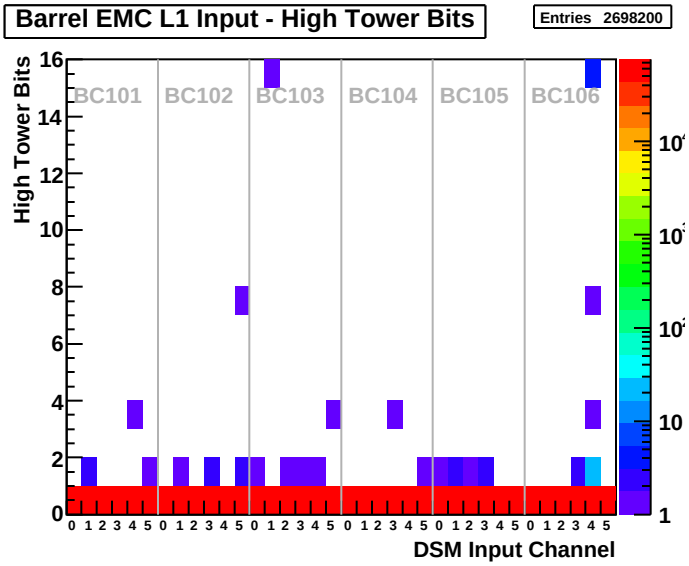
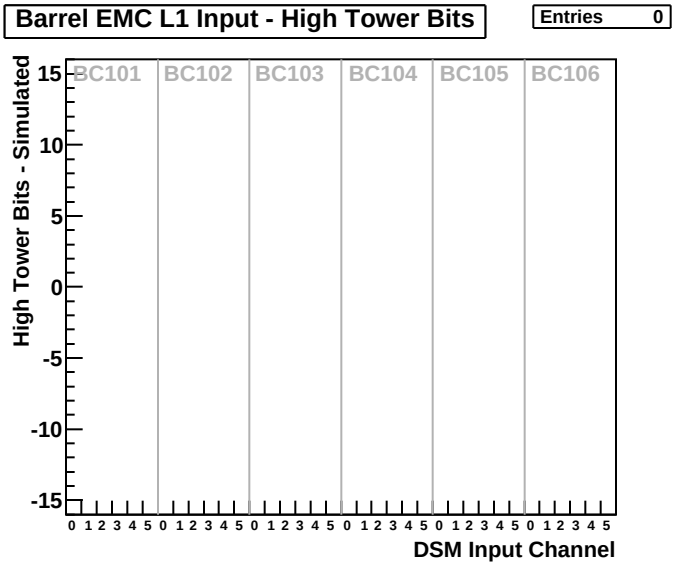

Barrel EMC L1 Input - Low Eta Sum
Entries 2698200
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

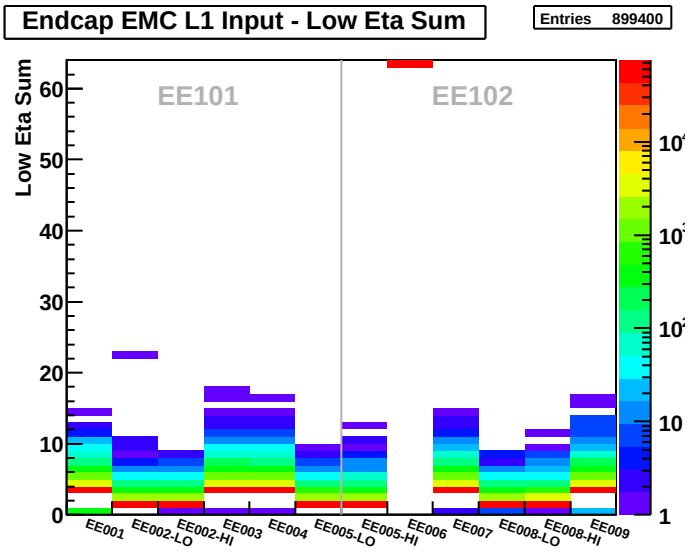

Barrel EMC L1 Input - Low Eta Sum
Entries 14
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

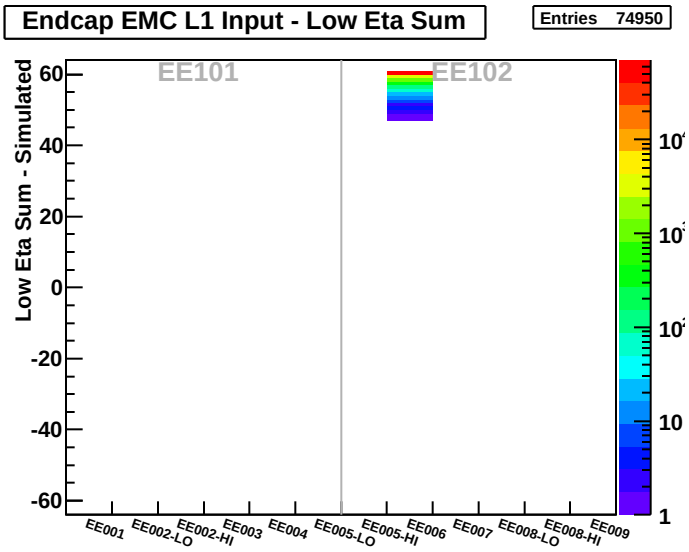

Barrel EMC L1 Input - High Eta Sum
Entries 2698200
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

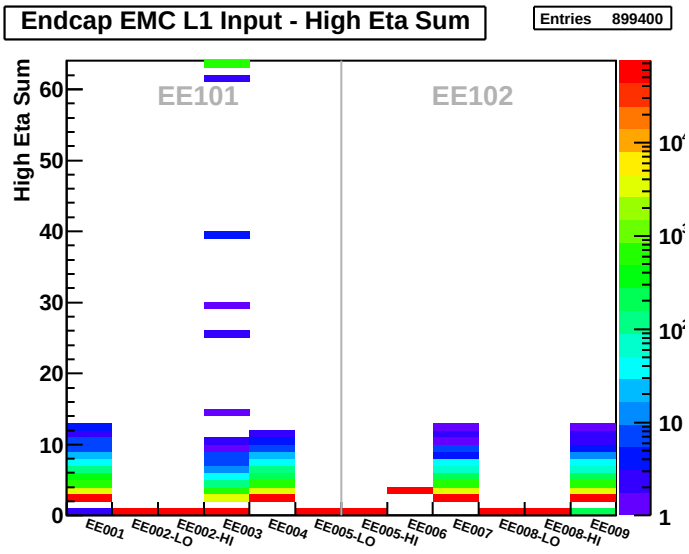

Barrel EMC L1 Input - High Eta Sum
Entries 12
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

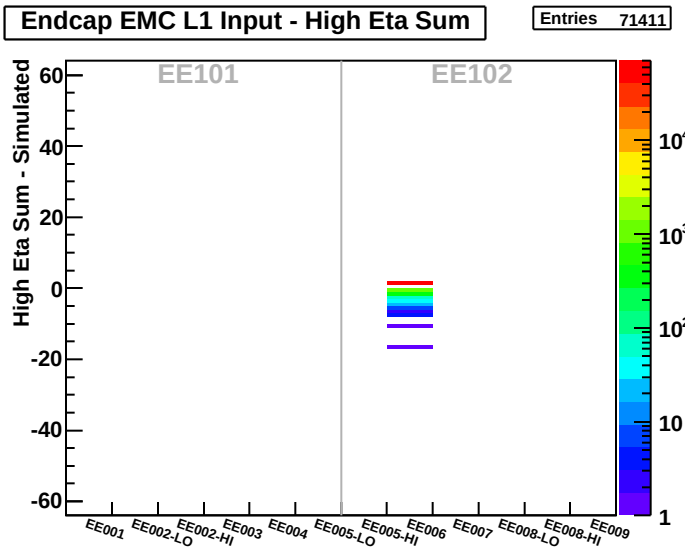

Barrel EMC L1 Input - High Tower Bits
Entries 2698200
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

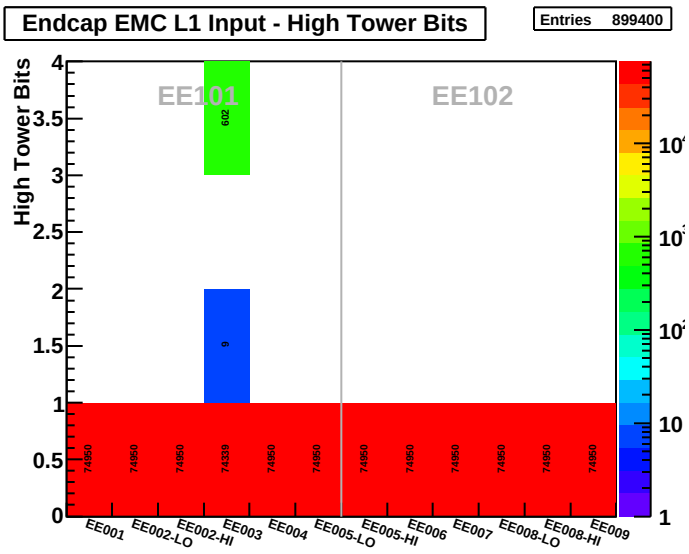

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

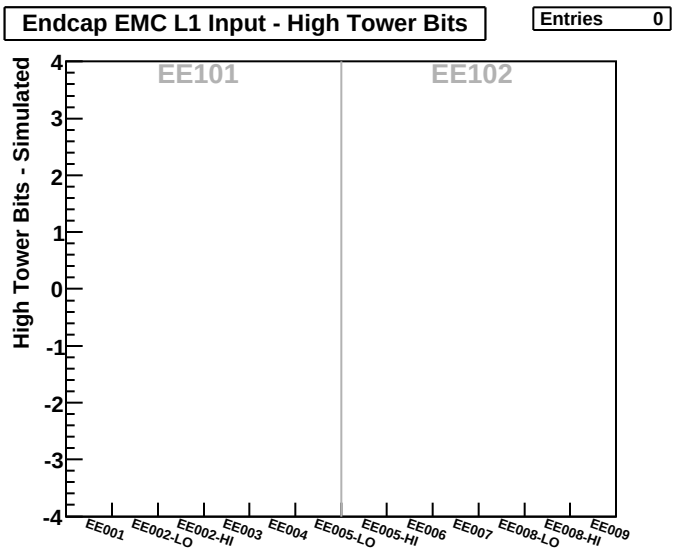

Endcap EMC L1 Input - Low Eta Sum
Entries 899400
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

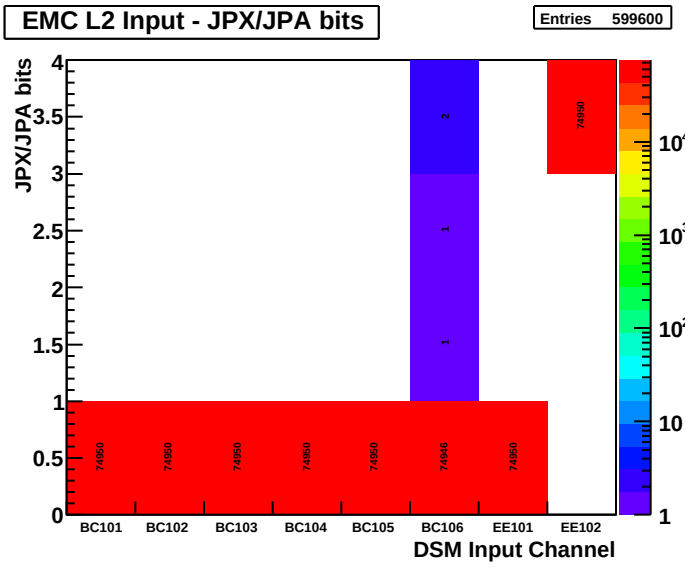

Endcap EMC L1 Input - Low Eta Sum
Entries 74950
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

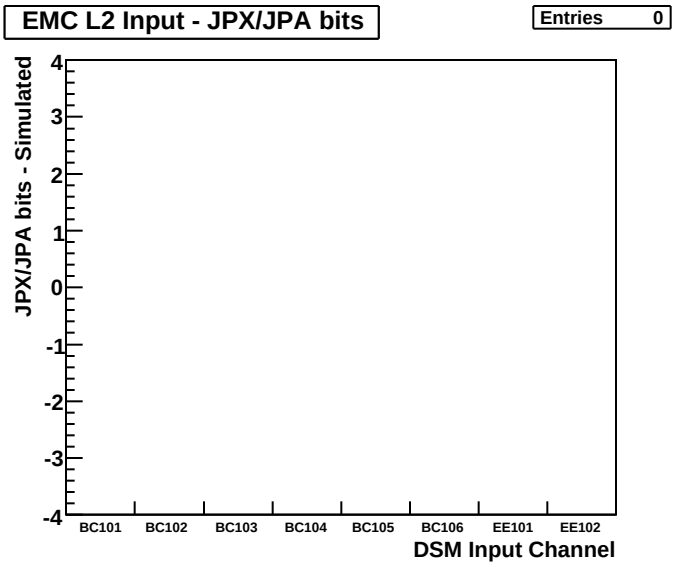

Endcap EMC L1 Input - High Eta Sum
Entries 899400
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

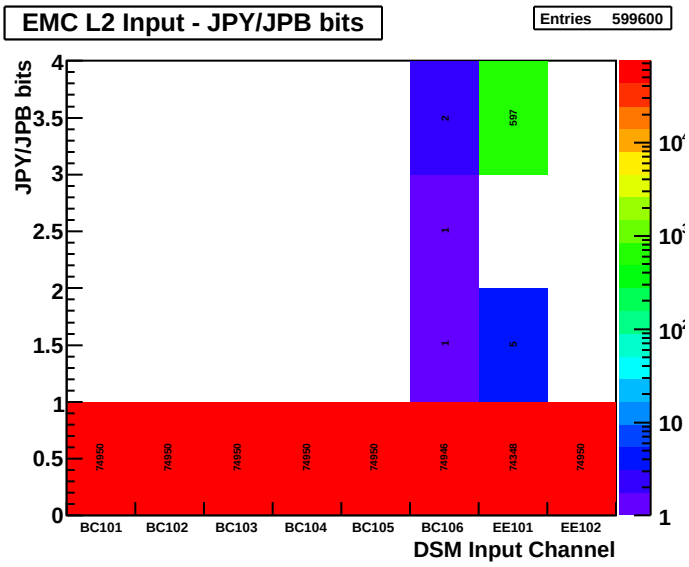

Endcap EMC L1 Input - High Eta Sum
Entries 71411
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

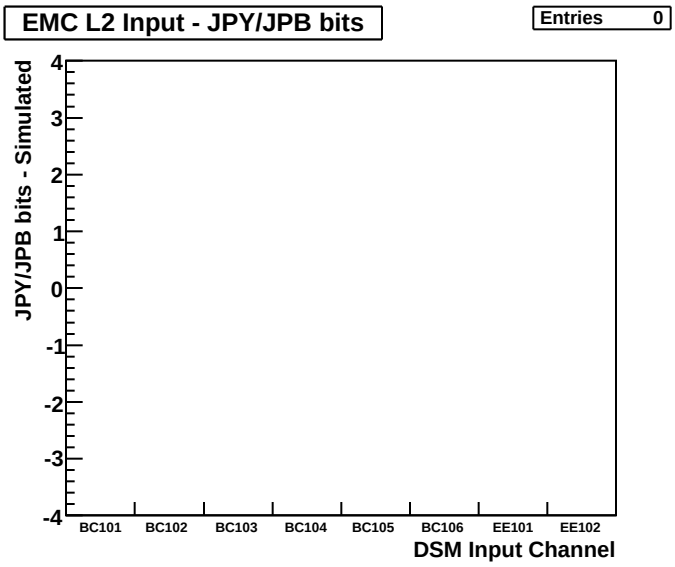

Endcap EMC L1 Input - High Tower Bits
Entries 899400
High Tower Bits
EE101
EE102
602
9
74950 74950 74950 74939 74950 74950 74950 74950 74950 74950 74950 74950
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

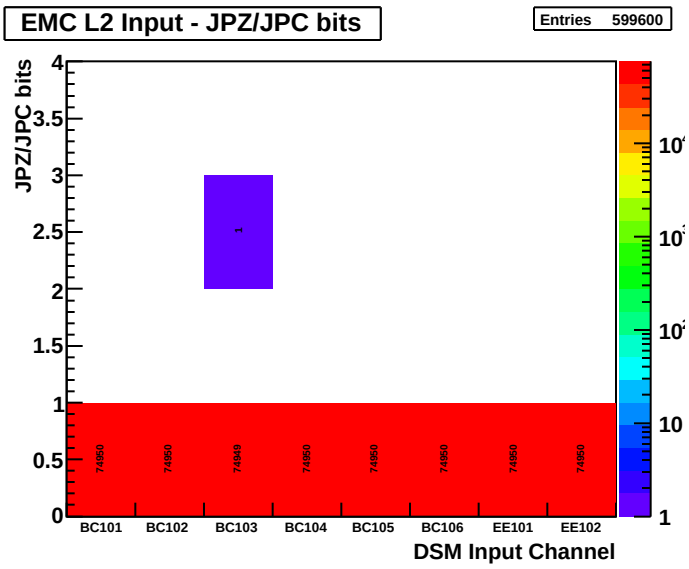

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

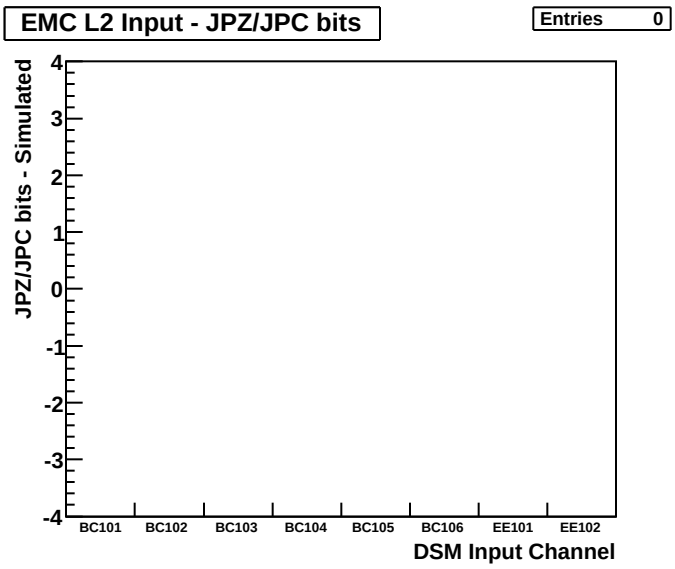

EMC L2 Input - JPX/JPA bits
Entries 599600
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

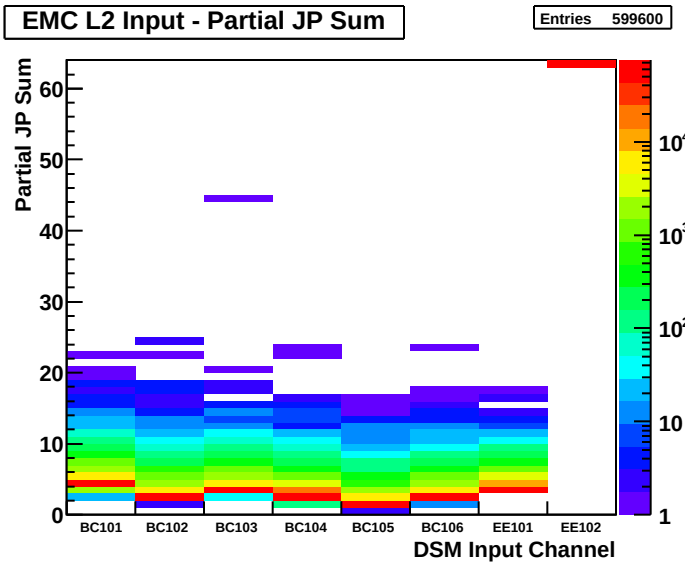

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

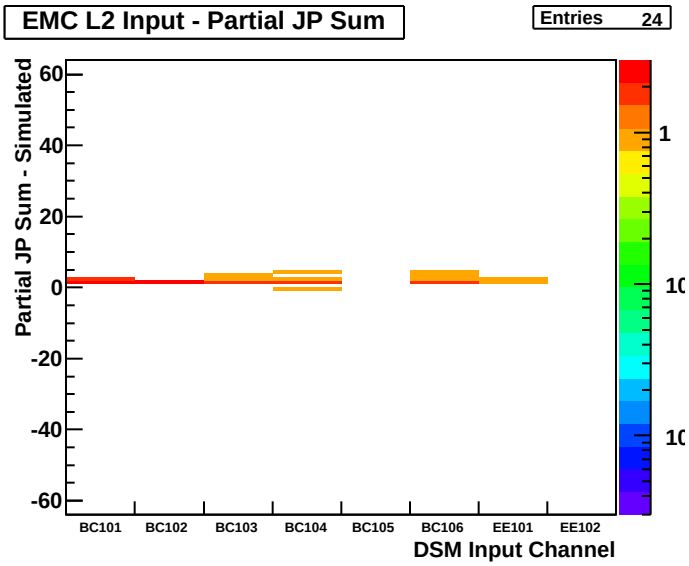

EMC L2 Input - JPY/JPB bits
Entries 599600
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

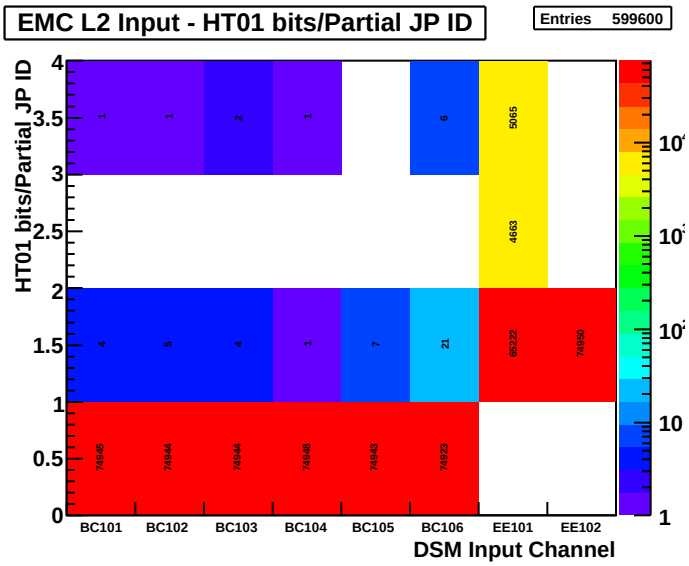

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

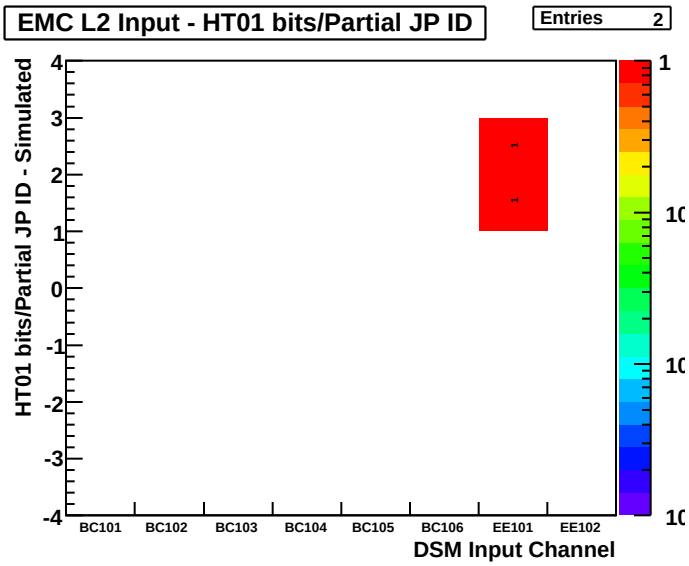

EMC L2 Input - JPZ/JPC bits
Entries 599600
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

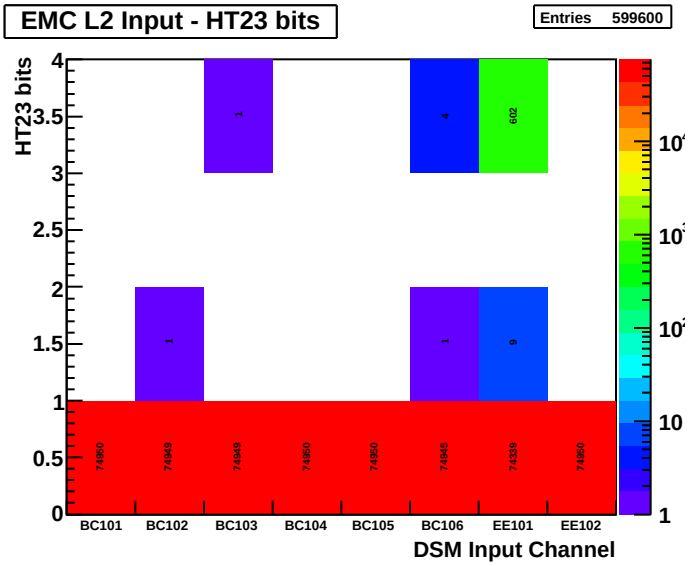

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

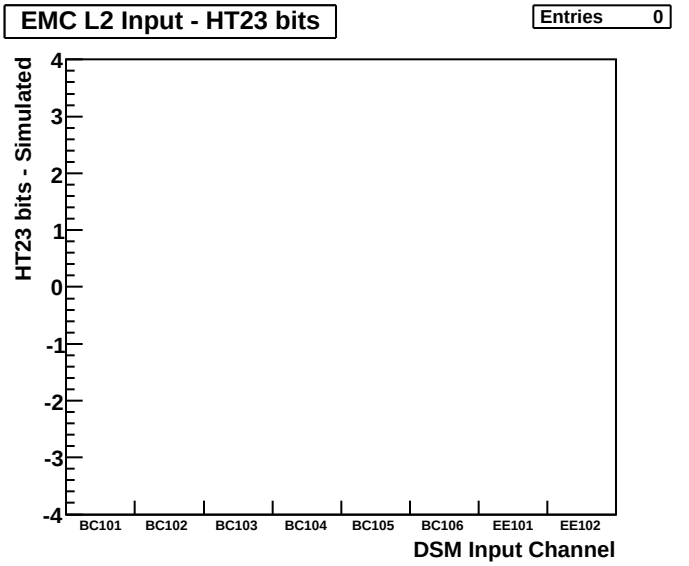

EMC L2 Input - Partial JP Sum
Entries 599600
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

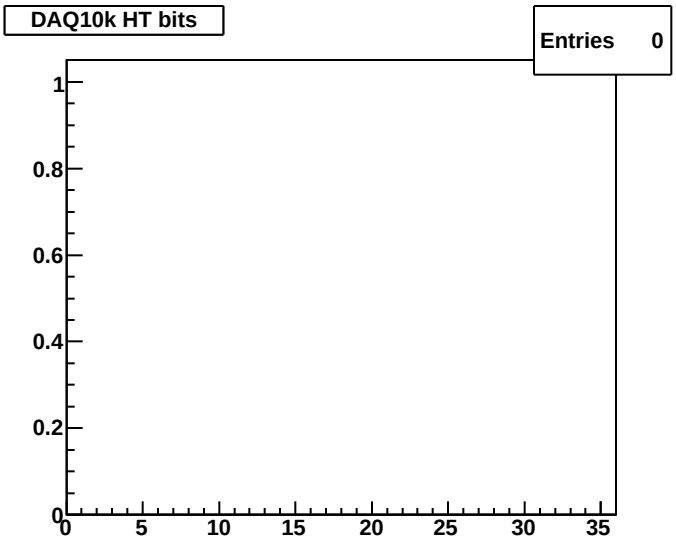

EMC L2 Input - Partial JP Sum
Entries 24
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

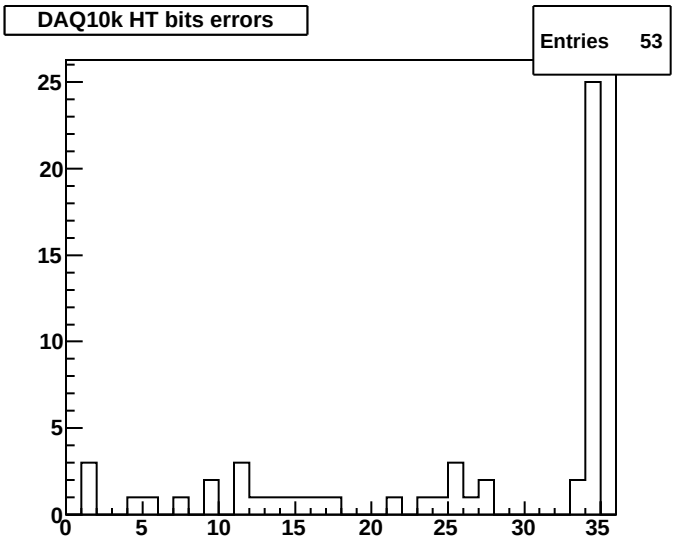

EMC L2 Input - HT01 bits/Partial JP ID
Entries 599600
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

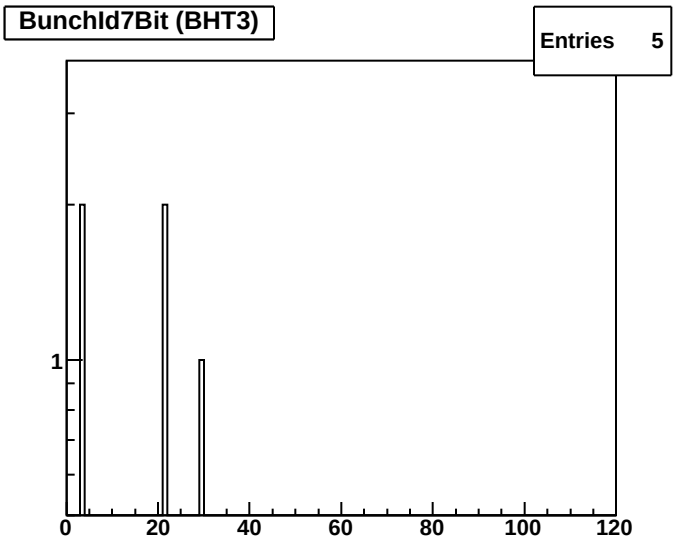

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

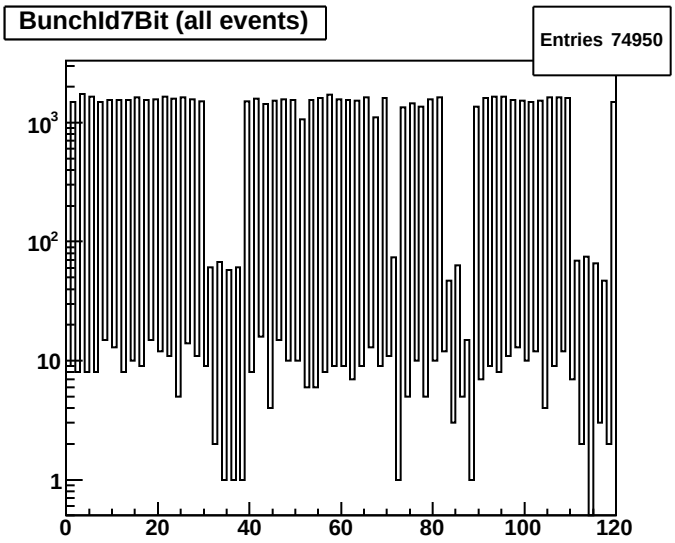

EMC L2 Input - HT23 bits
Entries 599600
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

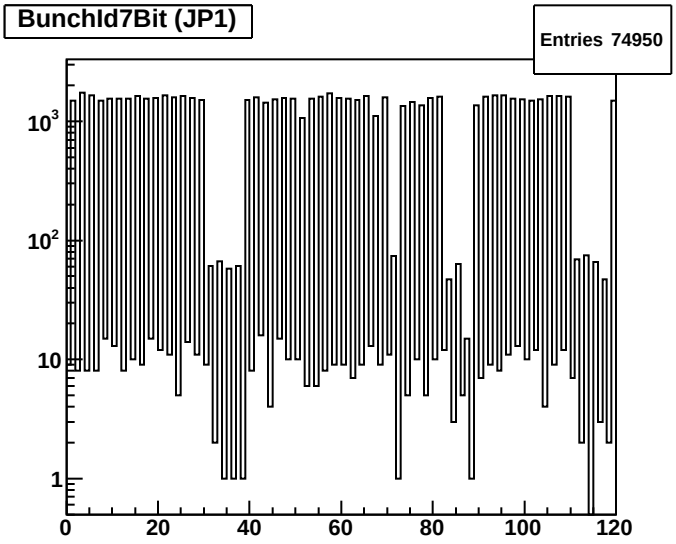

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

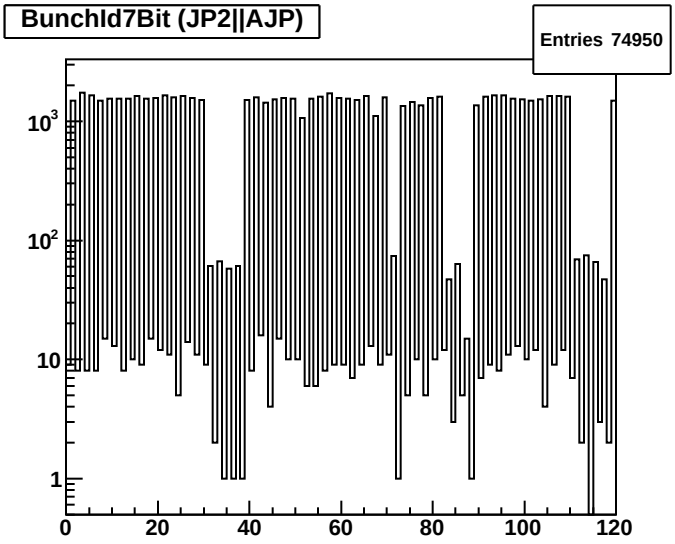

DAQ10k HT bits
Entries 0

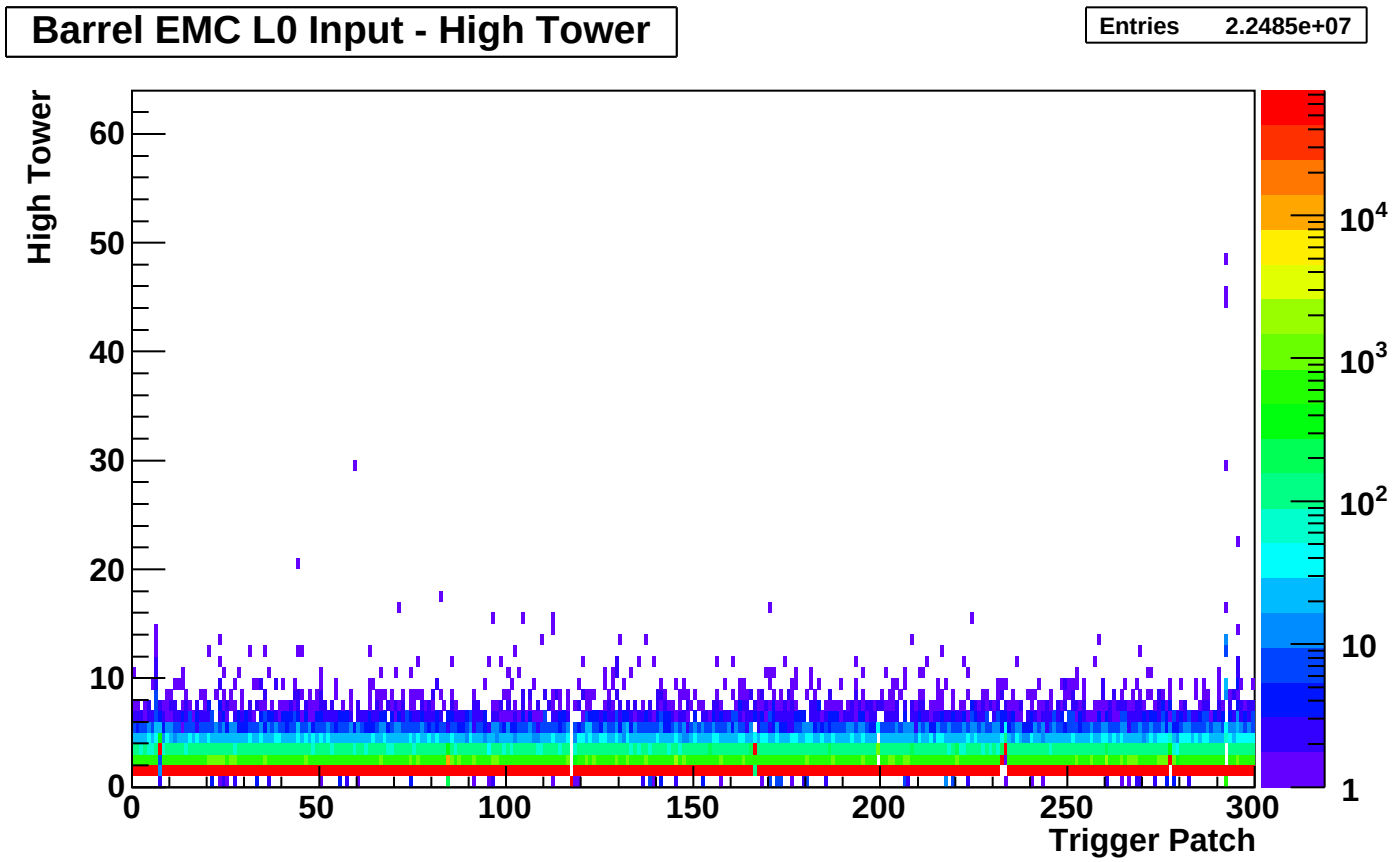

DAQ10k HT bits errors
Entries 53

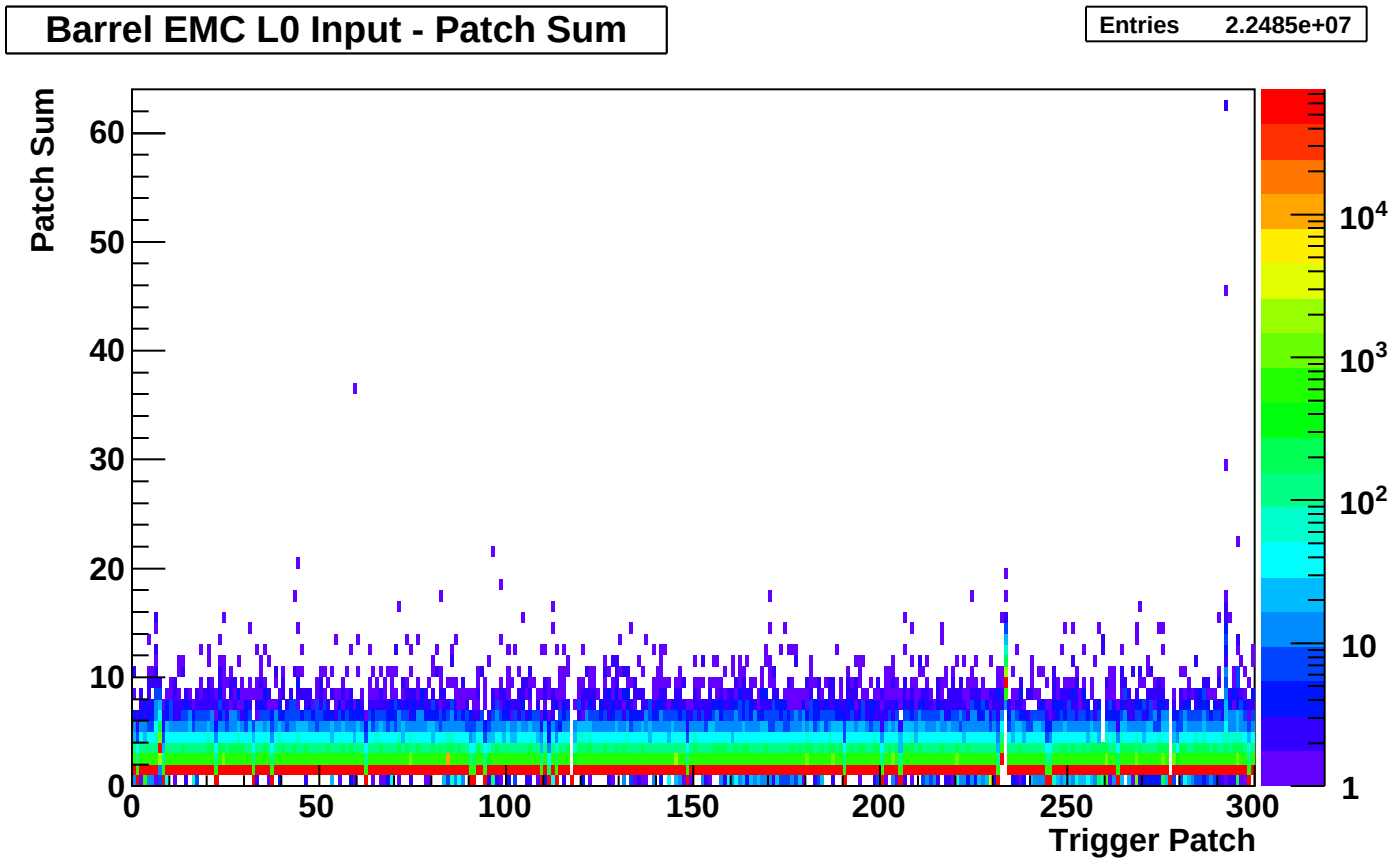

BunchId7Bit (BHT3)
Entries 5

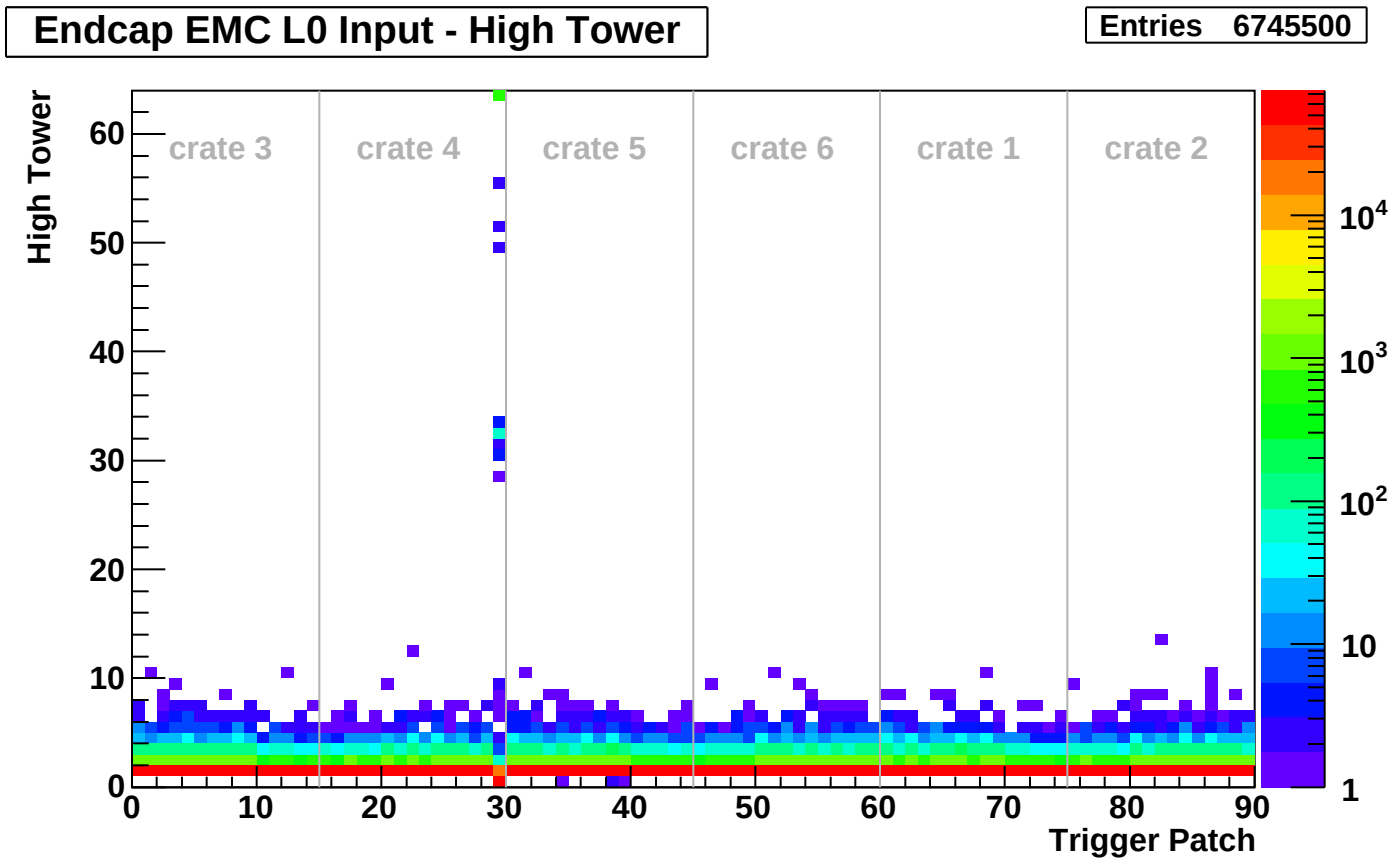

BunchId7Bit (all events)
Entries 74950

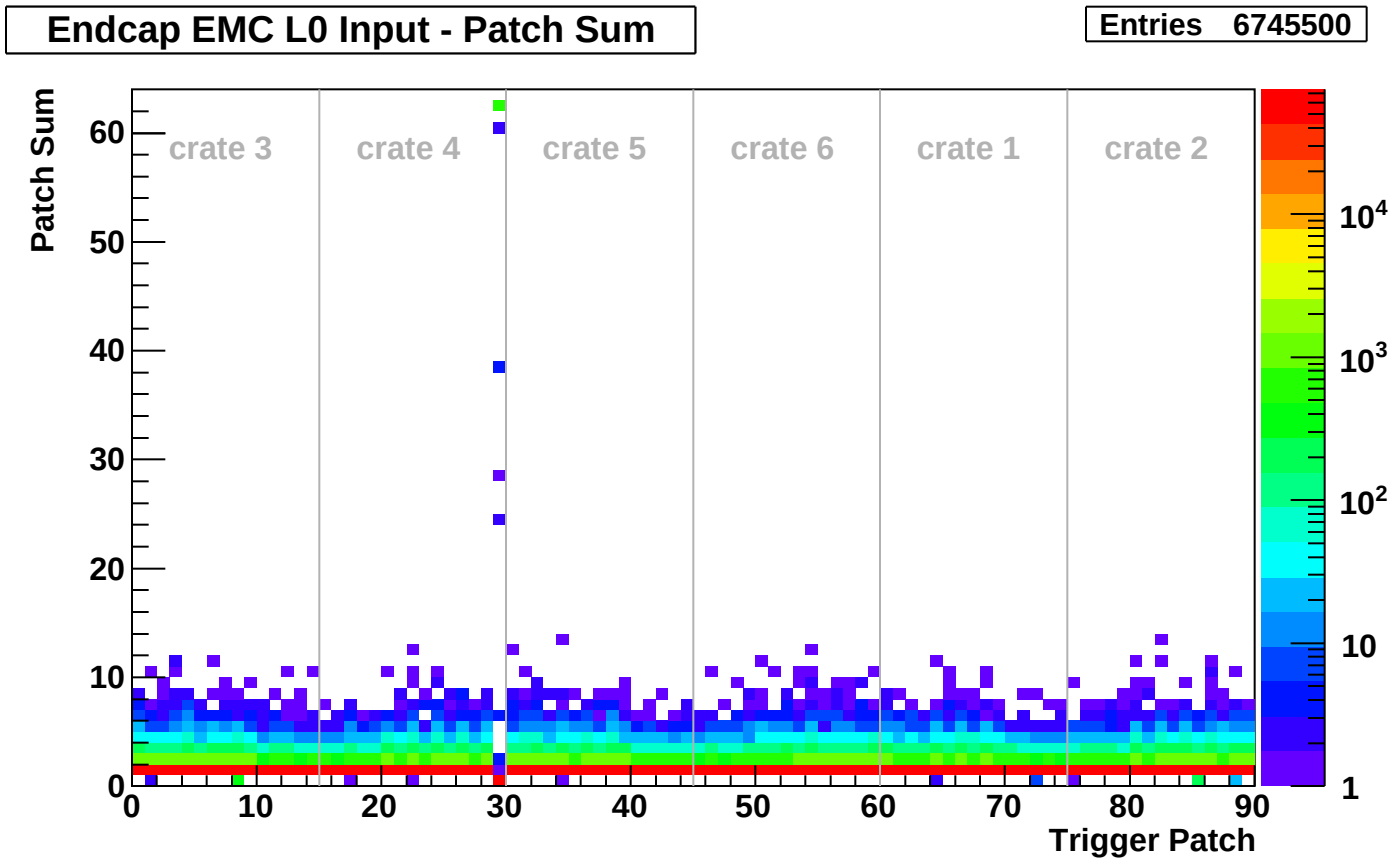

BunchId7Bit (JP1)
Entries 74950

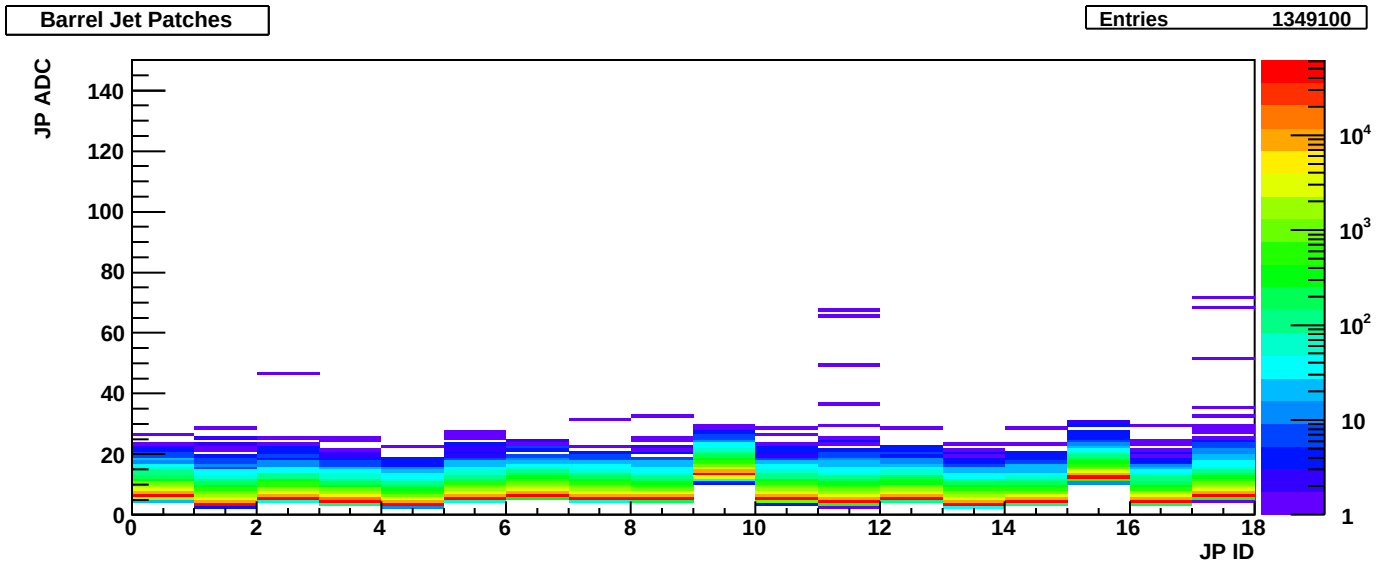

BunchId7Bit (JP2||AJP)
Entries 74950

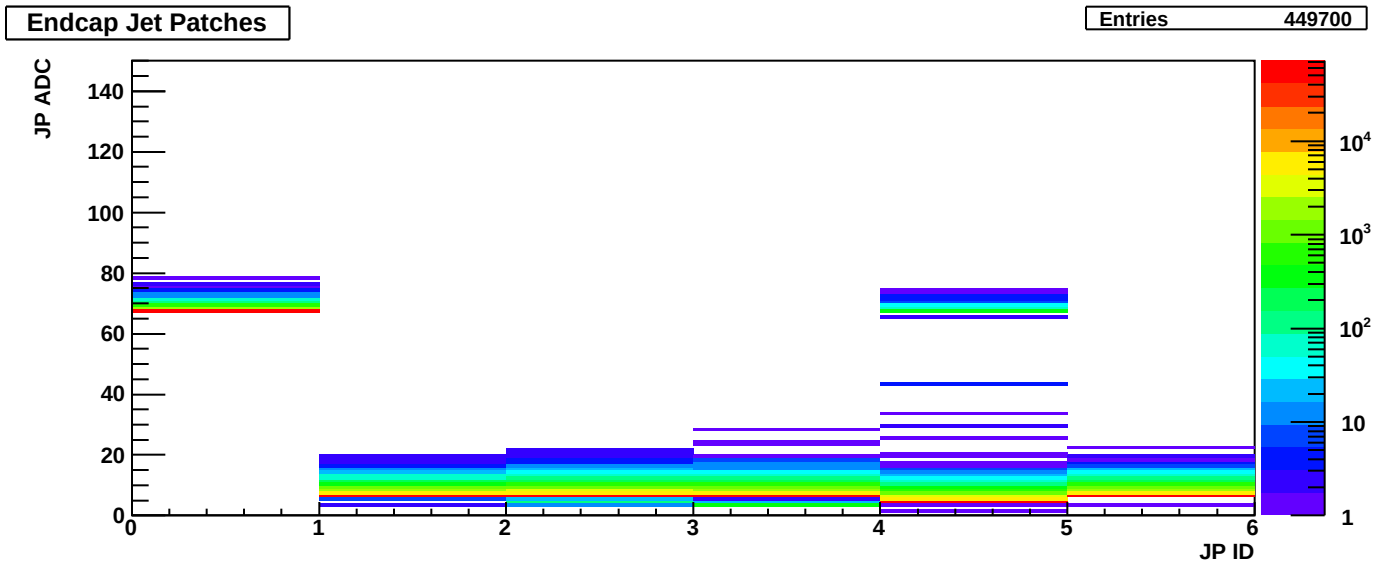

Barrel EMC L0 Input - High Tower
Entries 2.2485e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

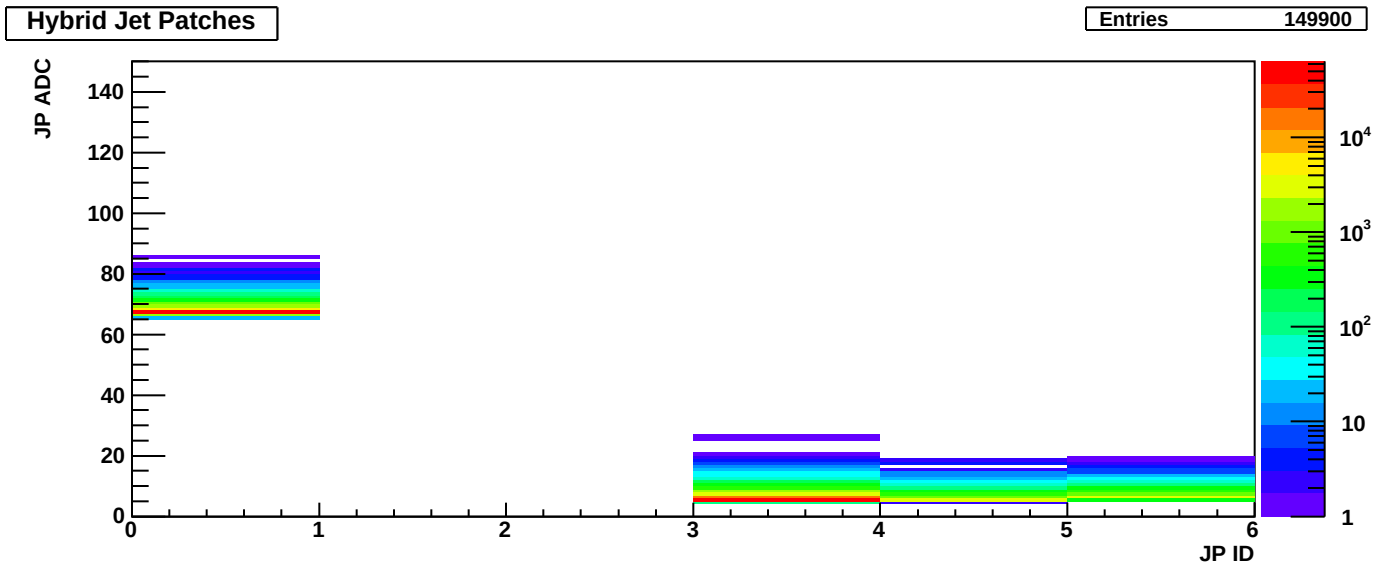

Barrel EMC L0 Input - Patch Sum
Entries 2.2485e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

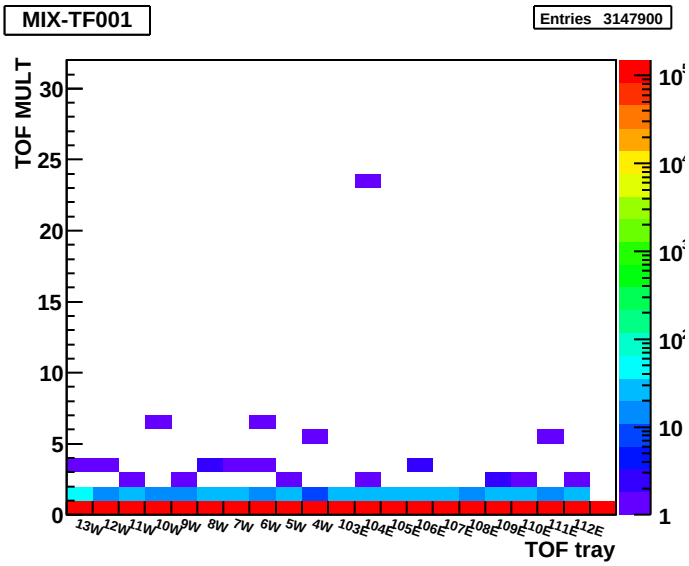

Endcap EMC L0 Input - High Tower
Entries 6745500
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

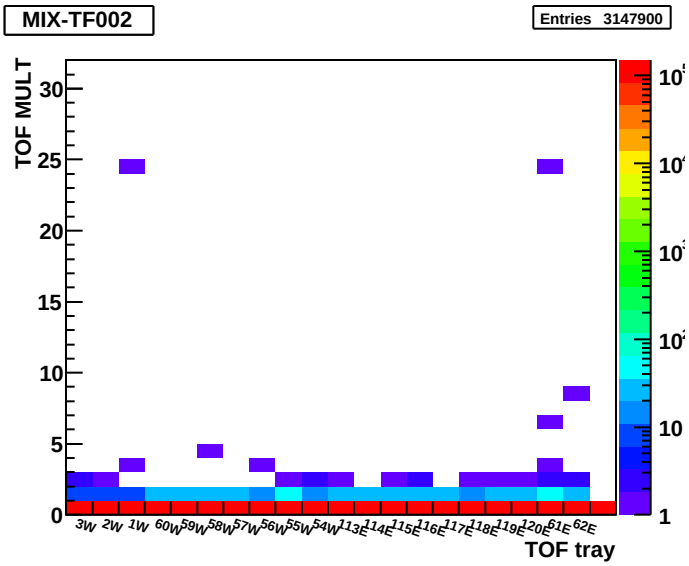

Endcap EMC L0 Input - Patch Sum
Entries 6745500
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

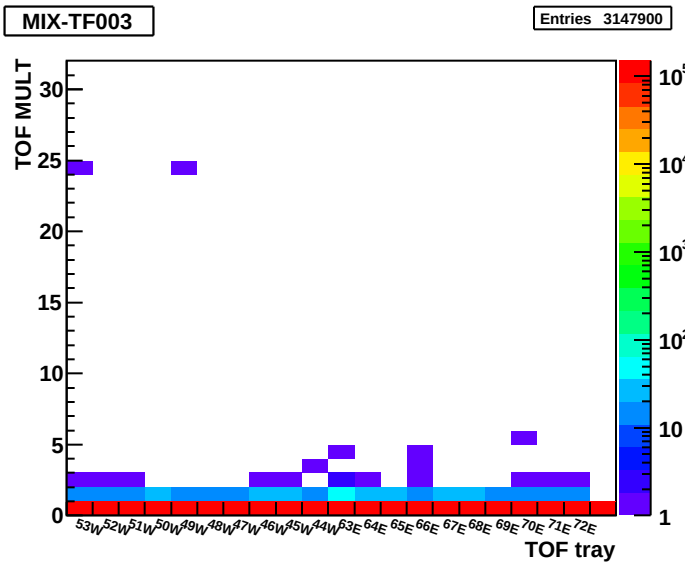

Barrel Jet Patches
Entries 1349100
JP ADC
JP ID

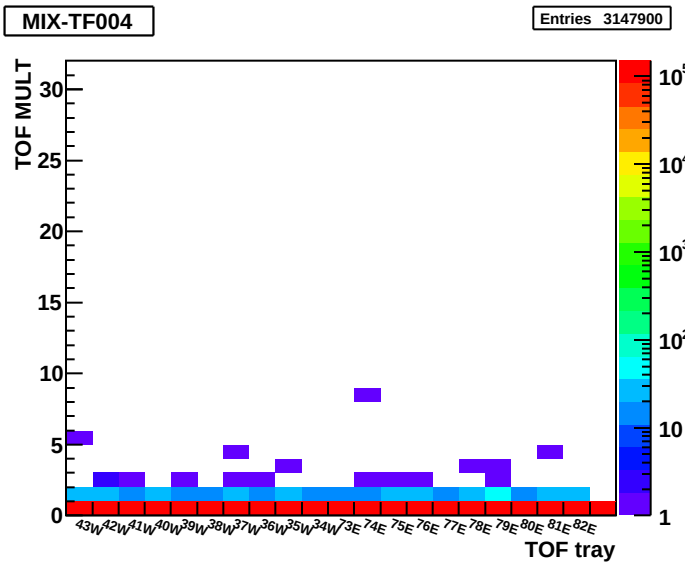

Endcap Jet Patches
Entries 449700
JP ADC
JP ID

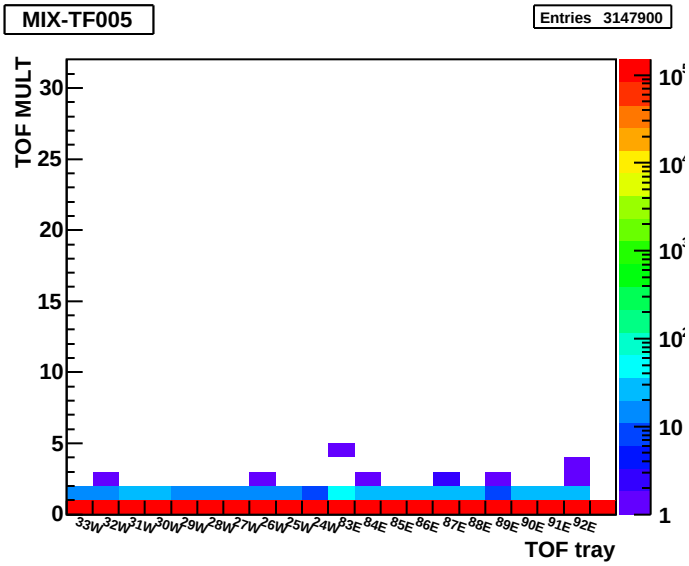

Hybrid Jet Patches
Entries 149900
JP ADC
JP ID

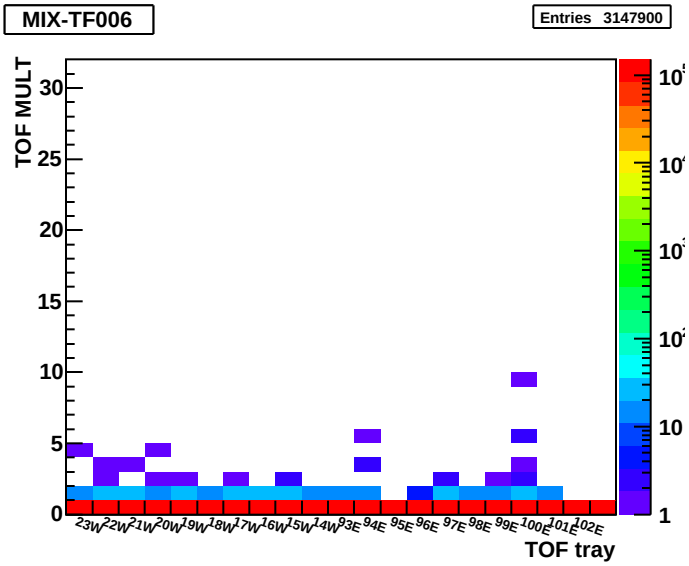

MIX-TF001
Entries 3147900
TOF MULT
TOF tray

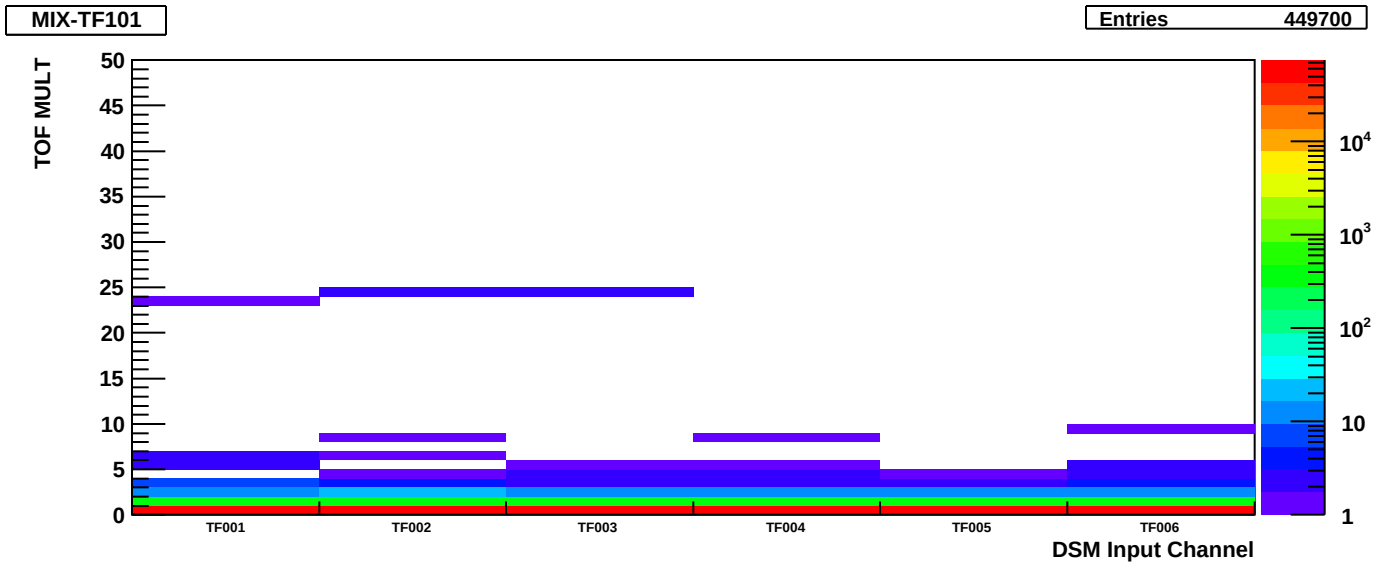

MIX-TF002
Entries 3147900
TOF MULT
TOF tray

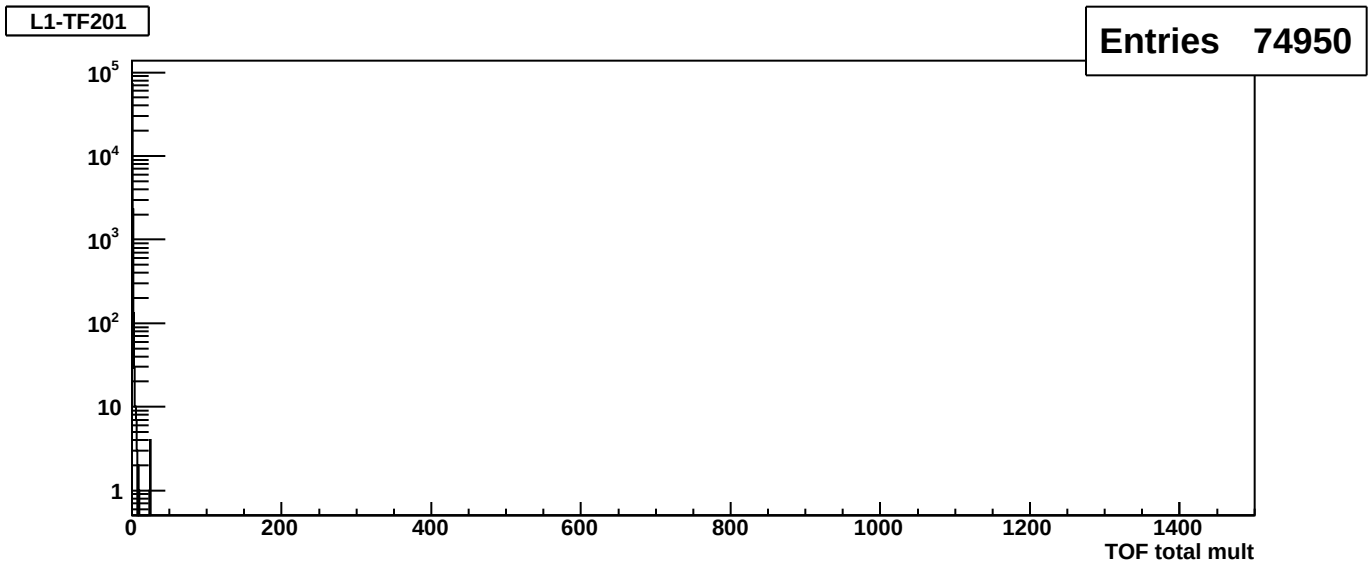

MIX-TF003
Entries 3147900
TOF MULT
TOF tray

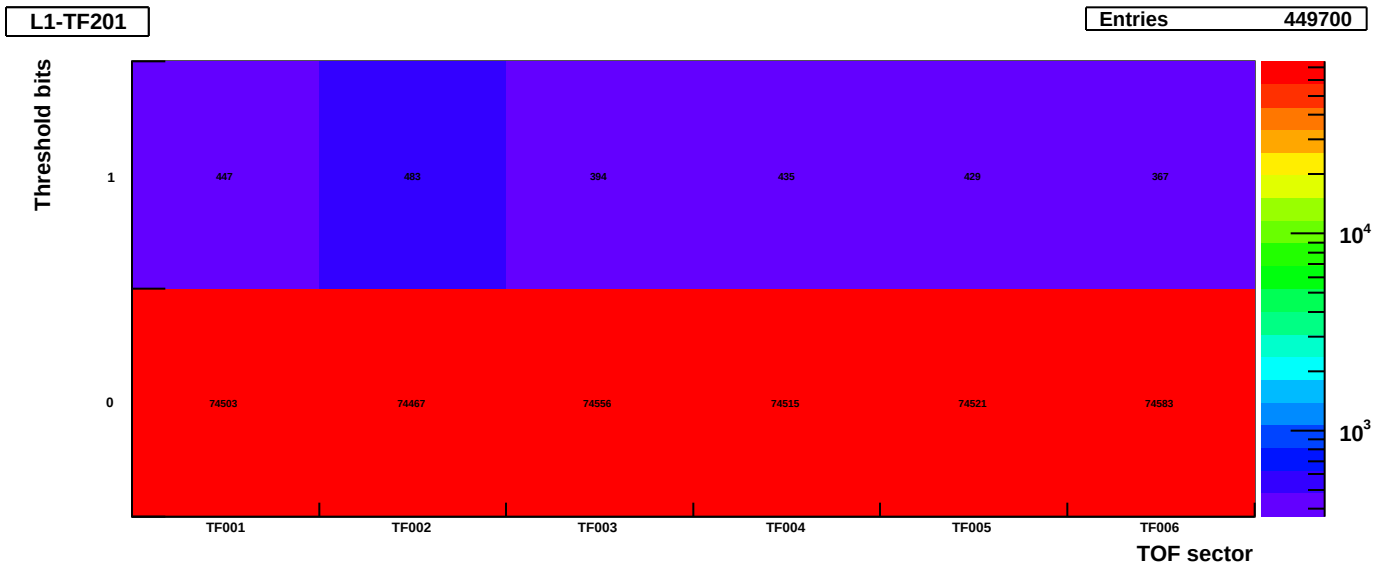

MIX-TF004
Entries 3147900
TOF MULT
TOF tray

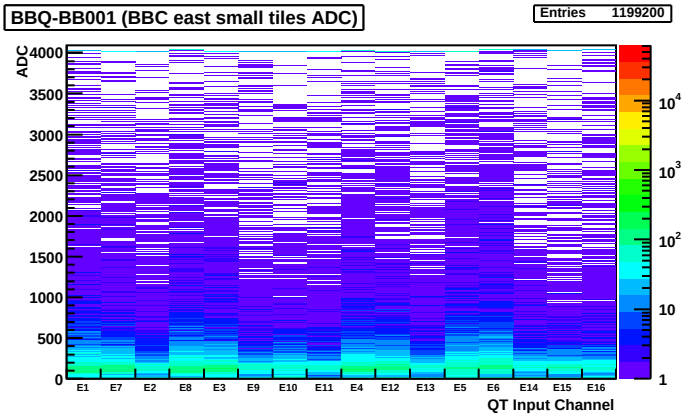

MIX-TF005
Entries 3147900
TOF MULT
TOF tray

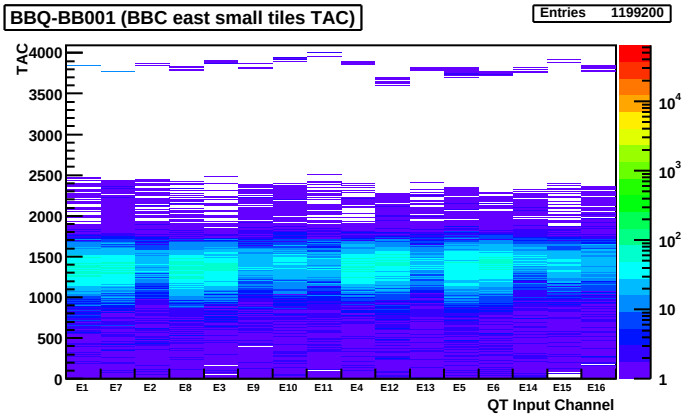

MIX-TF006
Entries 3147900
TOF MULT
TOF tray

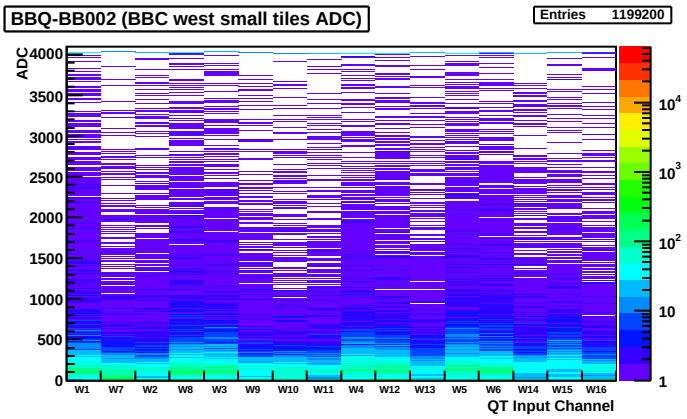

MIX-TF101
Entries 449700
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

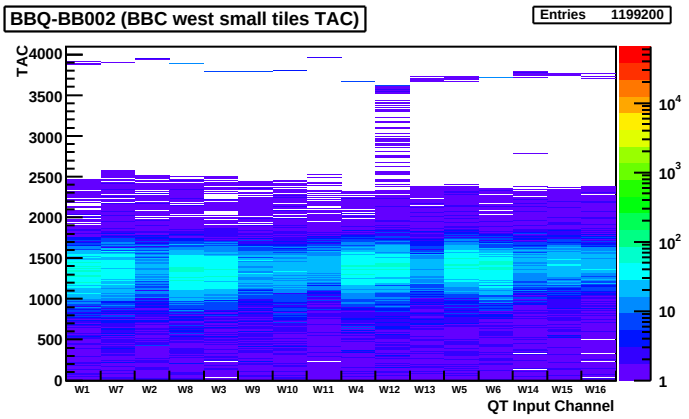

L1-TF201
Entries 74950
10^5
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

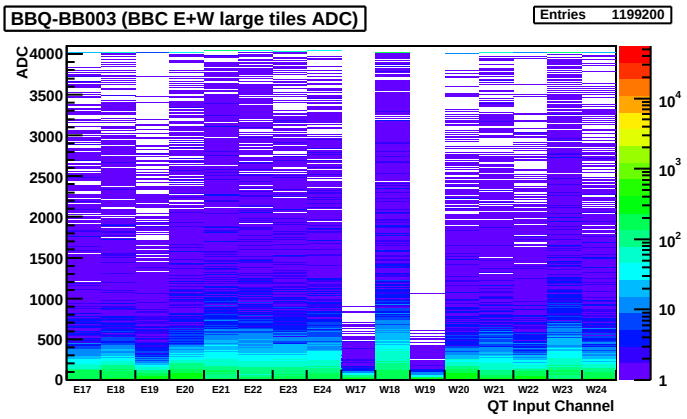

L1-TF201
Entries 449700
Threshold bits
1
0
447
483
394
435
429
367
74503
74467
74556
74515
74521
74583
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3

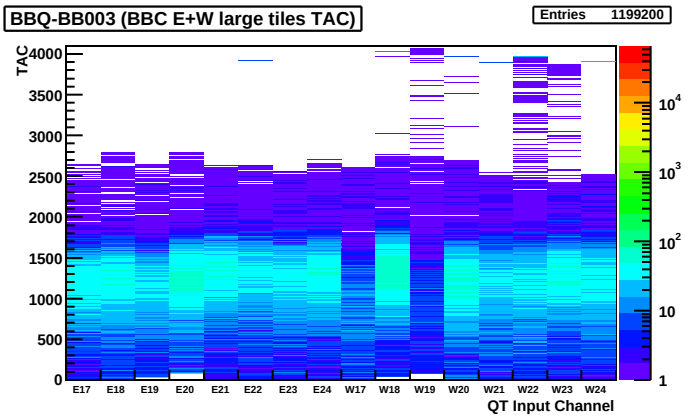

BBQ-BB001 (BBC east small tiles ADC)
Entries 1199200
ADC
QT Input Channel

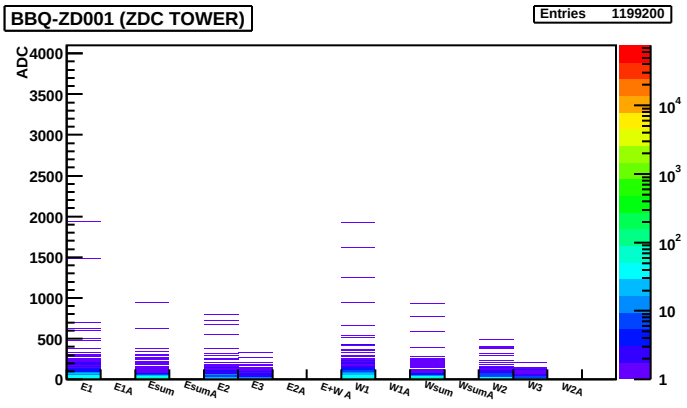

BBQ-BB001 (BBC east small tiles TAC)
Entries 1199200
TAC
QT Input Channel

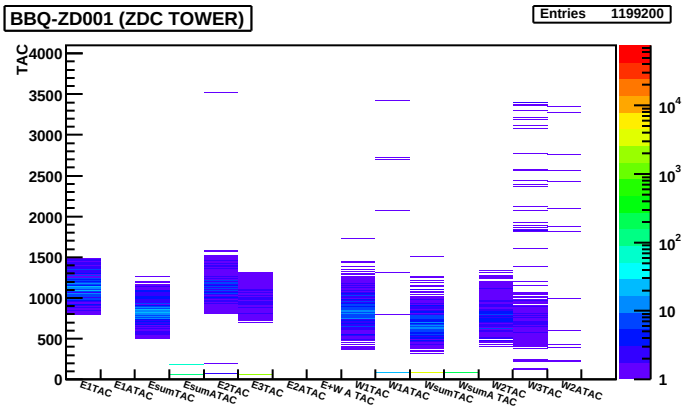

BBQ-BB002 (BBC west small tiles ADC)
Entries 1199200
ADC
QT Input Channel

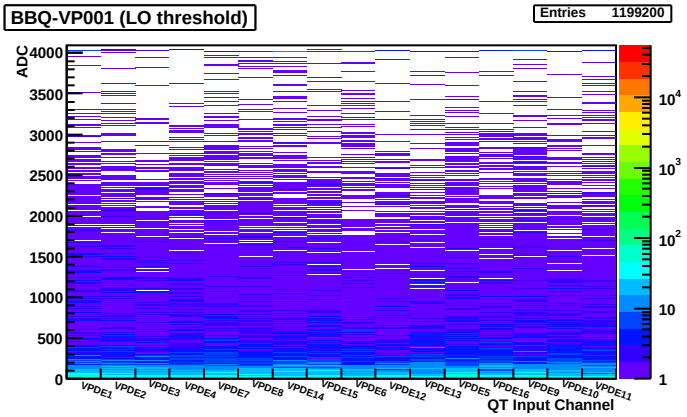

BBQ-BB002 (BBC west small tiles TAC)
Entries 1199200
TAC
QT Input Channel

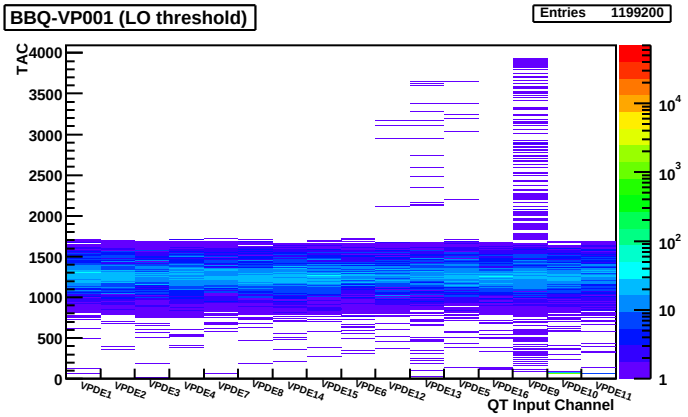

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1199200
ADC
QT Input Channel

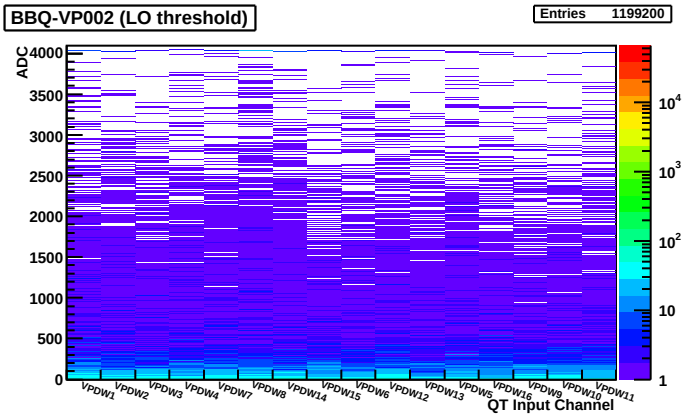

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1199200
TAC
QT Input Channel

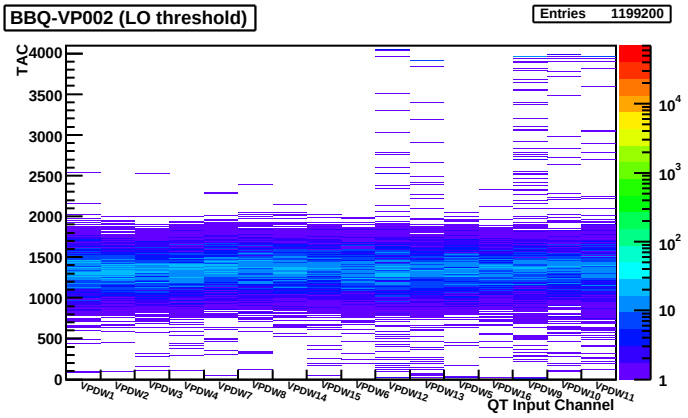

BBQ-ZD001 (ZDC TOWER)
Entries 1199200
ADC

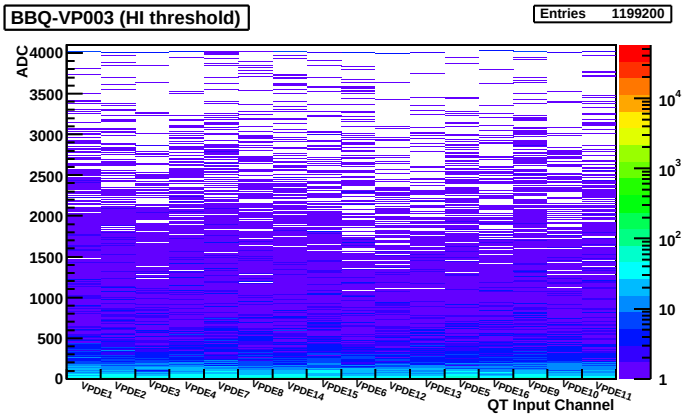

BBQ-ZD001 (ZDC TOWER)
Entries 1199200
TAC

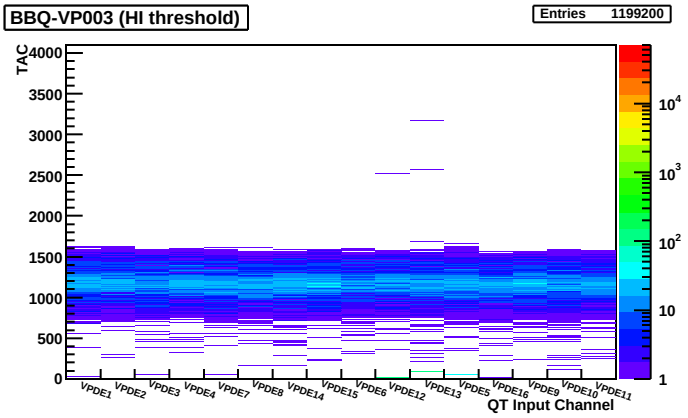

BBQ-VP001 (LO threshold)
Entries 1199200
ADC
QT Input Channel

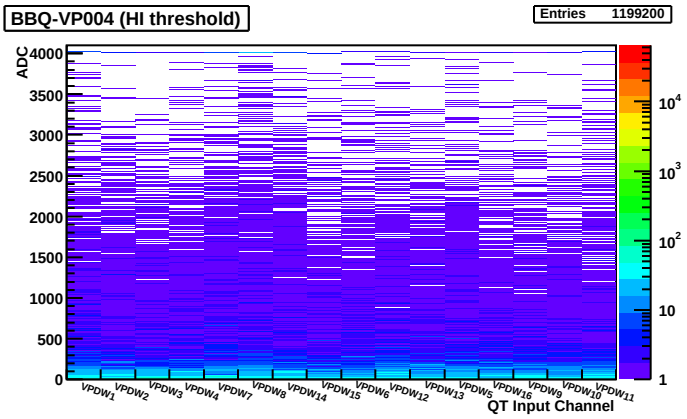

BBQ-VP001 (LO threshold)
Entries 1199200
TAC
QT Input Channel

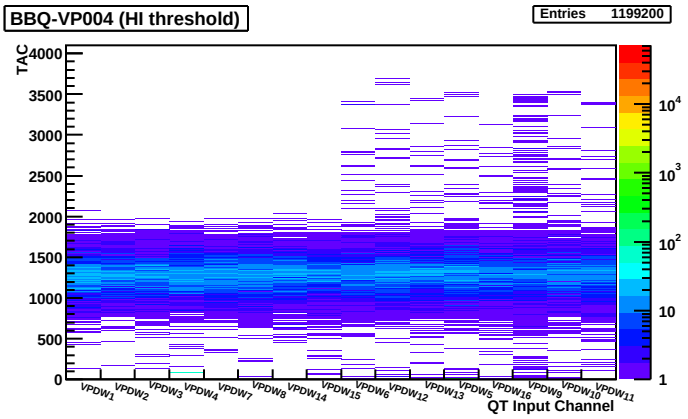

BBQ-VP002 (LO threshold)
Entries 1199200
ADC
QT Input Channel

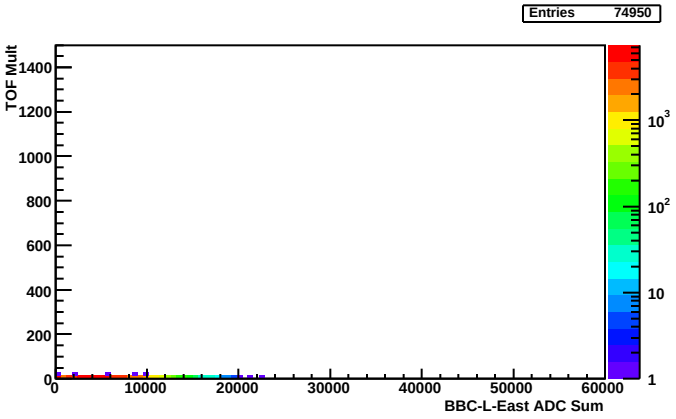

BBQ-VP002 (LO threshold)
Entries 1199200
TAC
QT Input Channel

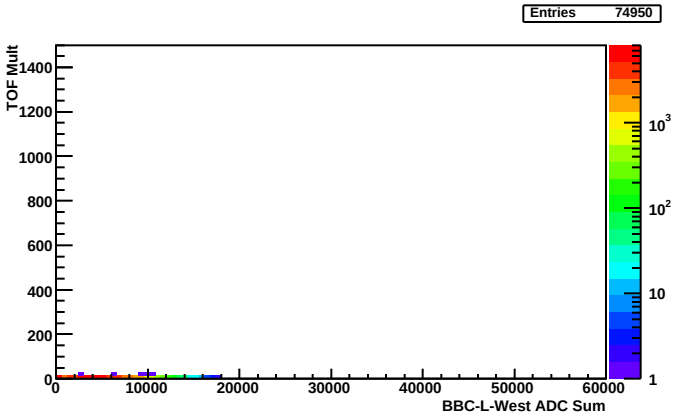

BBQ-VP003 (HI threshold)
Entries 1199200
ADC
QT Input Channel

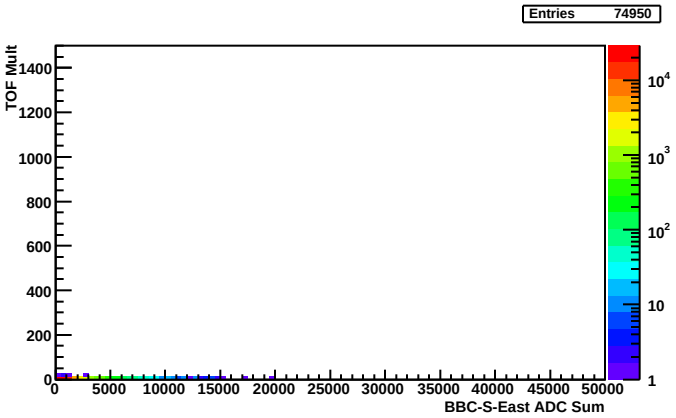

BBQ-VP003 (HI threshold)
Entries 1199200
TAC
QT Input Channel

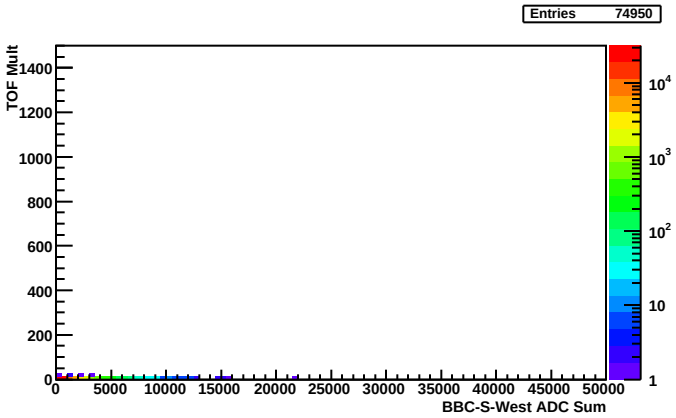

BBQ-VP004 (HI threshold)
Entries 1199200
ADC
QT Input Channel

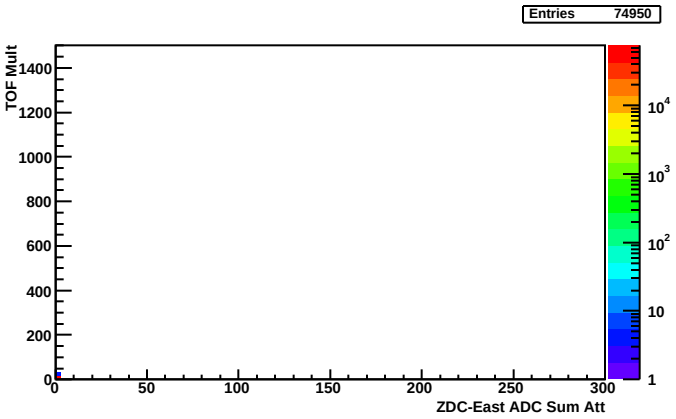

BBQ-VP004 (HI threshold)
Entries 1199200
TAC
QT Input Channel

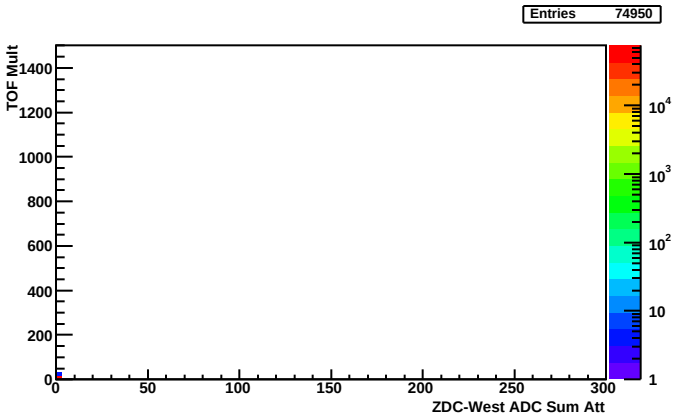

Entries 74950
TOF Mult
BBC-L-East ADC Sum

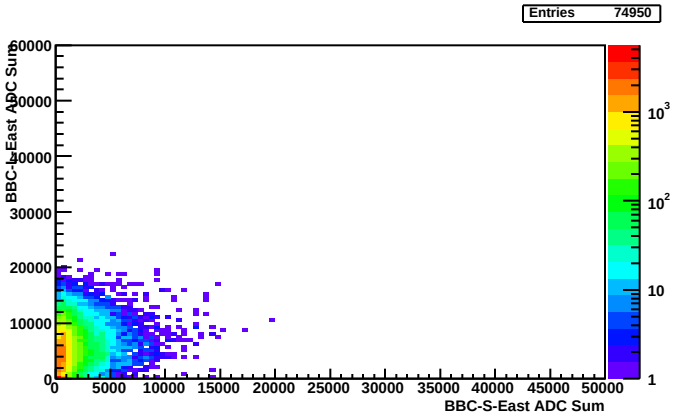

Entries 74950
TOF Mult
BBC-L-West ADC Sum

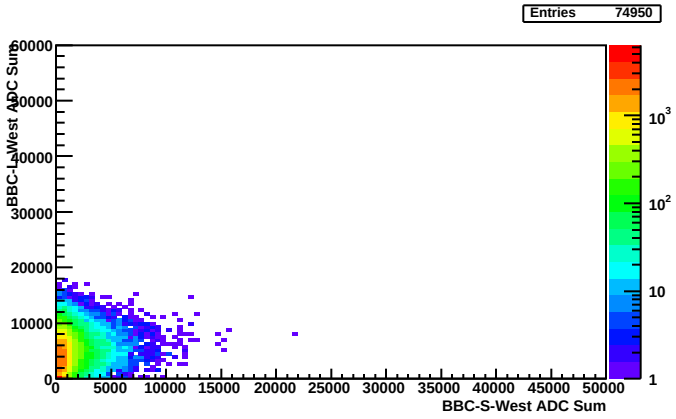

Entries 74950
TOF Mult
BBC-S-East ADC Sum

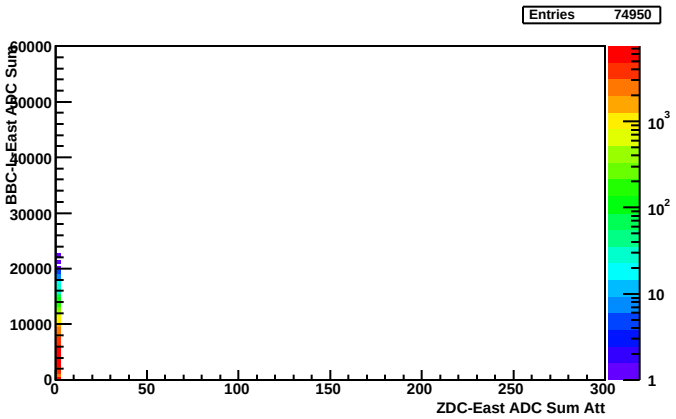

Entries 74950
TOF Mult
BBC-S-West ADC Sum

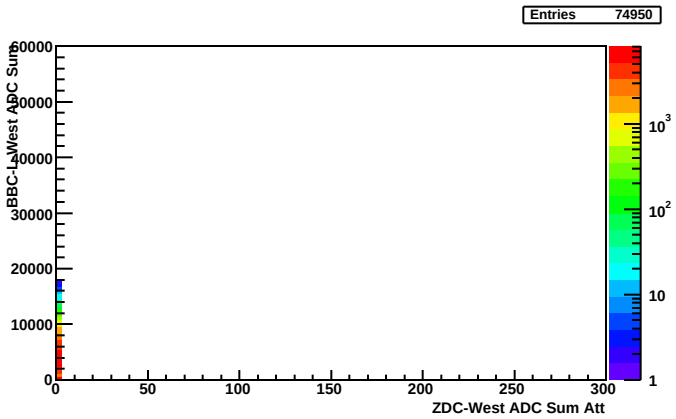

Entries 74950
TOF Mult
ZDC-East ADC Sum Att

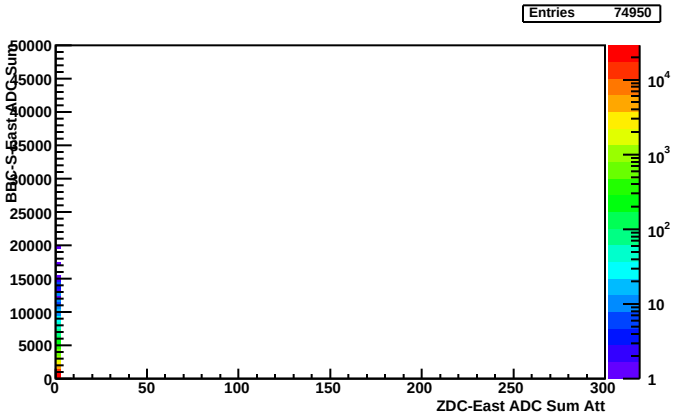

Entries 74950
TOF Mult
ZDC-West ADC Sum Att

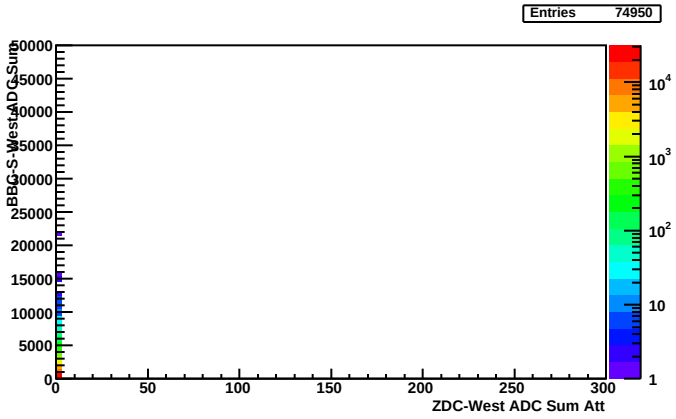

Entries 74950
BBC-L-East ADC Sum
BBC-S-East ADC Sum

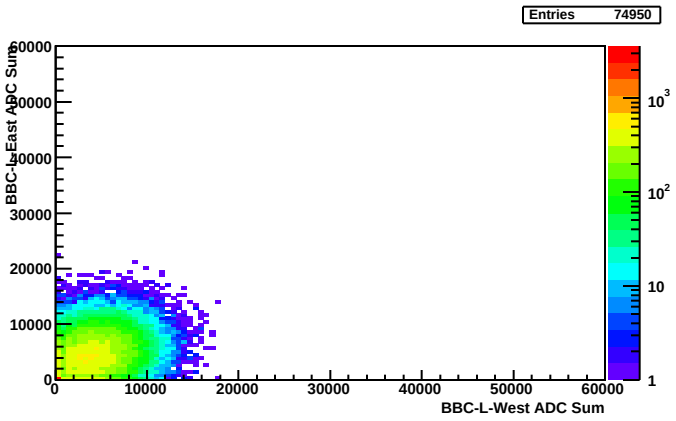

Entries 74950
BBC-L-West ADC Sum
BBC-S-West ADC Sum

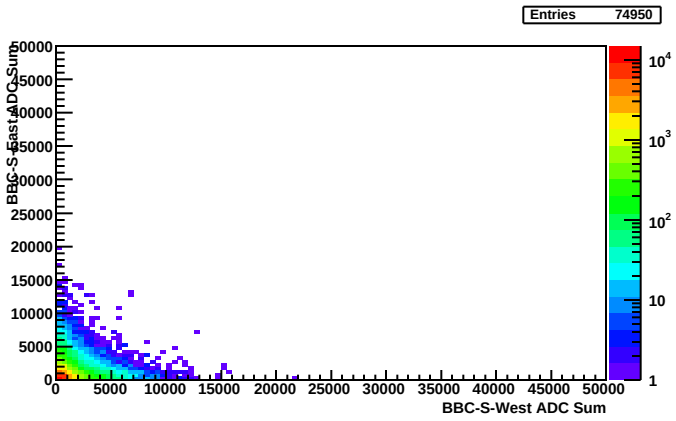

Entries 74950
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

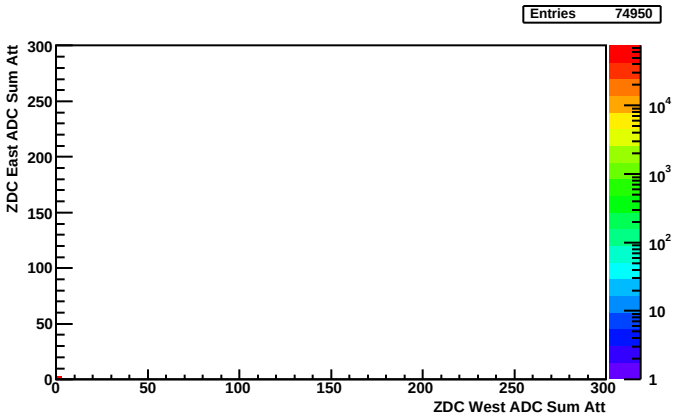

Entries 74950
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

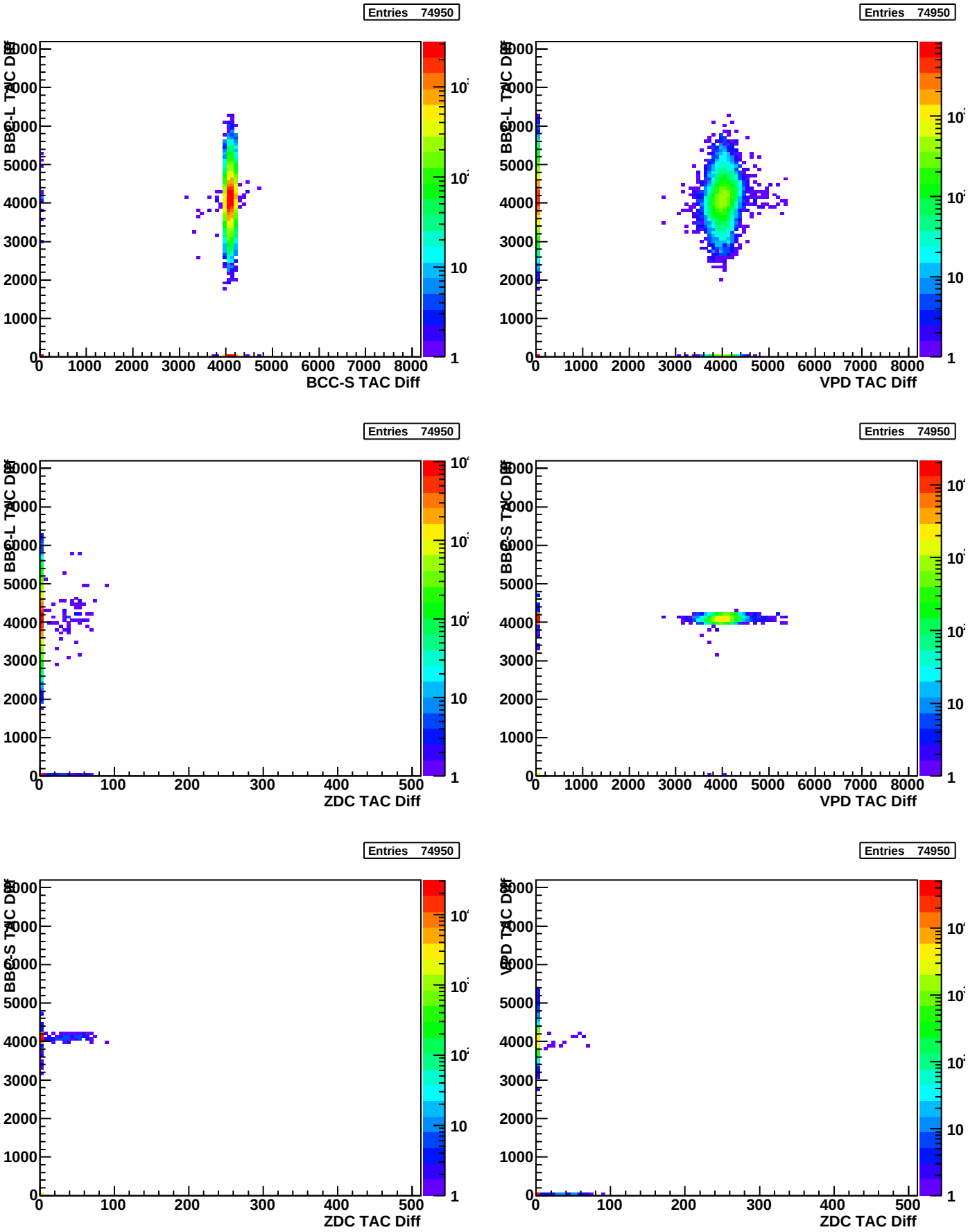

Entries 74950
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

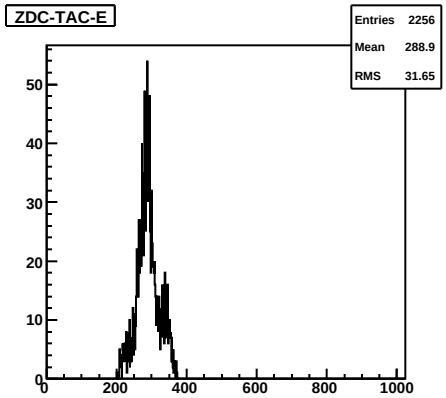

Entries 74950
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

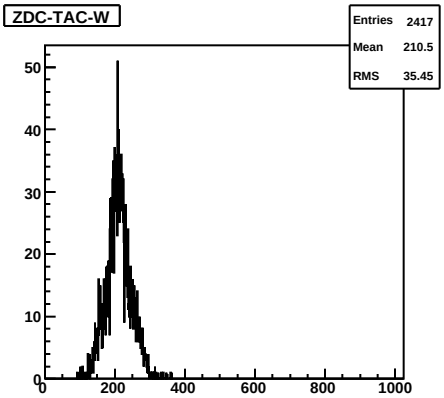

Entries 74950
BBC-L-East ADC Sum
BBC-L-West ADC Sum

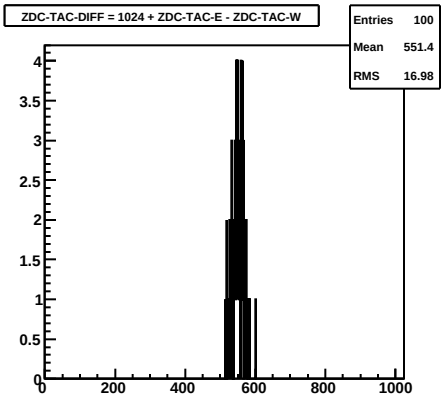

Entries 74950
BBC-S-East ADC Sum
BBC-S-West ADC Sum

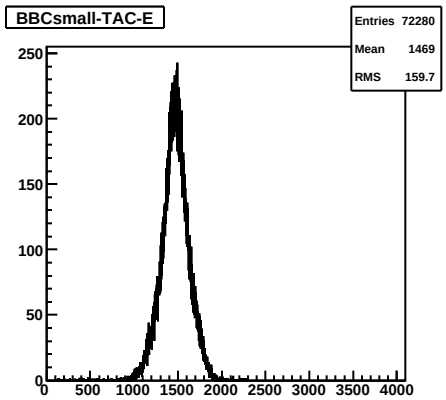

Entries 74950
ZDC East ADC Sum Att
ZDC West ADC Sum Att

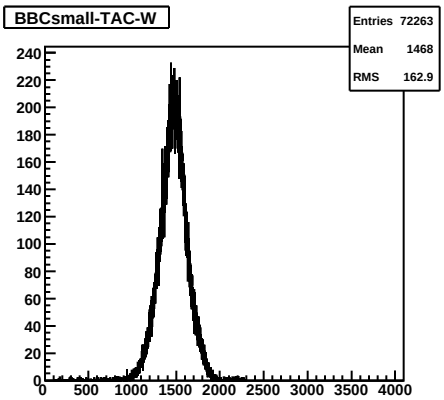

Entries 74950
BBC-L TAC Diff
BCC-S TAC Diff
Entries 74950
BBC-L TAC Diff
VPD TAC Diff
Entries 74950
BBC-L TAC Diff
ZDC TAC Diff
Entries 74950
BBC-S TAC Diff
VPD TAC Diff
Entries 74950
BBC-S TAC Diff
ZDC TAC Diff
Entries 74950
VPD TAC Diff
ZDC TAC Diff

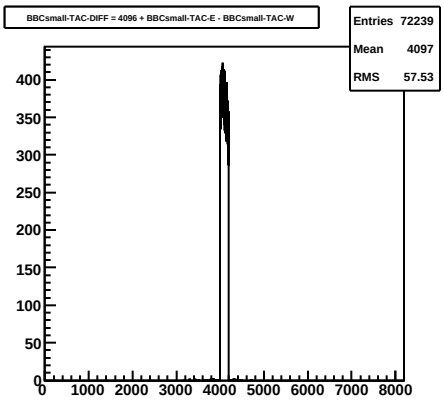

ZDC-TAC-E
Entries 2256
Mean 288.9
RMS 31.65

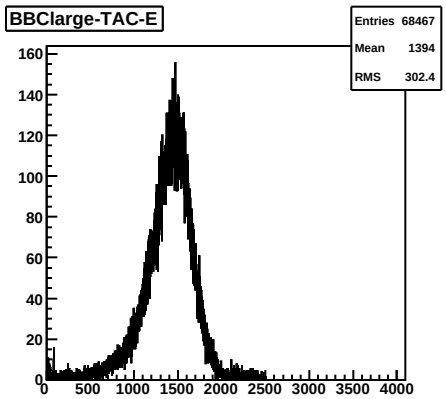

ZDC-TAC-W
Entries 2417
Mean 210.5
RMS 35.45

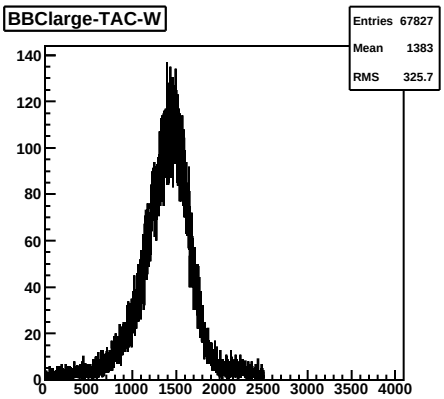

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 100
Mean 551.4
RMS 16.98

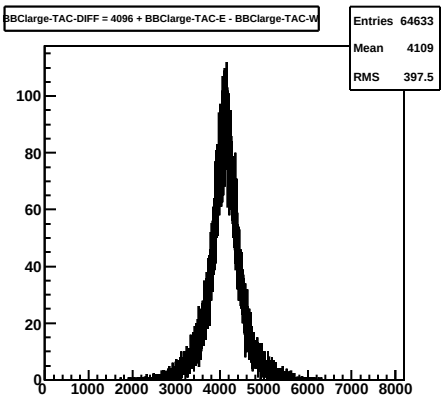

BBCsmall-TAC-E
Entries 72280
Mean 1469
RMS 159.7

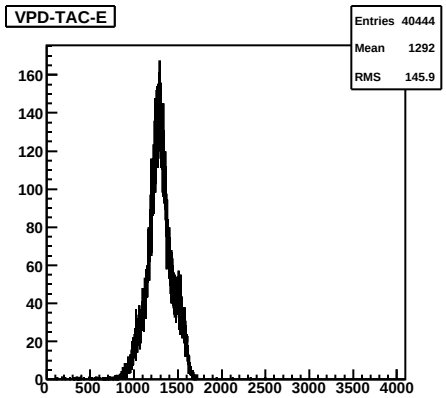

BBCsmall-TAC-W
Entries 72263
Mean 1468
RMS 162.9

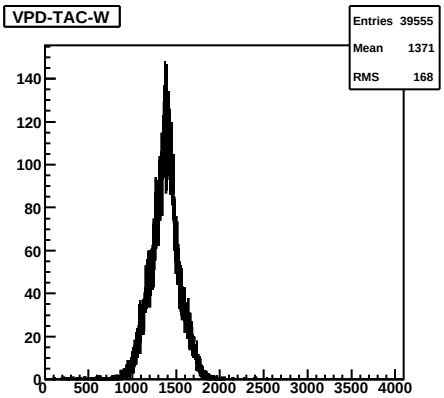

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 72239
Mean 4097
RMS 57.53

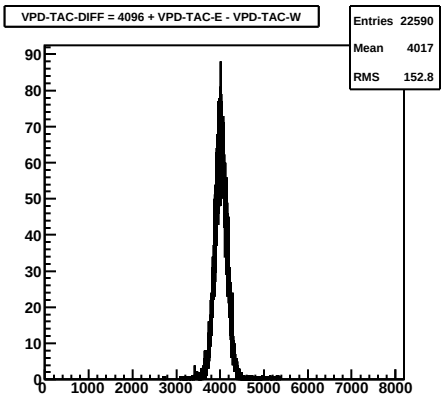

BBClarge-TAC-E
Entries 68467
Mean 1394
RMS 302.4

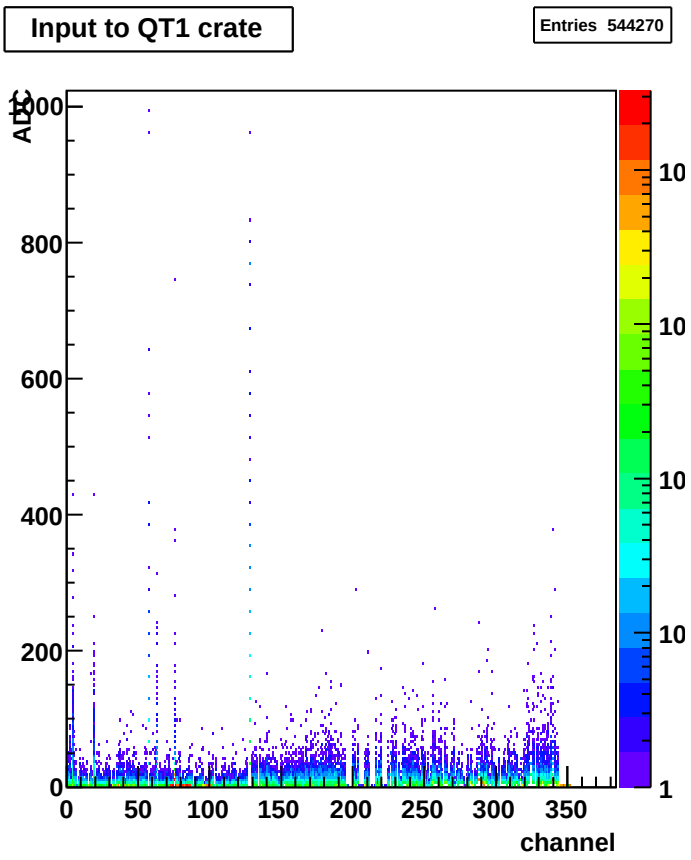

BBClarge-TAC-W
Entries 67827
Mean 1383
RMS 325.7

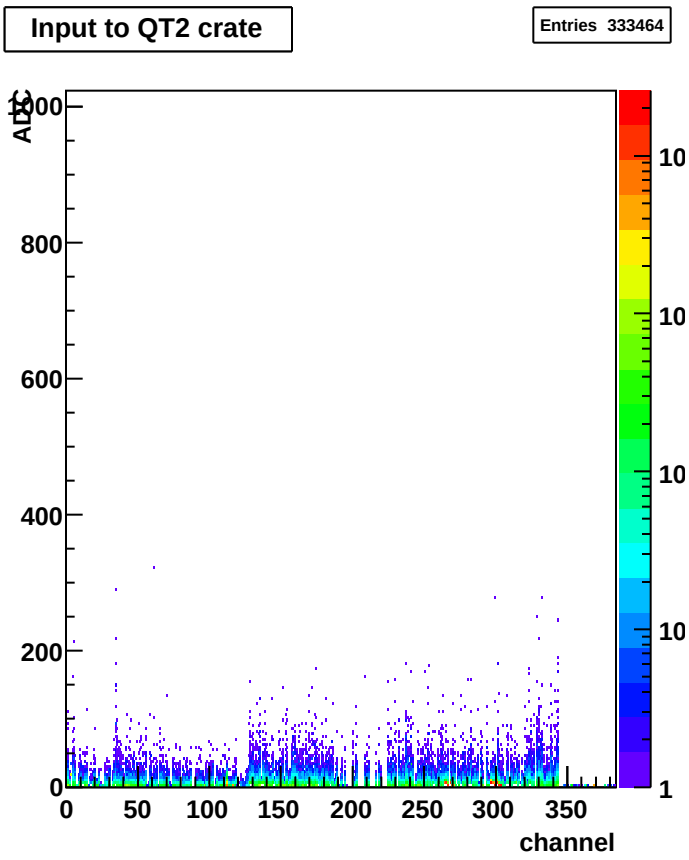

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 64633
Mean 4109
RMS 397.5

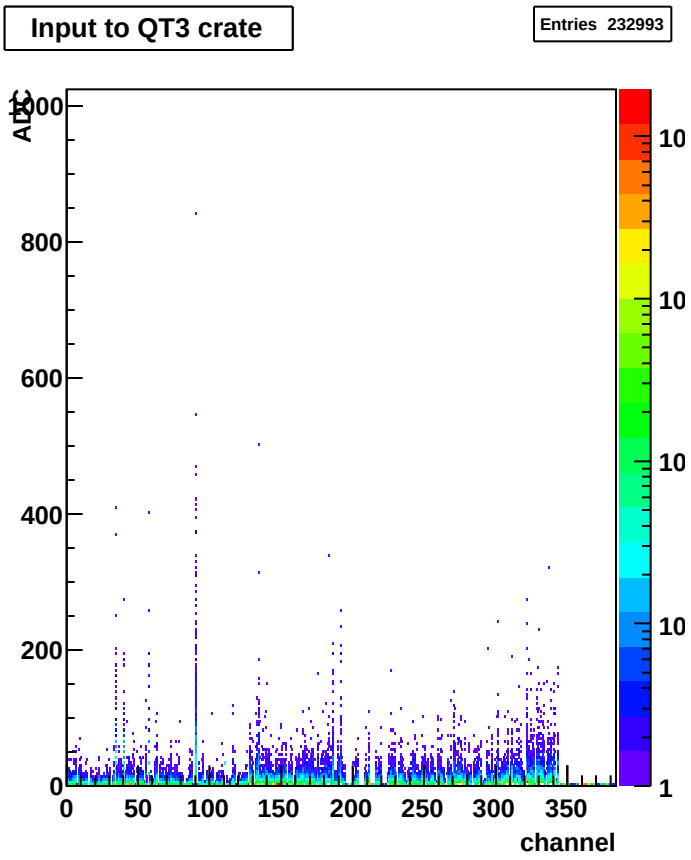

VPD-TAC-E
Entries 40444
Mean 1292
RMS 145.9

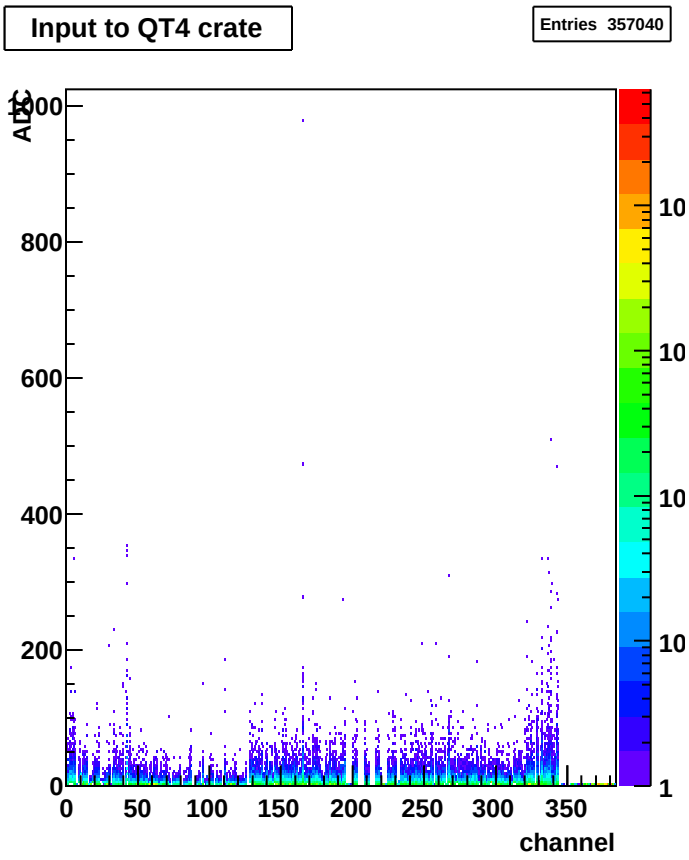

VPD-TAC-W
Entries 39555
Mean 1371
RMS 168

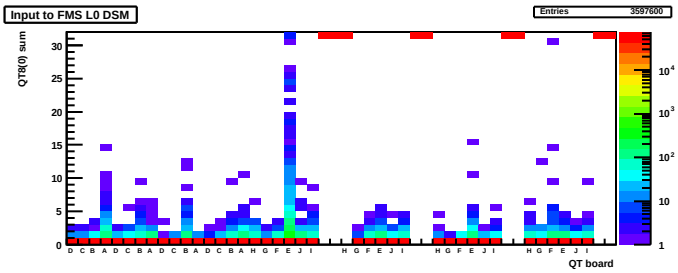

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 22590
Mean 4017
RMS 152.8

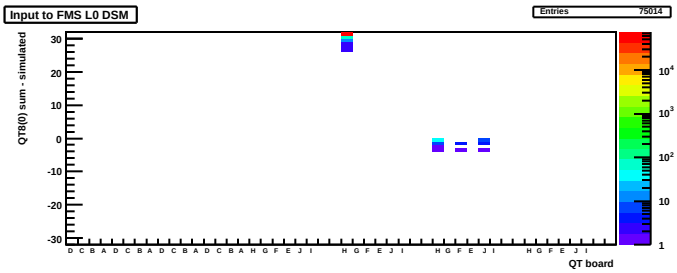

Input to QT1 crate
Entries 544270
ADC
channel

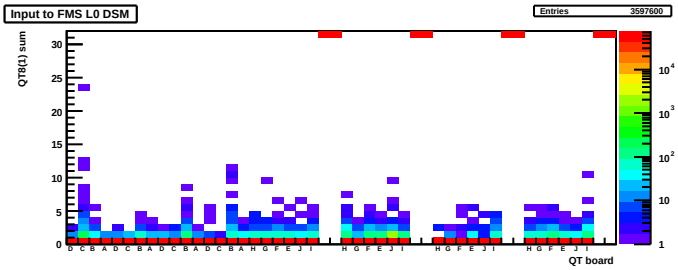

Input to QT2 crate
Entries 333464
ADC
channel

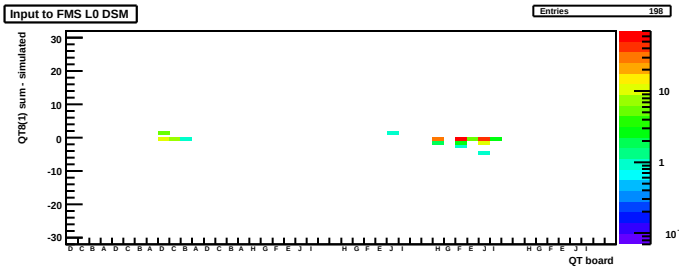

Input to QT3 crate
Entries 232993
ADC
channel

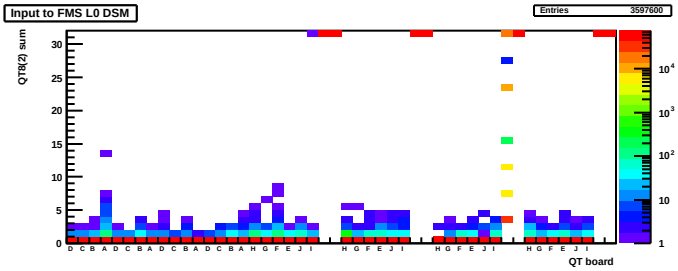

Input to QT4 crate
Entries 357040
ADC
channel

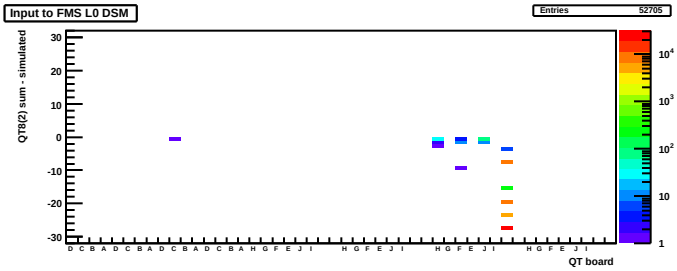

Input to FMS L0 DSM
Entries 3597600
QT8(0) sum
QT board

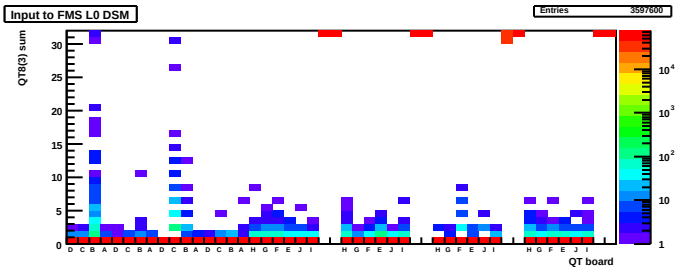

Input to FMS L0 DSM
Entries 75014
QT8(0) sum - simulated
QT board

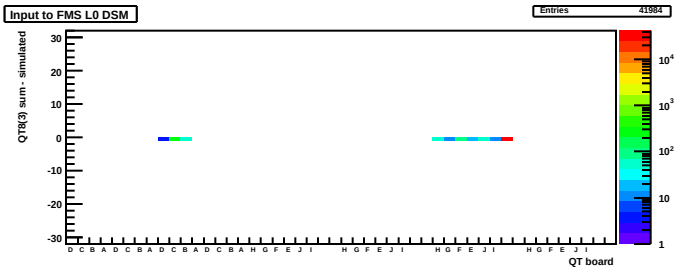

Input to FMS L0 DSM
Entries 3597600
QT8(1) sum
QT board

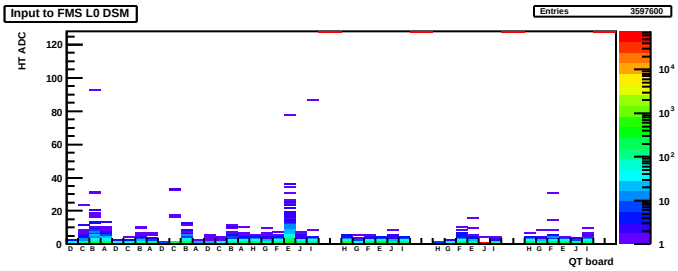

Input to FMS L0 DSM
Entries 198
QT8(1) sum - simulated
QT board

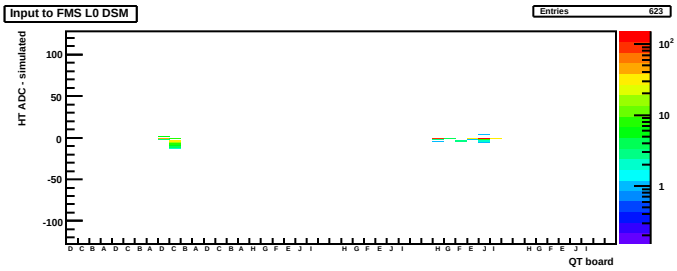

Input to FMS L0 DSM
Entries 3597600
QT8(2) sum
QT board

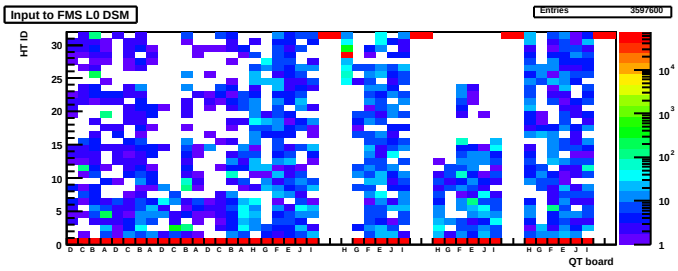

Input to FMS L0 DSM
Entries 52705
QT8(2) sum - simulated
QT board

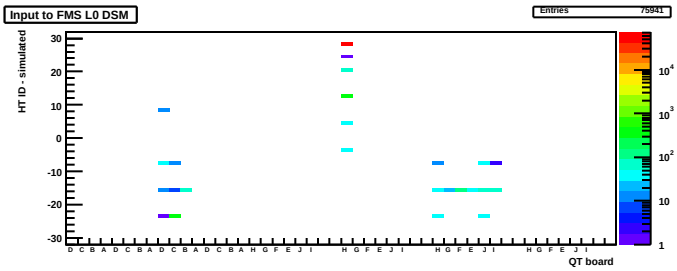

Input to FMS L0 DSM
Entries 3597600
QT8(3) sum
QT board

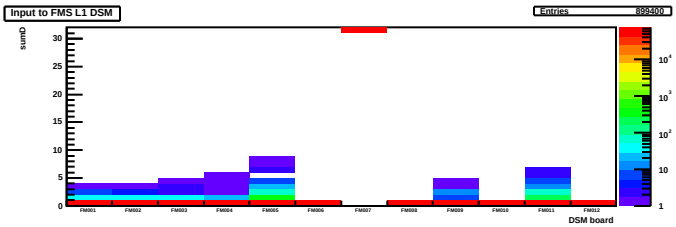

Input to FMS L0 DSM
Entries 41984
QT8(3) sum - simulated
QT board

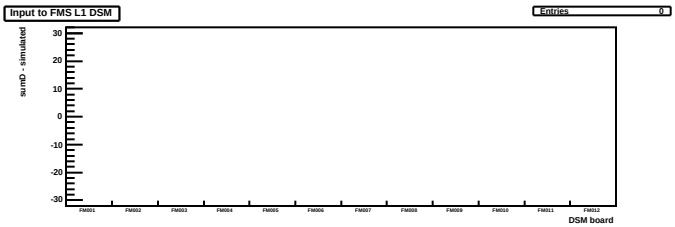

Input to FMS L0 DSM
Entries 3597600
HT ADC
QT board

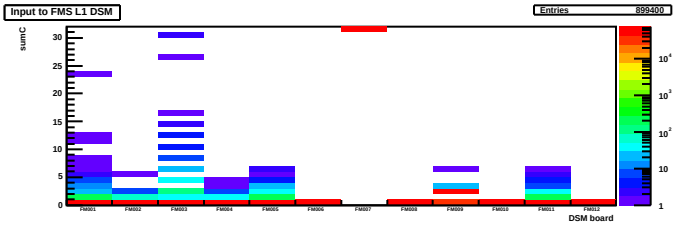

Input to FMS L0 DSM
Entries 623
HT ADC - simulated
QT board

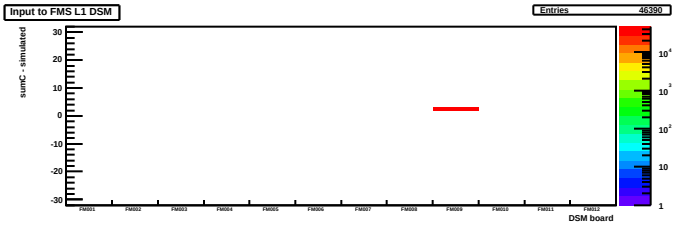

Input to FMS L0 DSM
Entries 3597600
HT ID
QT board

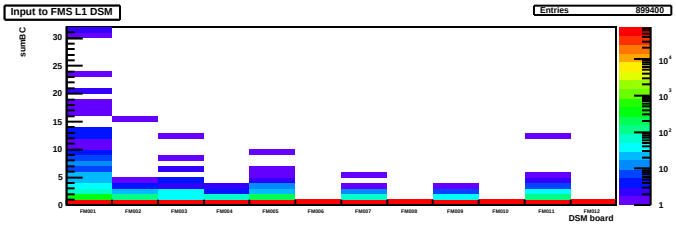

Input to FMS L0 DSM
Entries 75941
HT ID - simulated
QT board

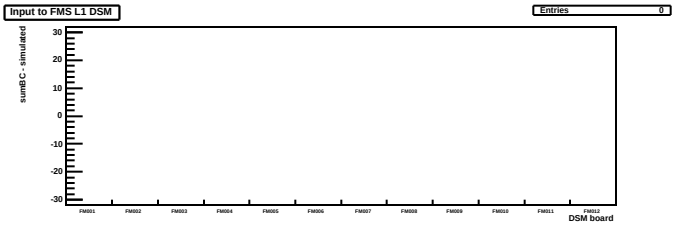

Input to FMS L1 DSM
Entries 899400
sumD
DSM board

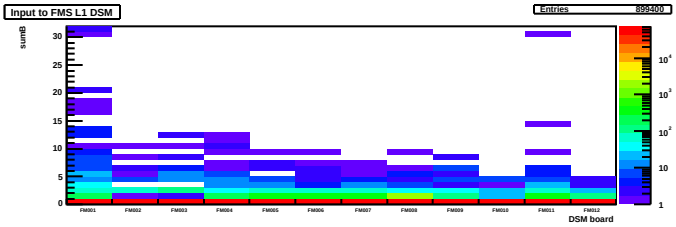

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

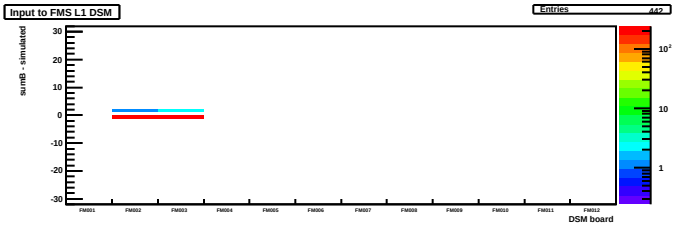

Input to FMS L1 DSM
Entries 899400
sumC
DSM board

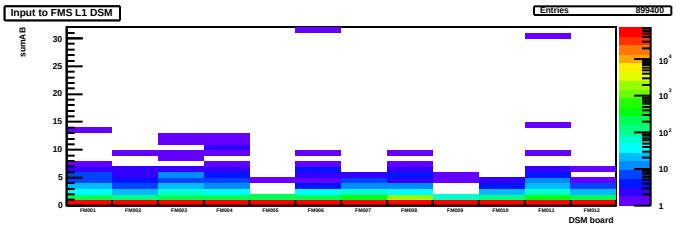

Input to FMS L1 DSM
Entries 46390
sumC - simulated
DSM board

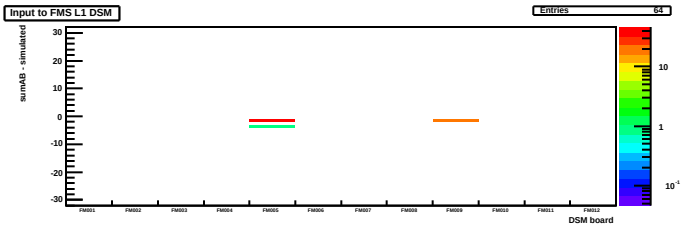

Input to FMS L1 DSM
Entries 899400
sumBC
DSM board

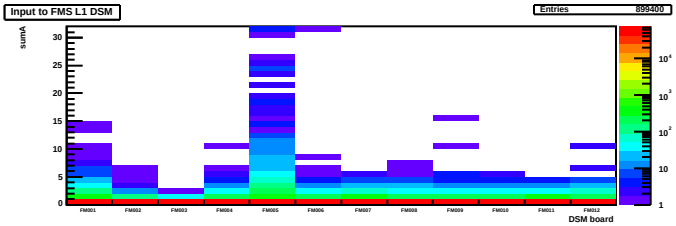

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

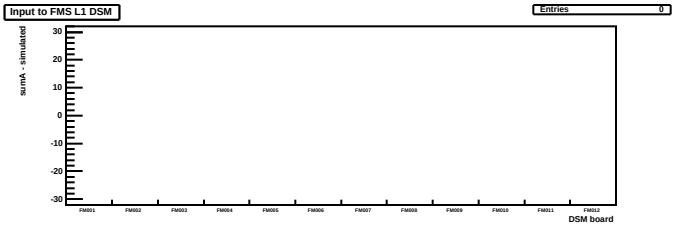

Input to FMS L1 DSM
Entries 899400
sumB
DSM board

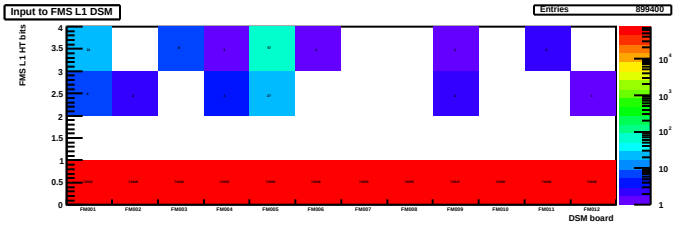

Input to FMS L1 DSM
Entries 442
sumB - simulated
DSM board

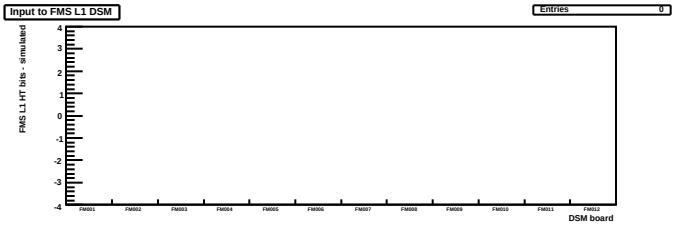

Input to FMS L1 DSM
Entries 899400
sumAB
DSM board

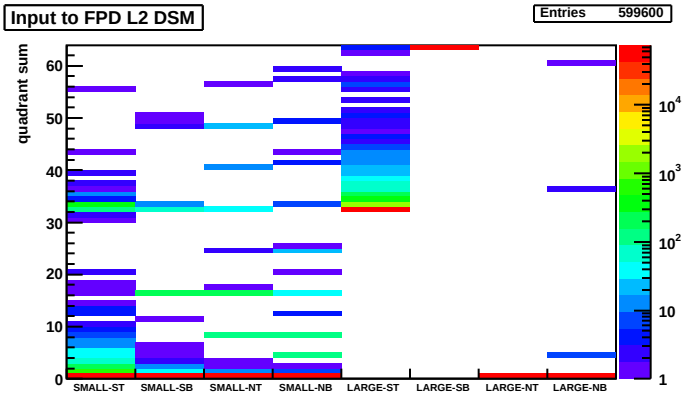

Input to FMS L1 DSM
Entries 64
sumAB - simulated
DSM board

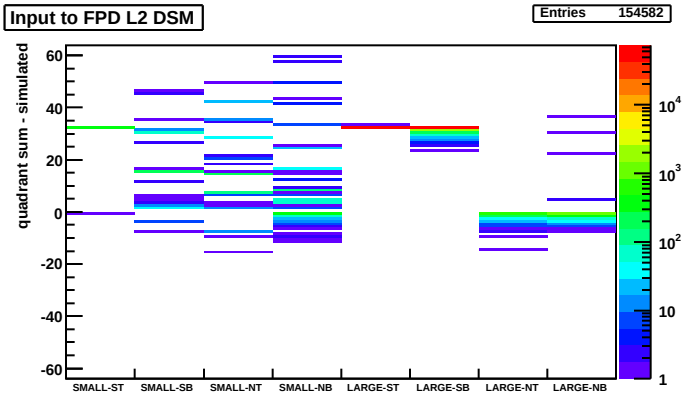

Input to FMS L1 DSM
Entries 899400
sumA
DSM board

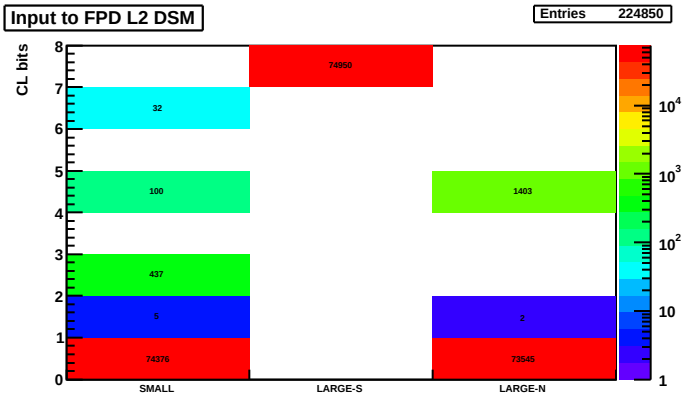

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

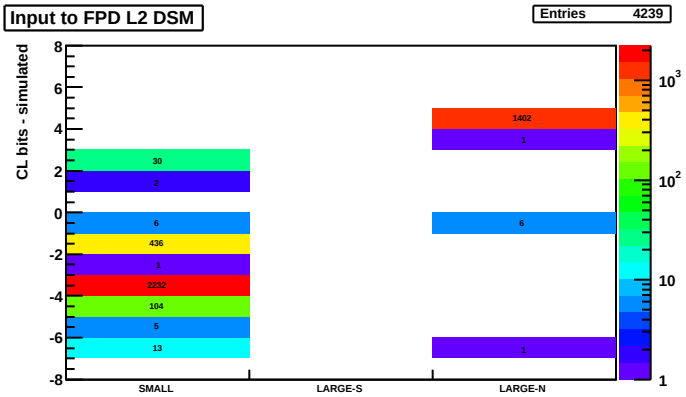

Input to FMS L1 DSM
Entries 899400
FMS L1 HT bits
DSM board

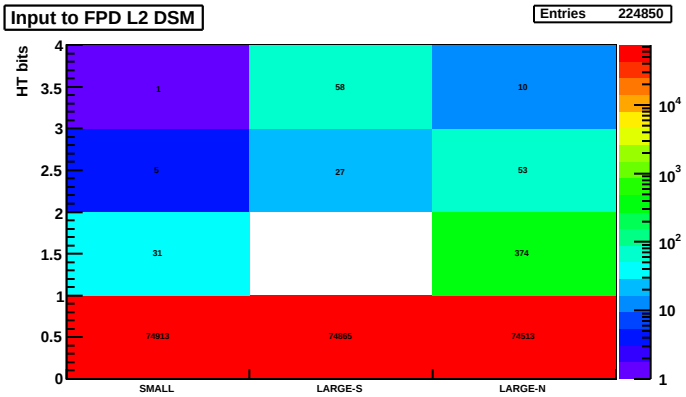

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

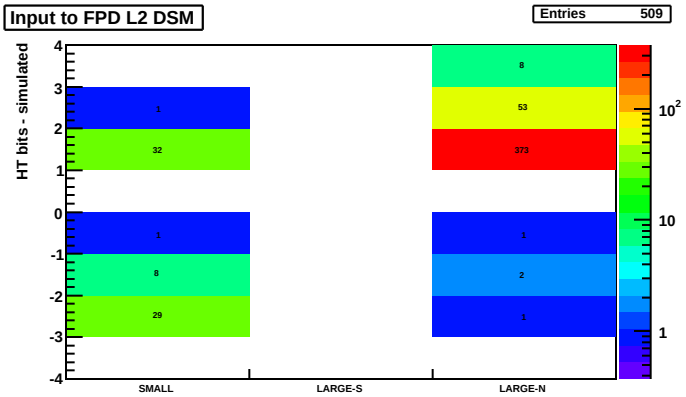

Input to FPD L2 DSM
Entries 599600
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

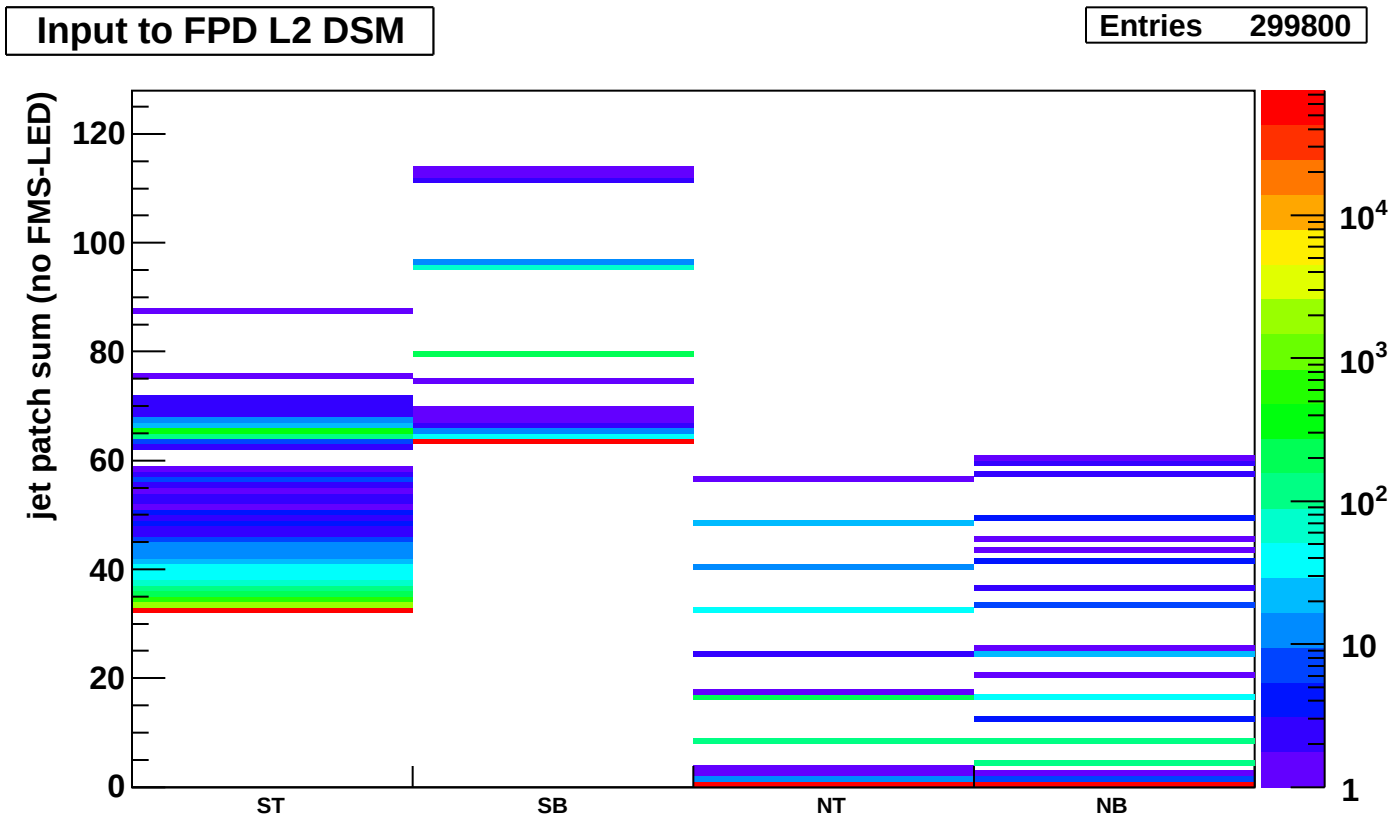

Input to FPD L2 DSM
Entries 154582
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

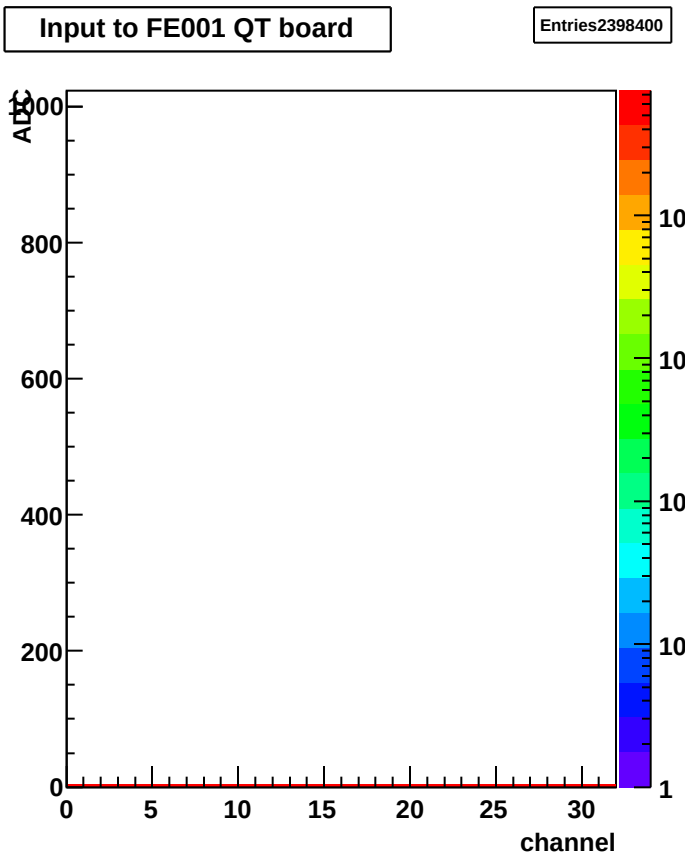

Input to FPD L2 DSM
Entries 224850
CL bits
74950
32
100
1403
437
5
2
74376
73545
SMALL
LARGE-S
LARGE-N

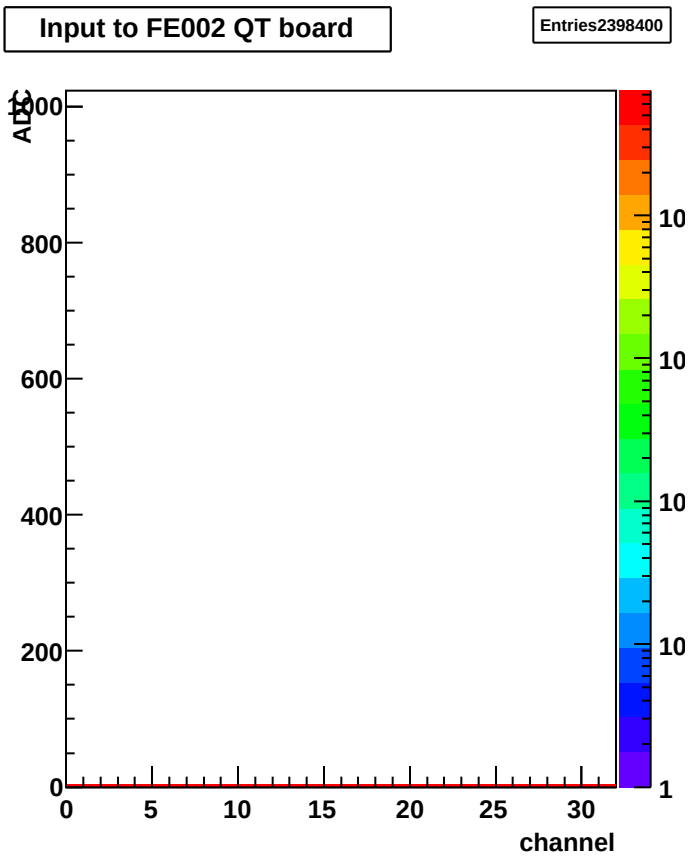

Input to FPD L2 DSM
Entries 4239
CL bits - simulated
1402
1
30
2
6
6
436
1
2232
104
5
13
1
SMALL
LARGE-S
LARGE-N

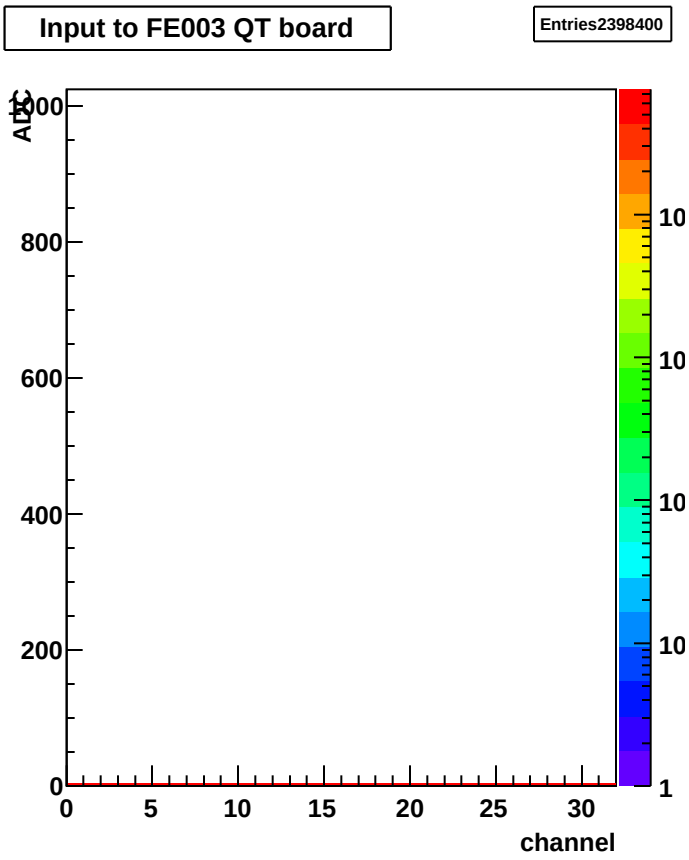

Input to FPD L2 DSM
Entries 224850
HT bits
1
58
10
5
27
53
31
374
74913
74865
74513
SMALL
LARGE-S
LARGE-N

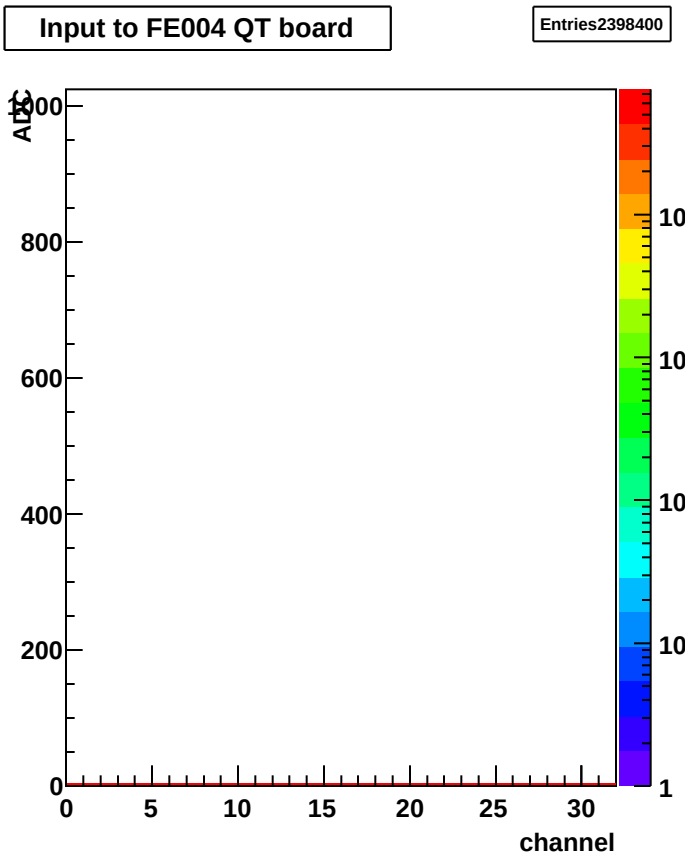

Input to FPD L2 DSM
Entries 509
HT bits - simulated
8
1
53
32
373
1
1
8
2
29
1
SMALL
LARGE-S
LARGE-N

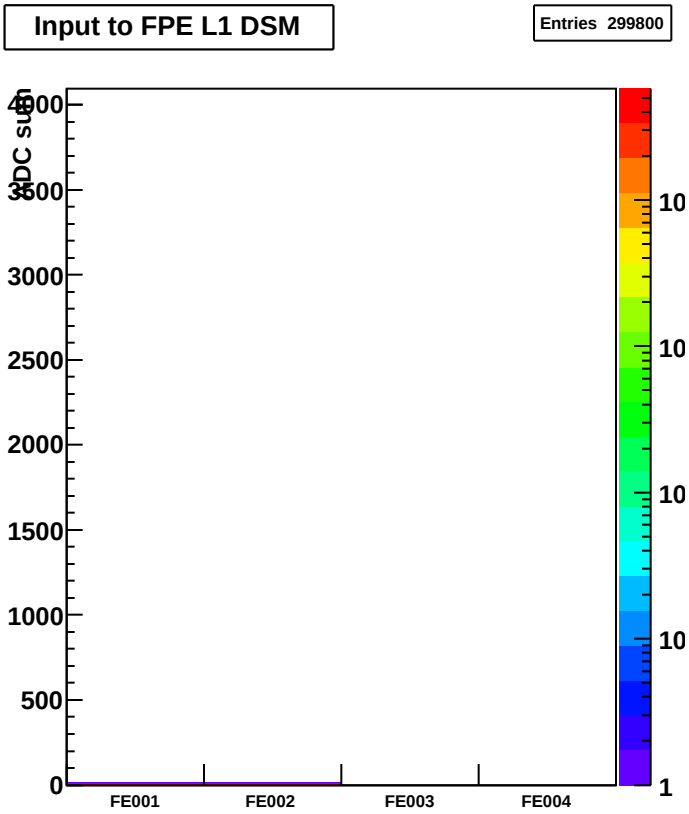

Input to FPD L2 DSM
Entries 299800
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

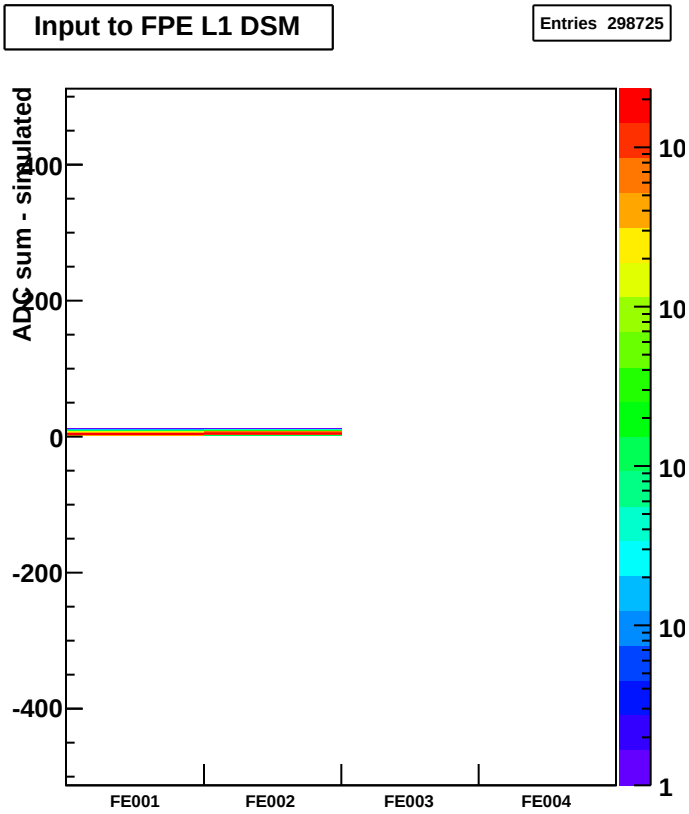

Input to FE001 QT board
Entries2398400
ADC
channel

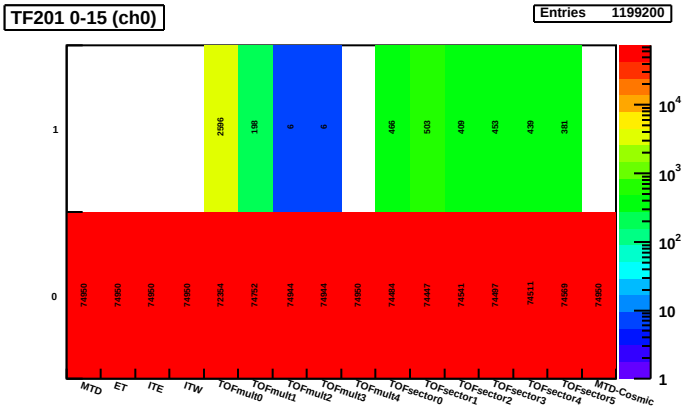

Input to FE002 QT board
Entries2398400
ADC
channel

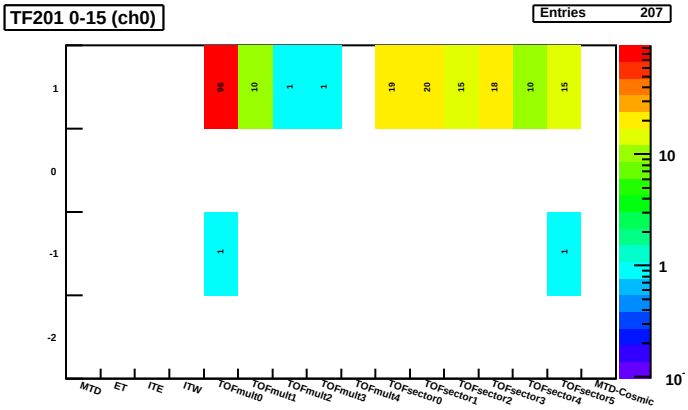

Input to FE003 QT board
Entries2398400
ADC
channel

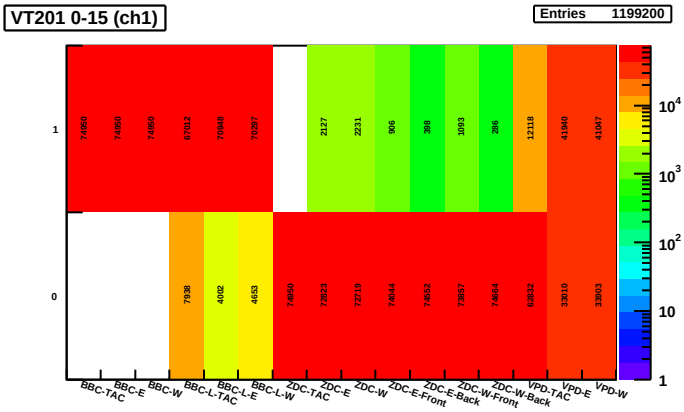

Input to FE004 QT board
Entries2398400
ADC
channel

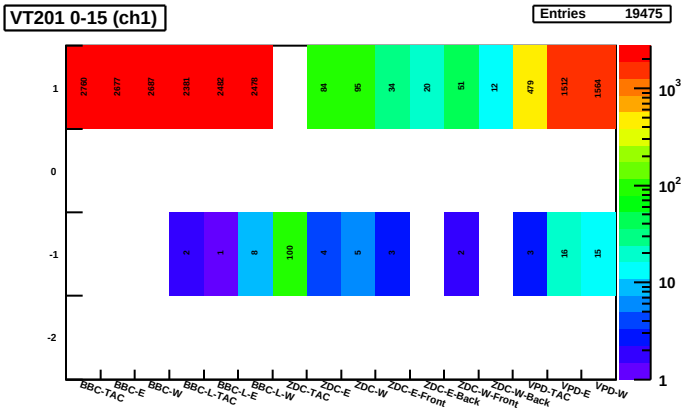

Input to FPE L1 DSM
Entries 299800
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

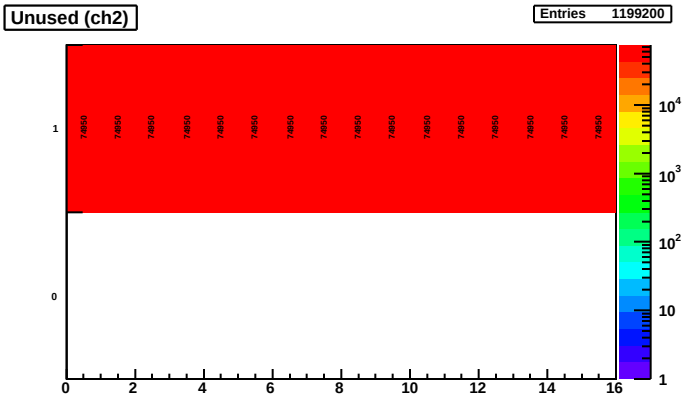

Input to FPE L1 DSM
Entries 298725
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1

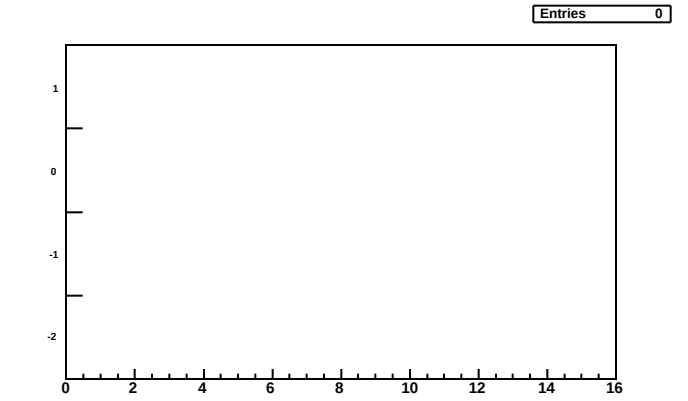

TF201 0-15 (ch0)
Entries 1199200
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

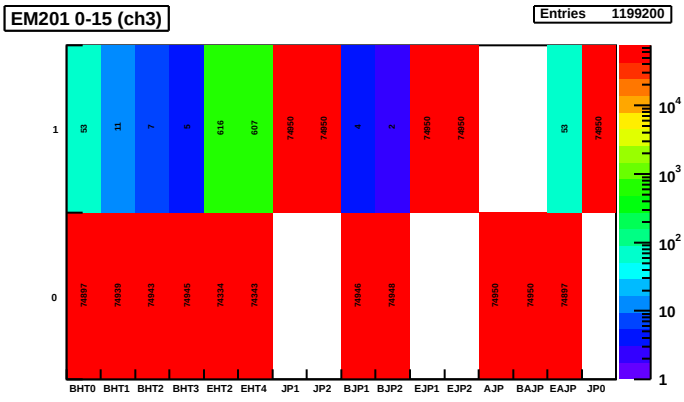

TF201 0-15 (ch0)
Entries 207
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

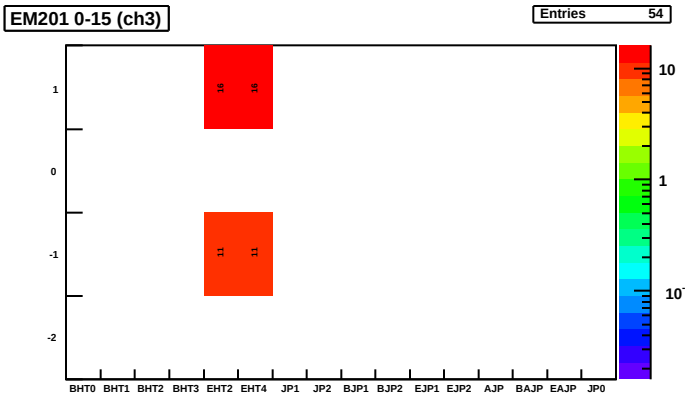

VT201 0-15 (ch1)
Entries 1199200
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 19475
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

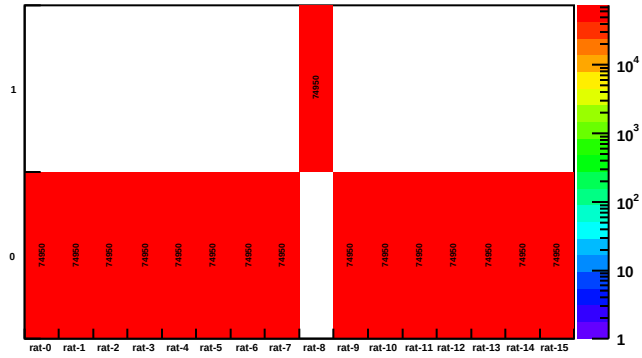

Unused (ch2)
Entries 1199200

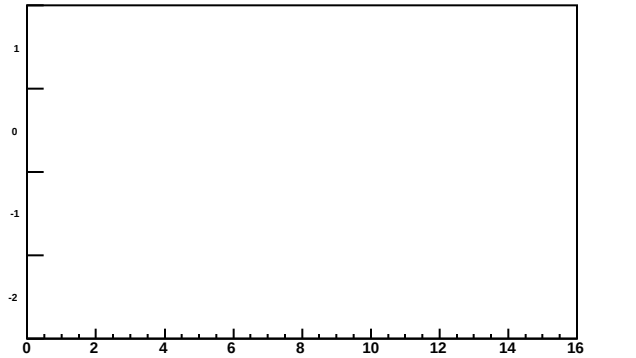

Entries 0


EM201 0-15 (ch3)
Entries 1199200
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

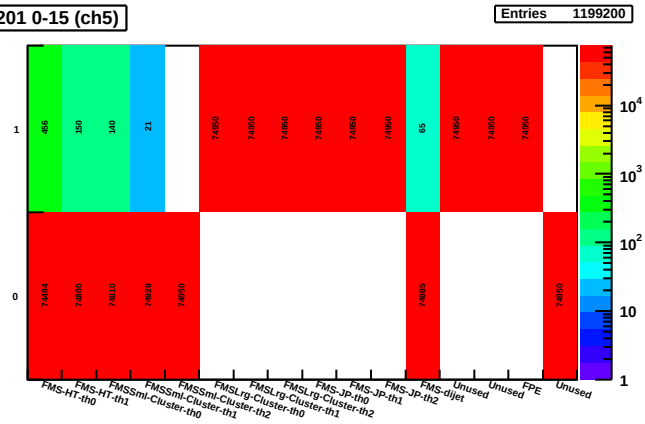

EM201 0-15 (ch3)
Entries 54
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

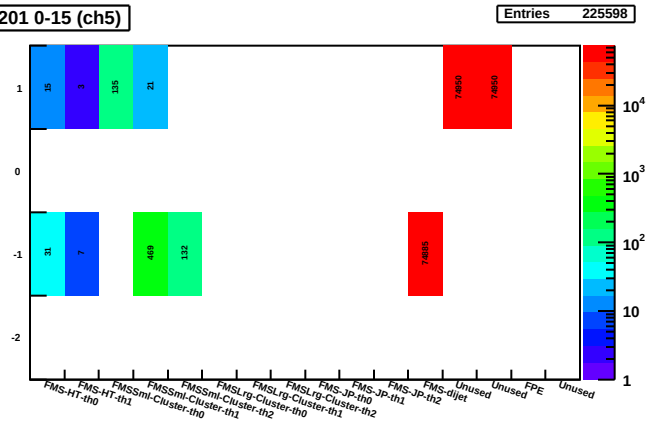




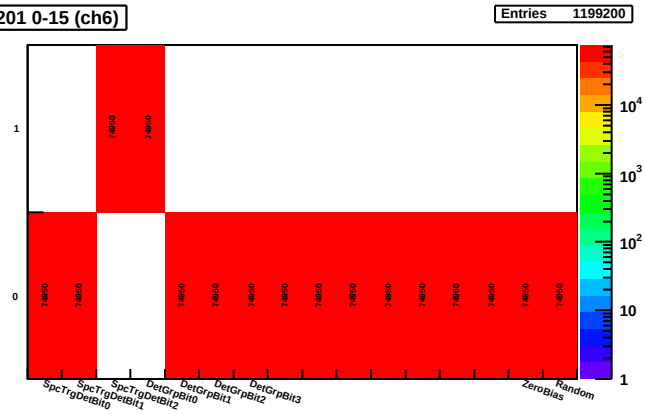
FP201 0-15 (ch5)

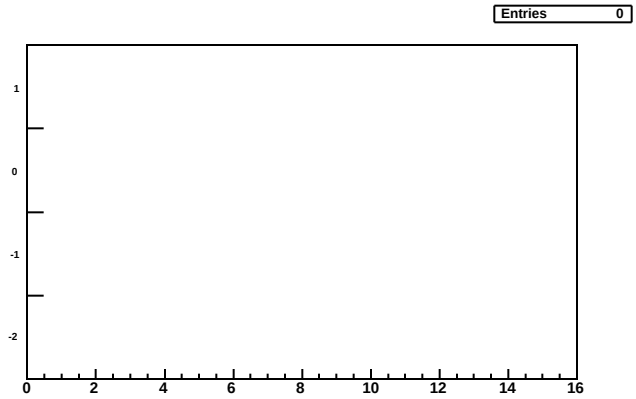
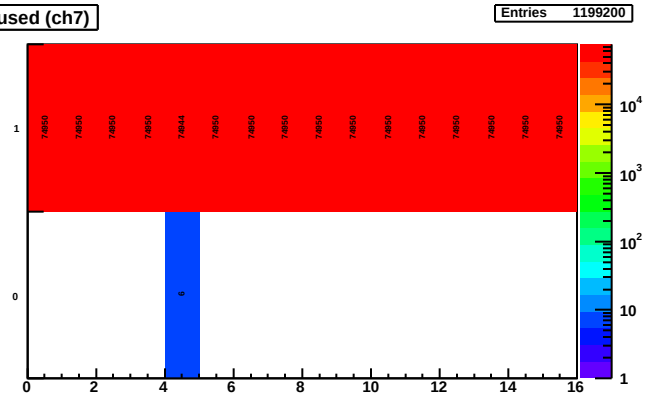
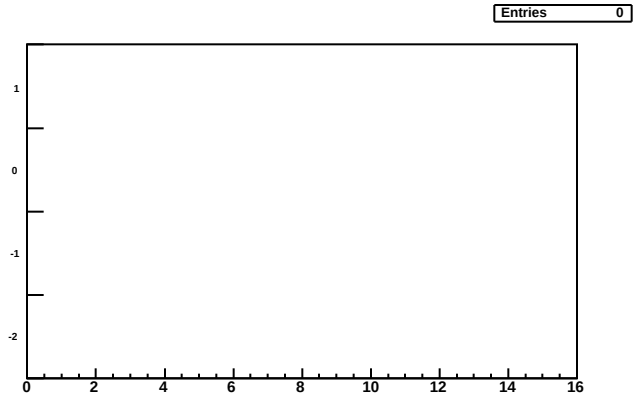


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)