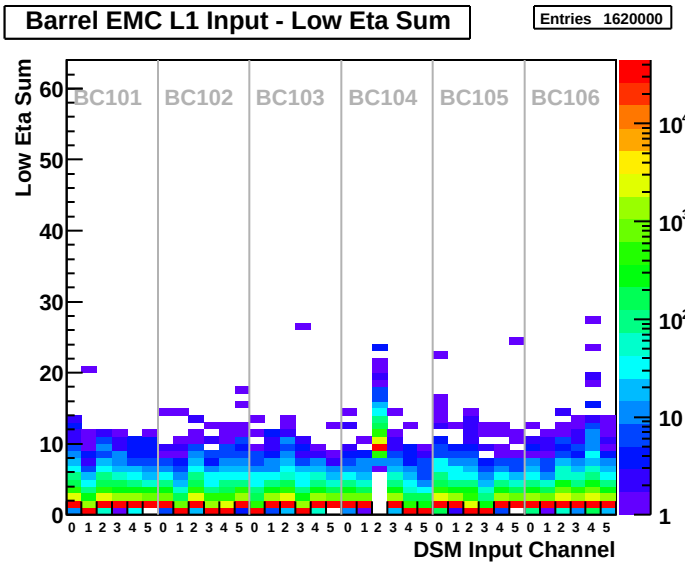
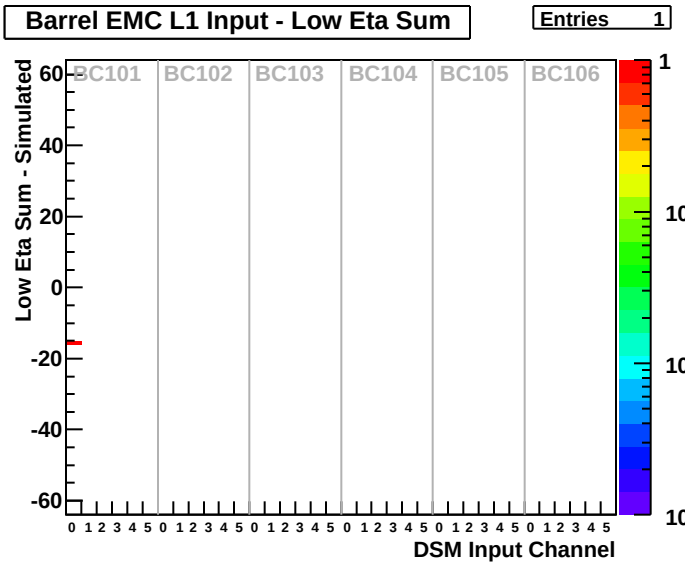
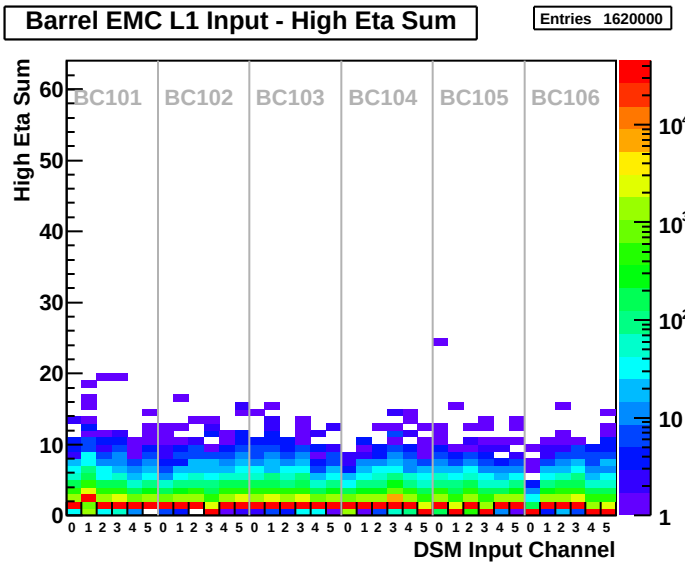
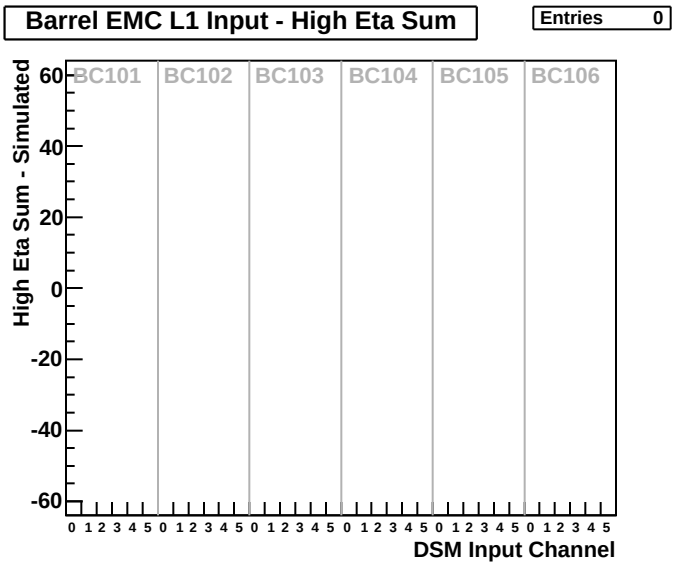
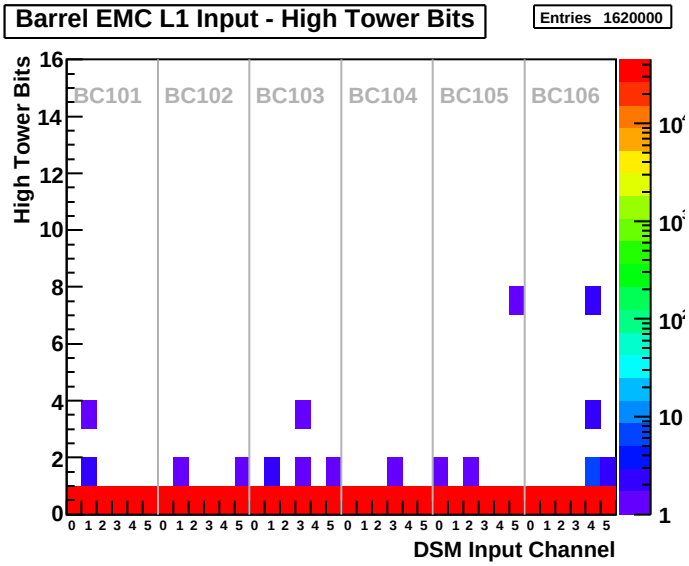
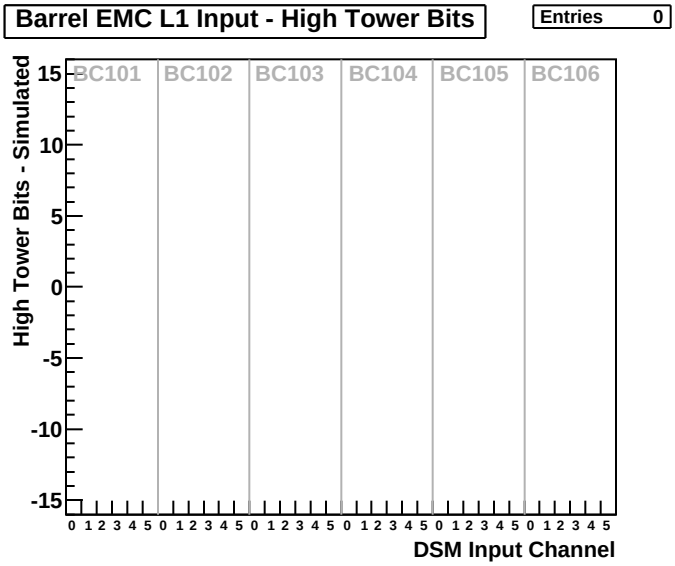

Barrel EMC L1 Input - Low Eta Sum
Entries 1620000
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

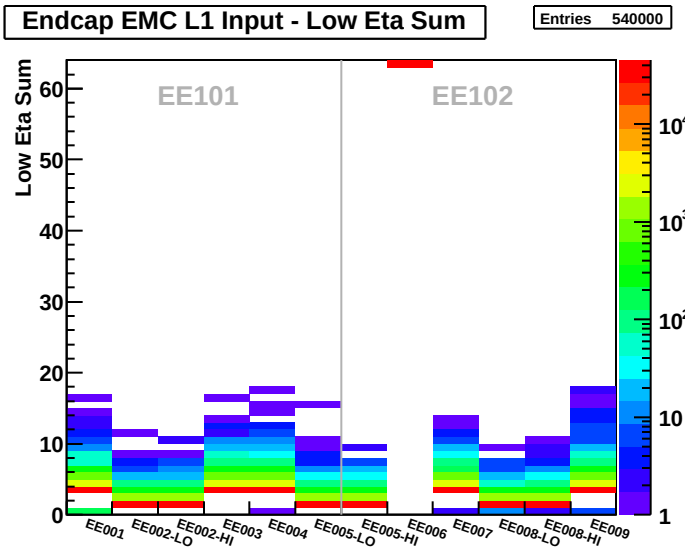

Barrel EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

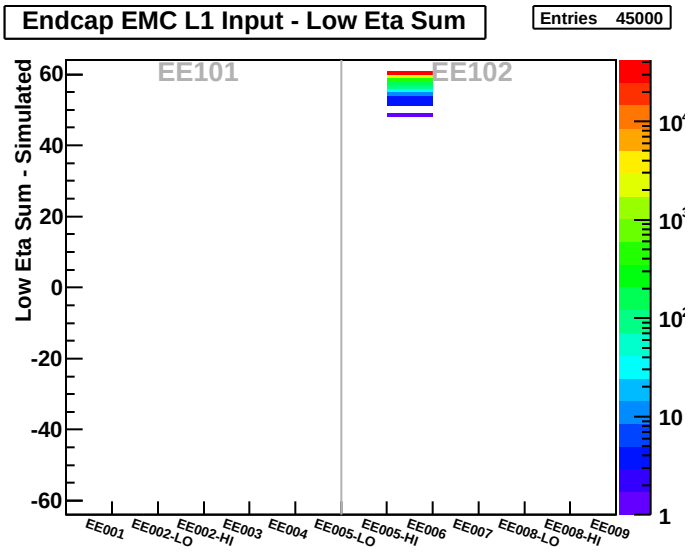

Barrel EMC L1 Input - High Eta Sum
Entries 1620000
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

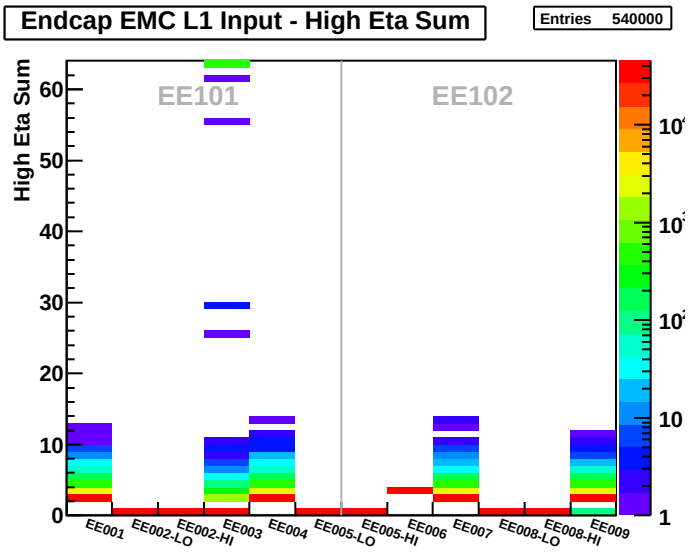

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

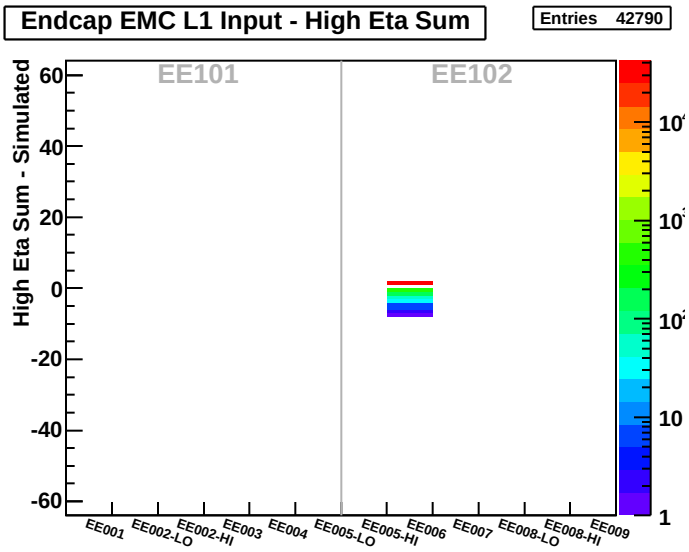

Barrel EMC L1 Input - High Tower Bits
Entries 1620000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

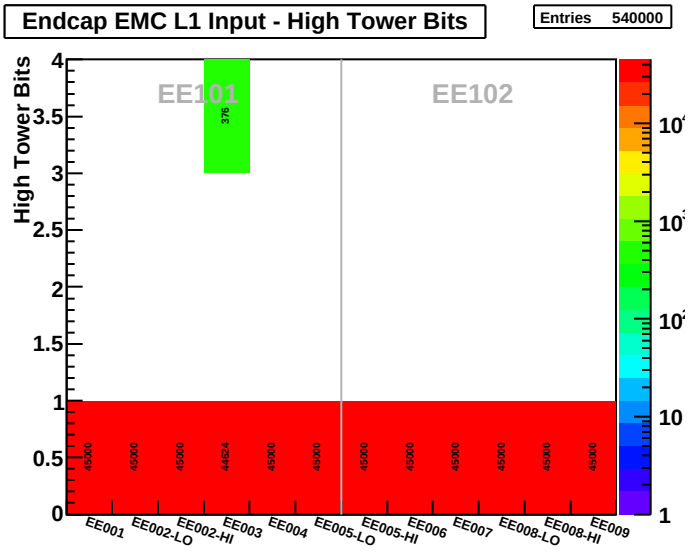

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

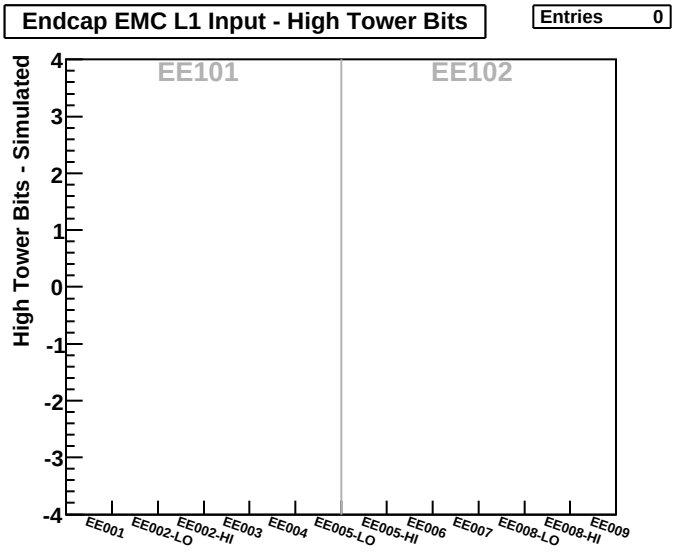

Endcap EMC L1 Input - Low Eta Sum
Entries 540000
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

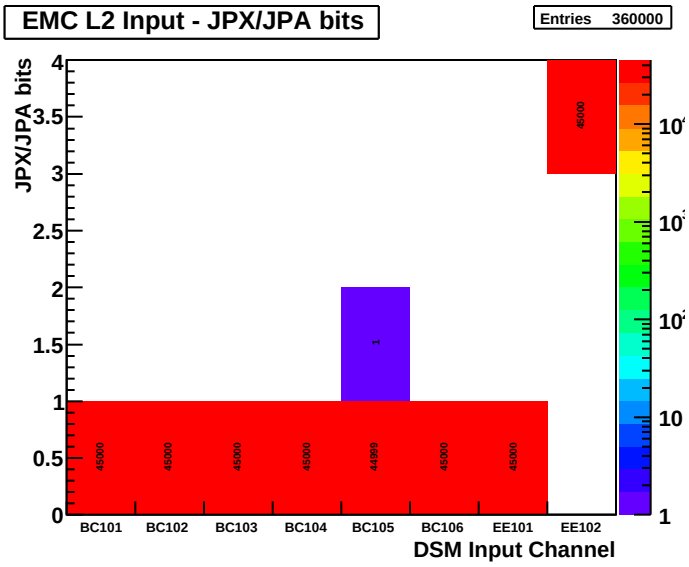

Endcap EMC L1 Input - Low Eta Sum
Entries 45000
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

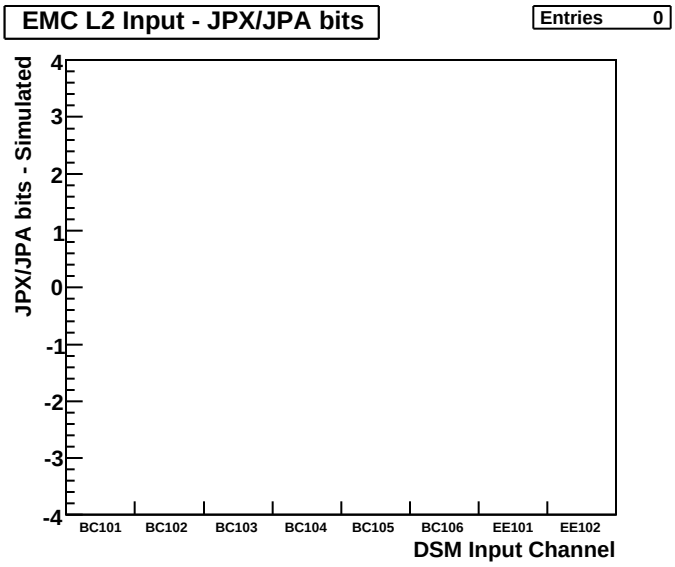

Endcap EMC L1 Input - High Eta Sum
Entries 540000
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

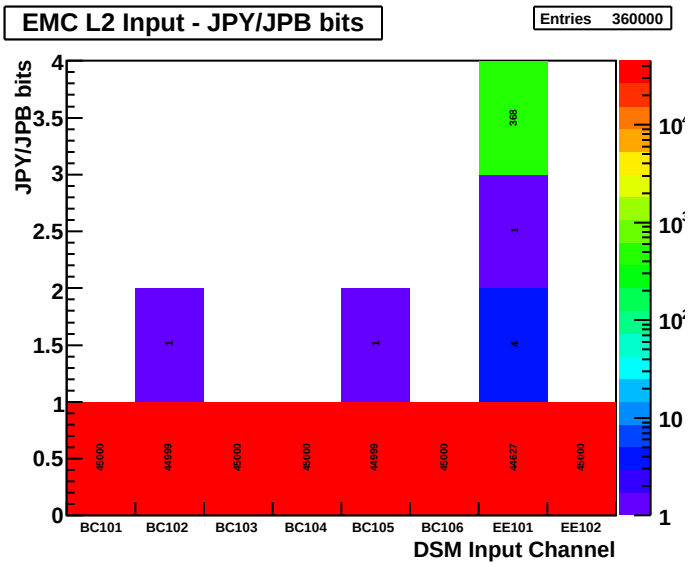

Endcap EMC L1 Input - High Eta Sum
Entries 42790
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

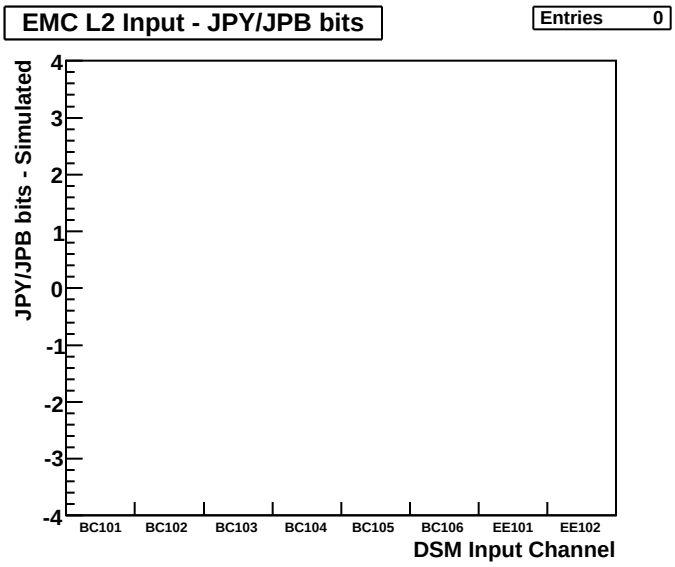

Endcap EMC L1 Input - High Tower Bits
Entries 540000
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

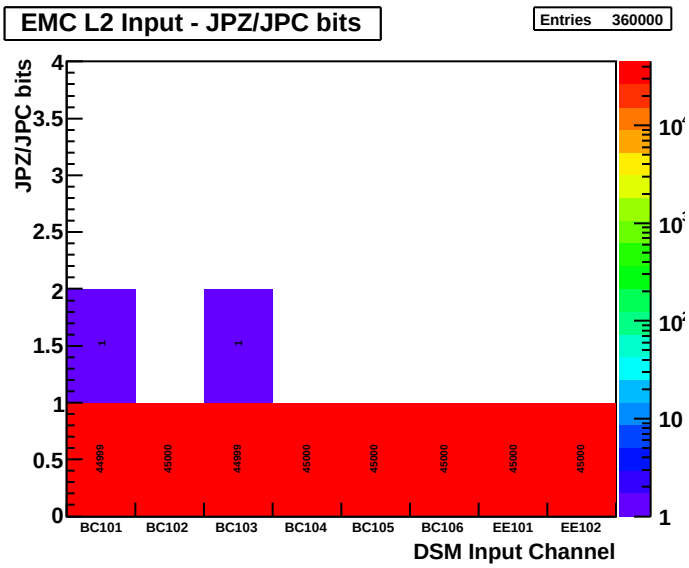

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

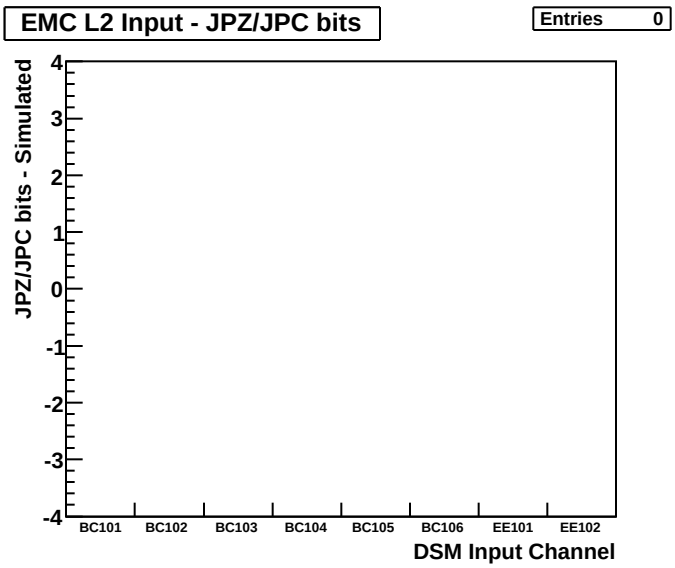

EMC L2 Input - JPX/JPA bits
Entries 360000
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

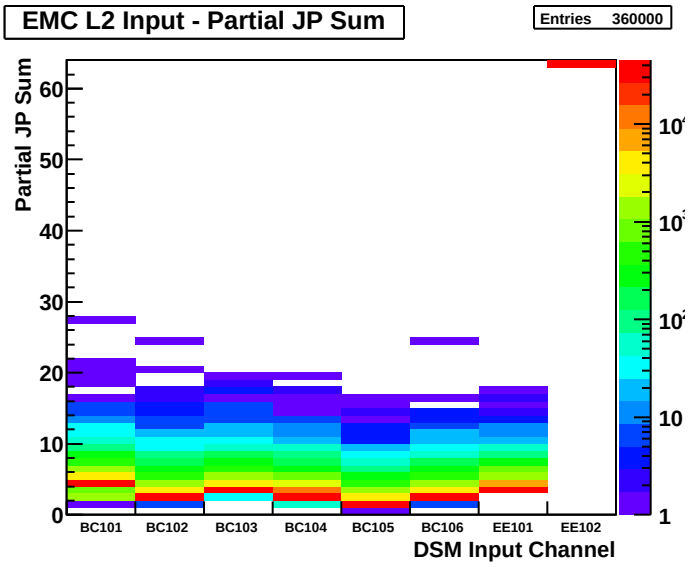

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

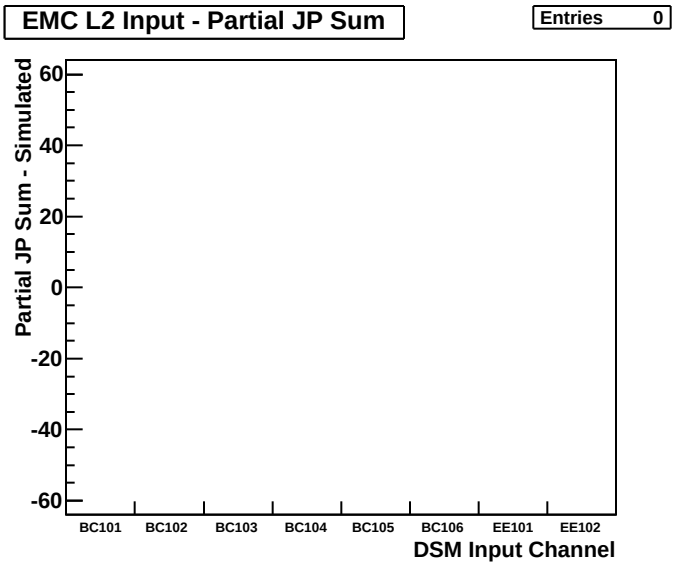

EMC L2 Input - JPY/JPB bits
Entries 360000
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

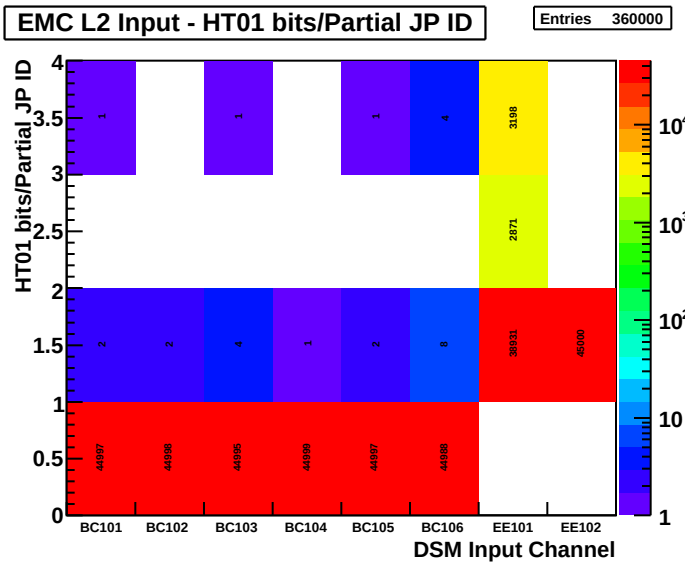

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

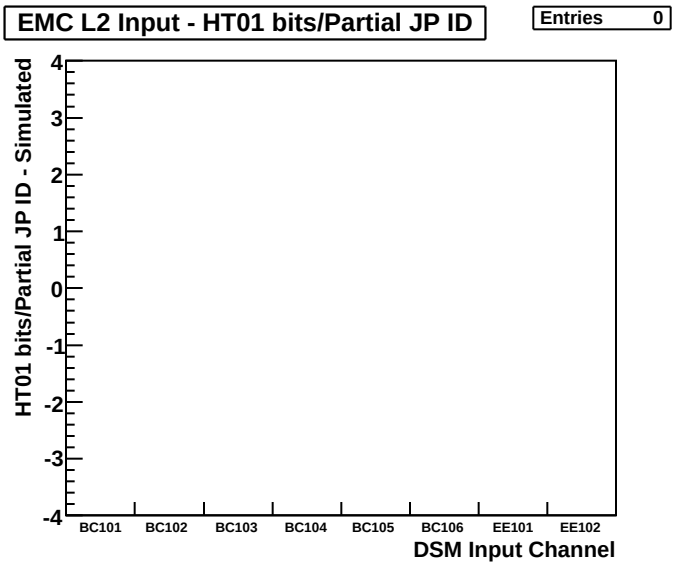

EMC L2 Input - JPZ/JPC bits
Entries 360000
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

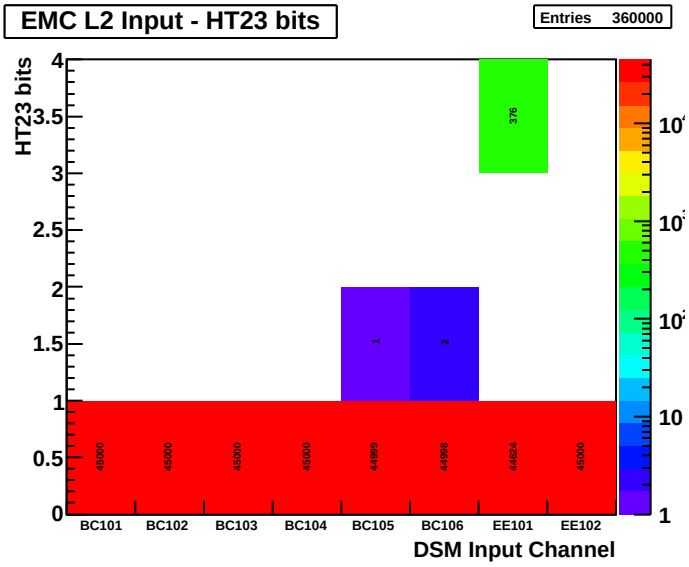

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

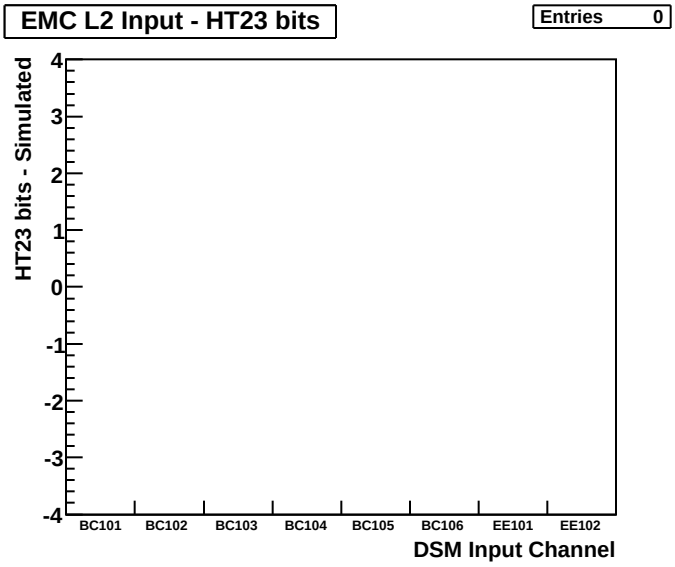

EMC L2 Input - Partial JP Sum
Entries 360000
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

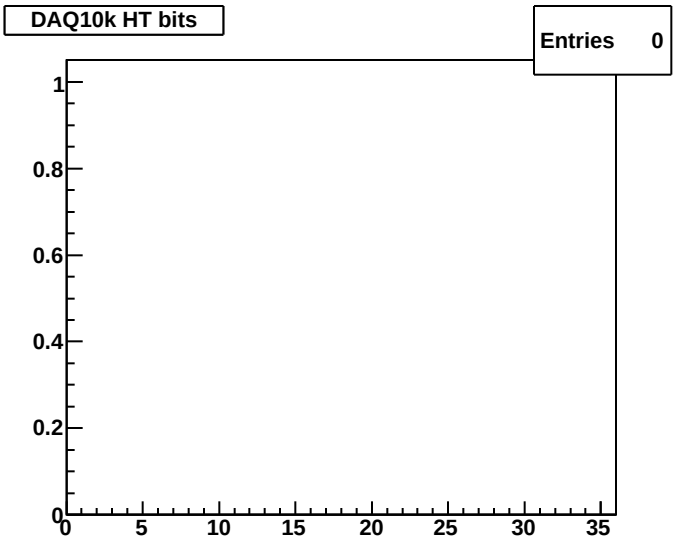

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

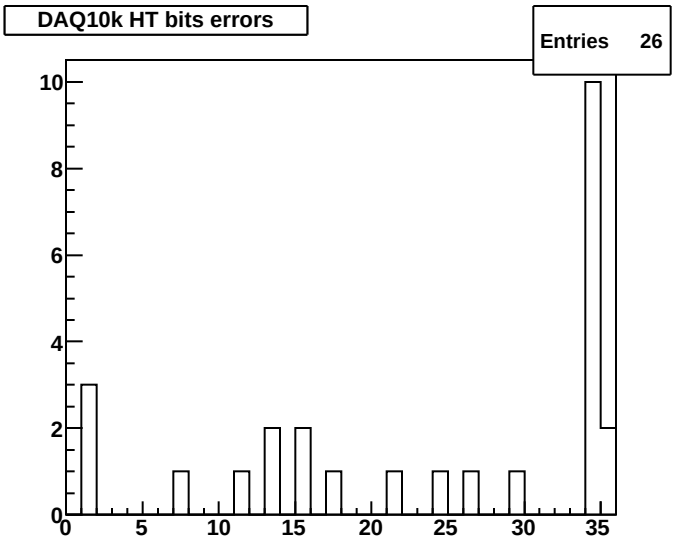

EMC L2 Input - HT01 bits/Partial JP ID
Entries 360000
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

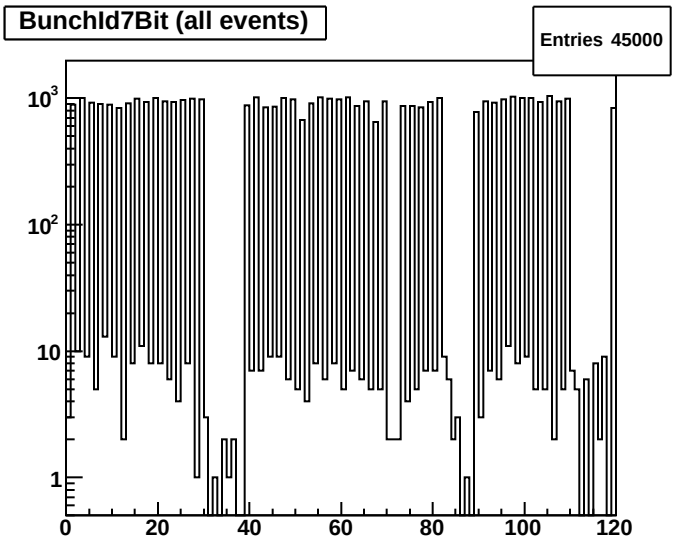

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

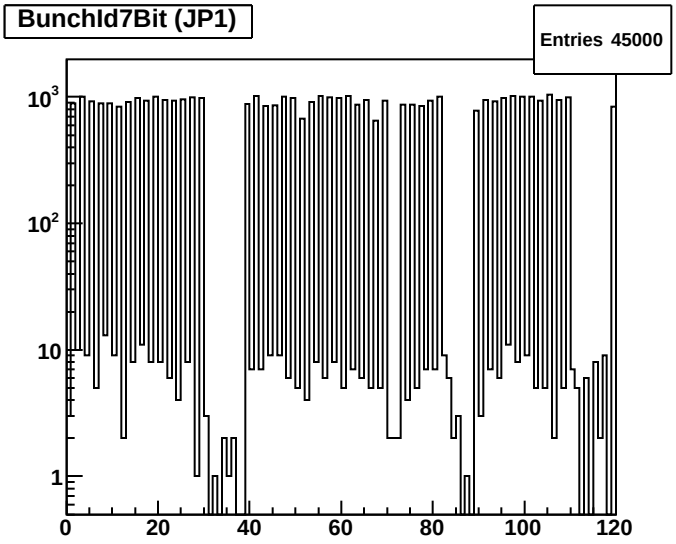

EMC L2 Input - HT23 bits
Entries 360000
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

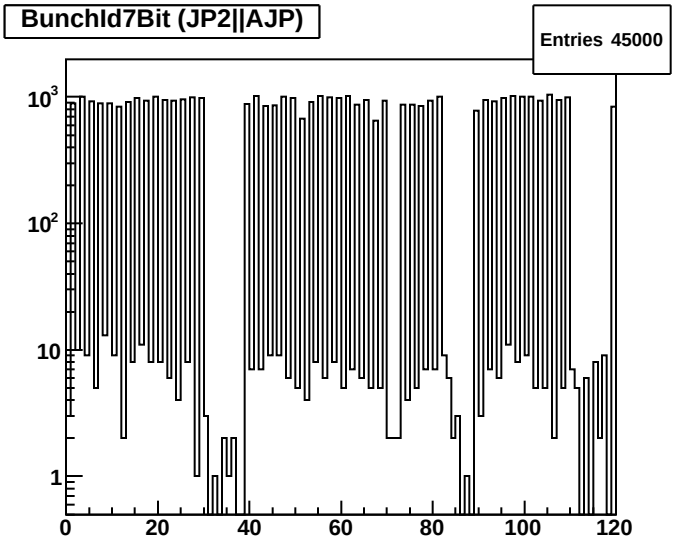

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

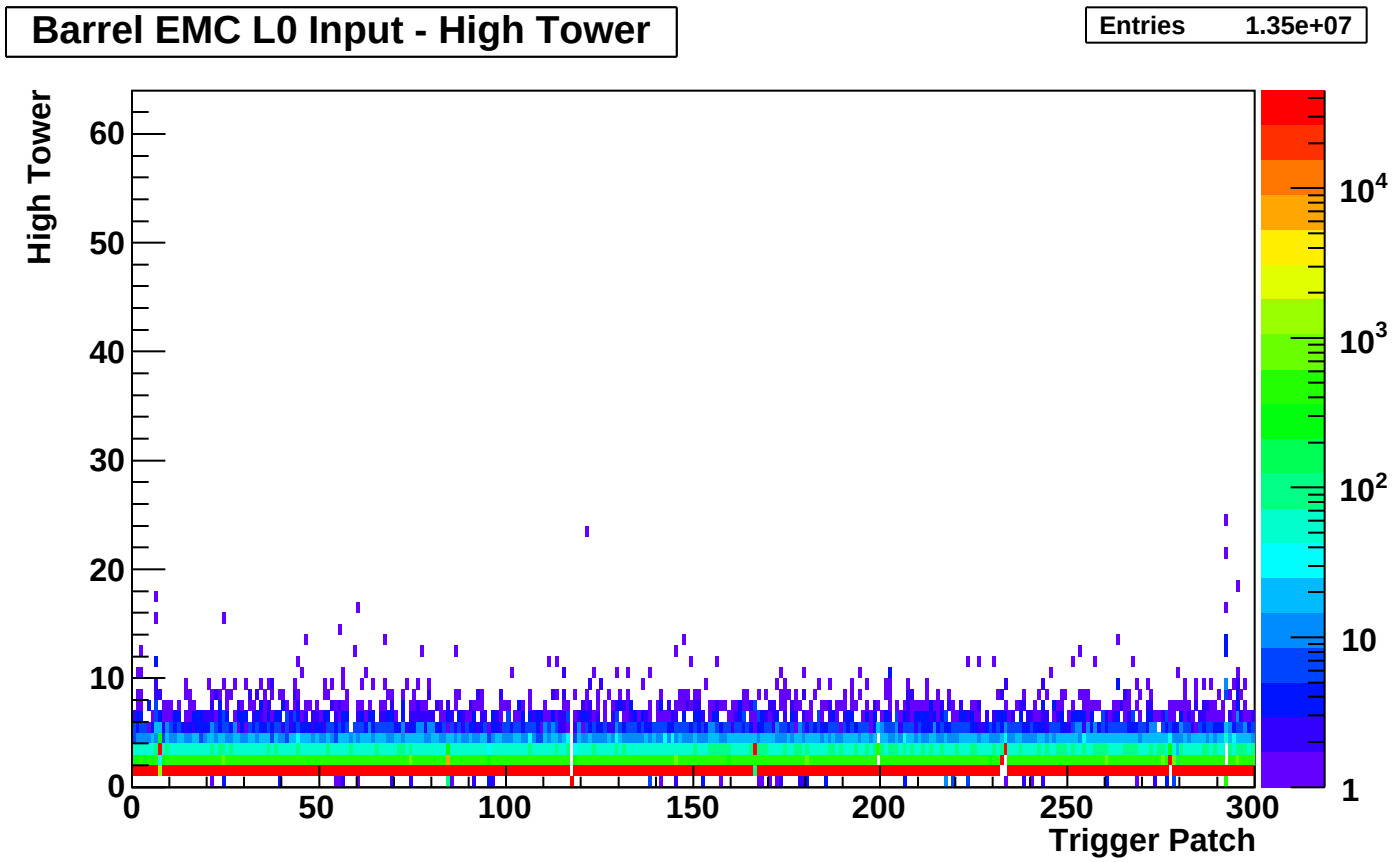

DAQ10k HT bits
Entries 0

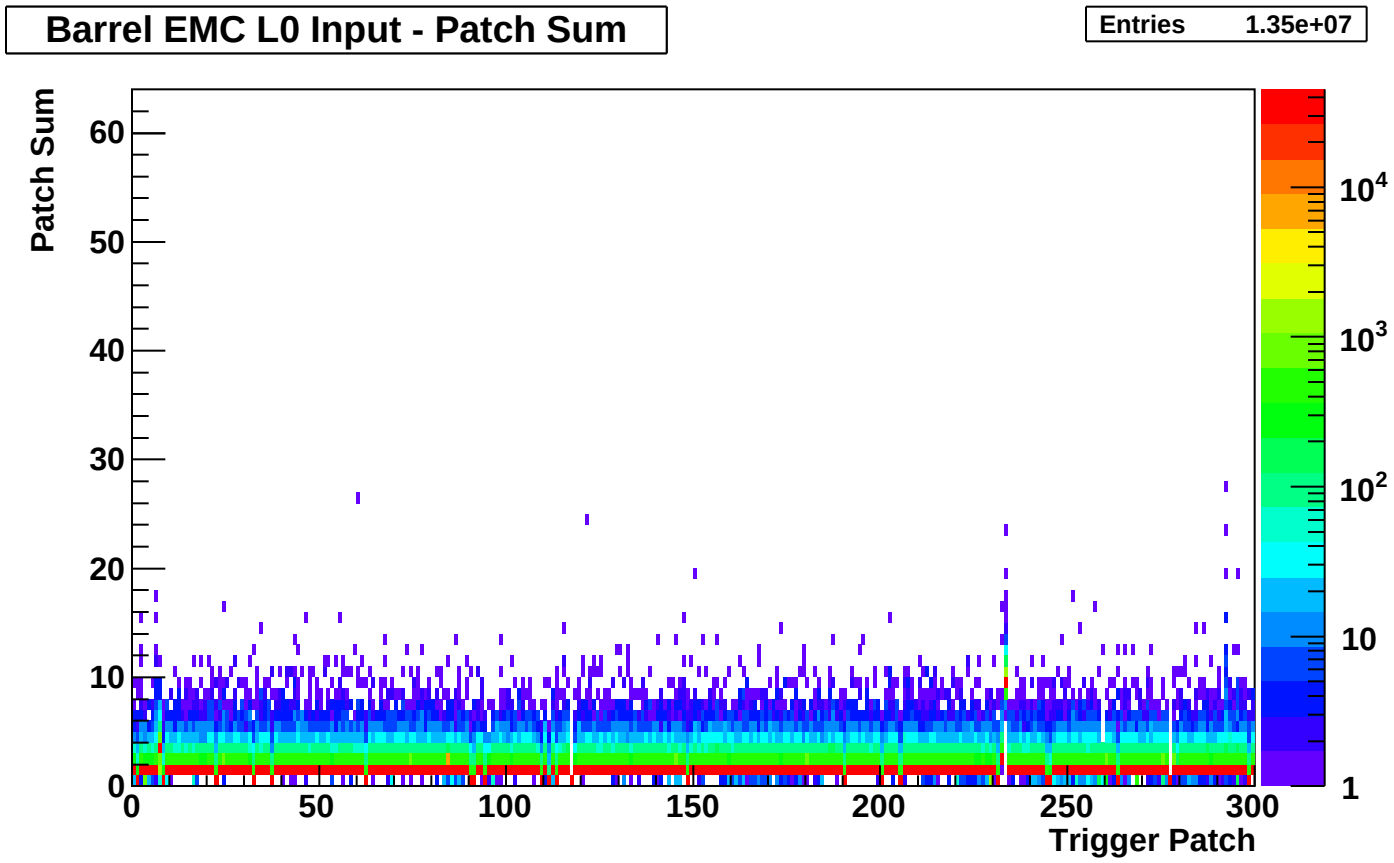

DAQ10k HT bits errors
Entries 26

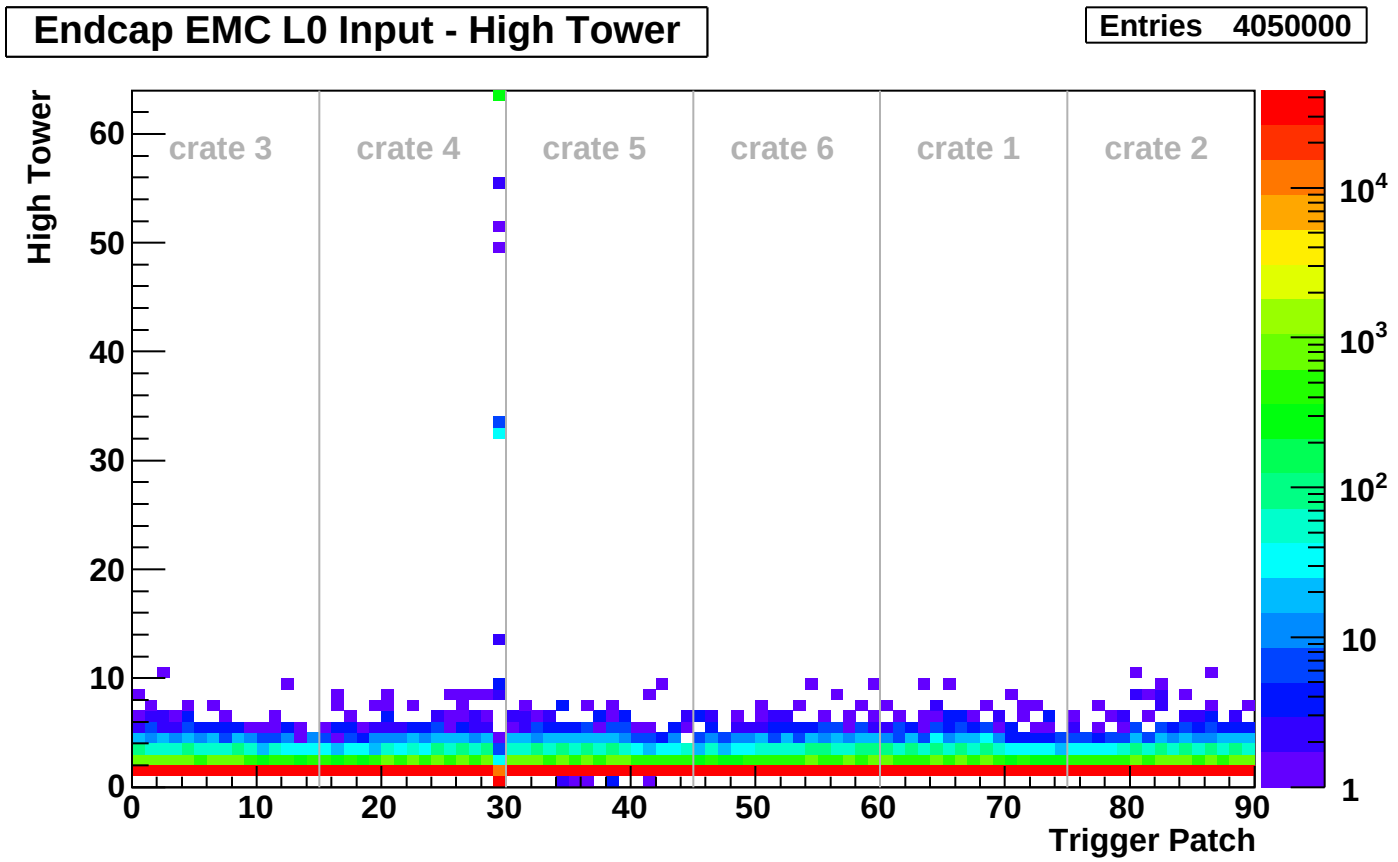

BunchId7Bit (all events)
Entries 45000

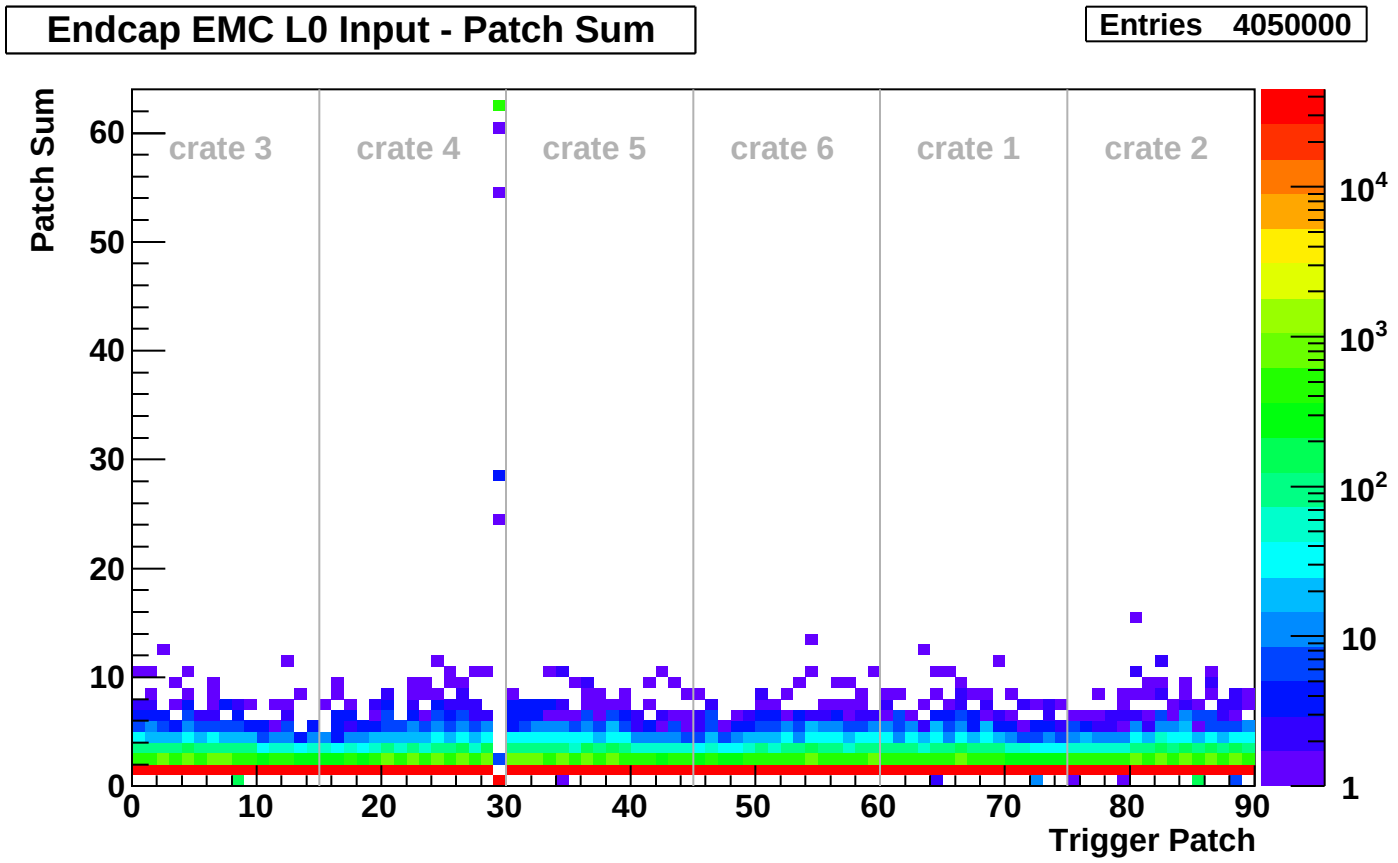

BunchId7Bit (JP1)
Entries 45000

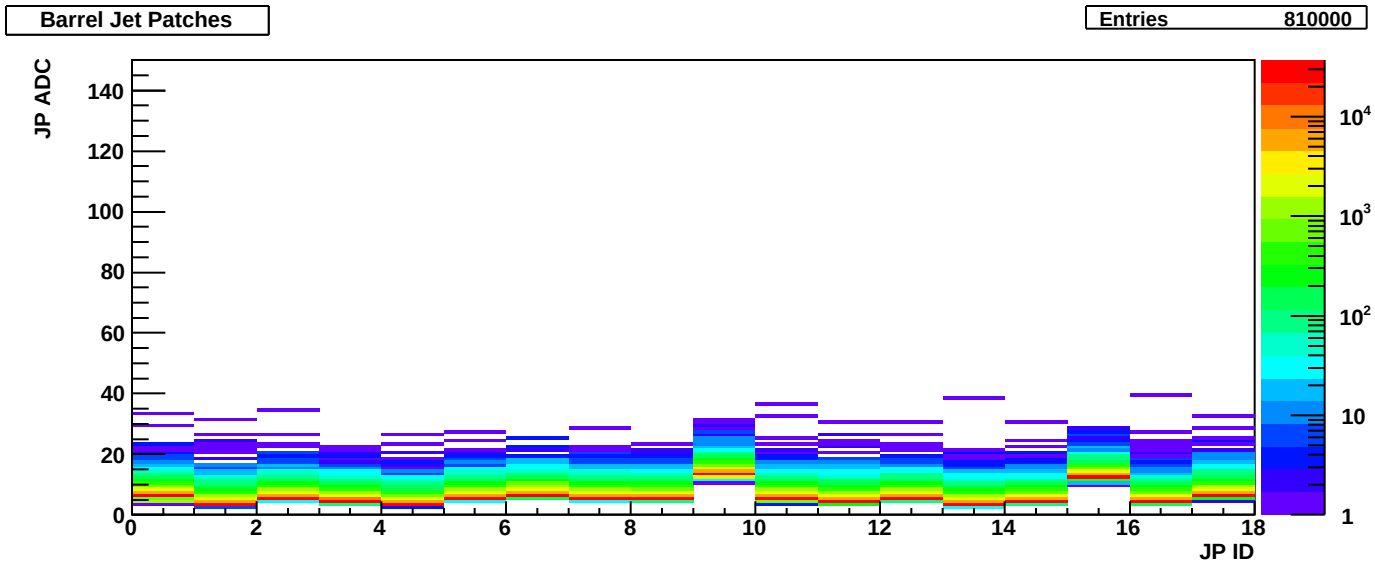

BunchId7Bit (JP2||AJP)
Entries 45000

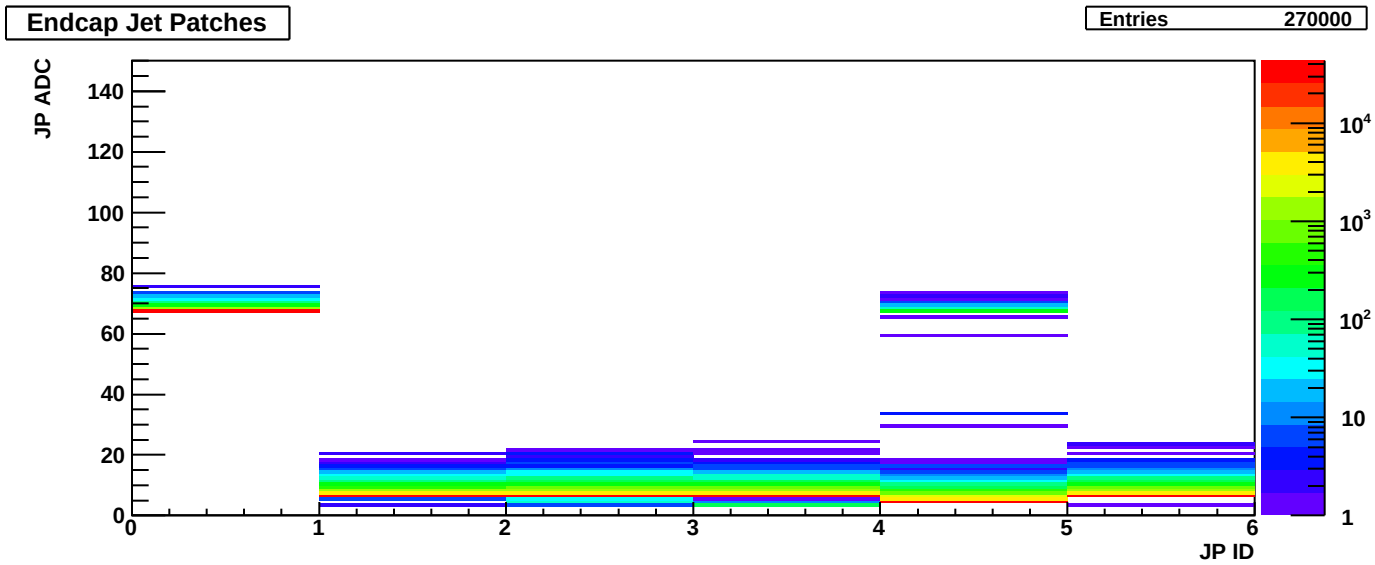

Barrel EMC L0 Input - High Tower
Entries 1.35e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

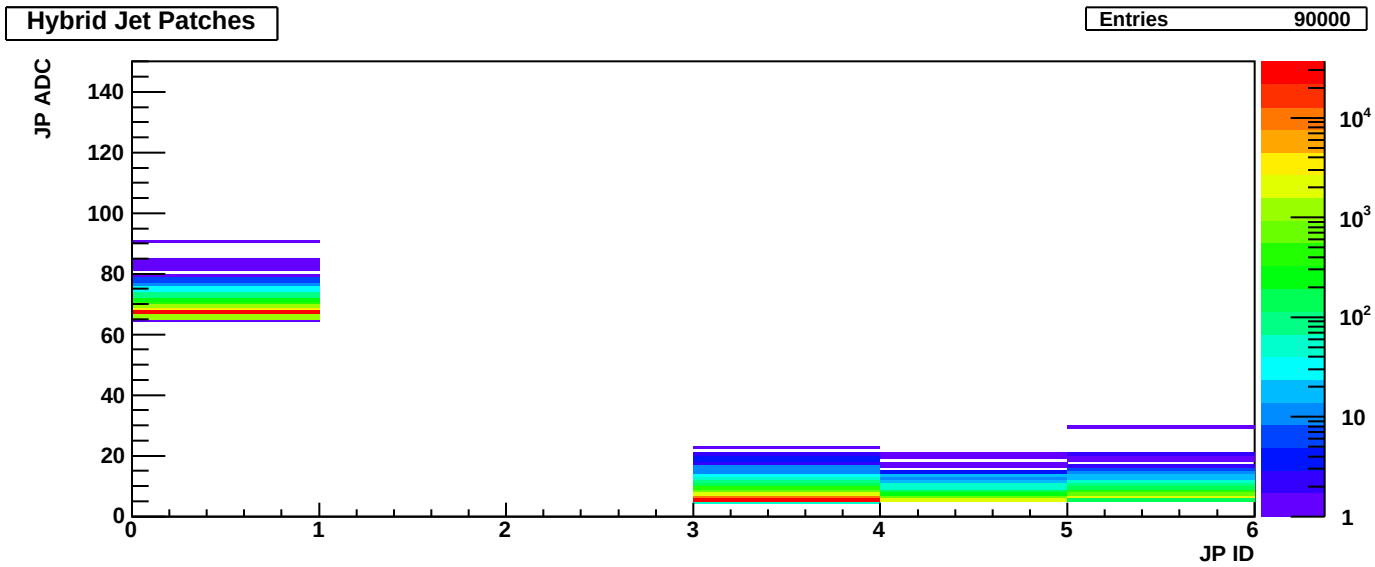

Barrel EMC L0 Input - Patch Sum
Entries 1.35e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

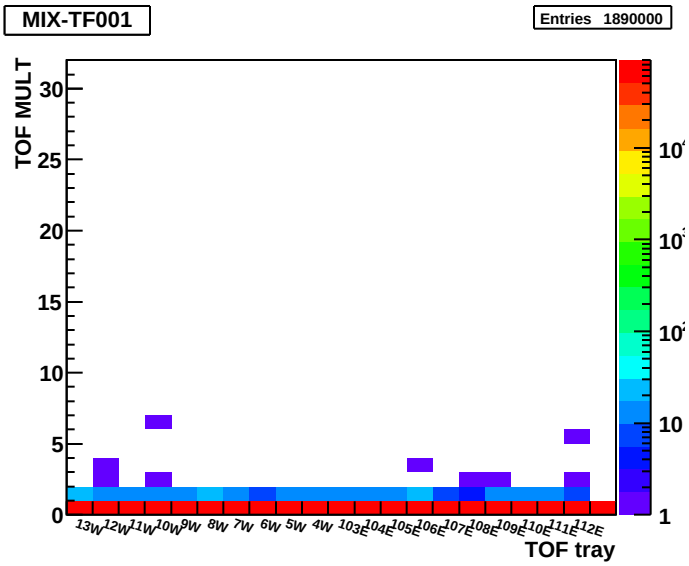

Endcap EMC L0 Input - High Tower
Entries 4050000
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

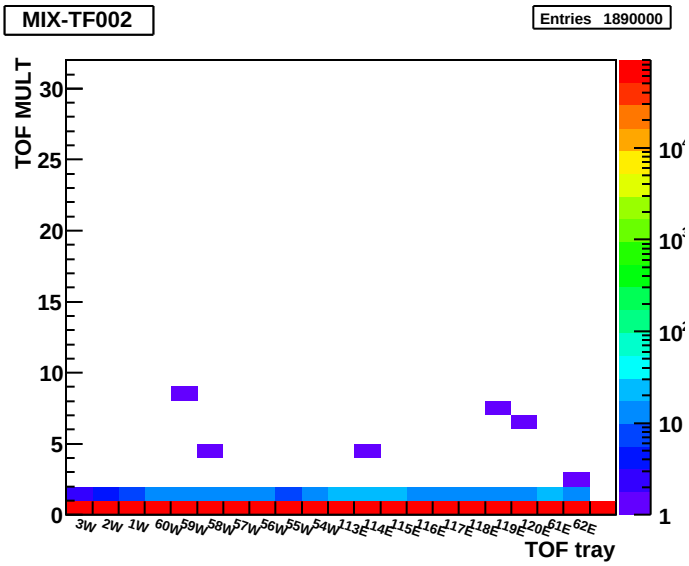

Endcap EMC L0 Input - Patch Sum
Entries 4050000
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

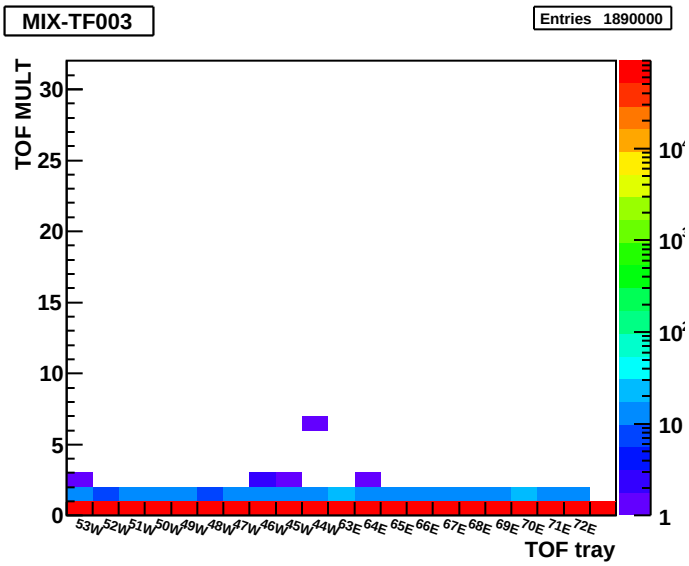

Barrel Jet Patches
Entries 810000
JP ADC
JP ID

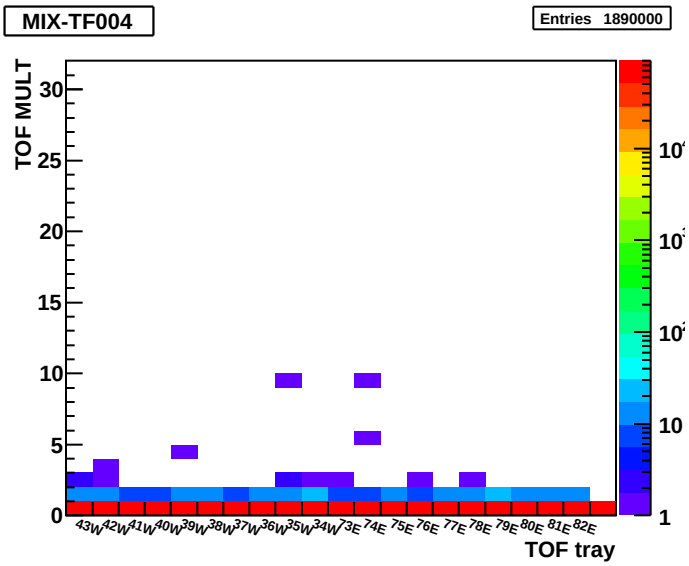

Endcap Jet Patches
Entries 270000
JP ADC
JP ID

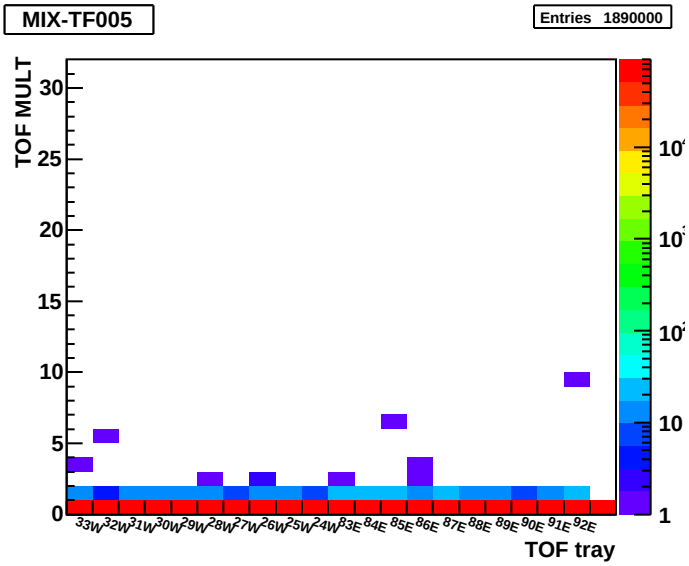

Hybrid Jet Patches
Entries 90000
JP ADC
JP ID

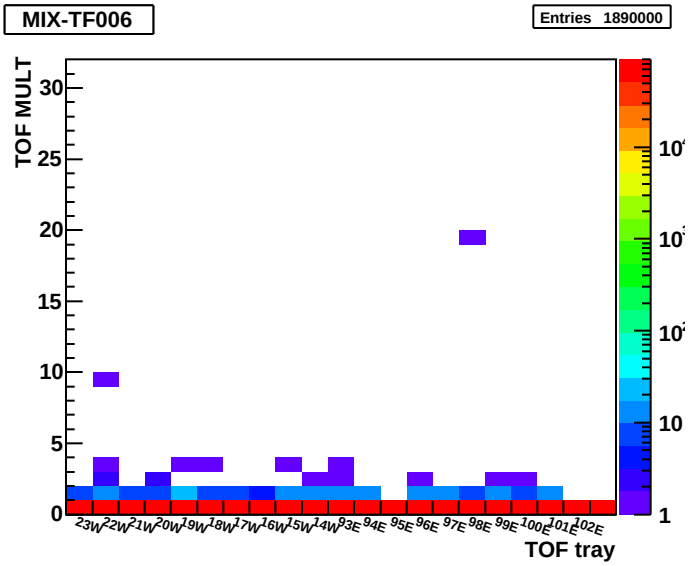

MIX-TF001
Entries 1890000
TOF MULT
TOF tray

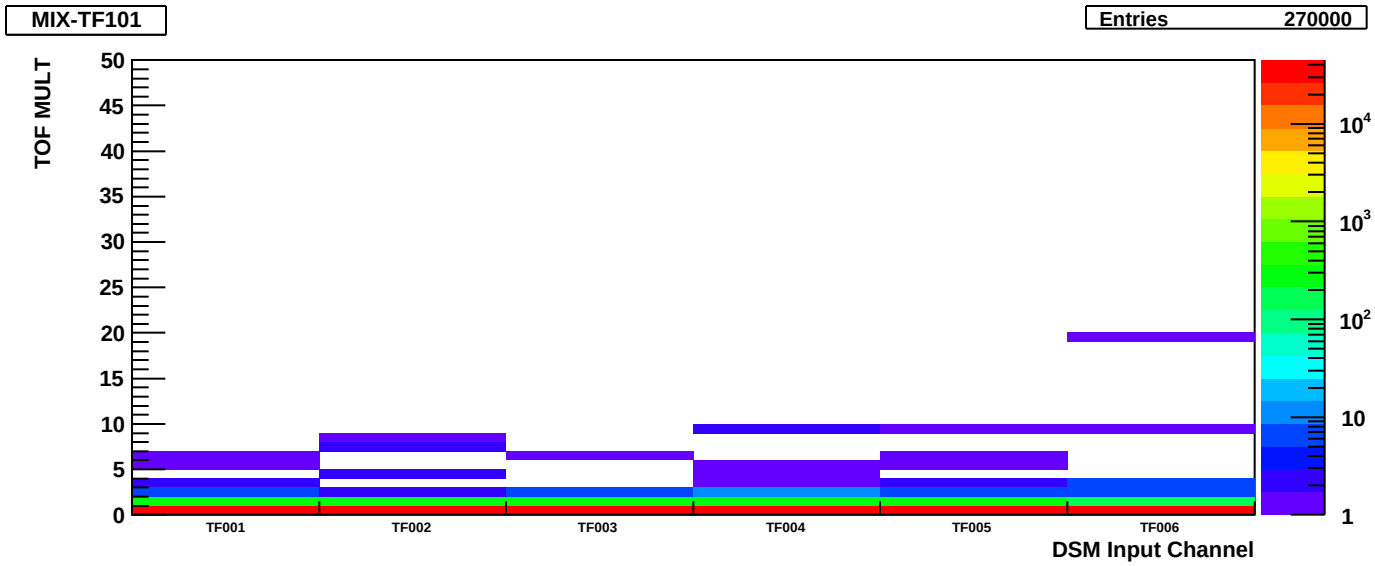

MIX-TF002
Entries 1890000
TOF MULT
TOF tray

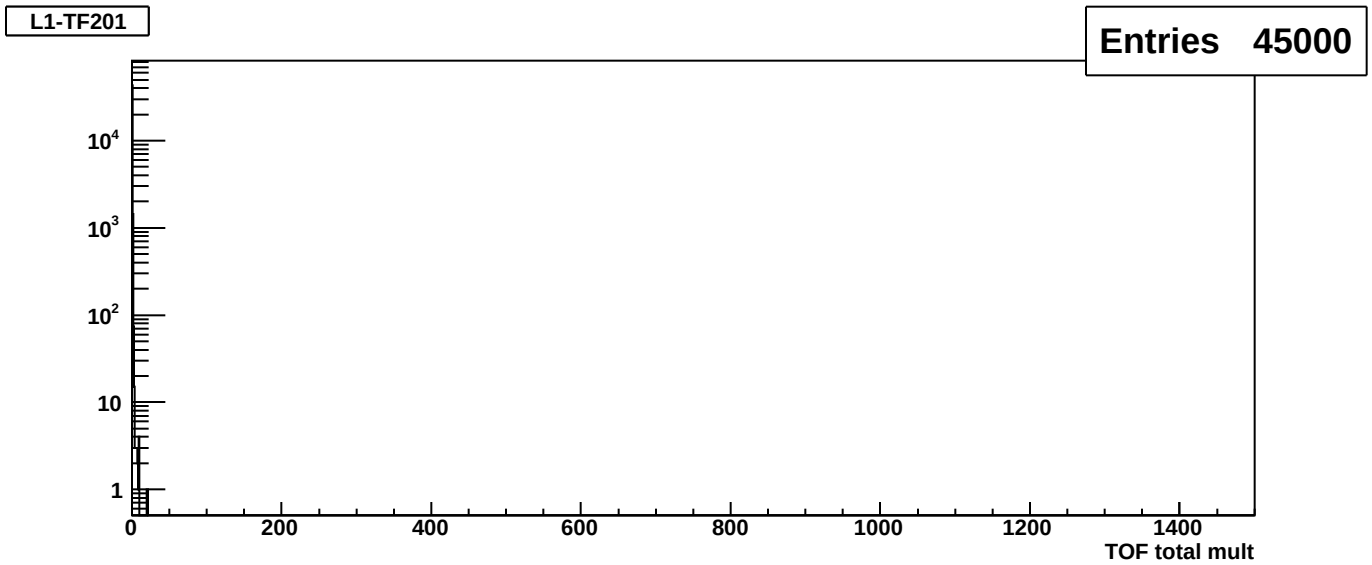

MIX-TF003
Entries 1890000
TOF MULT
TOF tray

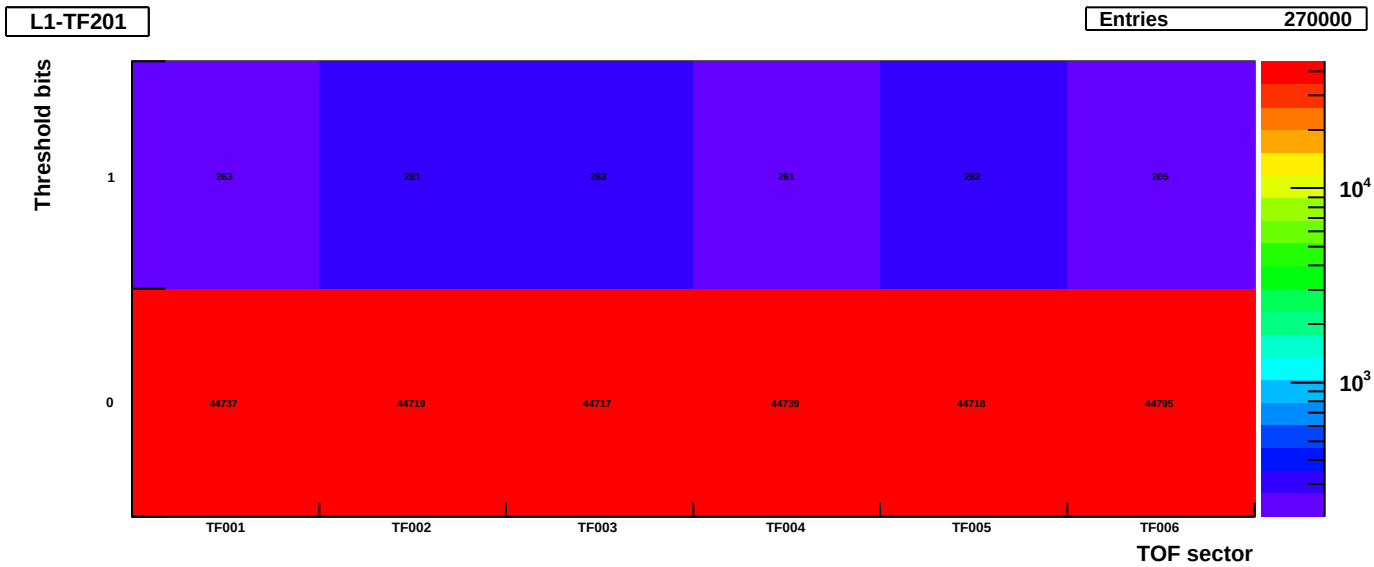

MIX-TF004
Entries 1890000
TOF MULT
TOF tray

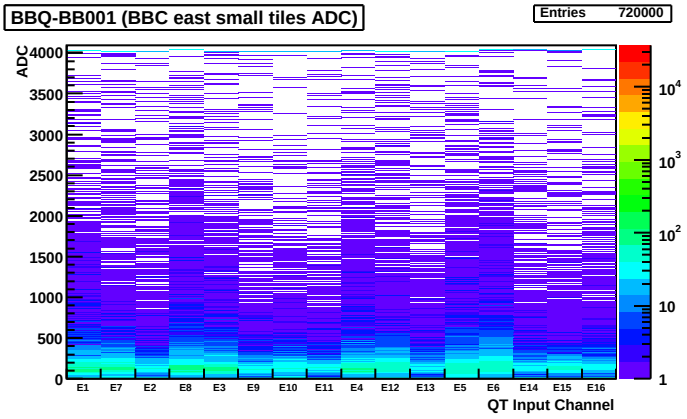

MIX-TF005
Entries 1890000
TOF MULT
TOF tray

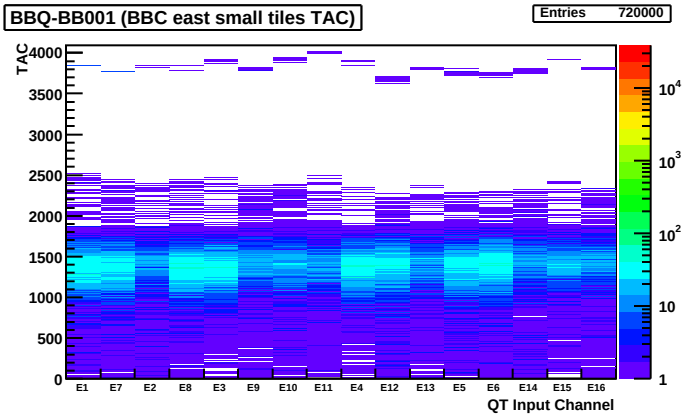

MIX-TF006
Entries 1890000
TOF MULT
TOF tray

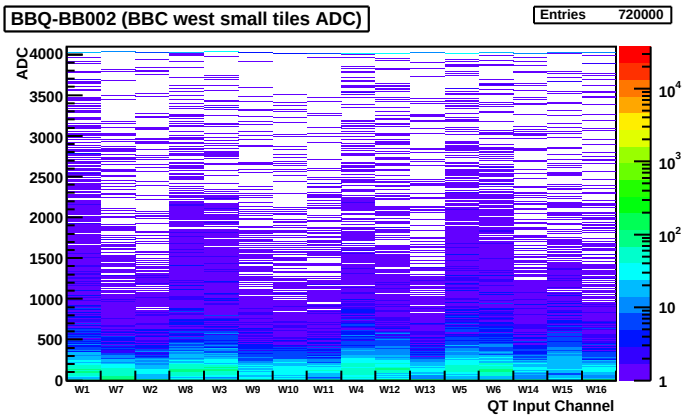

MIX-TF101
Entries 270000
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

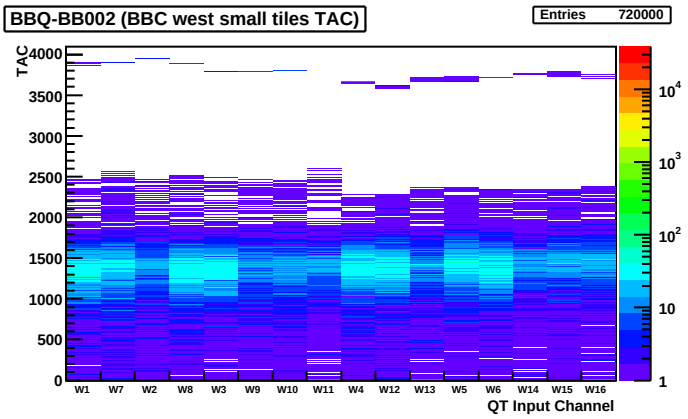

L1-TF201
Entries 45000
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

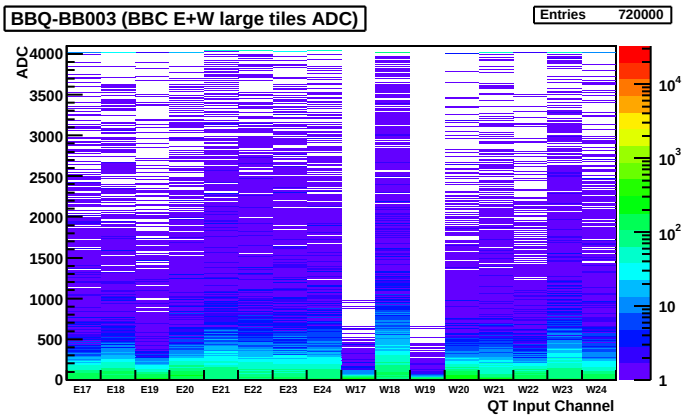

L1-TF201
Entries 270000
Threshold bits
1
0
263
281
283
261
282
205
44737
44719
44717
44739
44718
44795
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3

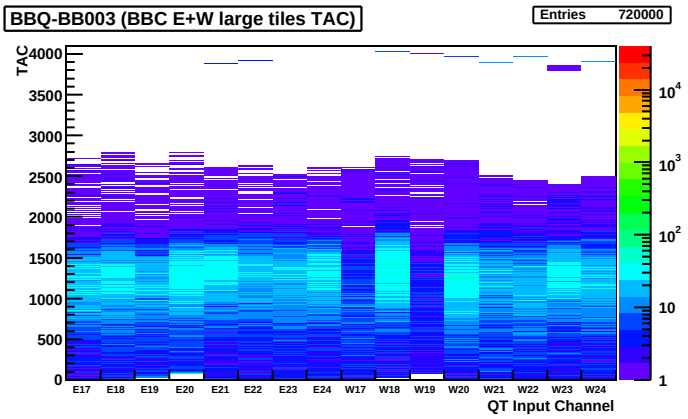

BBQ-BB001 (BBC east small tiles ADC)
Entries 720000
ADC
QT Input Channel

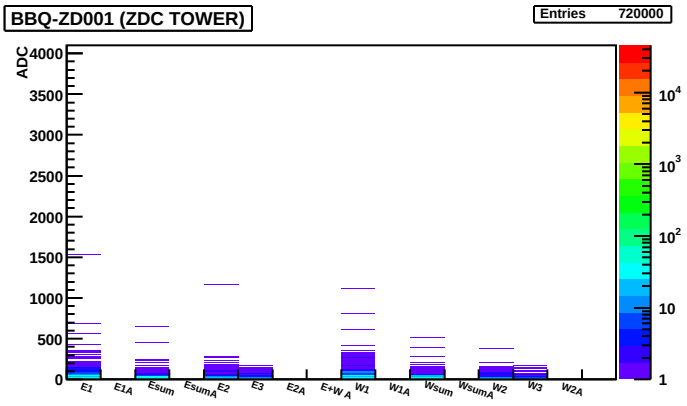

BBQ-BB001 (BBC east small tiles TAC)
Entries 720000
TAC
QT Input Channel

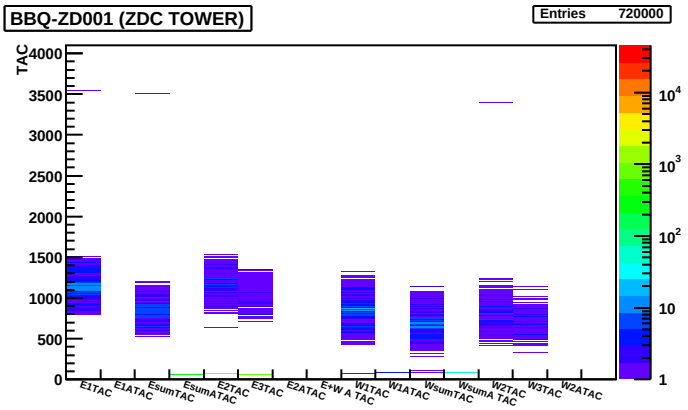

BBQ-BB002 (BBC west small tiles ADC)
Entries 720000
ADC
QT Input Channel

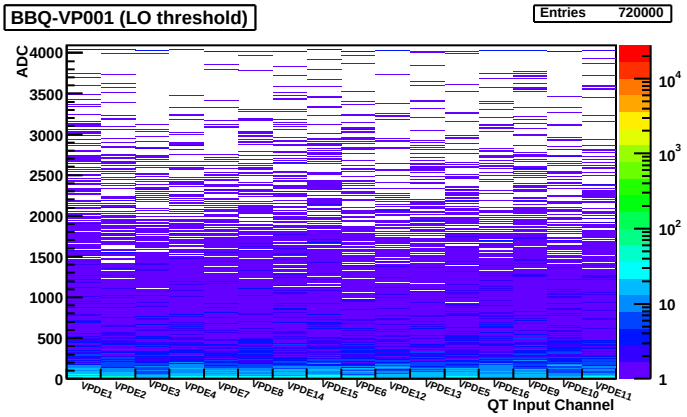

BBQ-BB002 (BBC west small tiles TAC)
Entries 720000
TAC
QT Input Channel

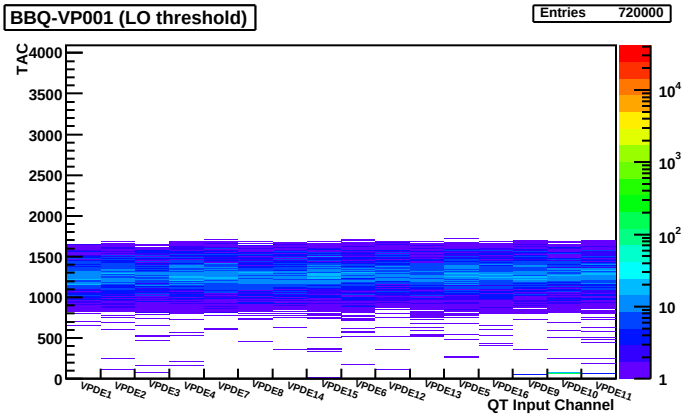

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 720000
ADC
QT Input Channel

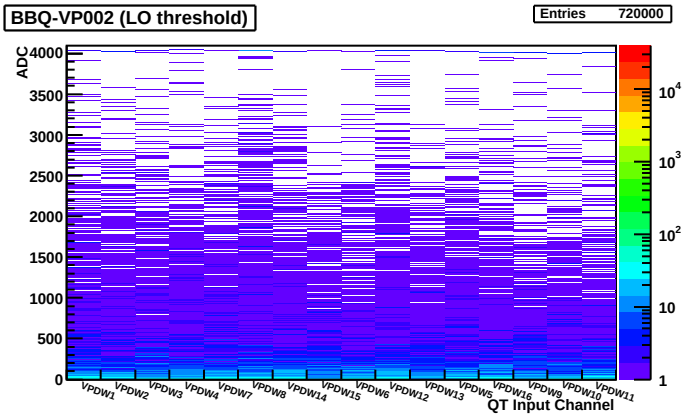

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 720000
TAC
QT Input Channel

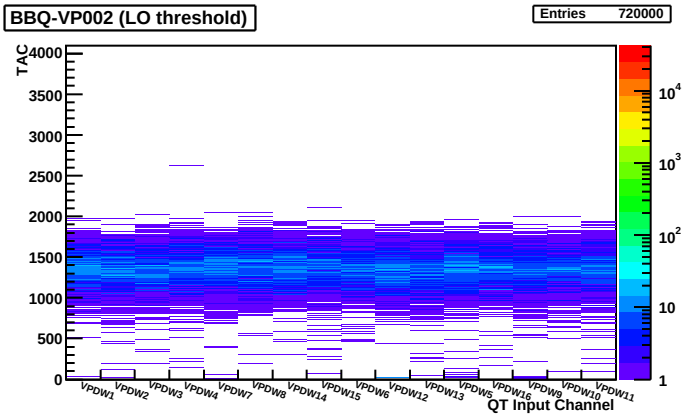

BBQ-ZD001 (ZDC TOWER)
Entries 720000
ADC

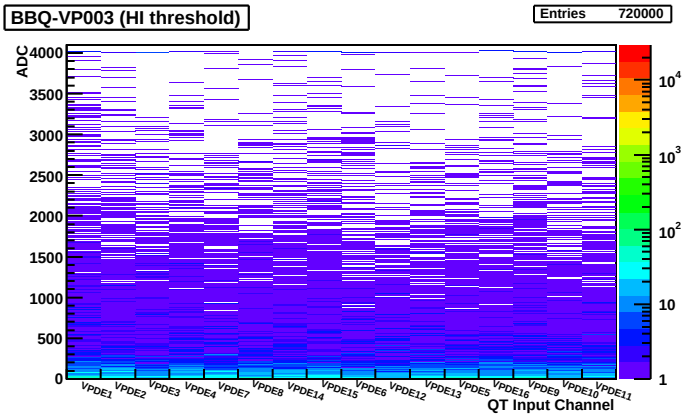

BBQ-ZD001 (ZDC TOWER)
Entries 720000
TAC

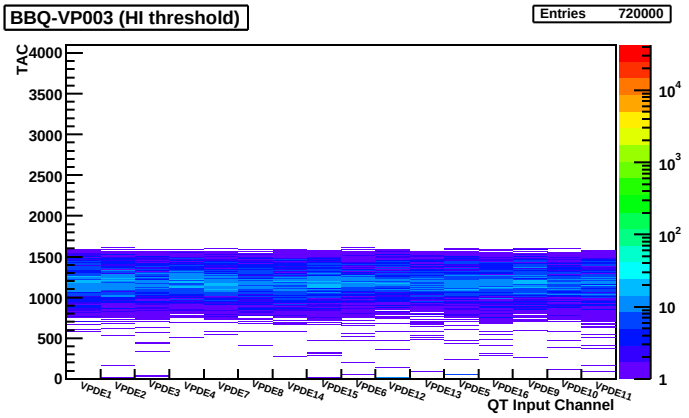

BBQ-VP001 (LO threshold)
Entries 720000
ADC
QT Input Channel

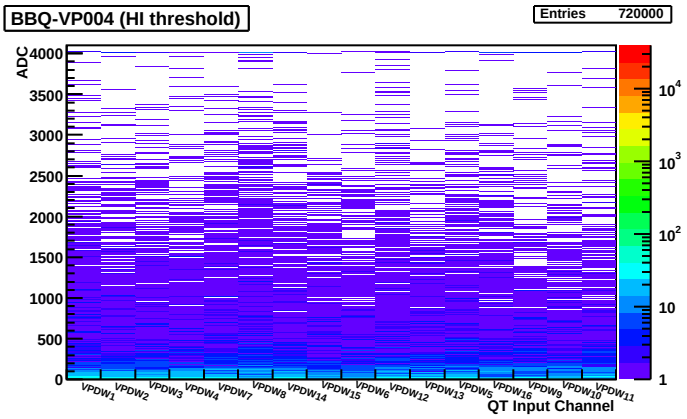

BBQ-VP001 (LO threshold)
Entries 720000
TAC
QT Input Channel

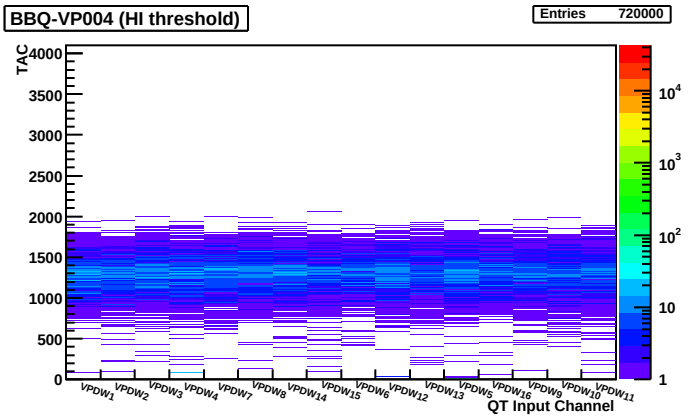

BBQ-VP002 (LO threshold)
Entries 720000
ADC
QT Input Channel

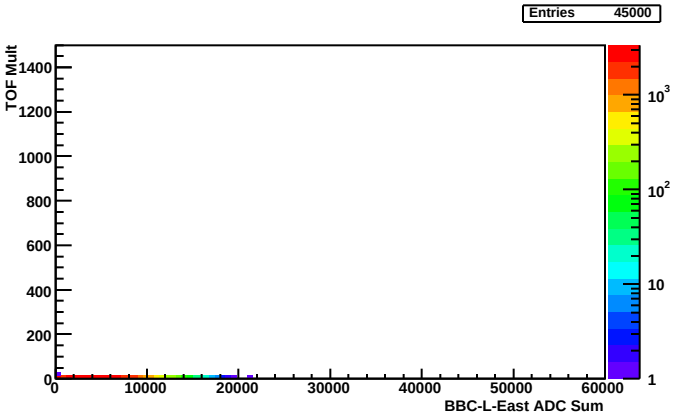

BBQ-VP002 (LO threshold)
Entries 720000
TAC
QT Input Channel

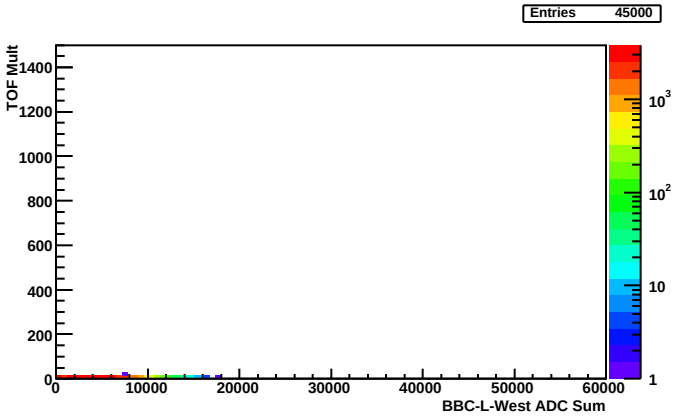

BBQ-VP003 (HI threshold)
Entries 720000
ADC
QT Input Channel

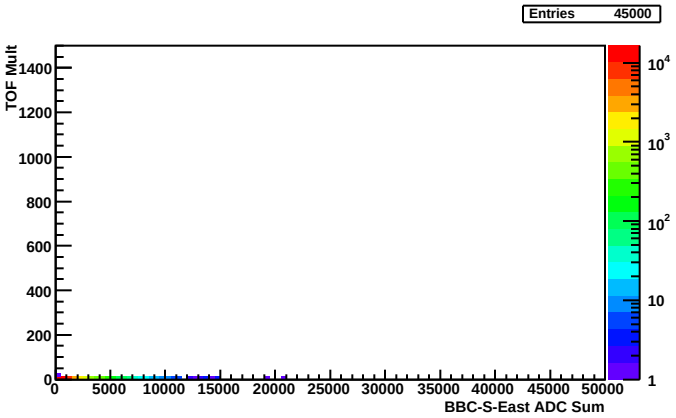

BBQ-VP003 (HI threshold)
Entries 720000
TAC
QT Input Channel

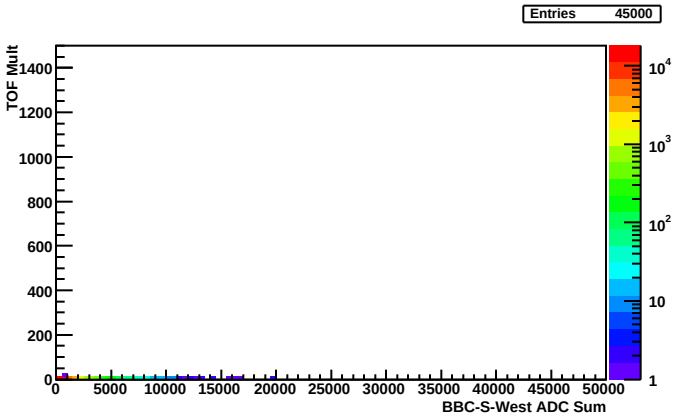

BBQ-VP004 (HI threshold)
Entries 720000
ADC
QT Input Channel

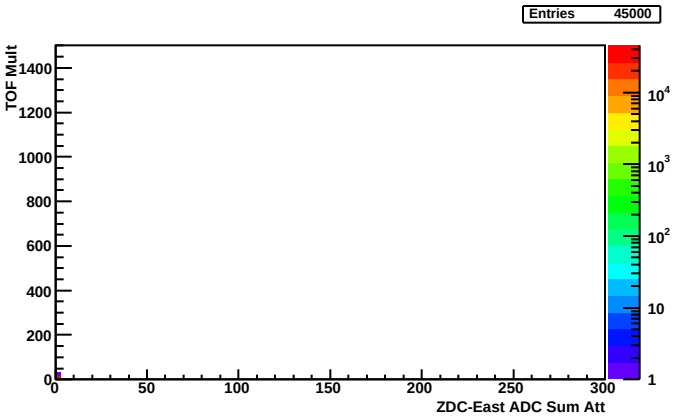

BBQ-VP004 (HI threshold)
Entries 720000
TAC
QT Input Channel

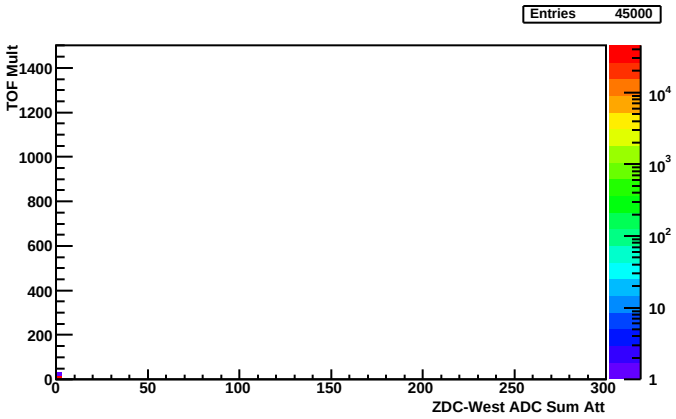

Entries 45000
TOF Mult
BBC-L-East ADC Sum

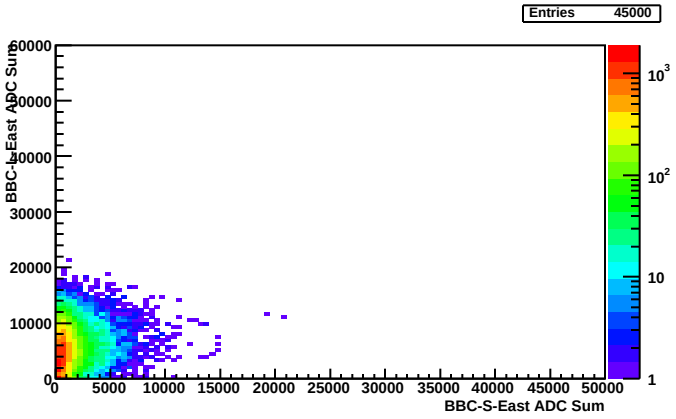

Entries 45000
TOF Mult
BBC-L-West ADC Sum

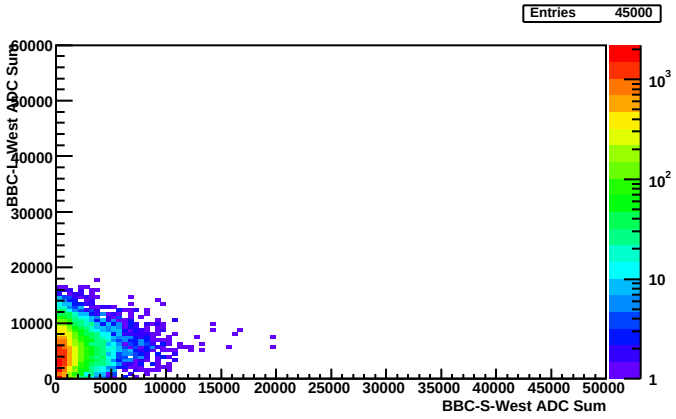

Entries 45000
TOF Mult
BBC-S-East ADC Sum

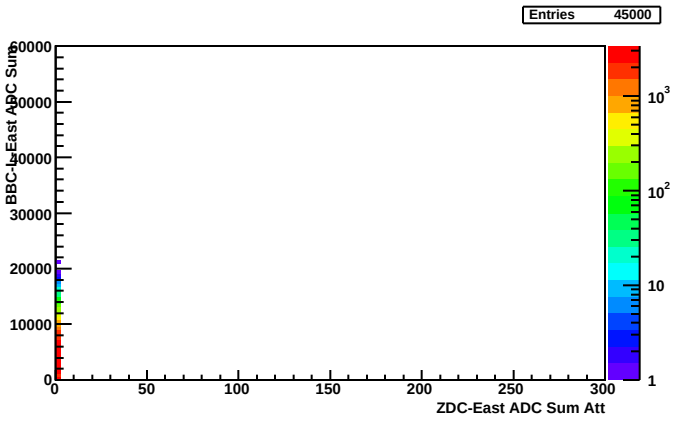

Entries 45000
TOF Mult
BBC-S-West ADC Sum

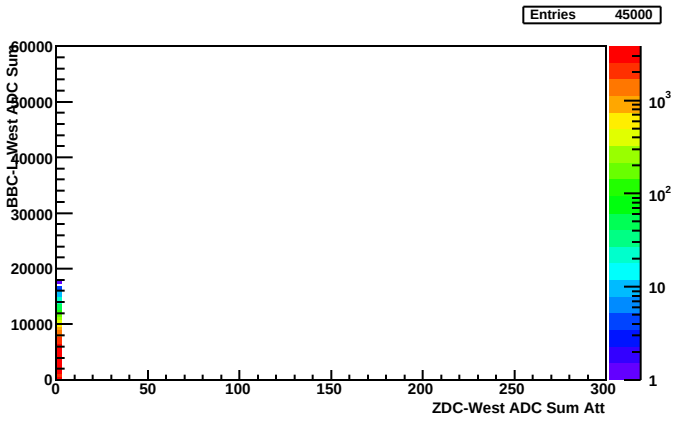

Entries 45000
TOF Mult
ZDC-East ADC Sum Att

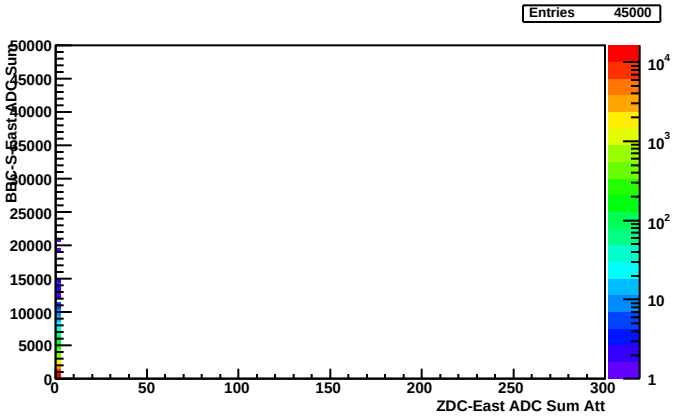

Entries 45000
TOF Mult
ZDC-West ADC Sum Att

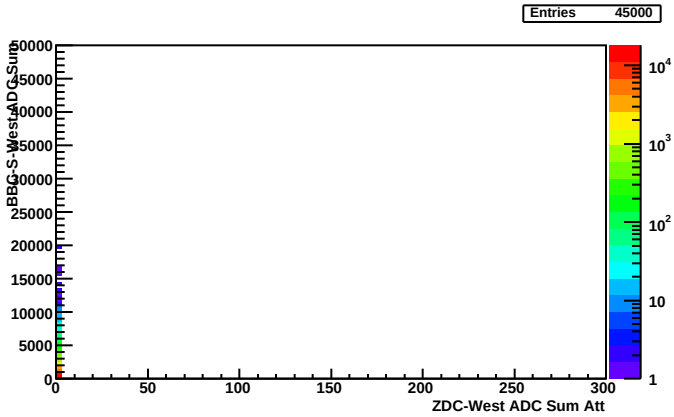

Entries 45000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

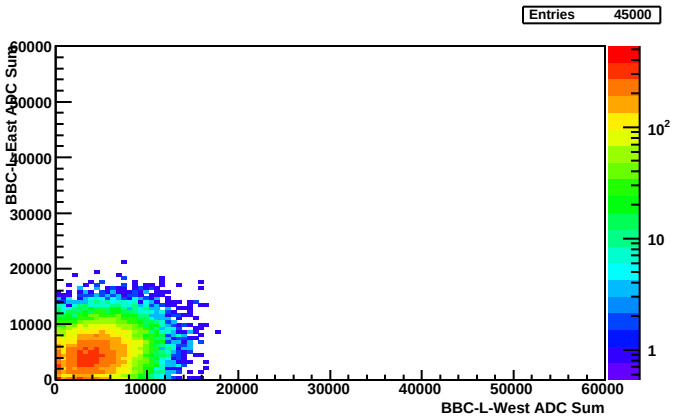

Entries 45000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

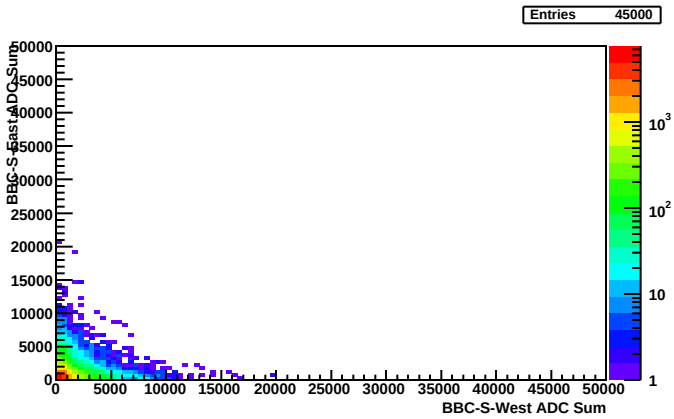

Entries 45000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

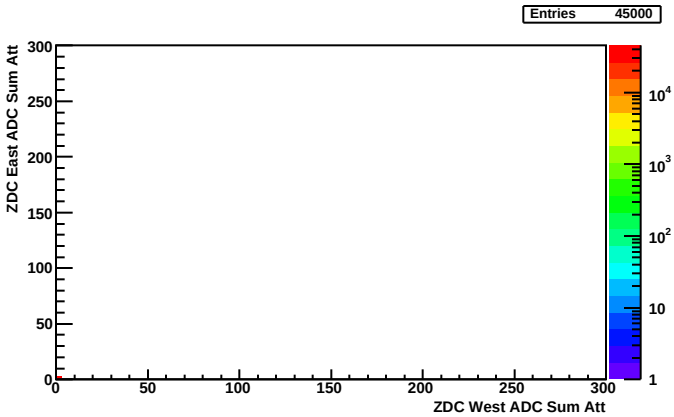

Entries 45000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

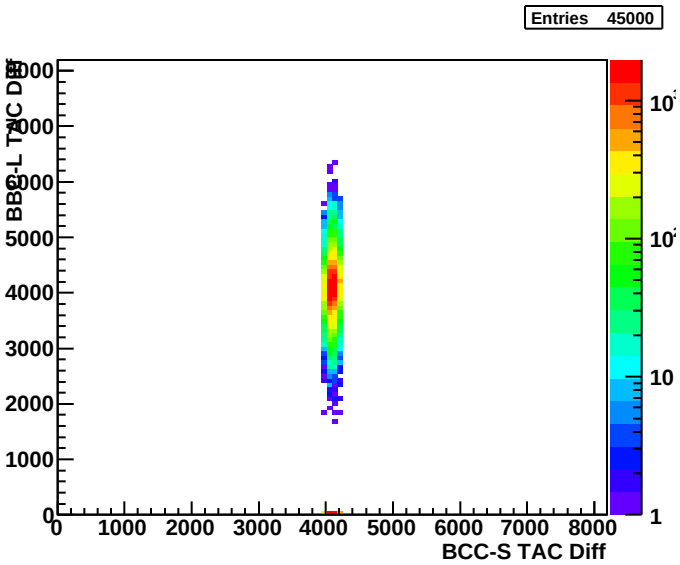

Entries 45000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

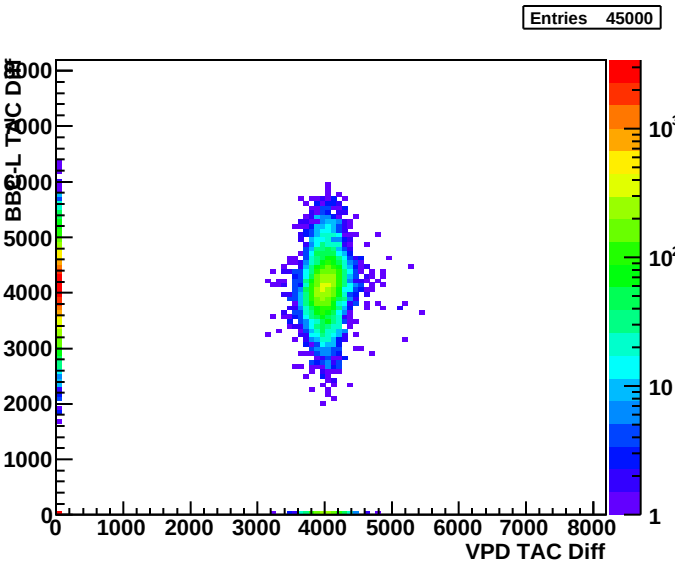

Entries 45000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

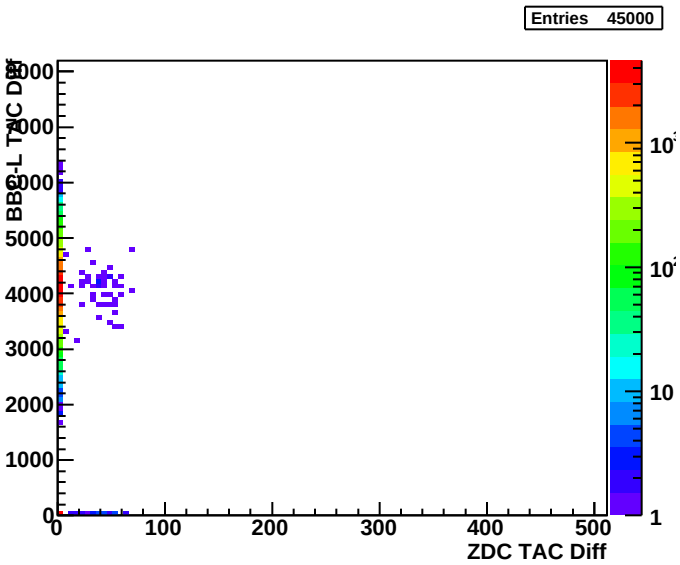

Entries 45000
BBC-L-East ADC Sum
BBC-L-West ADC Sum

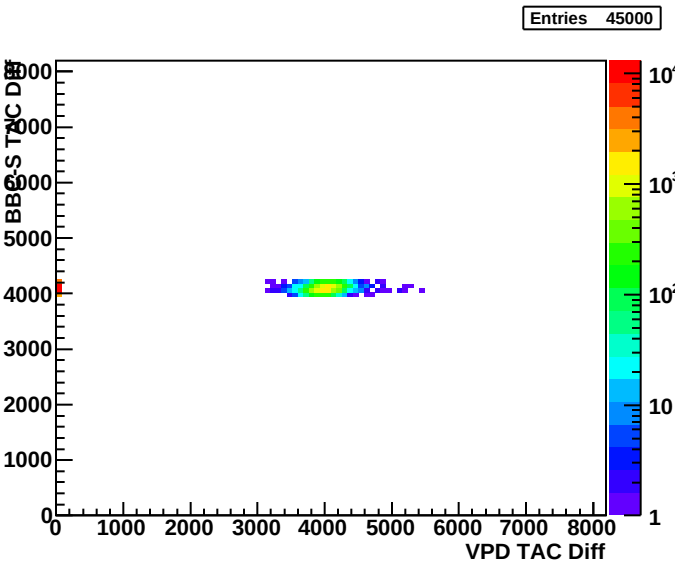

Entries 45000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

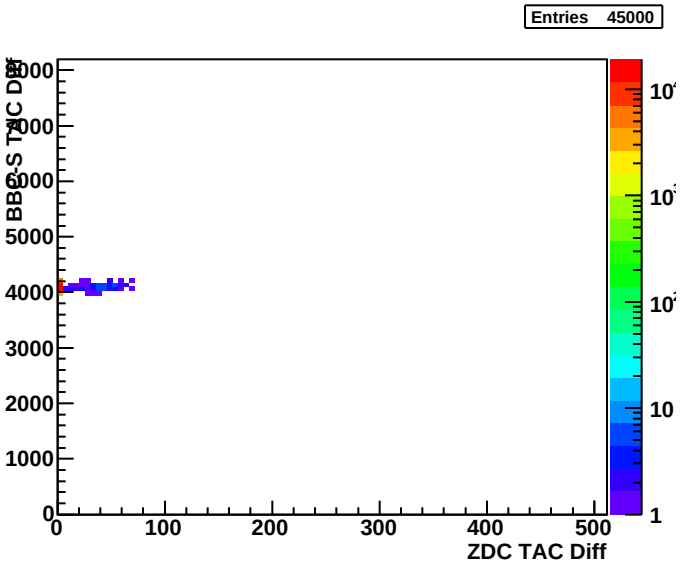

Entries 45000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

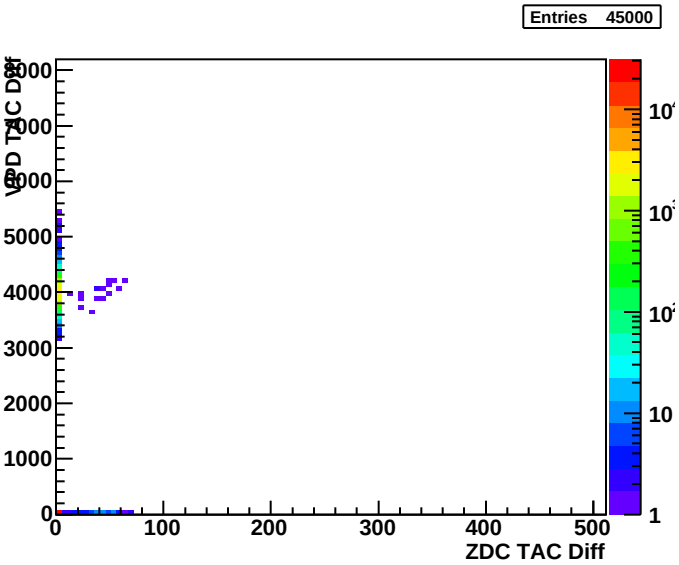

Entries 45000
BBC-L TAC Diff
BCC-S TAC Diff

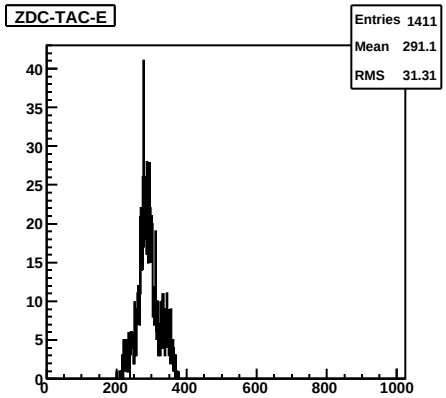

Entries 45000
BBC-L TAC Diff
VPD TAC Diff

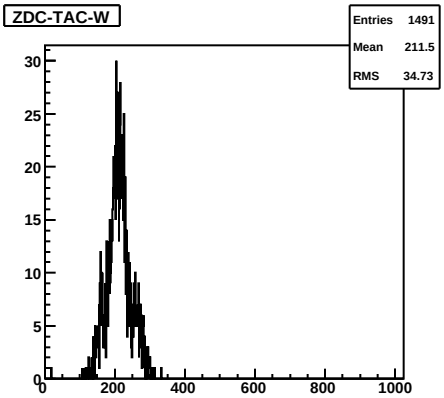

Entries 45000
BBC-L TAC Diff
ZDC TAC Diff

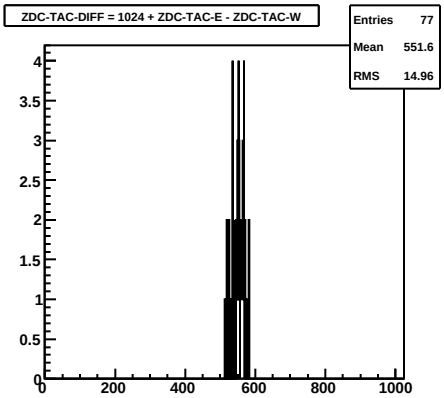

Entries 45000
BBC-S TAC Diff
VPD TAC Diff

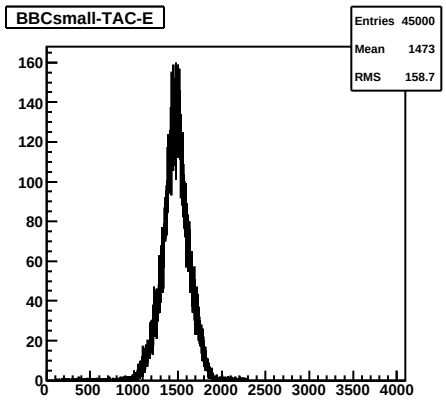

Entries 45000
BBC-S TAC Diff
ZDC TAC Diff

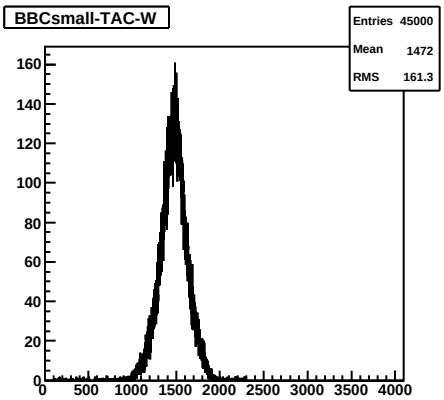

Entries 45000
VPD TAC Diff
ZDC TAC Diff

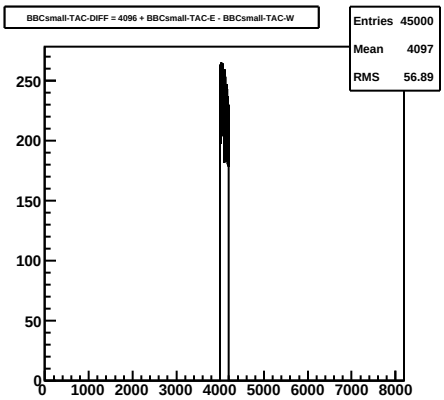

ZDC-TAC-E
Entries 1411
Mean 291.1
RMS 31.31

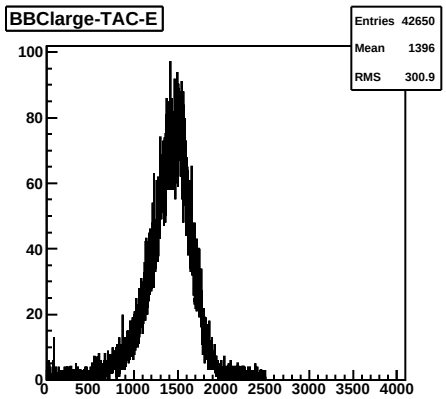

ZDC-TAC-W
Entries 1491
Mean 211.5
RMS 34.73

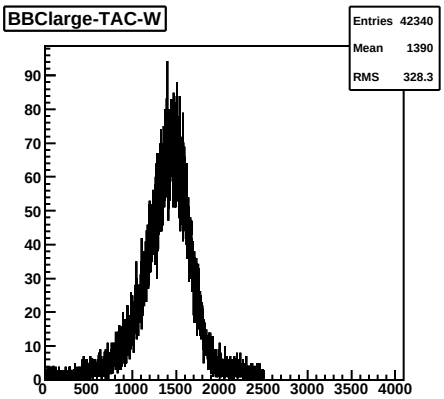

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 77
Mean 551.6
RMS 14.96

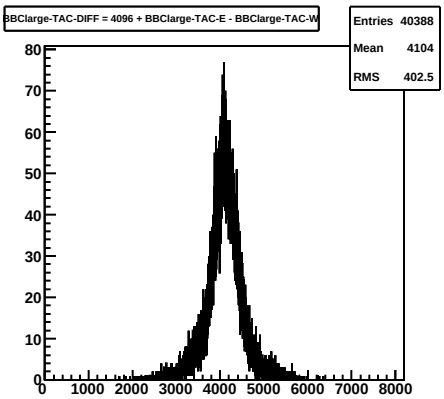

BBCsmall-TAC-E
Entries 45000
Mean 1473
RMS 158.7

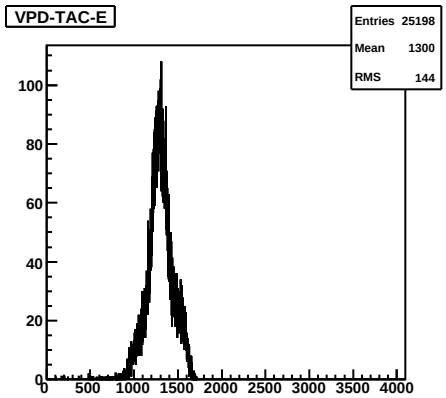

BBCsmall-TAC-W
Entries 45000
Mean 1472
RMS 161.3

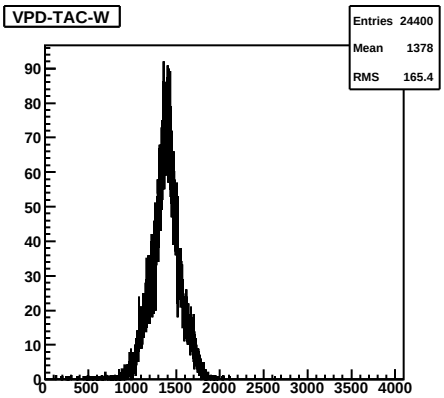

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 45000
Mean 4097
RMS 56.89

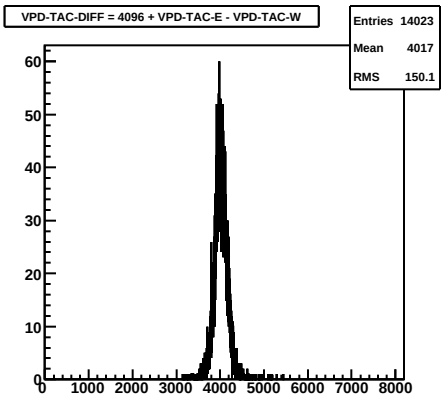

BBClarge-TAC-E
Entries 42650
Mean 1396
RMS 300.9

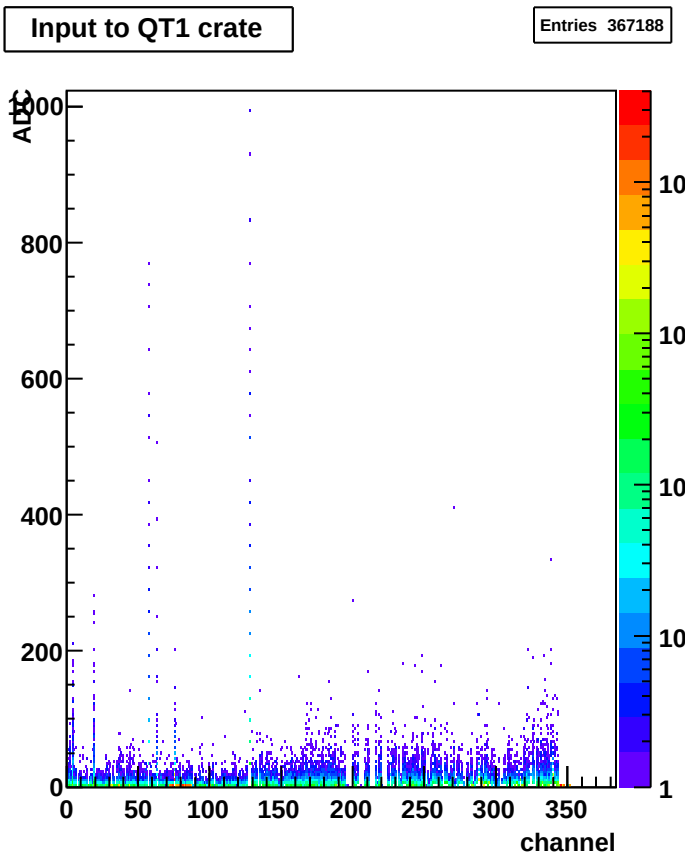

BBClarge-TAC-W
Entries 42340
Mean 1390
RMS 328.3

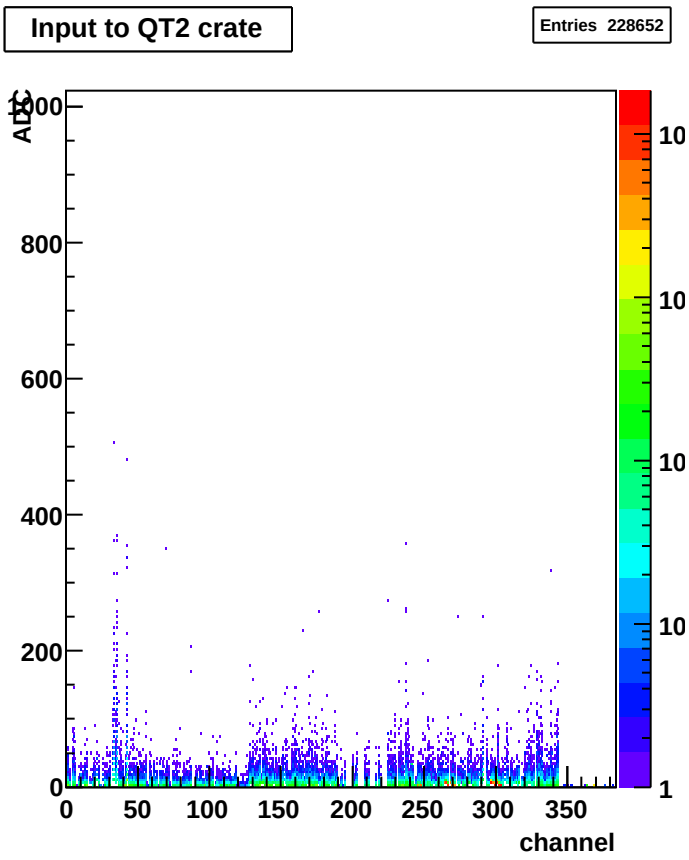

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 40388
Mean 4104
RMS 402.5

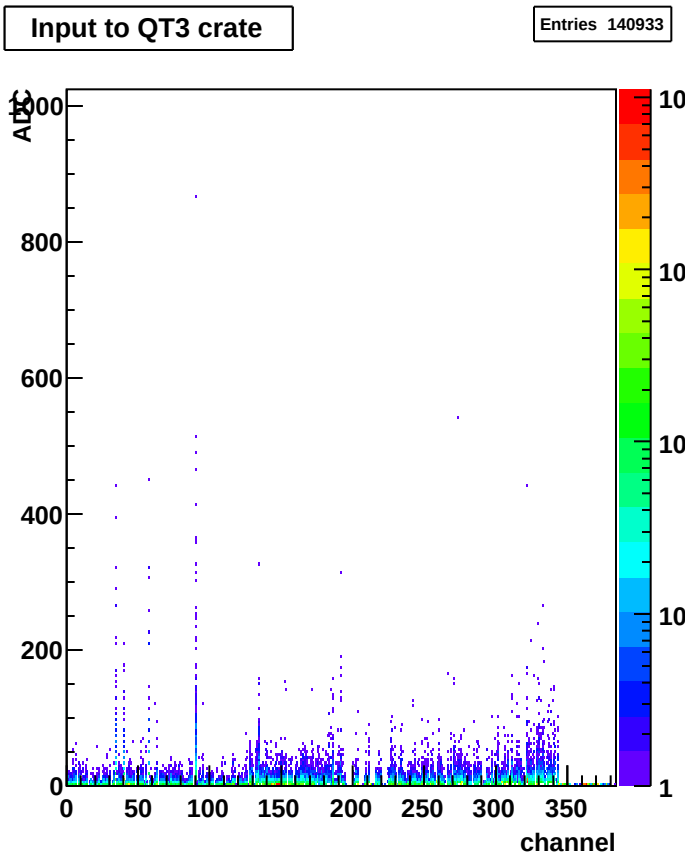

VPD-TAC-E
Entries 25198
Mean 1300
RMS 144

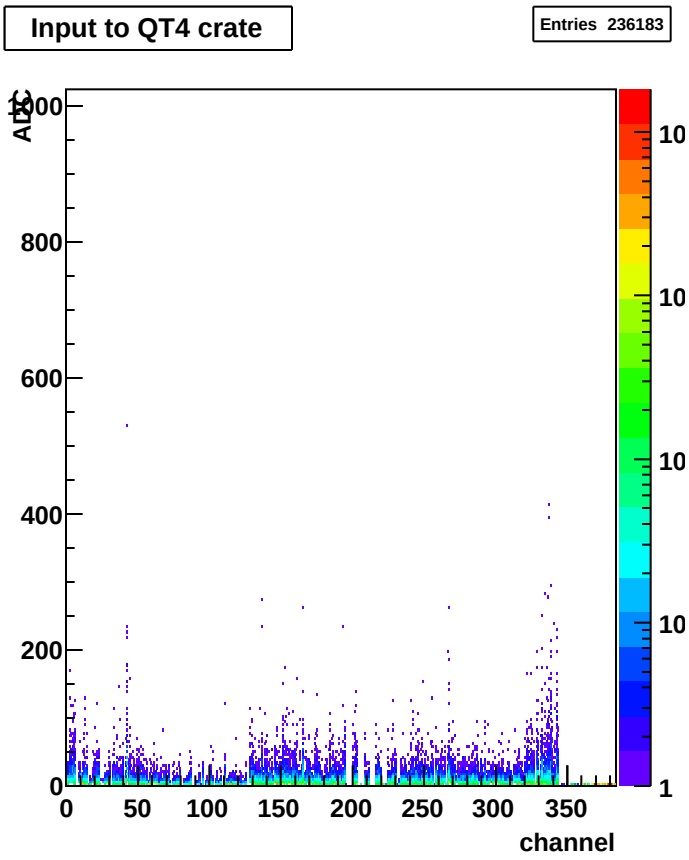

VPD-TAC-W
Entries 24400
Mean 1378
RMS 165.4

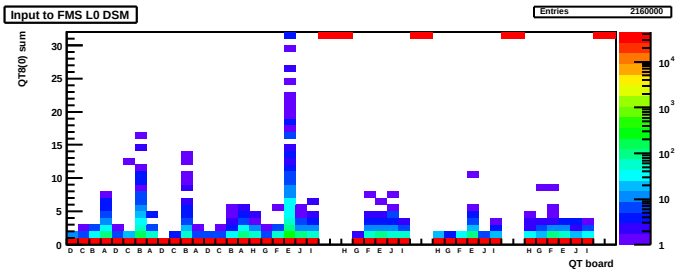

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 14023
Mean 4017
RMS 150.1

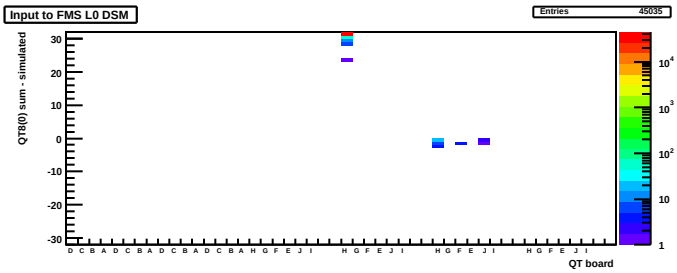

Input to QT1 crate
Entries 367188
ADC
channel

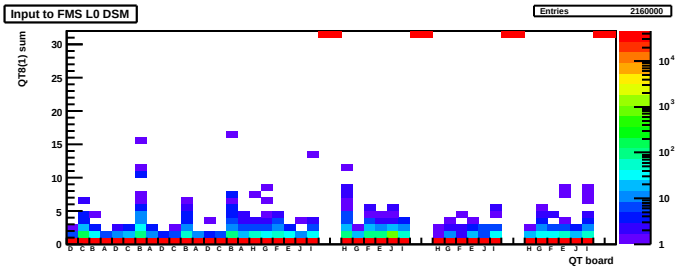

Input to QT2 crate
Entries 228652
ADC
channel

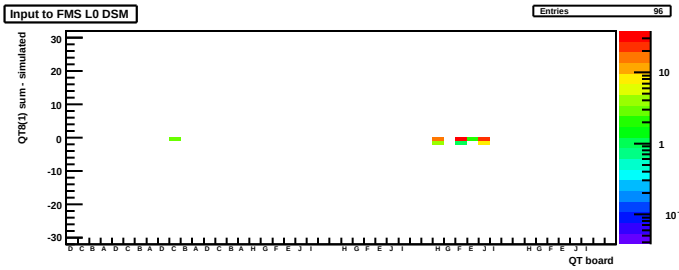

Input to QT3 crate
Entries 140933
ADC
channel

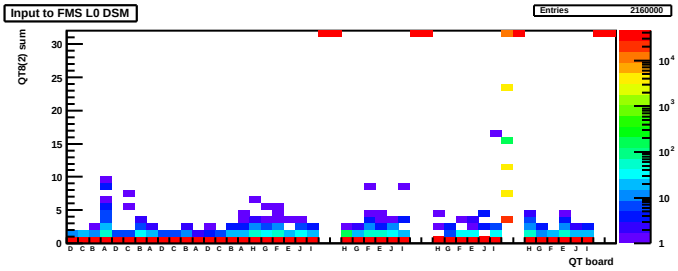

Input to QT4 crate
Entries 236183
ADC
channel

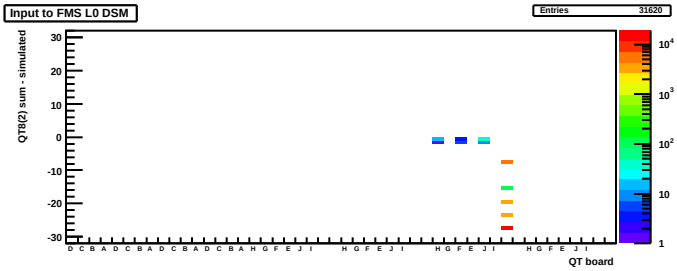

Input to FMS L0 DSM
Entries 2160000
QT8(0) sum
QT board

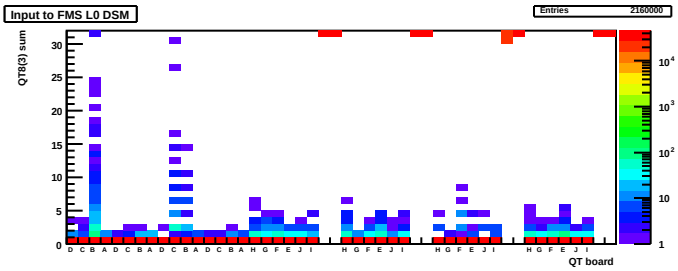

Input to FMS L0 DSM
Entries 45035
QT8(0) sum - simulated
QT board

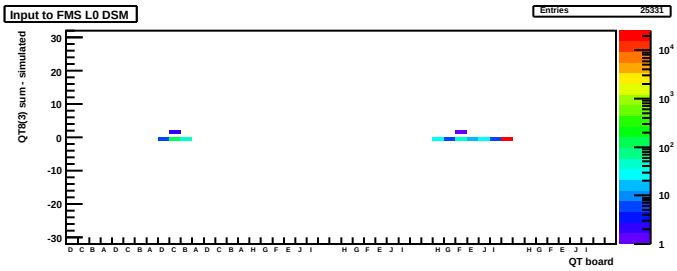

Input to FMS L0 DSM
Entries 2160000
QT8(1) sum
QT board

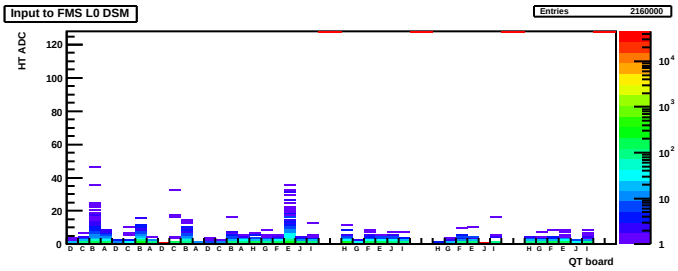

Input to FMS L0 DSM
Entries 96
QT8(1) sum - simulated
QT board

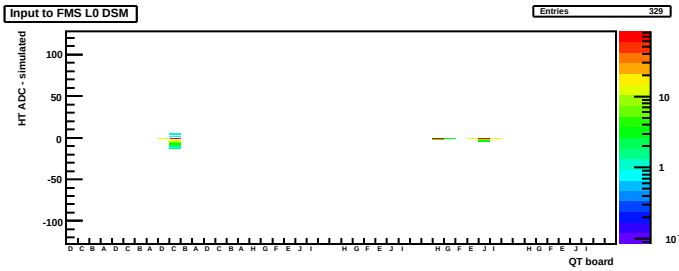

Input to FMS L0 DSM
Entries 2160000
QT8(2) sum
QT board

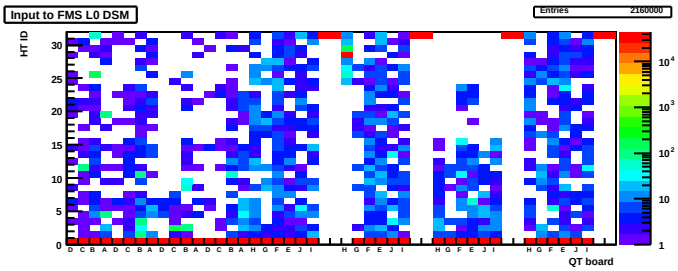

Input to FMS L0 DSM
Entries 31620
QT8(2) sum - simulated
QT board

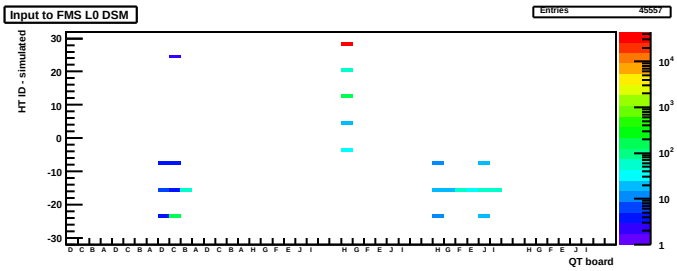

Input to FMS L0 DSM
Entries 2160000
QT8(3) sum
QT board

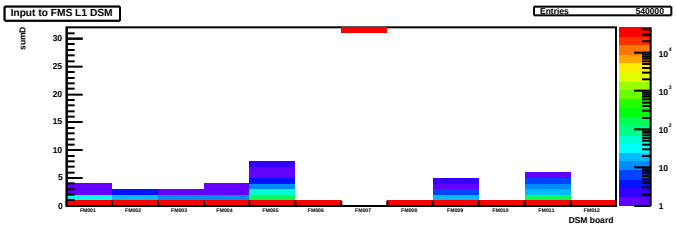

Input to FMS L0 DSM
Entries 25331
QT8(3) sum - simulated
QT board

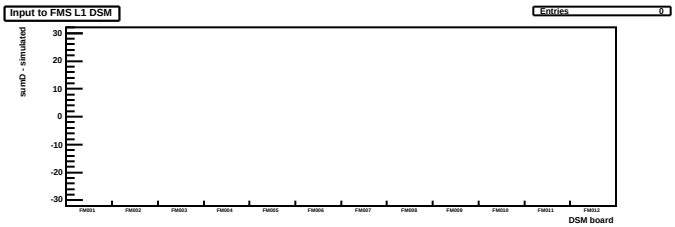

Input to FMS L0 DSM
Entries 2160000
HT ADC
QT board

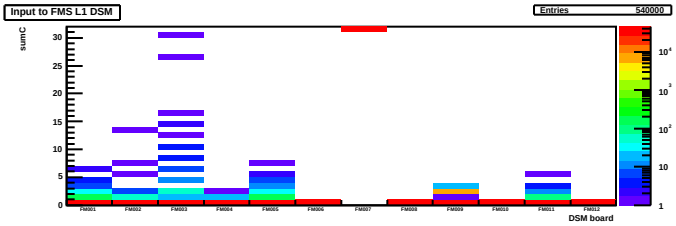

Input to FMS L0 DSM
Entries 329
HT ADC - simulated
QT board

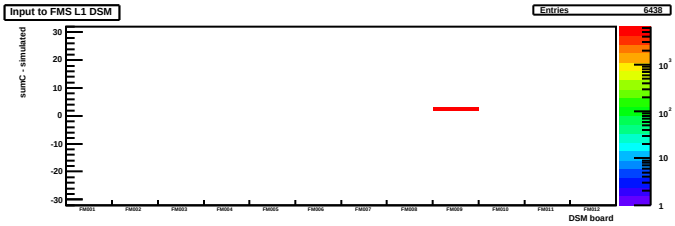

Input to FMS L0 DSM
Entries 2160000
HT ID
QT board

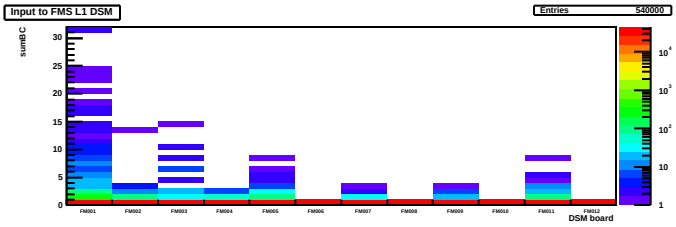

Input to FMS L0 DSM
Entries 45557
HT ID - simulated
QT board

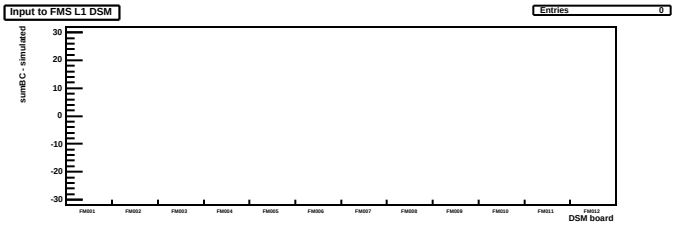

Input to FMS L1 DSM
Entries 540000
sumD
DSM board

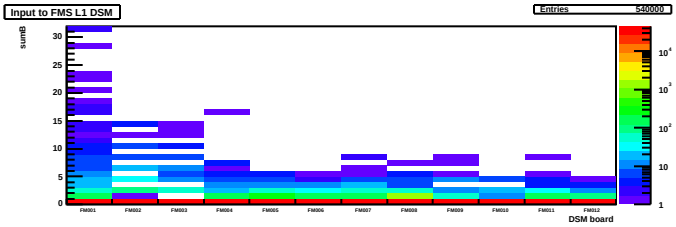

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

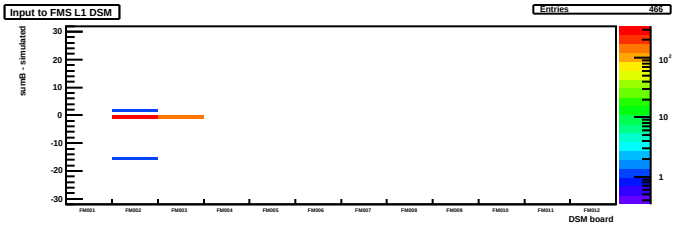

Input to FMS L1 DSM
Entries 540000
sumC
DSM board

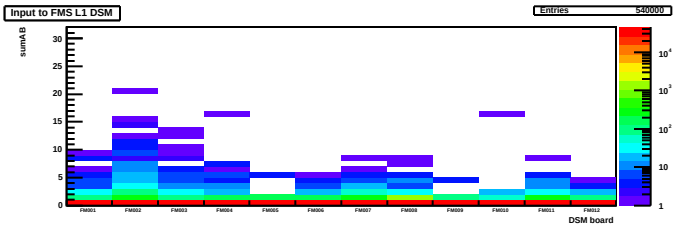

Input to FMS L1 DSM
Entries 6438
sumC - simulated
DSM board

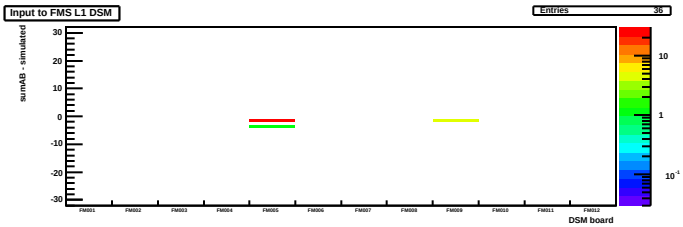

Input to FMS L1 DSM
Entries 540000
sumBC
DSM board

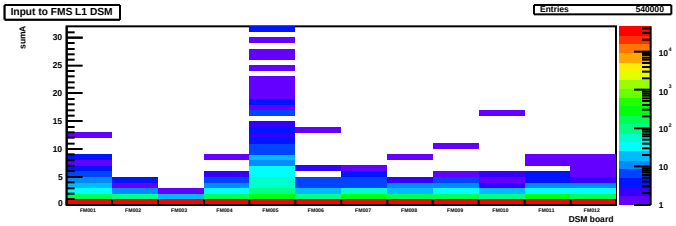

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

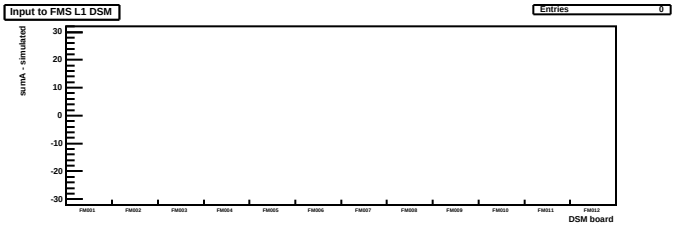

Input to FMS L1 DSM
Entries 540000
sumB
DSM board

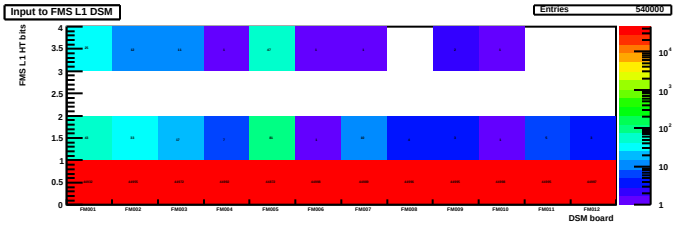

Input to FMS L1 DSM
Entries 466
sumB - simulated
DSM board

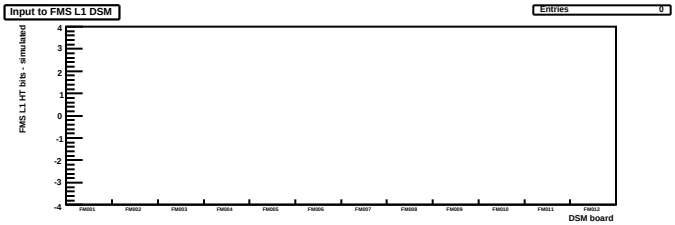

Input to FMS L1 DSM
Entries 540000
sumAB
DSM board

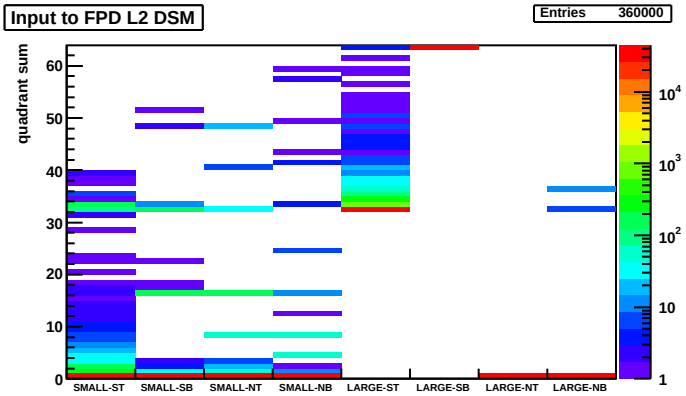

Input to FMS L1 DSM
Entries 36
sumAB - simulated
DSM board

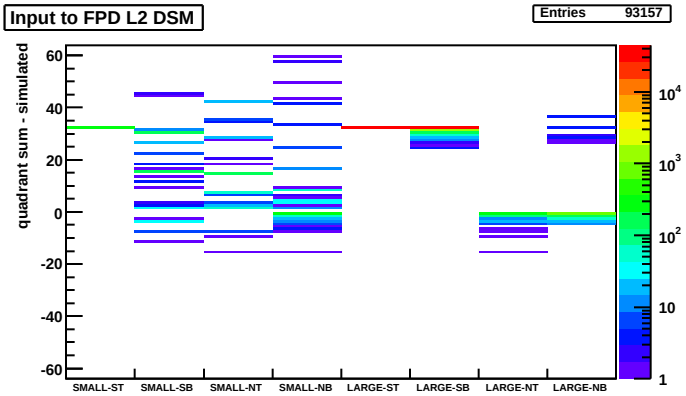

Input to FMS L1 DSM
Entries 540000
sumA
DSM board

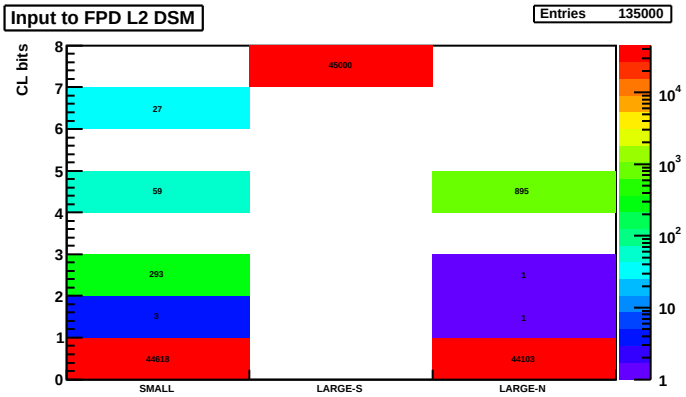

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

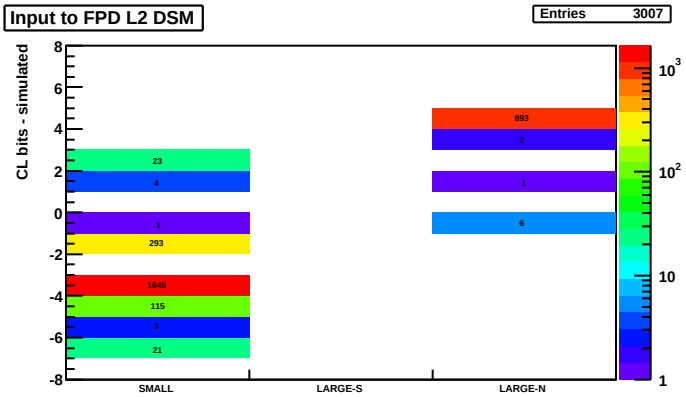

Input to FMS L1 DSM
Entries 540000
FMS L1 HT bits
DSM board

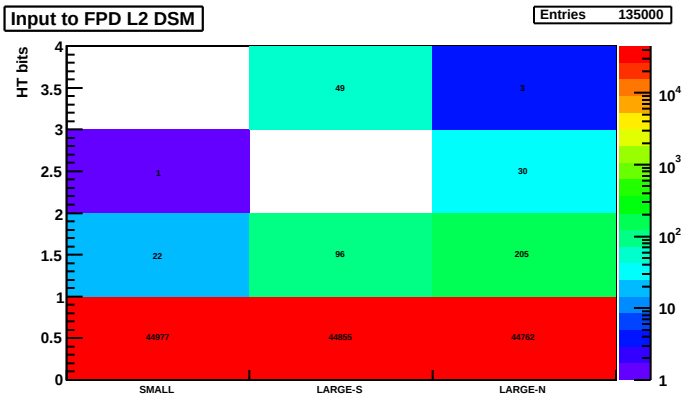

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

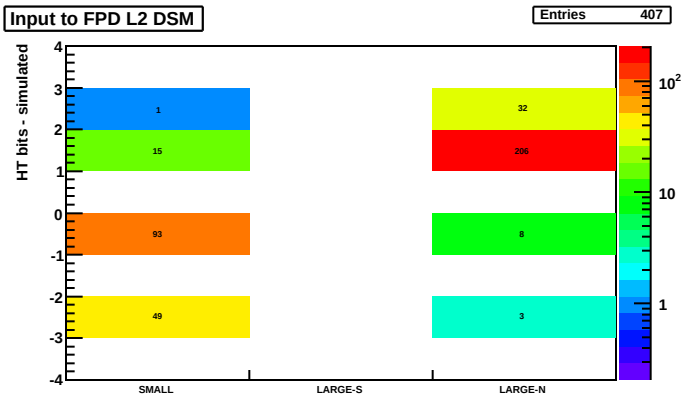

Input to FPD L2 DSM
Entries 360000
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

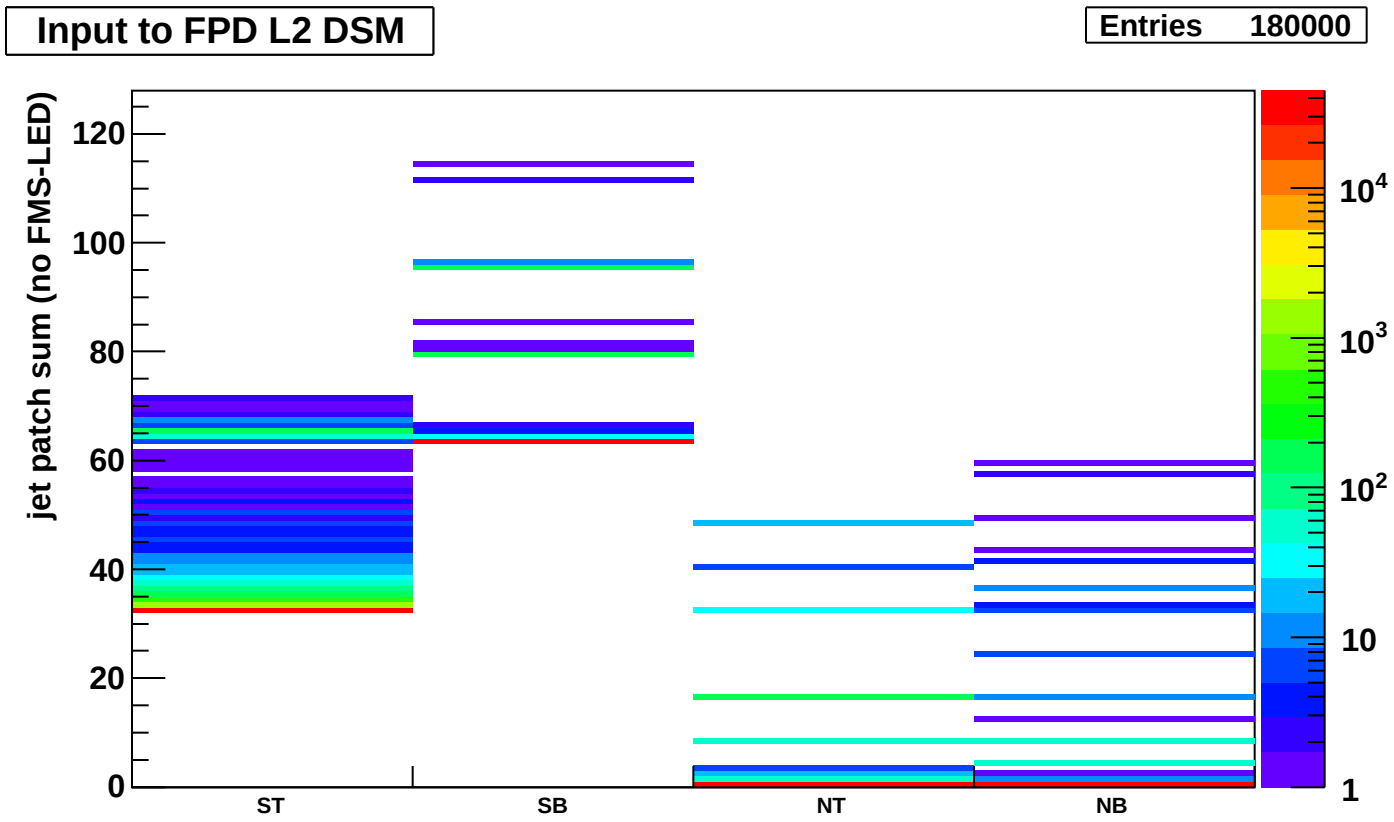

Input to FPD L2 DSM
Entries 93157
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

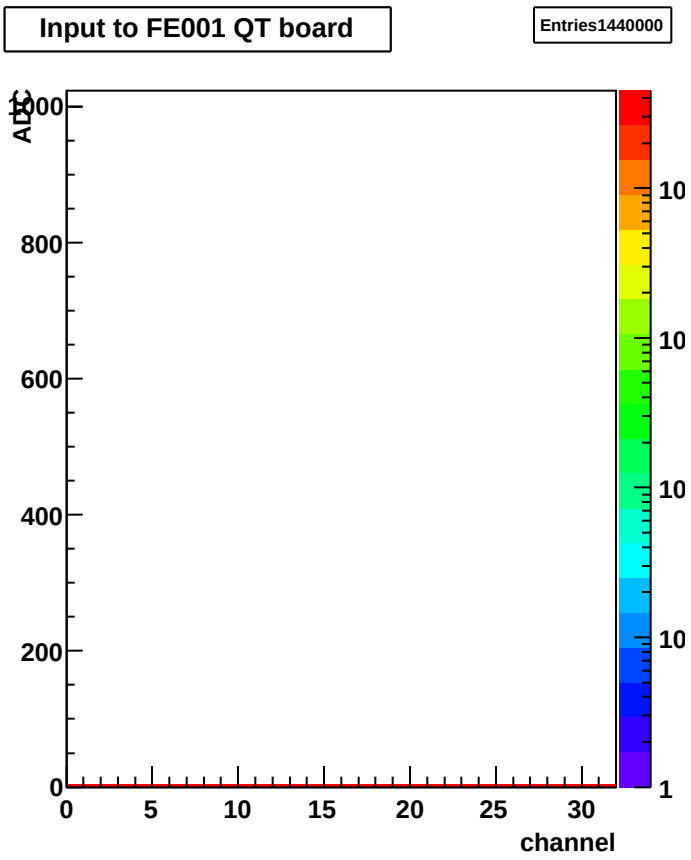

Input to FPD L2 DSM
Entries 135000
CL bits
45000
27
59
895
293
1
3
1
44618
44103
SMALL
LARGE-S
LARGE-N

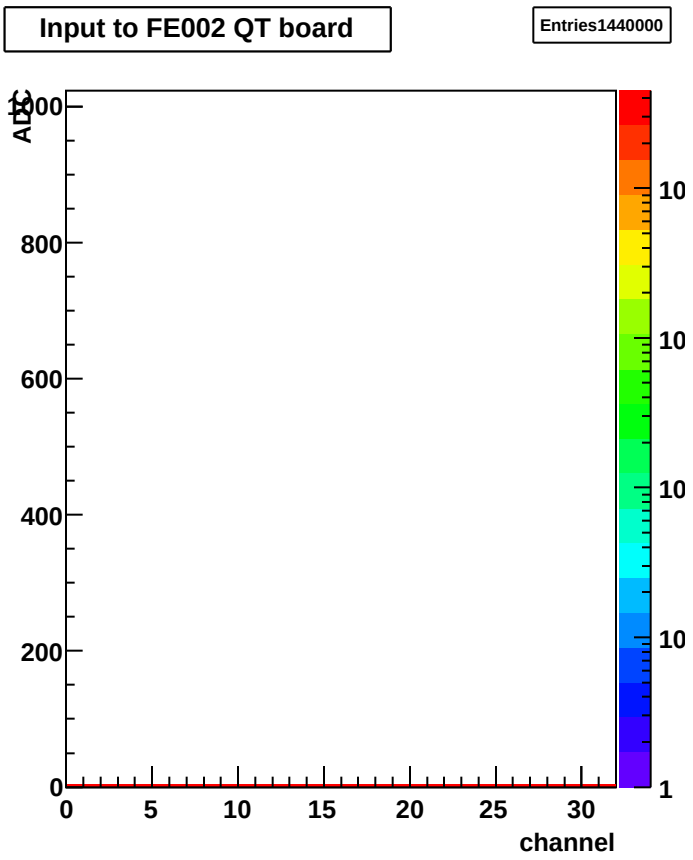

Input to FPD L2 DSM
Entries 3007
CL bits - simulated
23
4
1
293
1645
115
3
21
893
2
1
6
SMALL
LARGE-S
LARGE-N

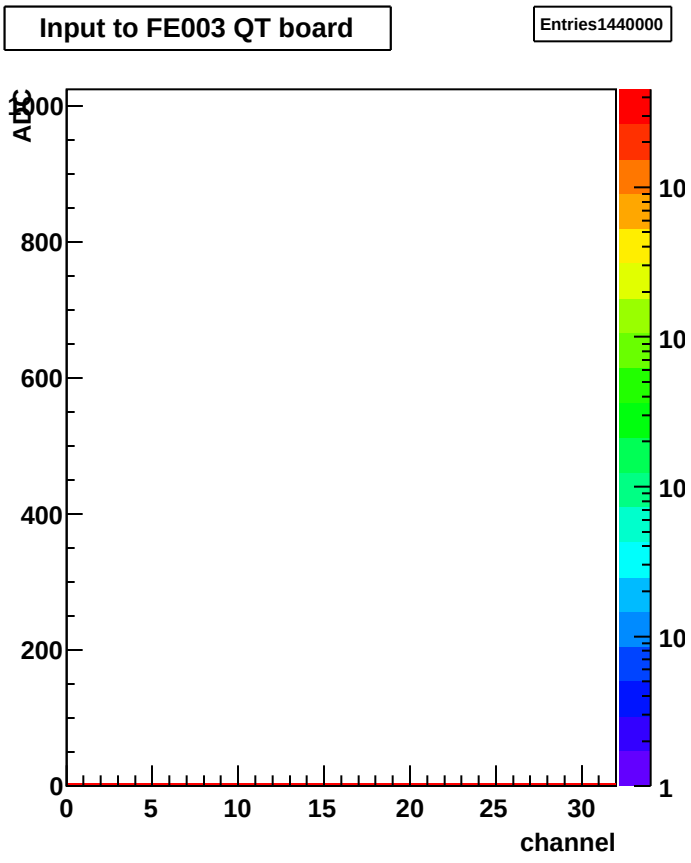

Input to FPD L2 DSM
Entries 135000
HT bits
49
3
1
30
22
96
205
44977
44855
44762
SMALL
LARGE-S
LARGE-N

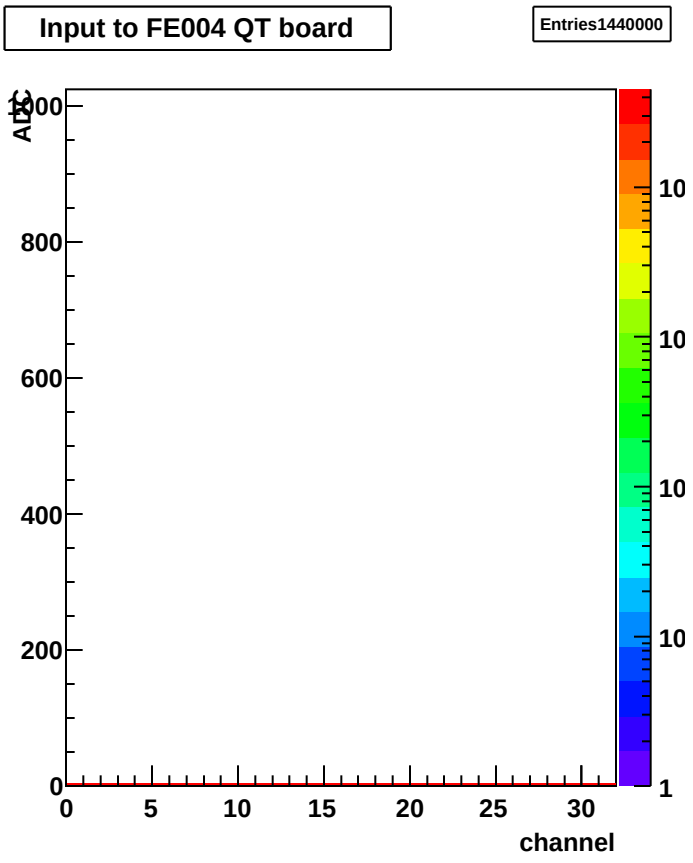

Input to FPD L2 DSM
Entries 407
HT bits - simulated
1
15
93
49
32
206
8
3
SMALL
LARGE-S
LARGE-N

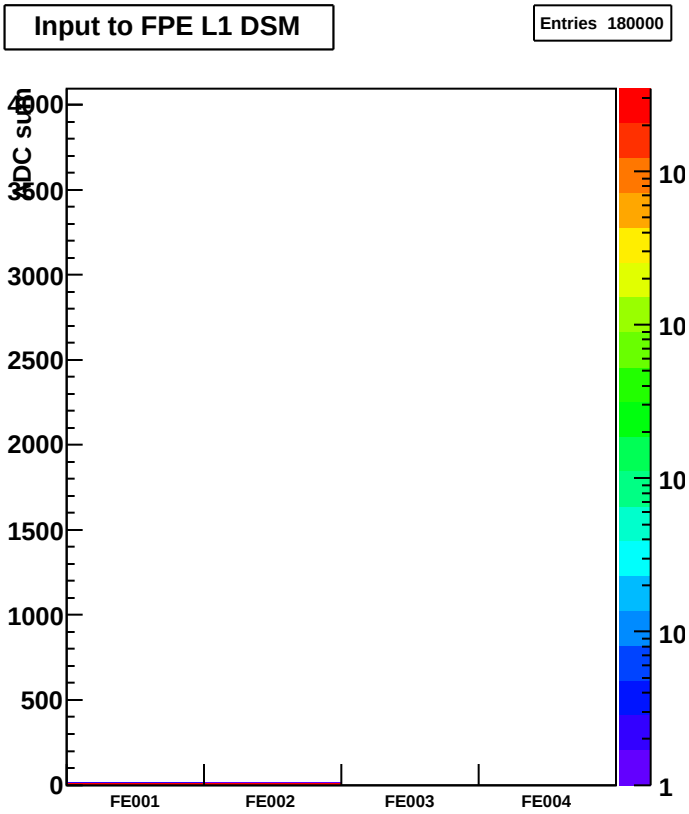

Input to FPD L2 DSM
Entries 180000
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

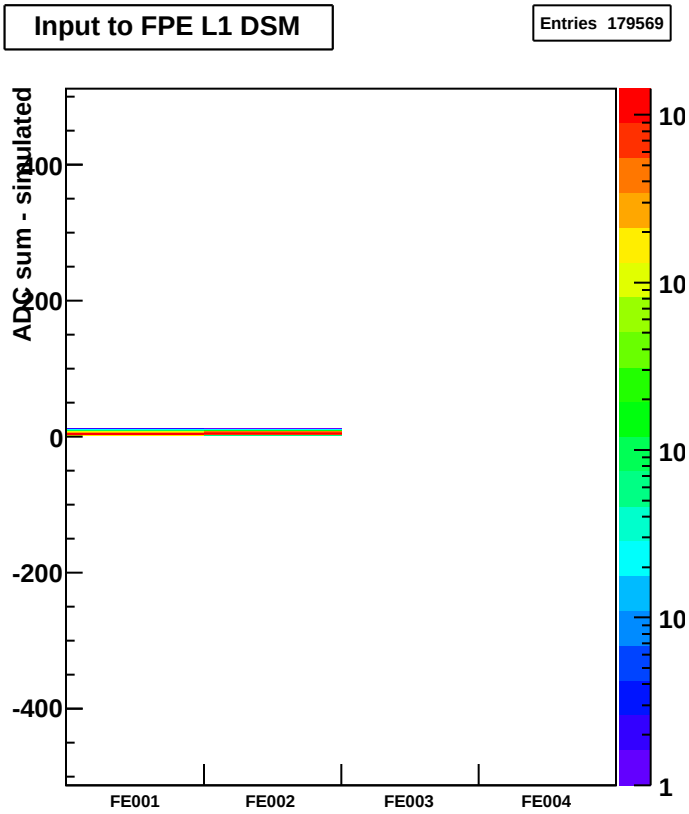

Input to FE001 QT board
Entries1440000
ADC
channel

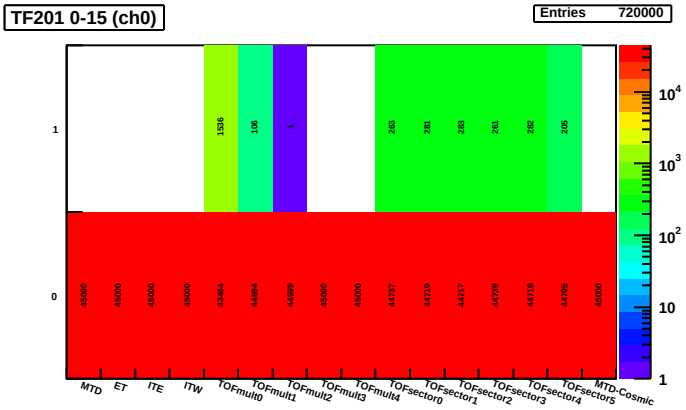

Input to FE002 QT board
Entries1440000
ADC
channel

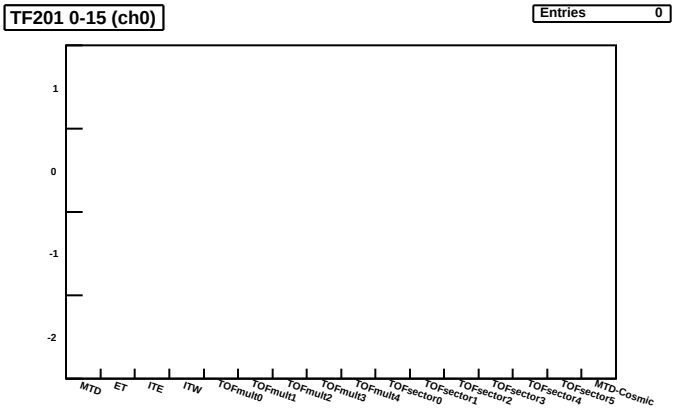

Input to FE003 QT board
Entries1440000
ADC
channel

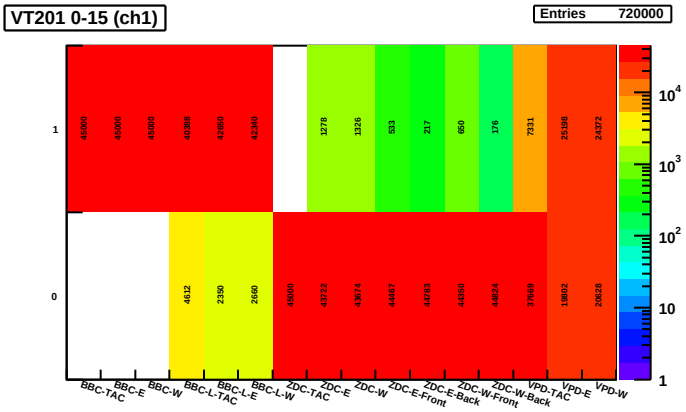

Input to FE004 QT board
Entries1440000
ADC
channel

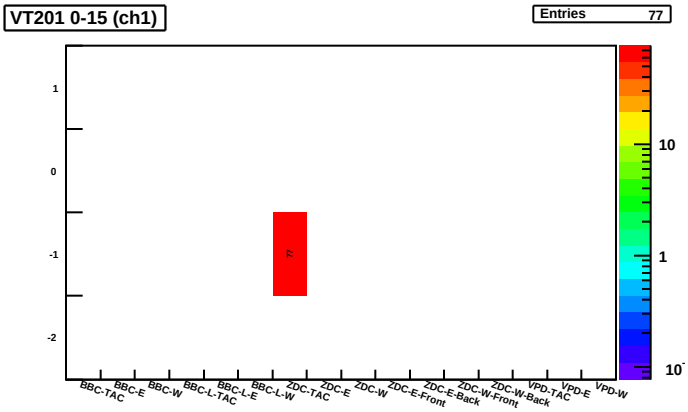

Input to FPE L1 DSM
Entries 180000
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

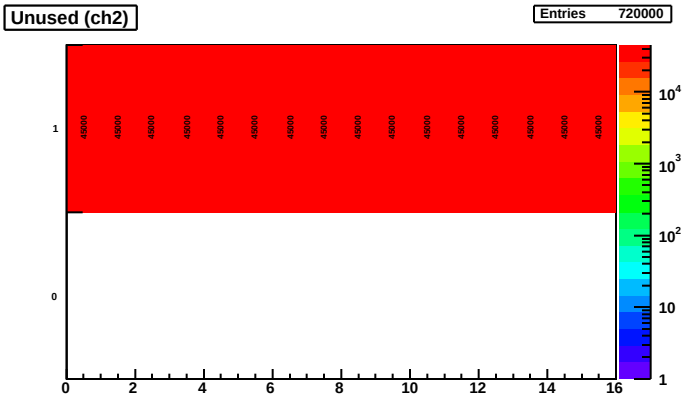

Input to FPE L1 DSM
Entries 179569
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1

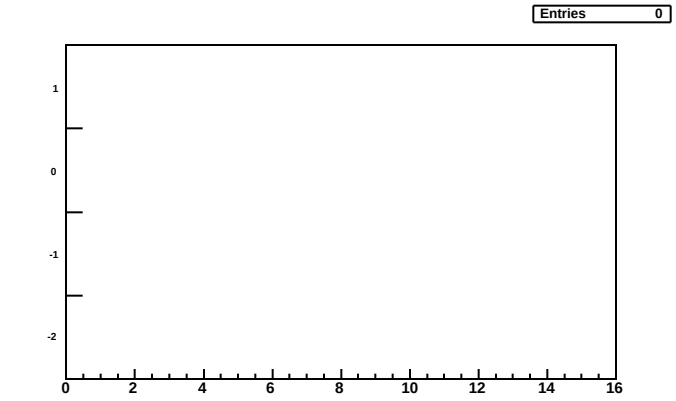

TF201 0-15 (ch0)
Entries 720000
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

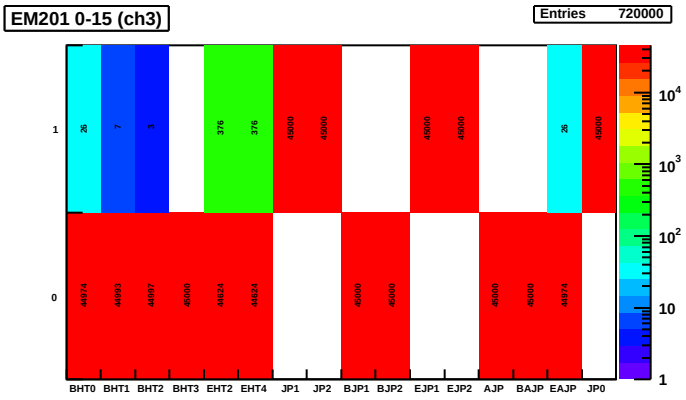

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

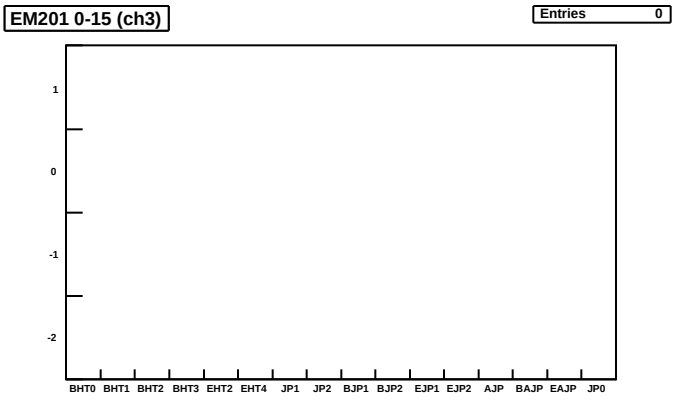

VT201 0-15 (ch1)
Entries 720000
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 77
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

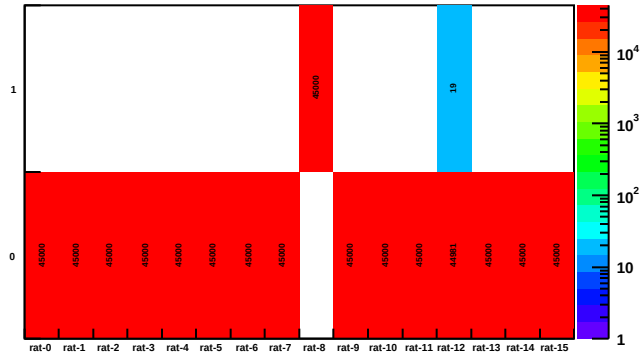

Unused (ch2)
Entries 720000

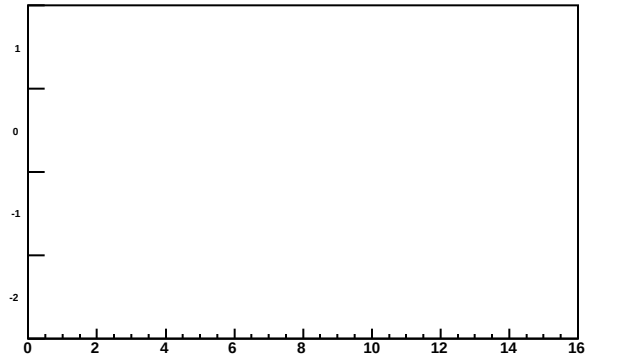

Entries 0


EM201 0-15 (ch3)
Entries 720000
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

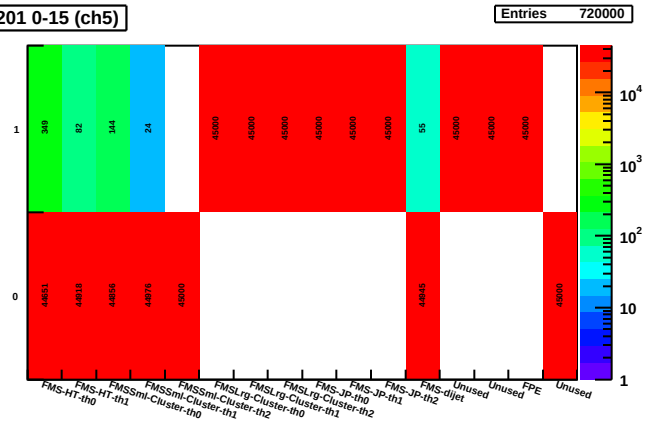

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

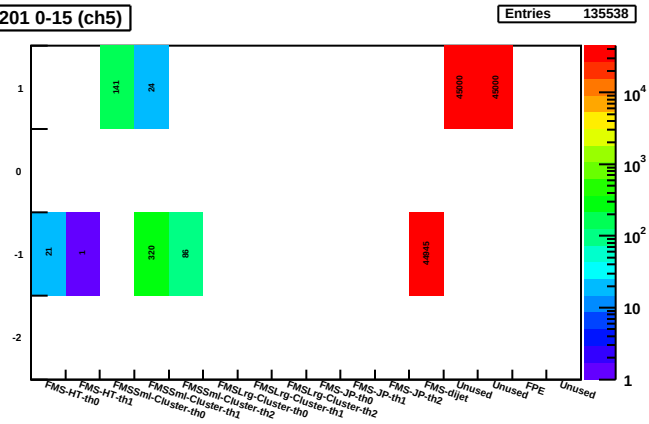




FP201 0-15 (ch5)



FP201 0-15 (ch5)



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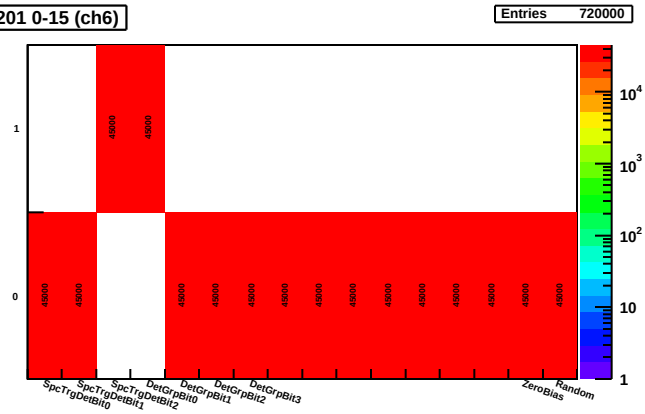

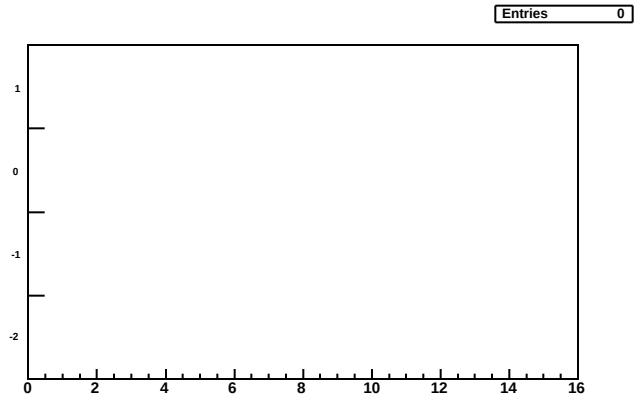
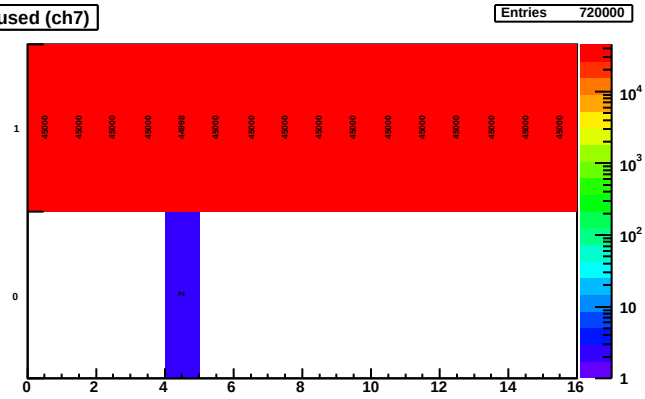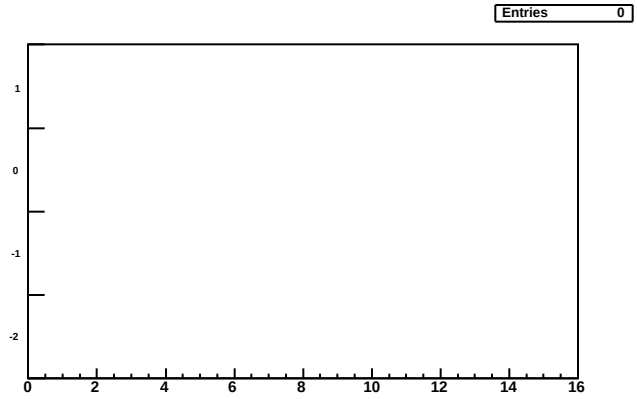
Unused (ch7)