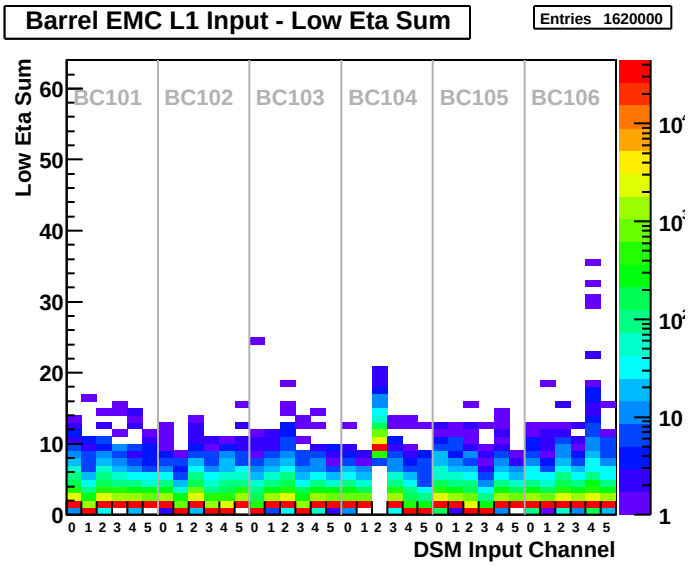
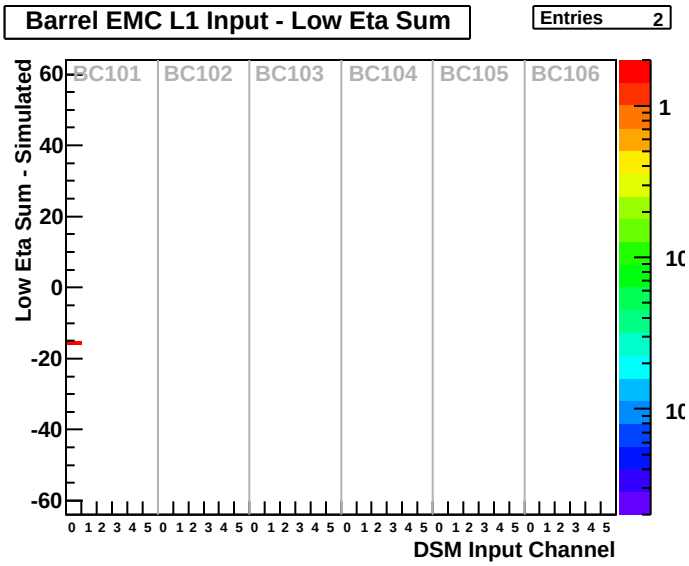
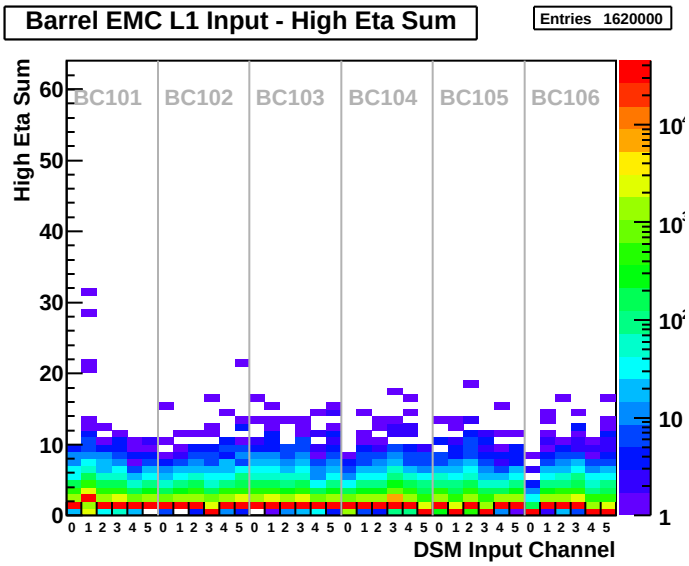
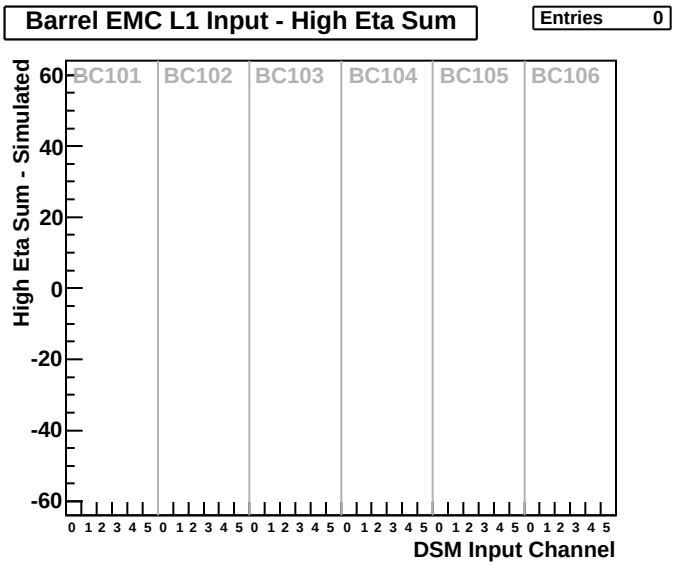
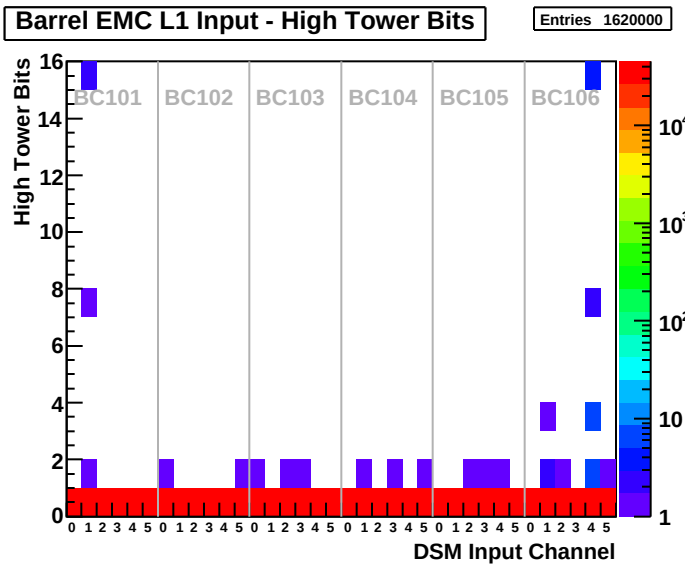
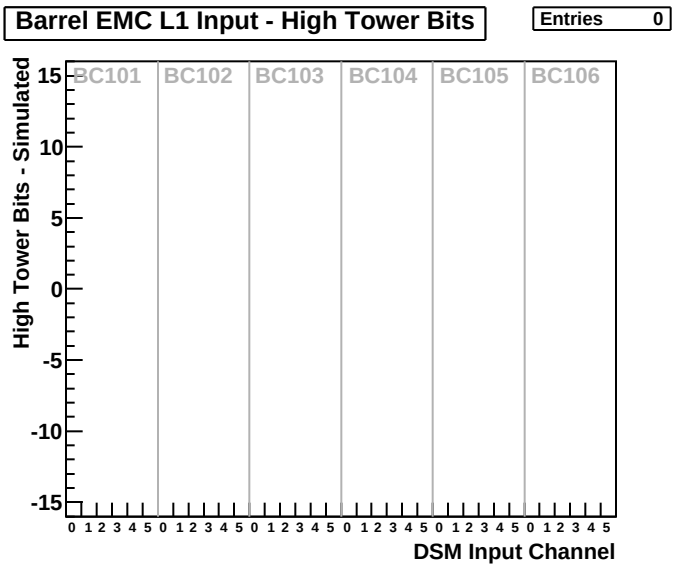

Barrel EMC L1 Input - Low Eta Sum
Entries 1620000
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

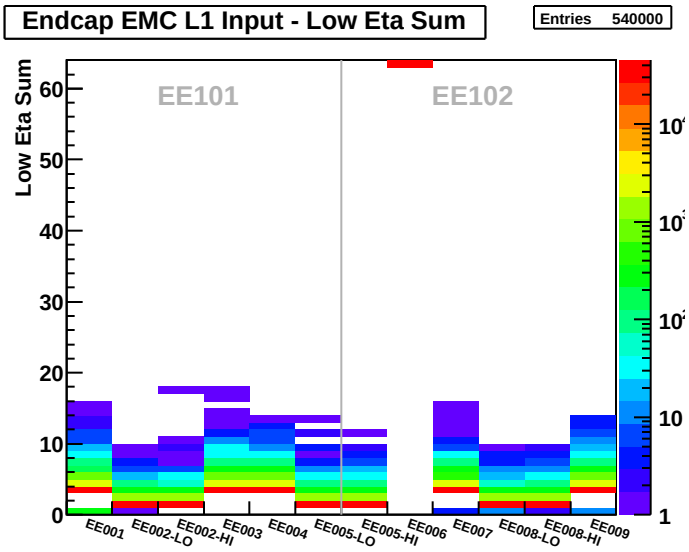

Barrel EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

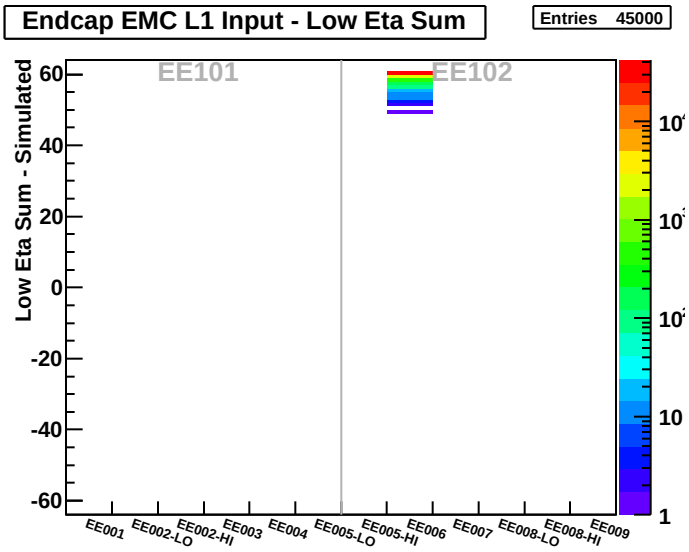

Barrel EMC L1 Input - High Eta Sum
Entries 1620000
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

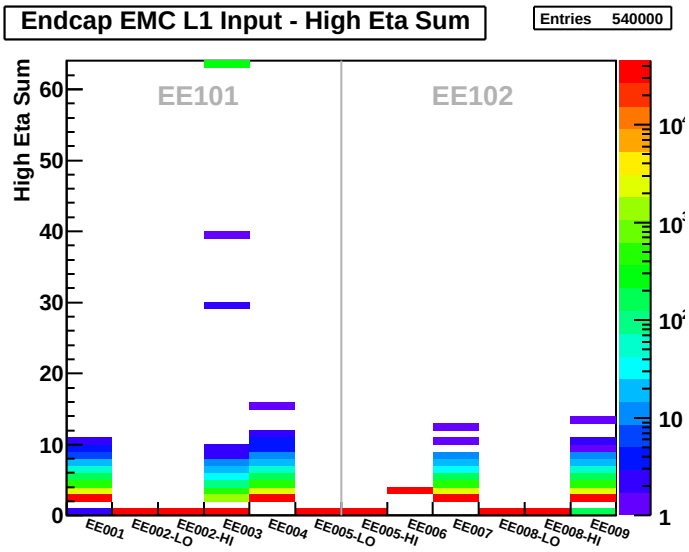

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

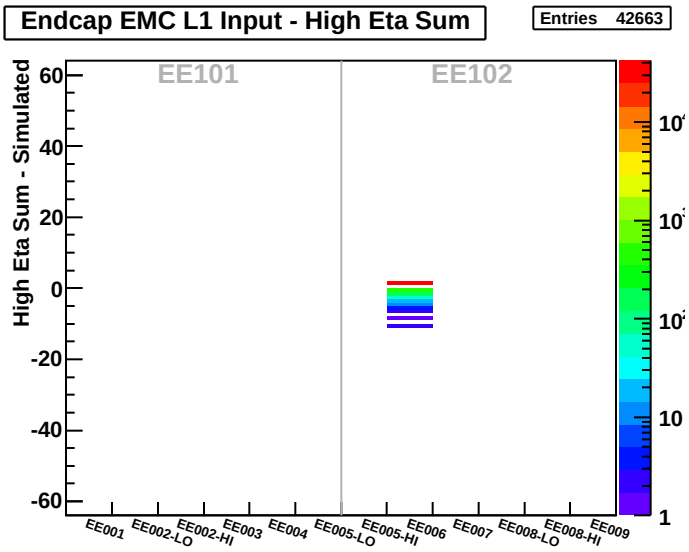

Barrel EMC L1 Input - High Tower Bits
Entries 1620000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

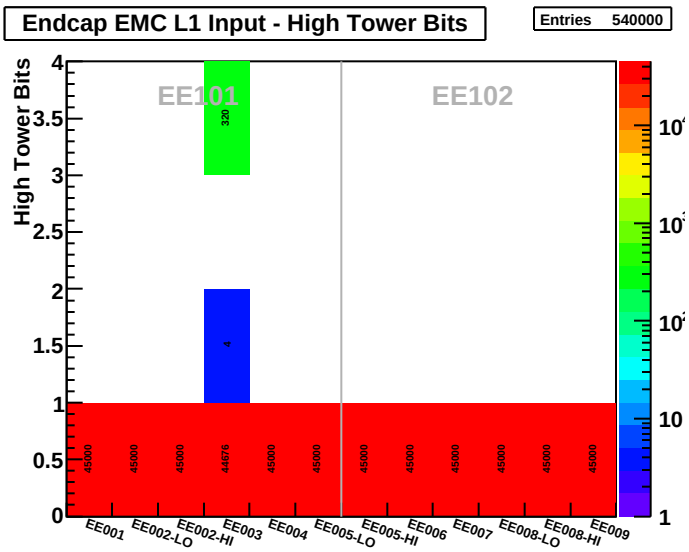

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

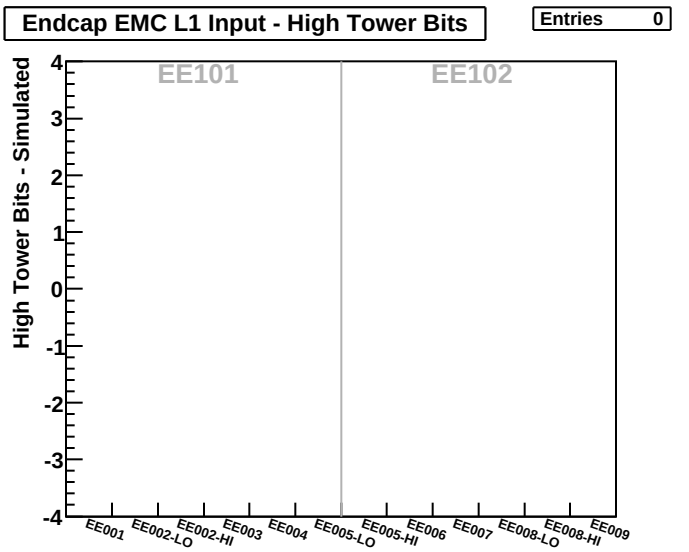

Endcap EMC L1 Input - Low Eta Sum
Entries 540000
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

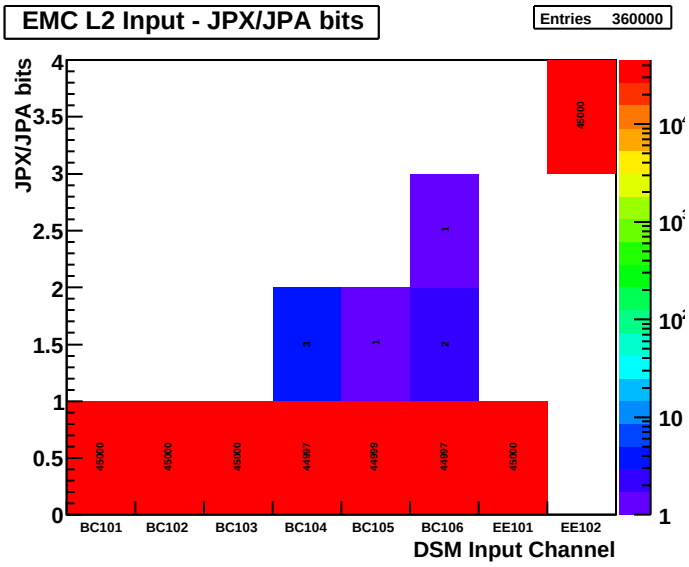

Endcap EMC L1 Input - Low Eta Sum
Entries 45000
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

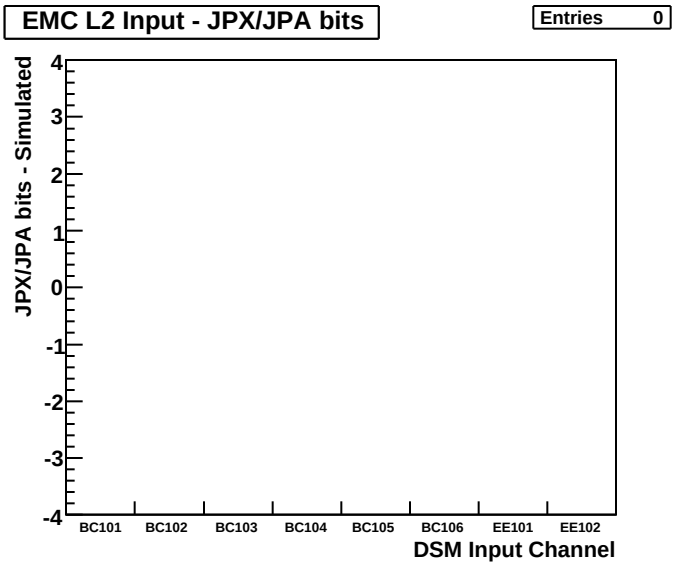

Endcap EMC L1 Input - High Eta Sum
Entries 540000
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

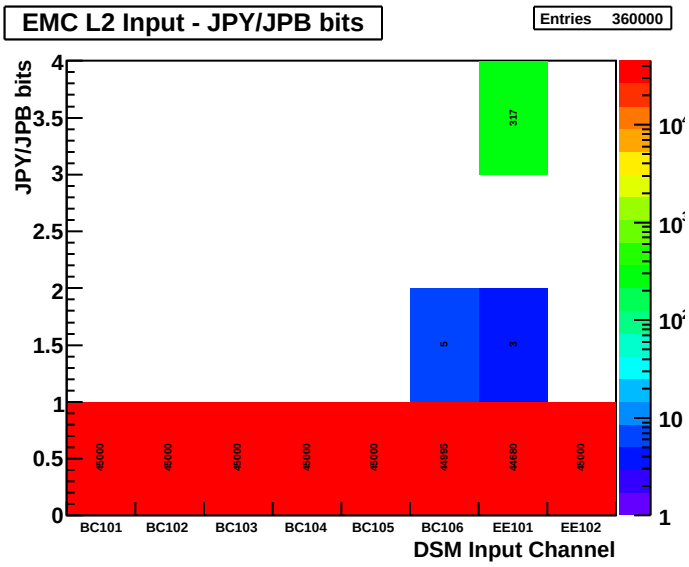

Endcap EMC L1 Input - High Eta Sum
Entries 42663
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

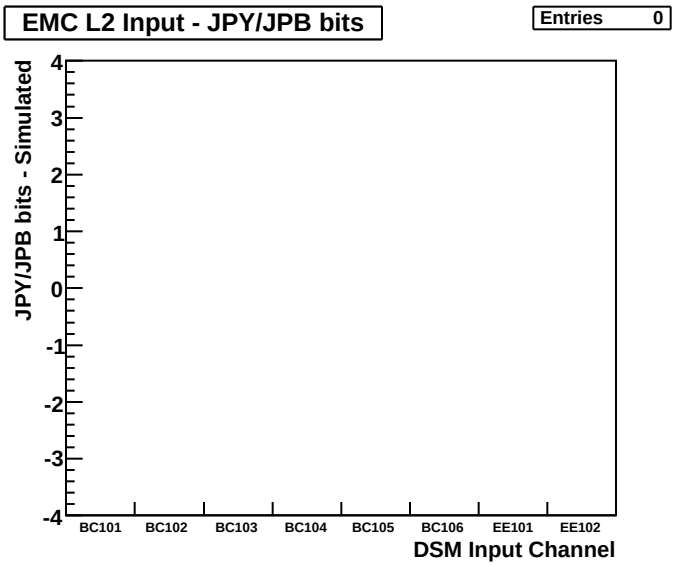

Endcap EMC L1 Input - High Tower Bits
Entries 540000
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

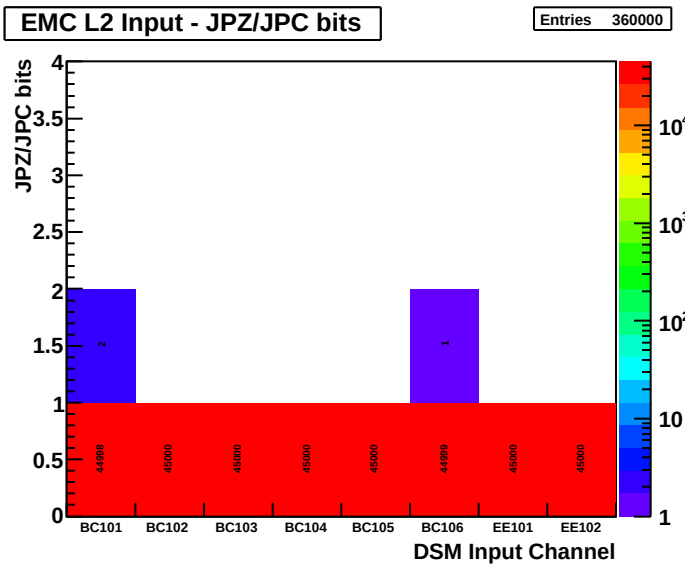

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

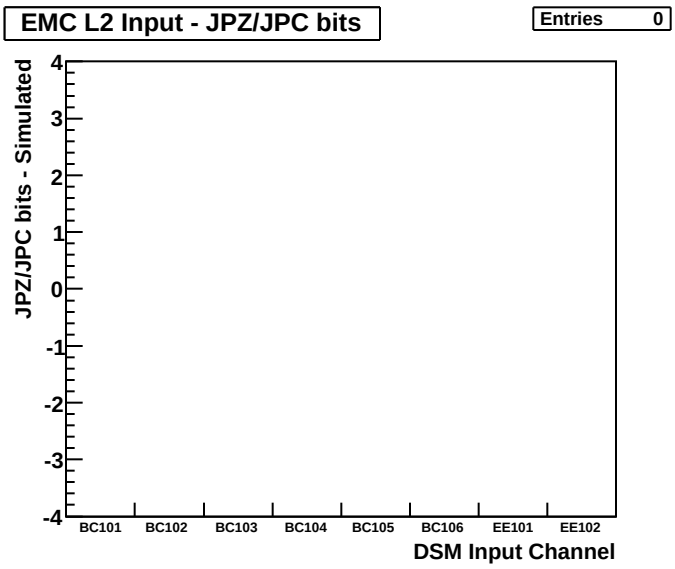

EMC L2 Input - JPX/JPA bits
Entries 360000
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

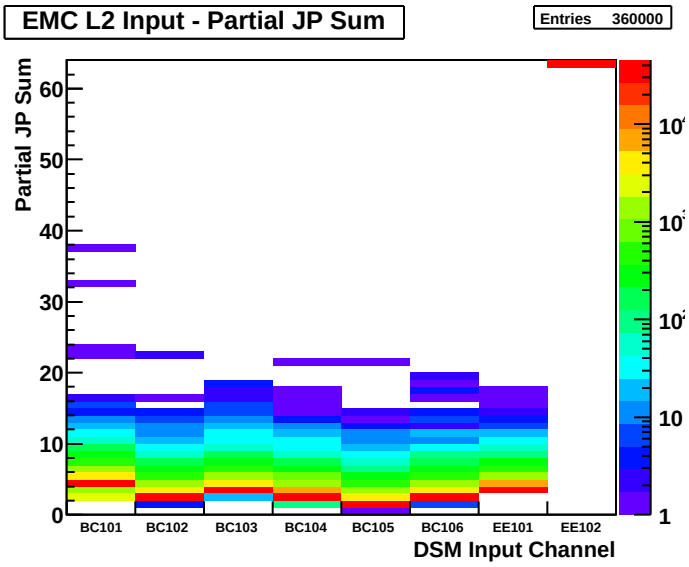

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

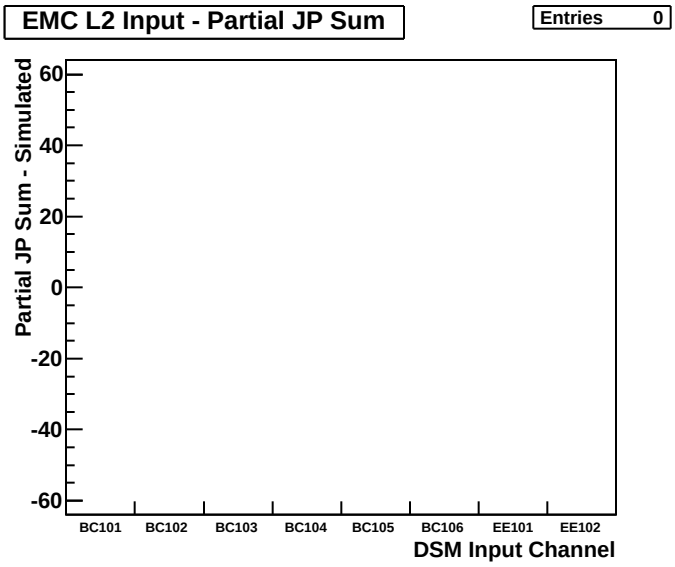

EMC L2 Input - JPY/JPB bits
Entries 360000
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

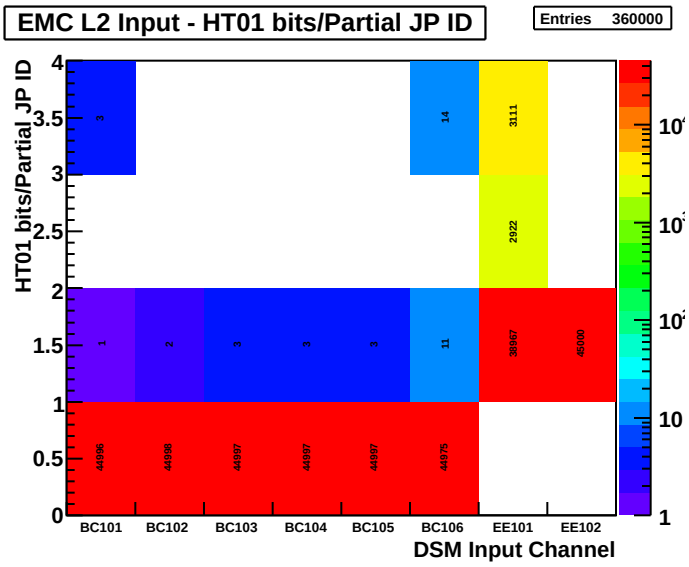

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

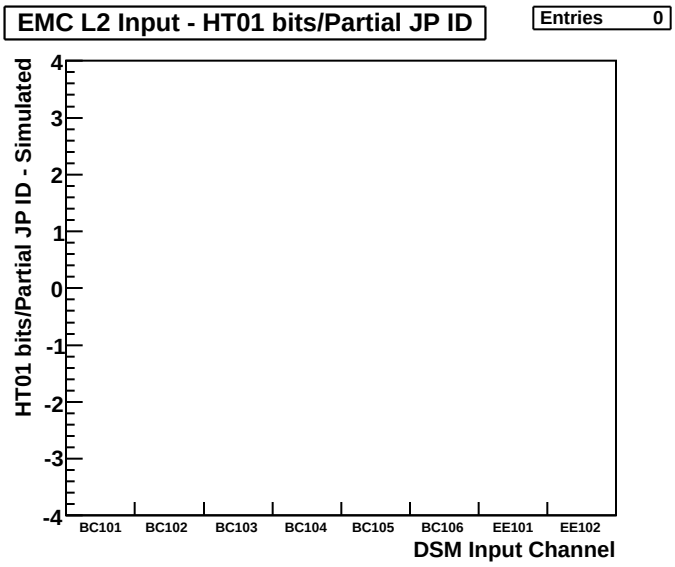

EMC L2 Input - JPZ/JPC bits
Entries 360000
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

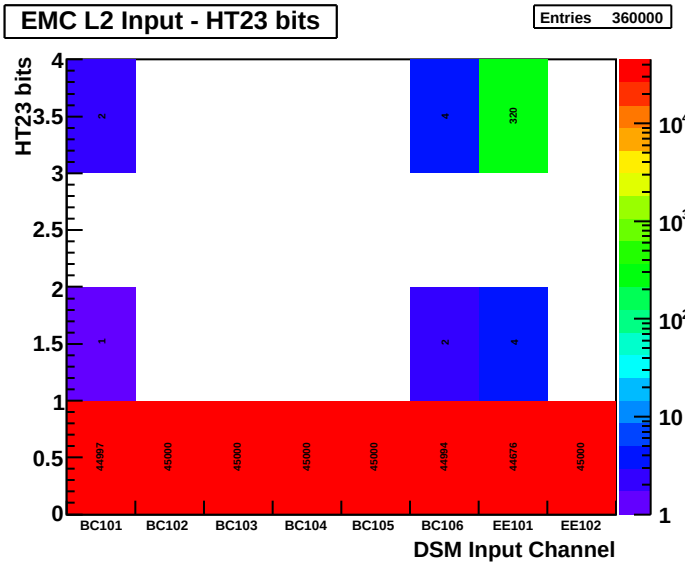

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

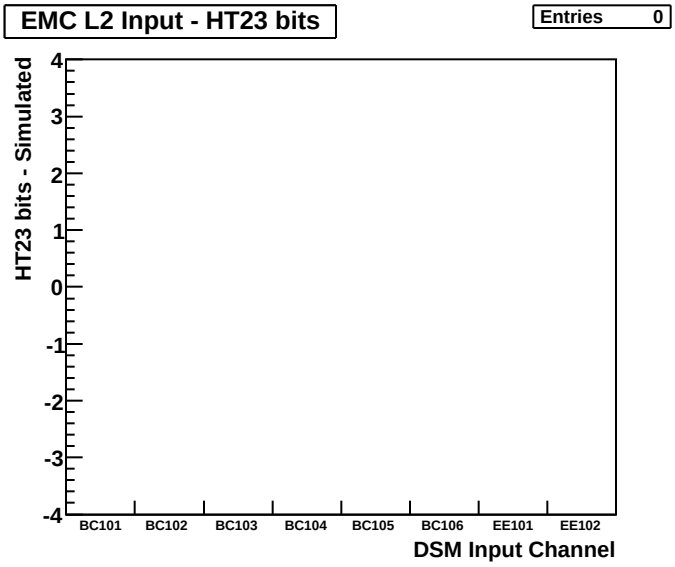

EMC L2 Input - Partial JP Sum
Entries 360000
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

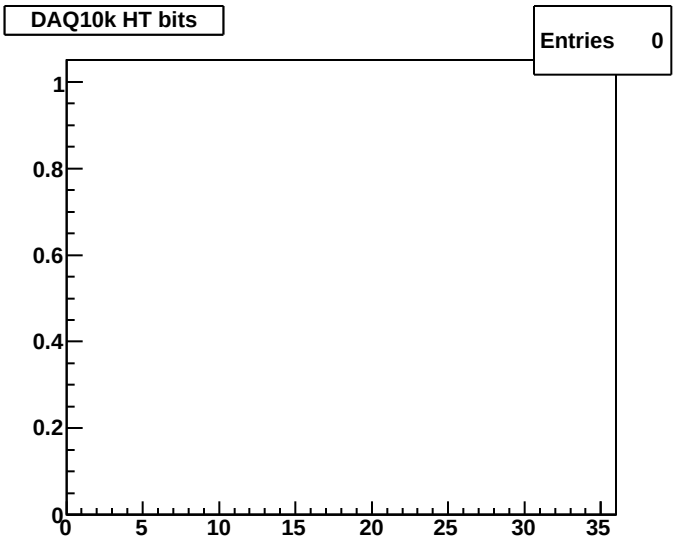

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

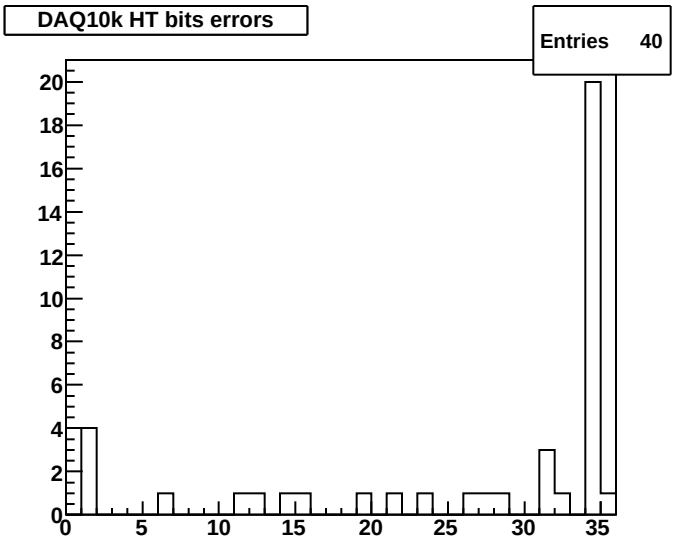

EMC L2 Input - HT01 bits/Partial JP ID
Entries 360000
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

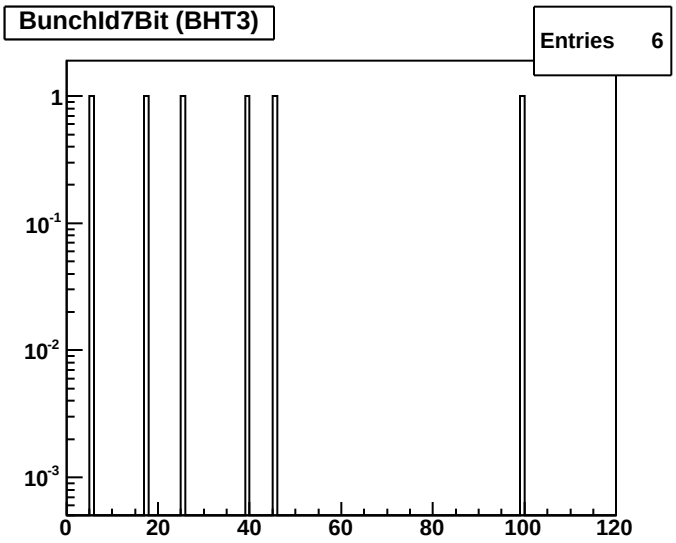

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

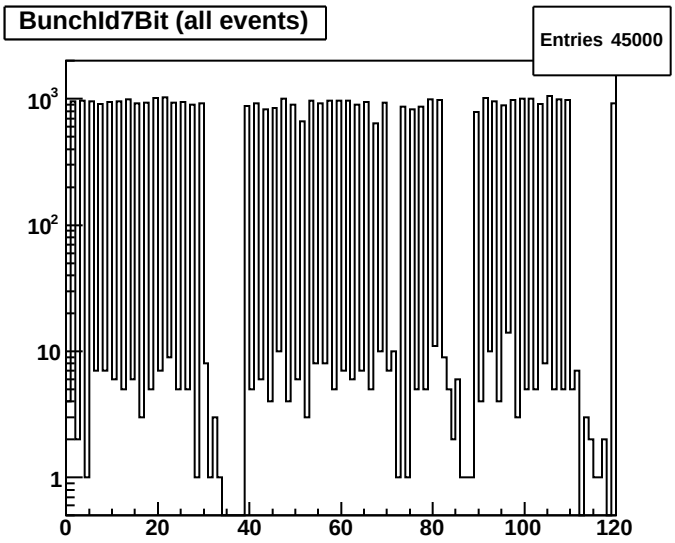

EMC L2 Input - HT23 bits
Entries 360000
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

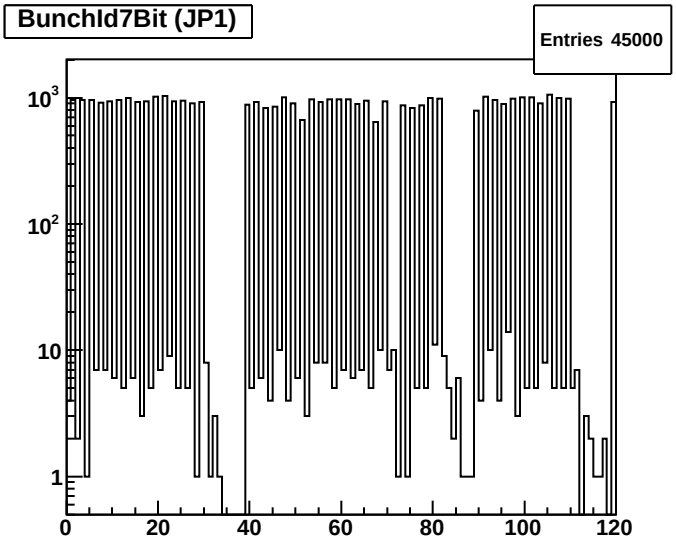

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

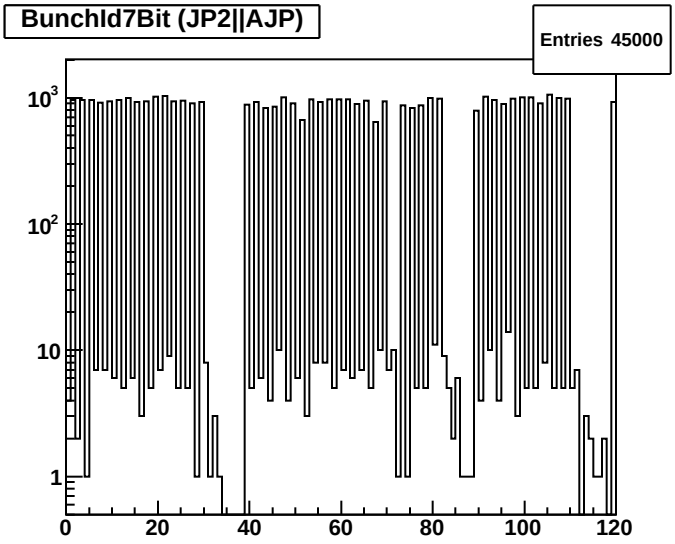

DAQ10k HT bits
Entries 0

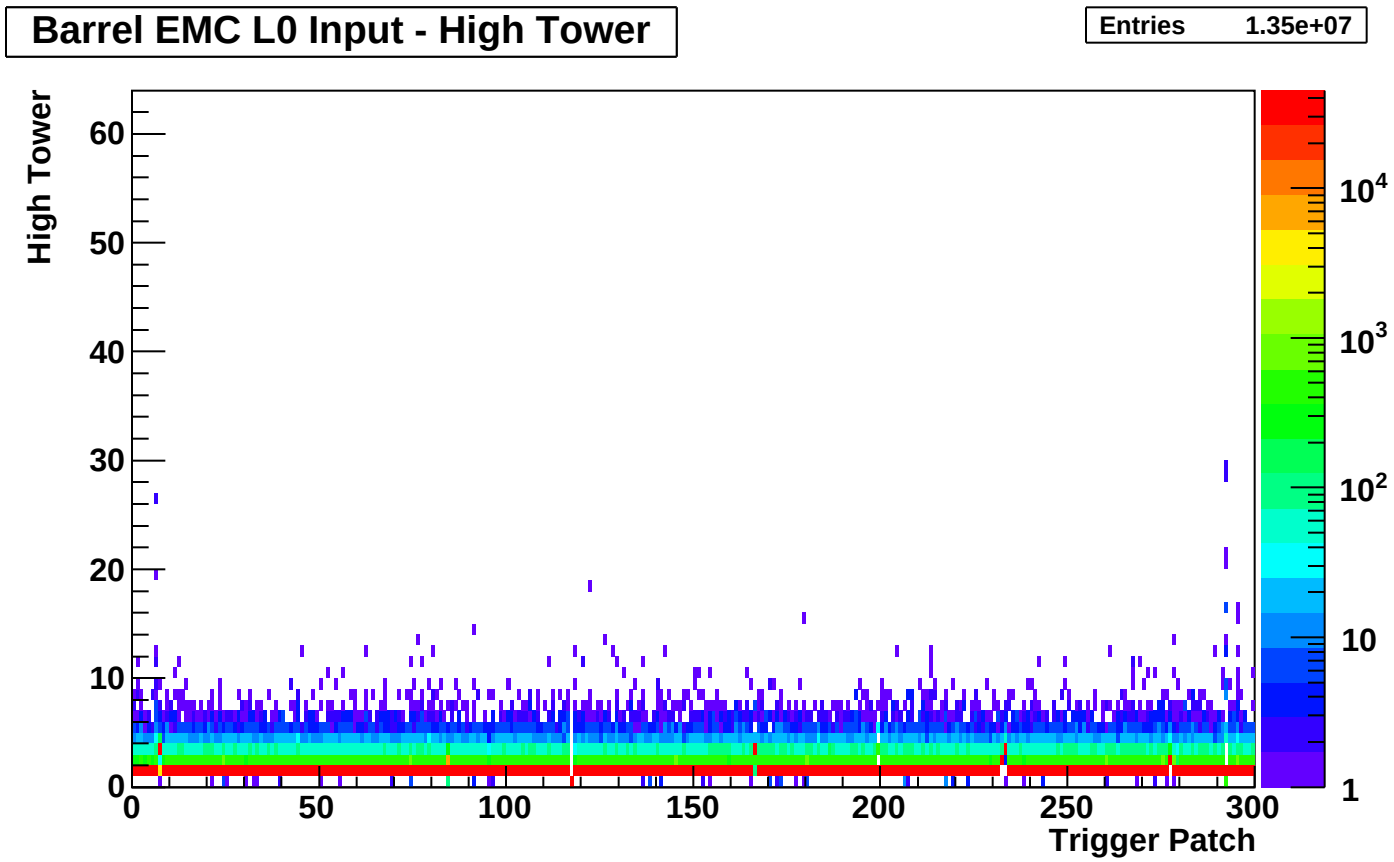

DAQ10k HT bits errors
Entries 40

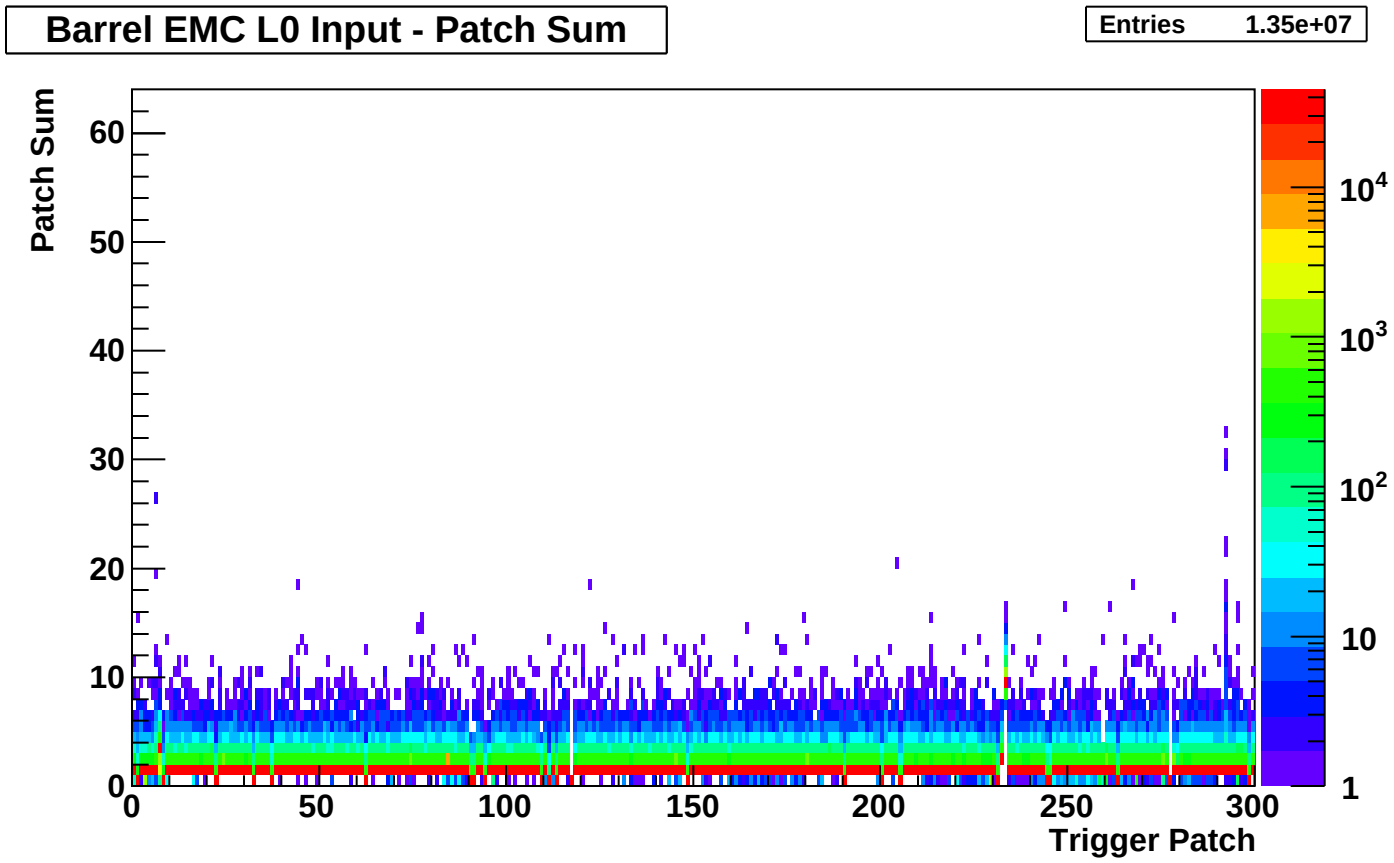

BunchId7Bit (BHT3)
Entries 6

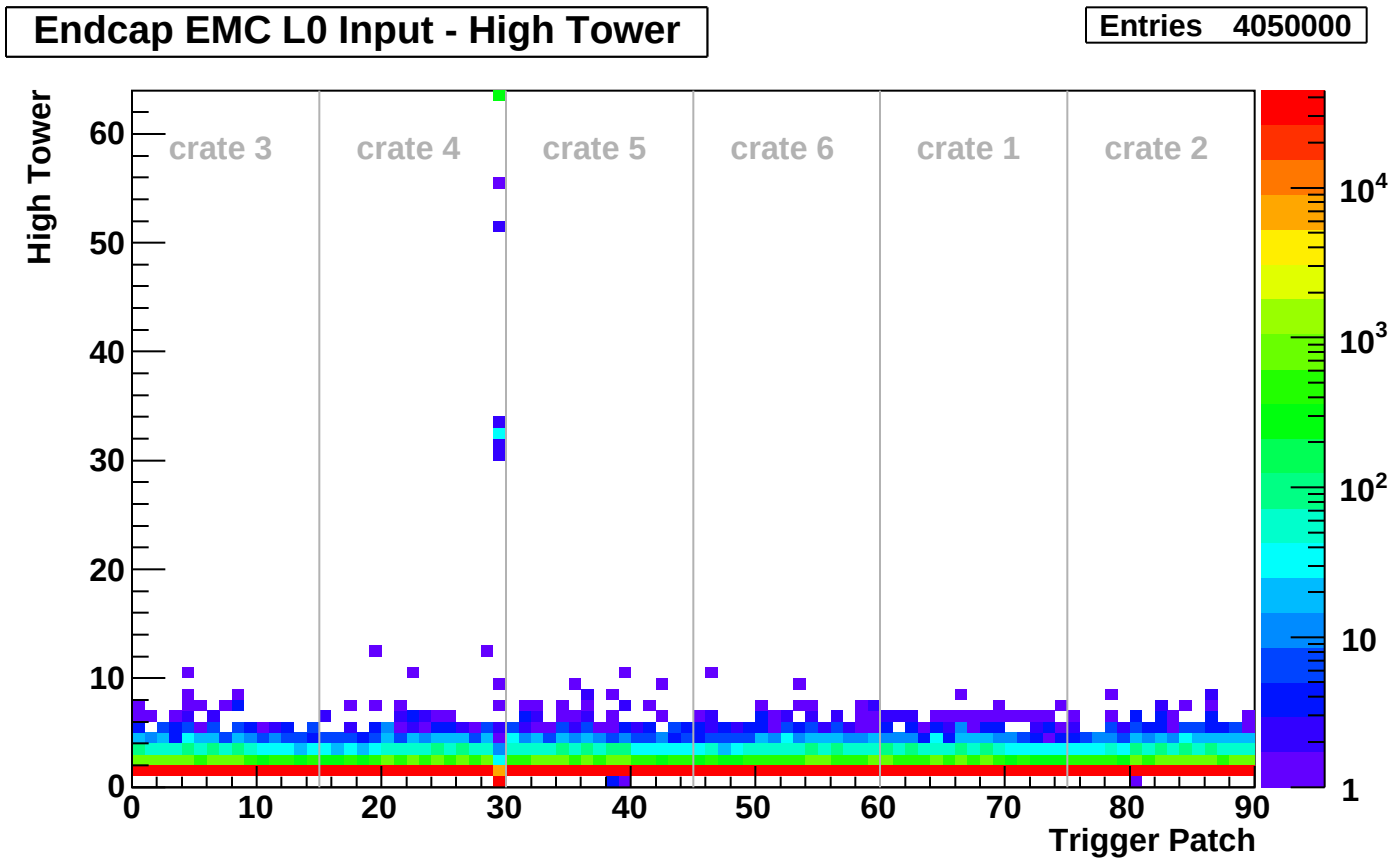

BunchId7Bit (all events)
Entries 45000

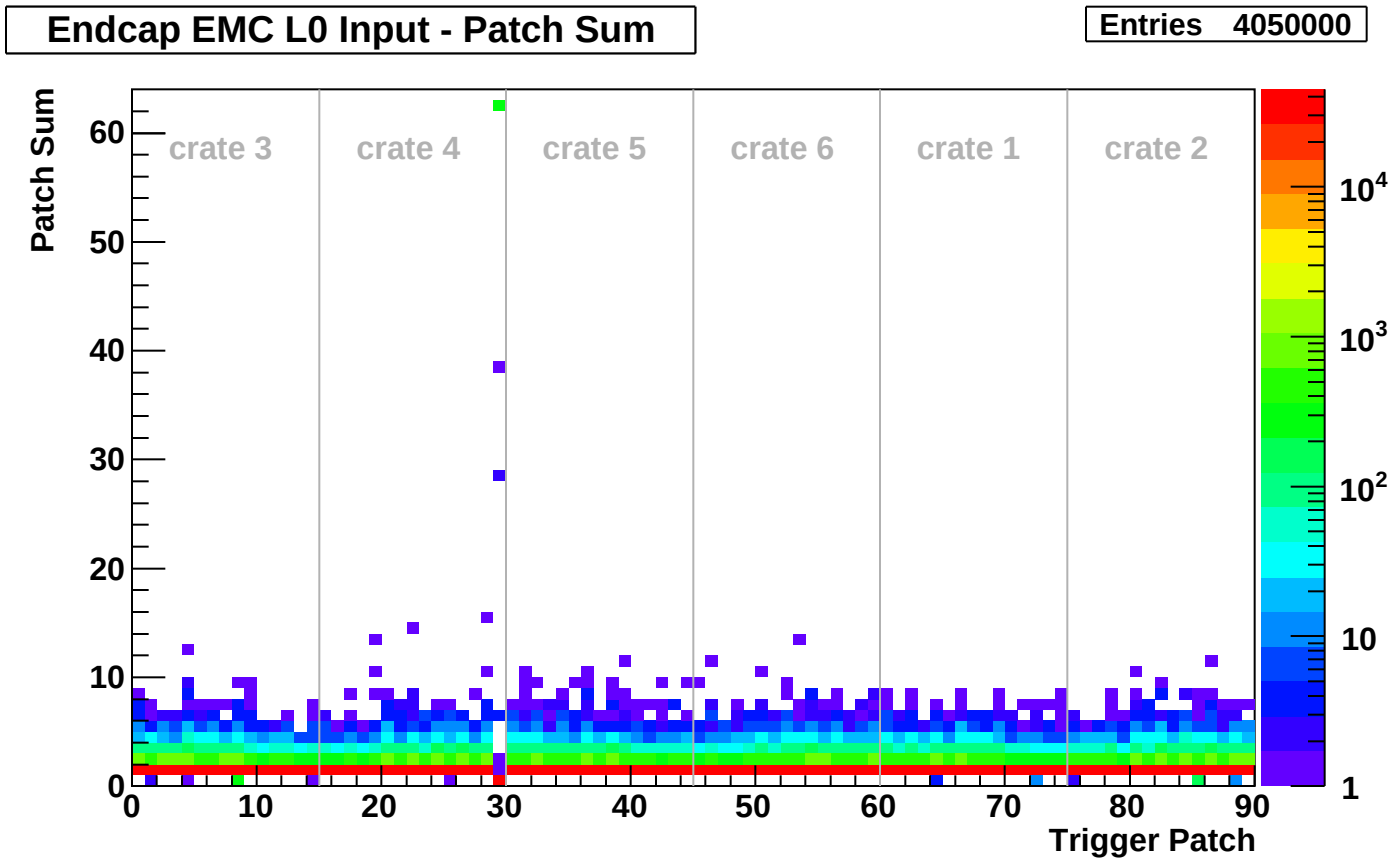

BunchId7Bit (JP1)
Entries 45000

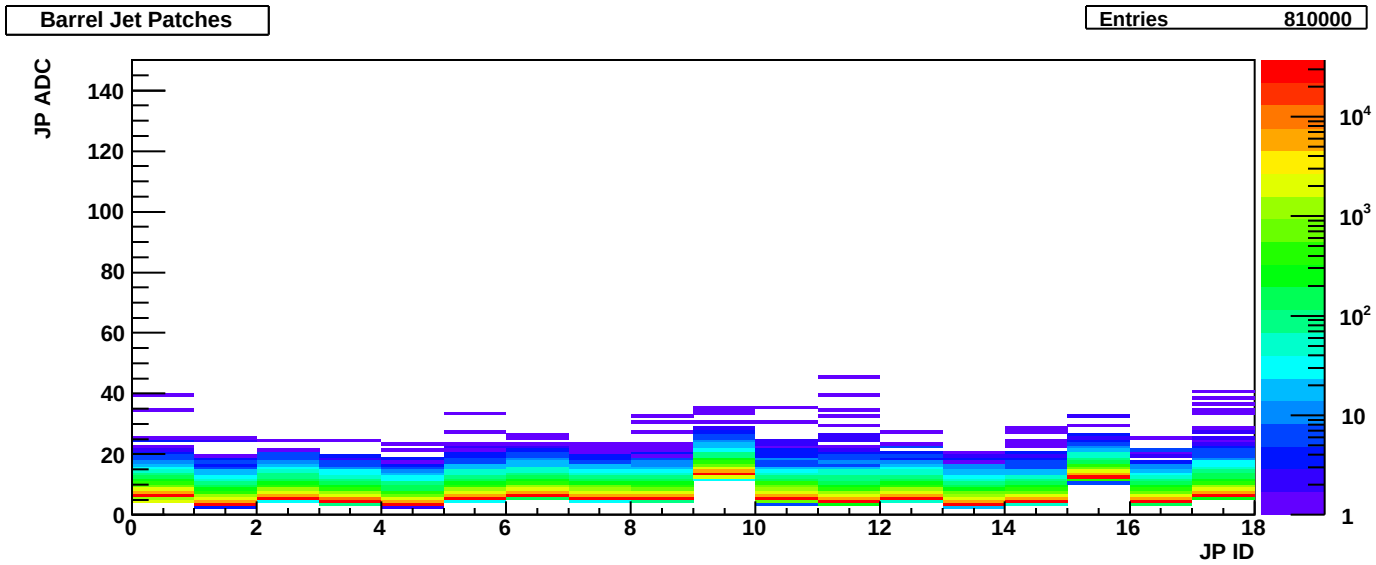

BunchId7Bit (JP2||AJP)
Entries 45000

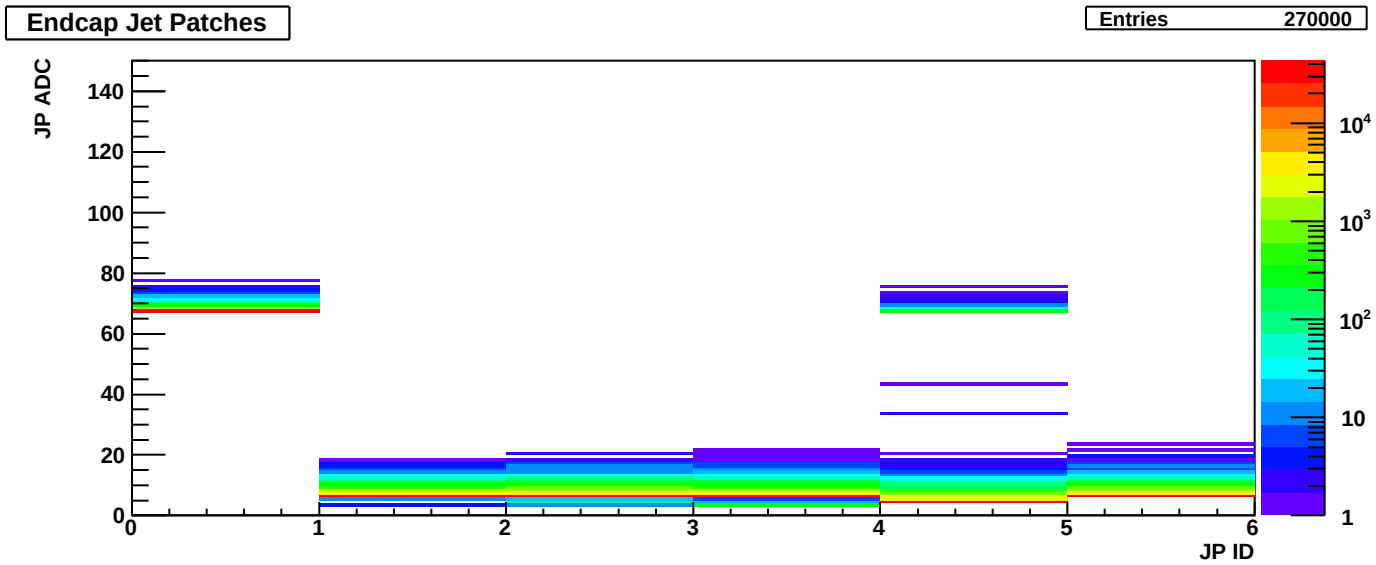

Barrel EMC L0 Input - High Tower
Entries 1.35e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

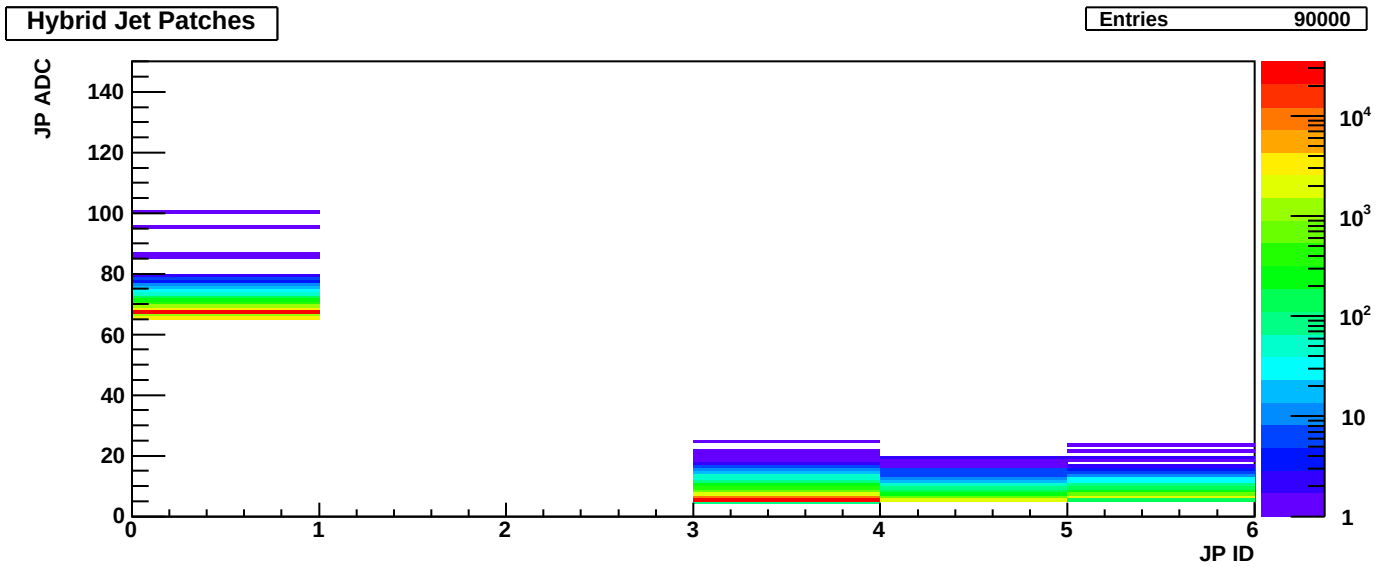

Barrel EMC L0 Input - Patch Sum
Entries 1.35e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

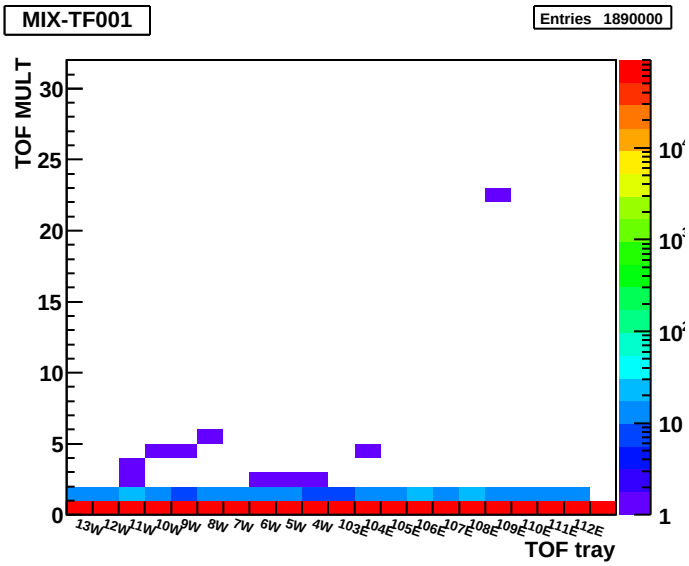

Endcap EMC L0 Input - High Tower
Entries 4050000
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

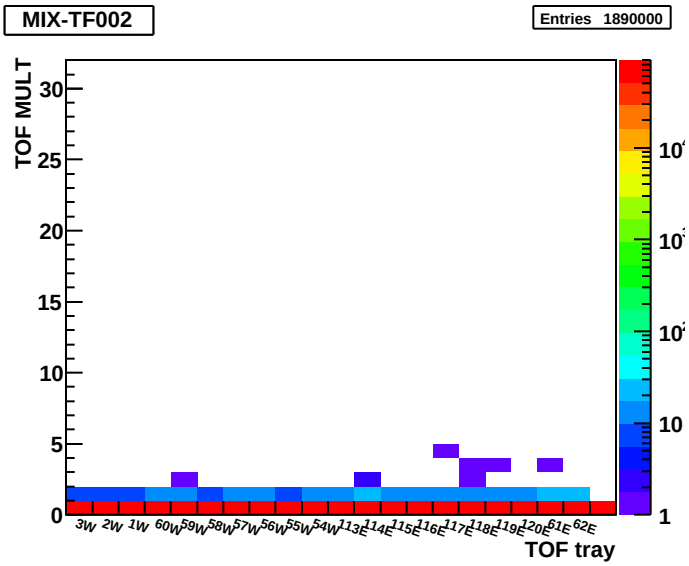

Endcap EMC L0 Input - Patch Sum
Entries 4050000
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

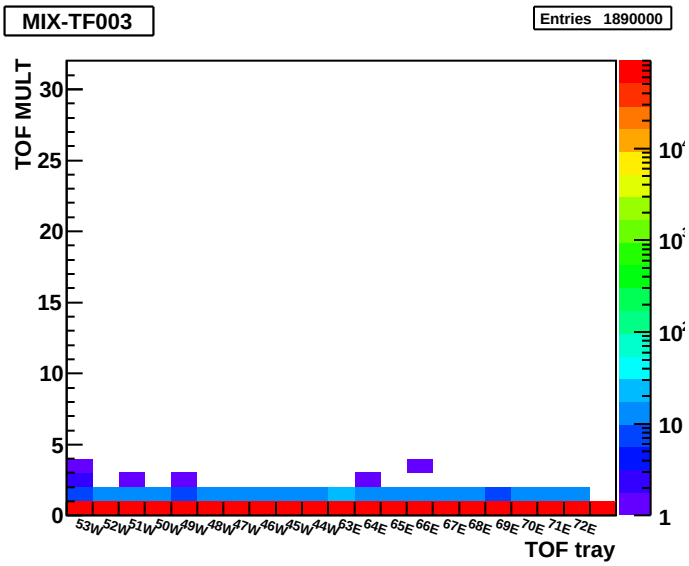

Barrel Jet Patches
Entries 810000
JP ADC
JP ID

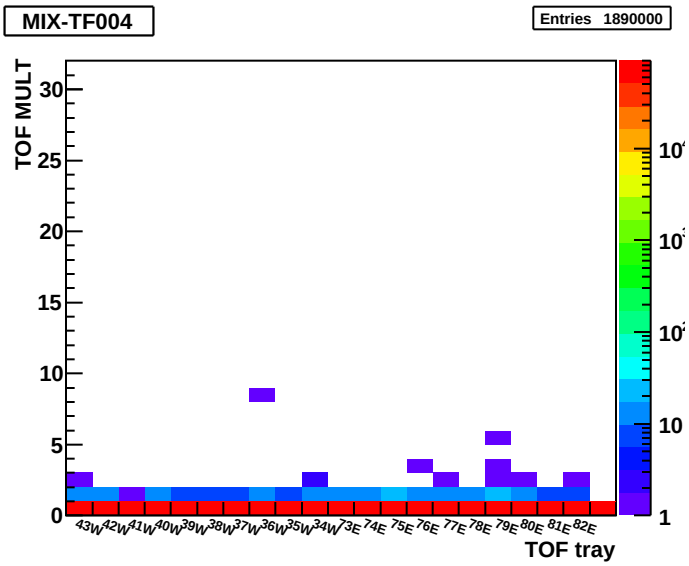

Endcap Jet Patches
Entries 270000
JP ADC
JP ID

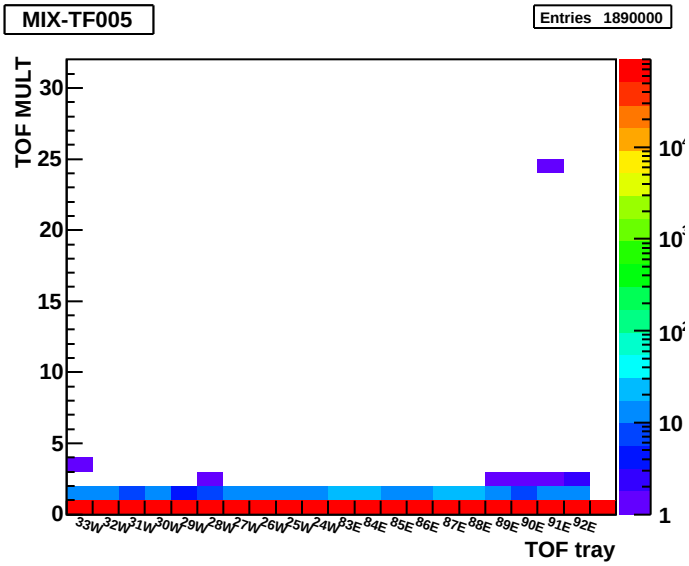

Hybrid Jet Patches
Entries 90000
JP ADC
JP ID

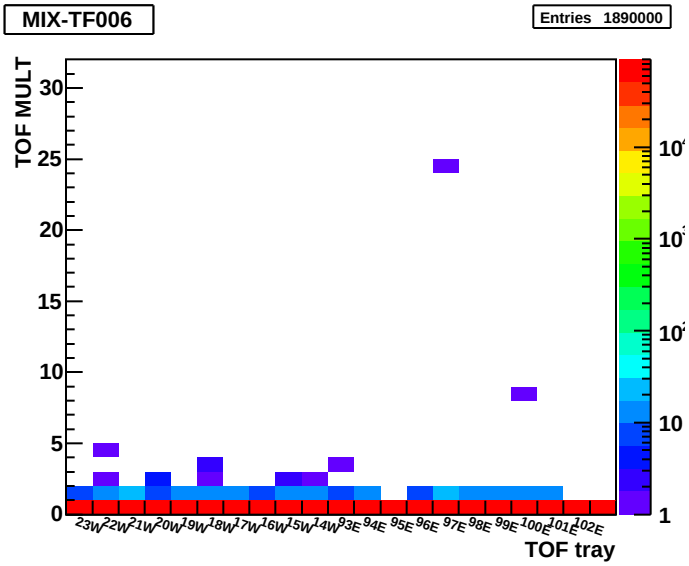

MIX-TF001
Entries 1890000
TOF MULT
TOF tray

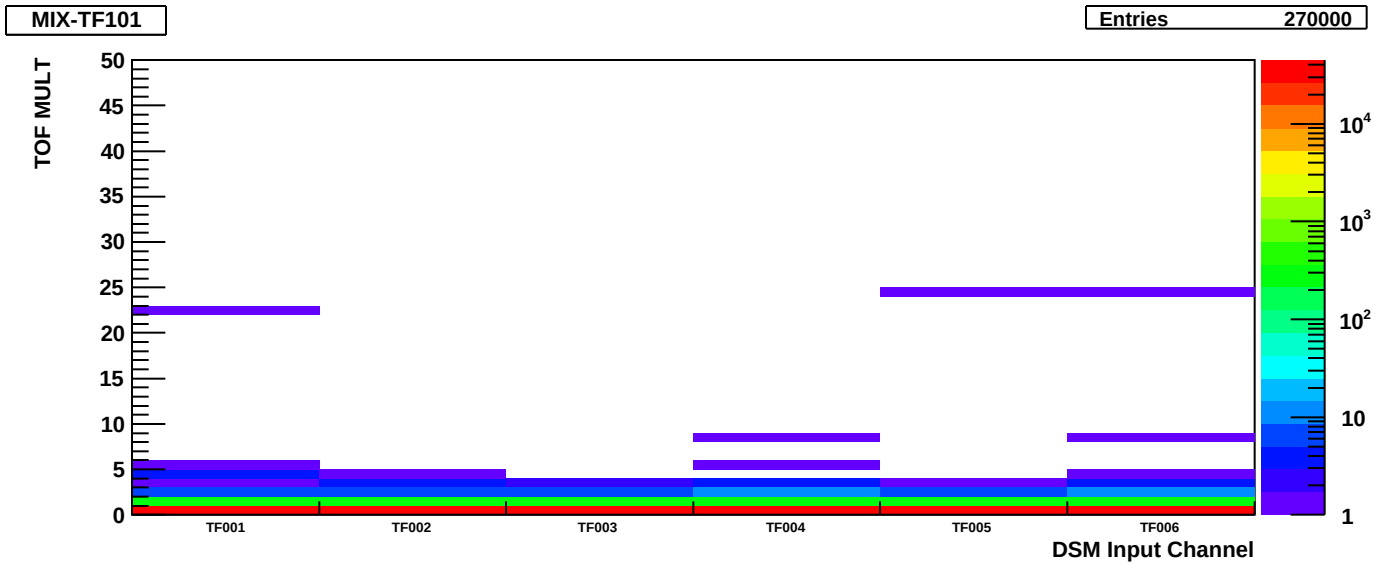

MIX-TF002
Entries 1890000
TOF MULT
TOF tray

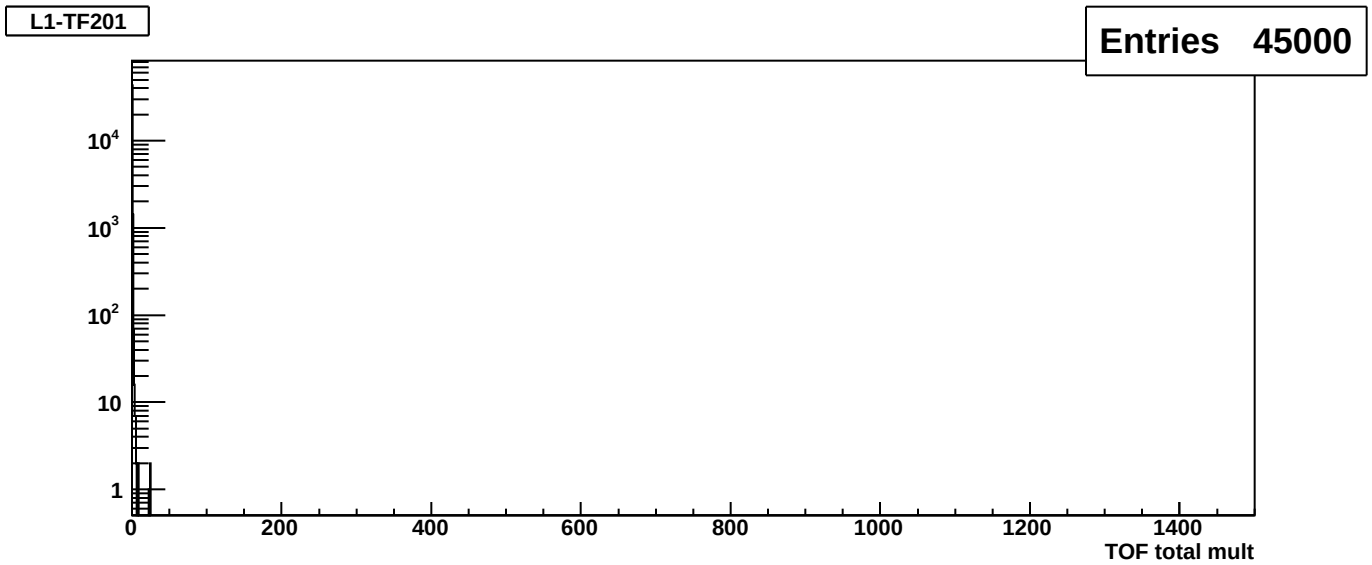

MIX-TF003
Entries 1890000
TOF MULT
TOF tray

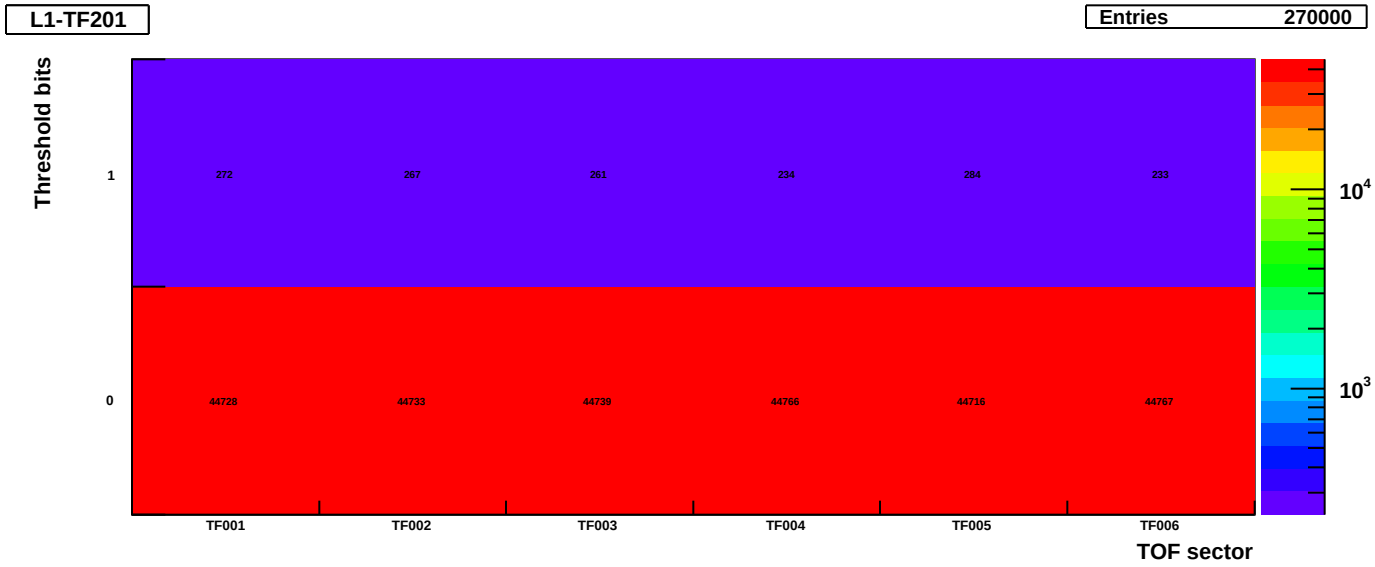

MIX-TF004
Entries 1890000
TOF MULT
TOF tray

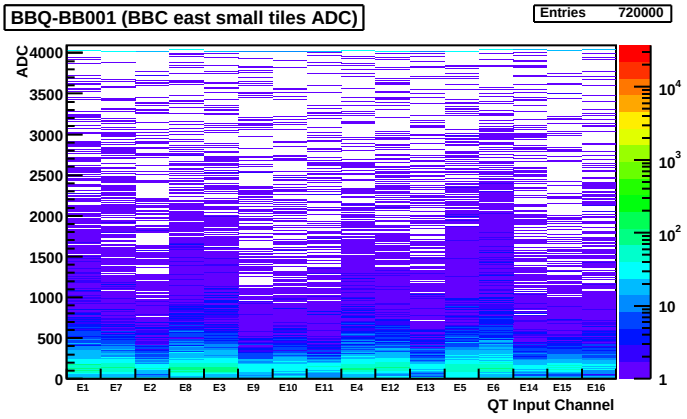

MIX-TF005
Entries 1890000
TOF MULT
TOF tray

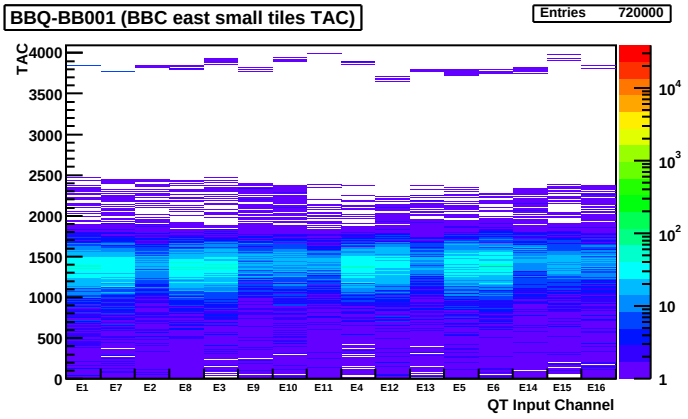

MIX-TF006
Entries 1890000
TOF MULT
TOF tray

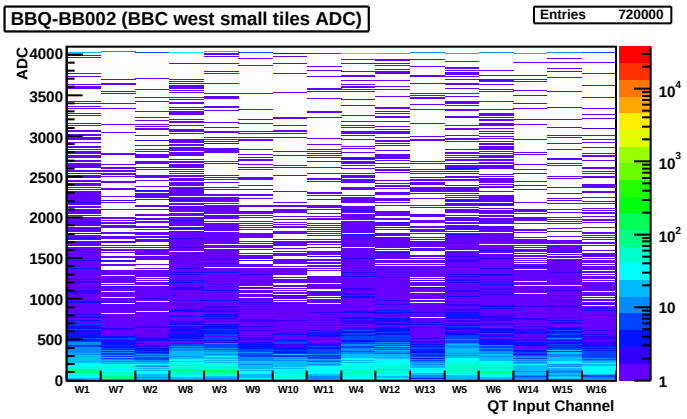

MIX-TF101
Entries 270000
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

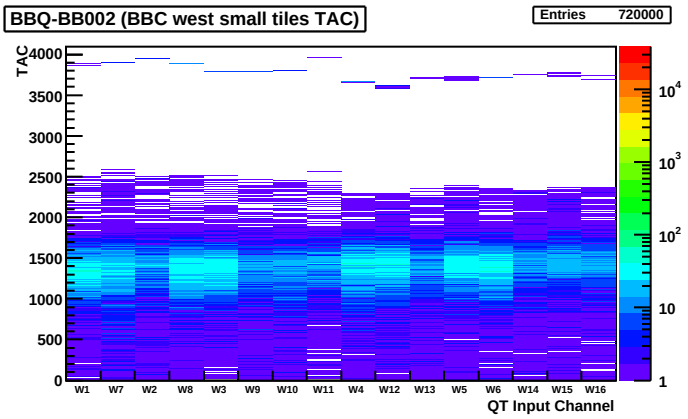

L1-TF201
Entries 45000
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

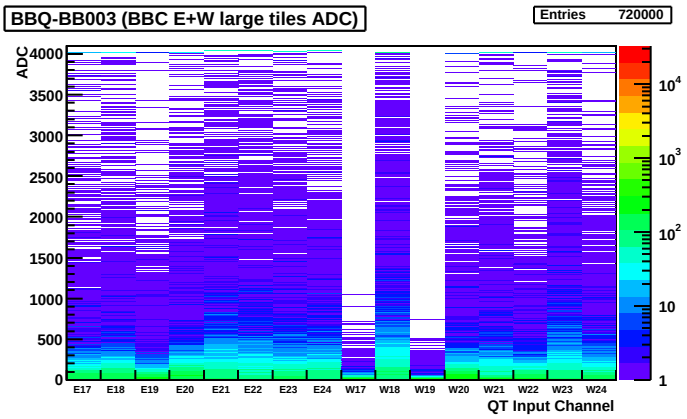

L1-TF201
Entries 270000
Threshold bits
1
0
272
267
261
234
284
233
44728
44733
44739
44766
44716
44767
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3

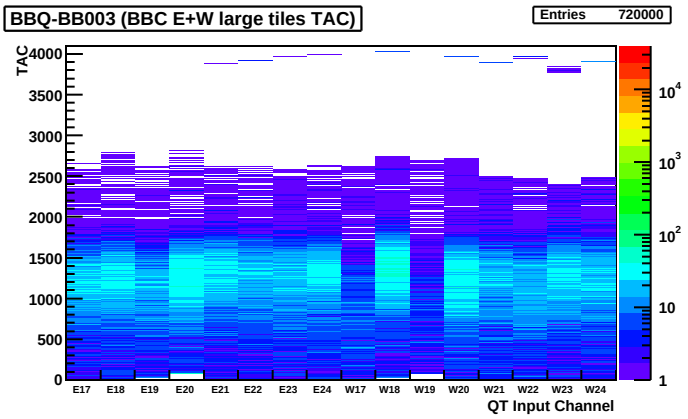

BBQ-BB001 (BBC east small tiles ADC)
Entries 720000
ADC
QT Input Channel

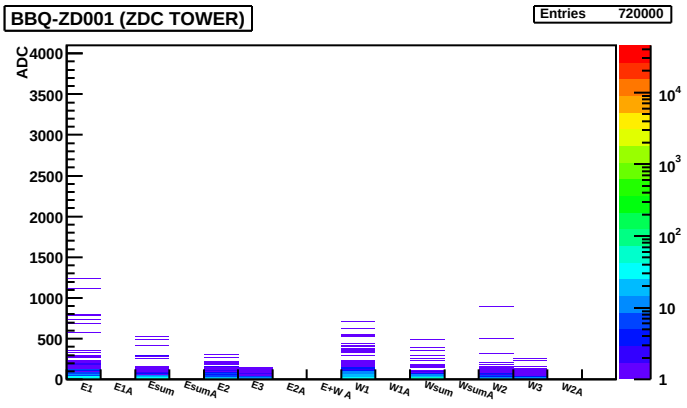

BBQ-BB001 (BBC east small tiles TAC)
Entries 720000
TAC
QT Input Channel

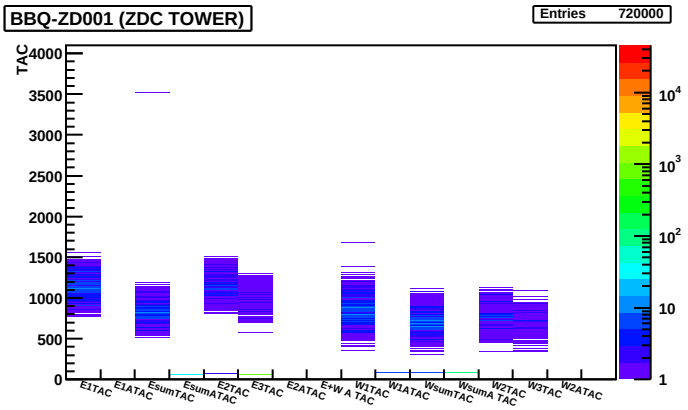

BBQ-BB002 (BBC west small tiles ADC)
Entries 720000
ADC
QT Input Channel

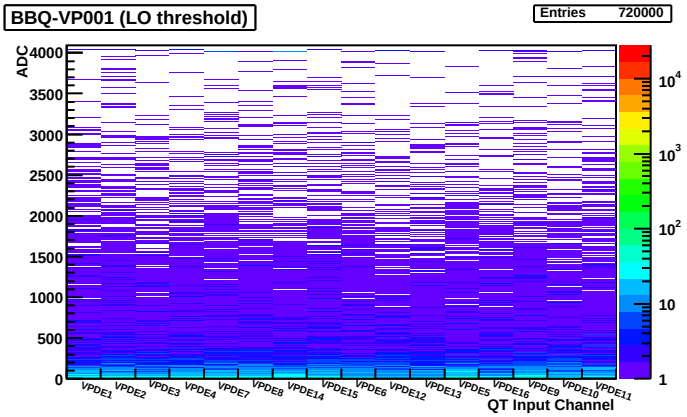

BBQ-BB002 (BBC west small tiles TAC)
Entries 720000
TAC
QT Input Channel

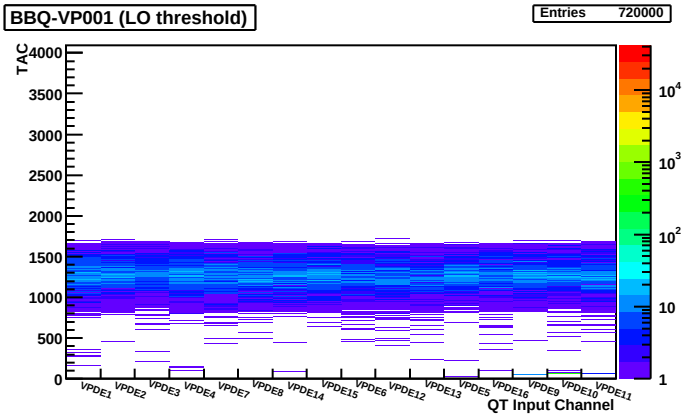

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 720000
ADC
QT Input Channel

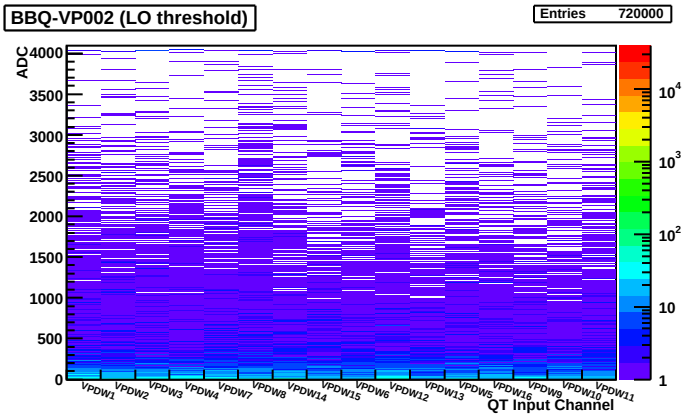

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 720000
TAC
QT Input Channel

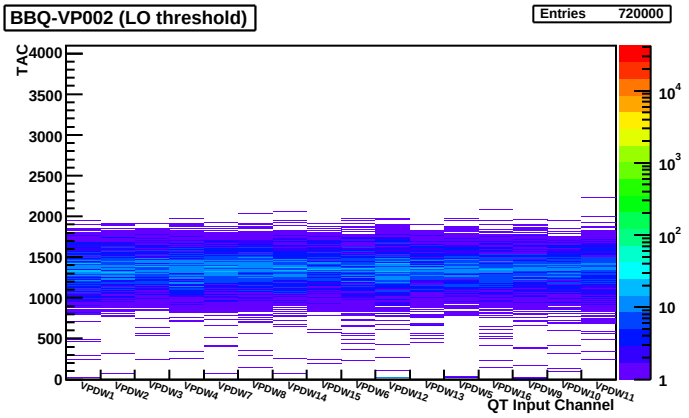

BBQ-ZD001 (ZDC TOWER)
Entries 720000
ADC

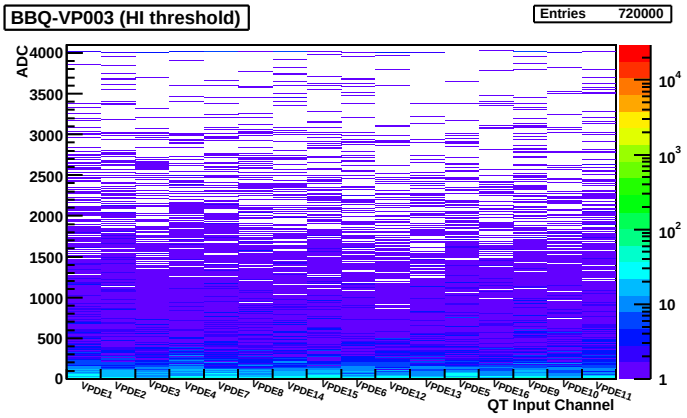

BBQ-ZD001 (ZDC TOWER)
Entries 720000
TAC

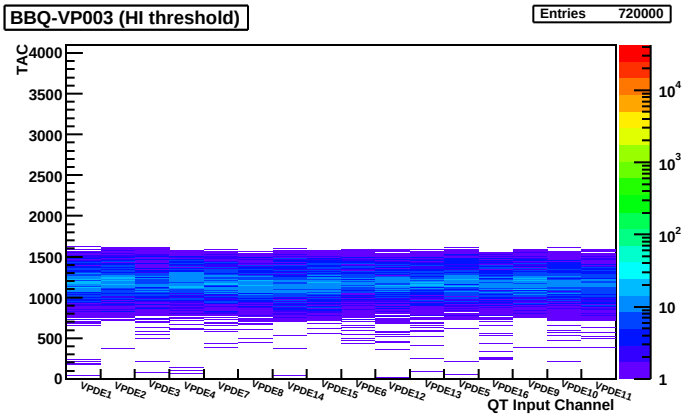

BBQ-VP001 (LO threshold)
Entries 720000
ADC
QT Input Channel

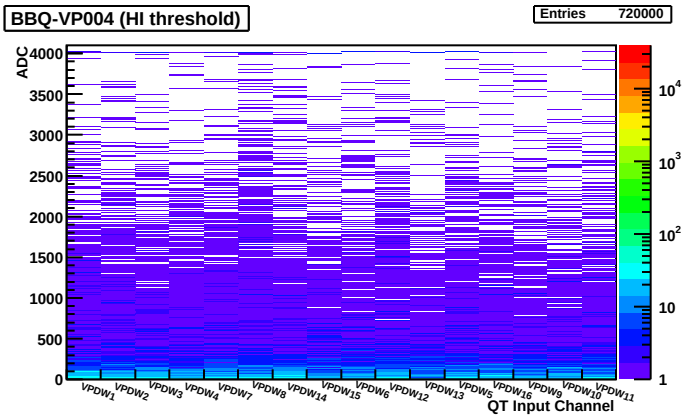

BBQ-VP001 (LO threshold)
Entries 720000
TAC
QT Input Channel

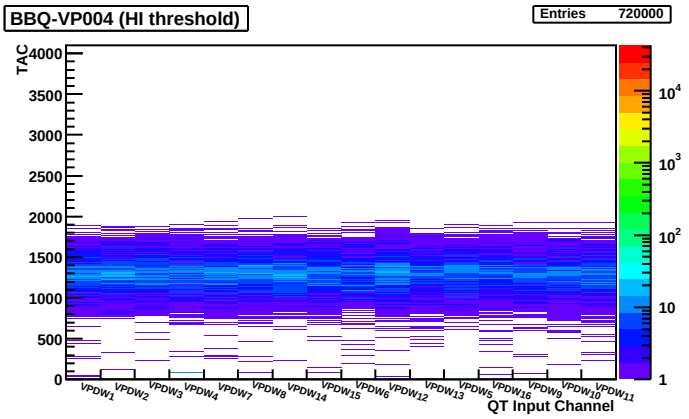

BBQ-VP002 (LO threshold)
Entries 720000
ADC
QT Input Channel

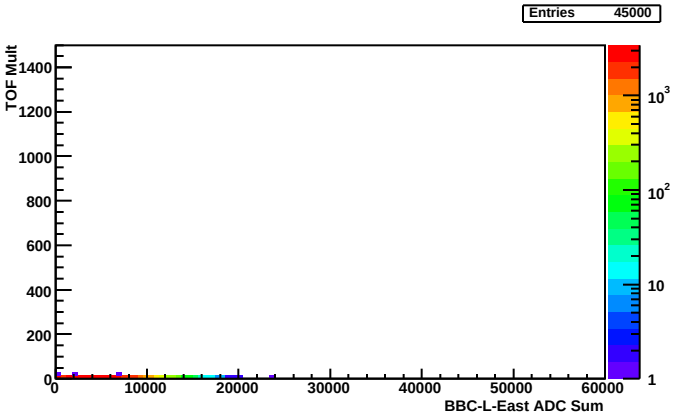

BBQ-VP002 (LO threshold)
Entries 720000
TAC
QT Input Channel

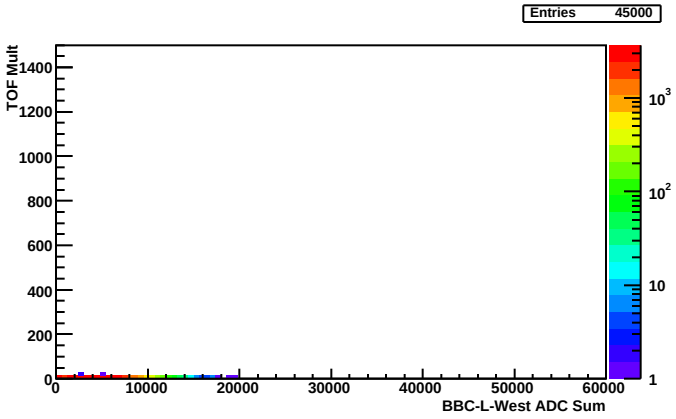

BBQ-VP003 (HI threshold)
Entries 720000
ADC
QT Input Channel

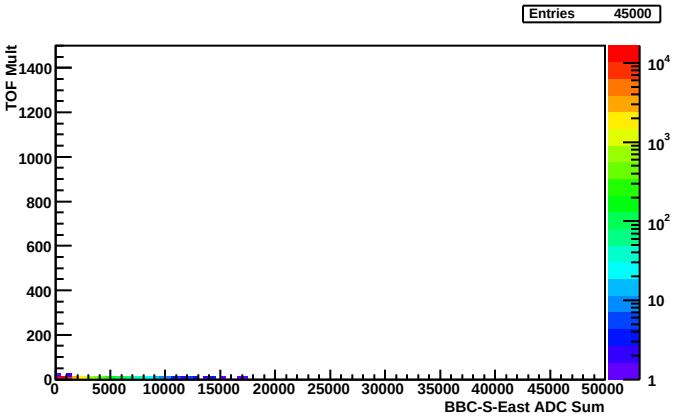

BBQ-VP003 (HI threshold)
Entries 720000
TAC
QT Input Channel

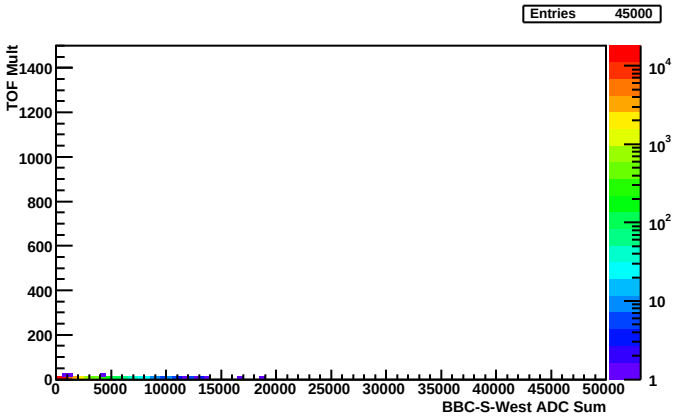

BBQ-VP004 (HI threshold)
Entries 720000
ADC
QT Input Channel

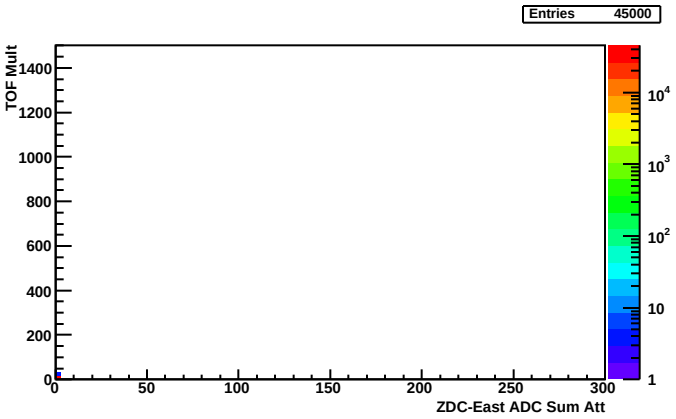

BBQ-VP004 (HI threshold)
Entries 720000
TAC
QT Input Channel

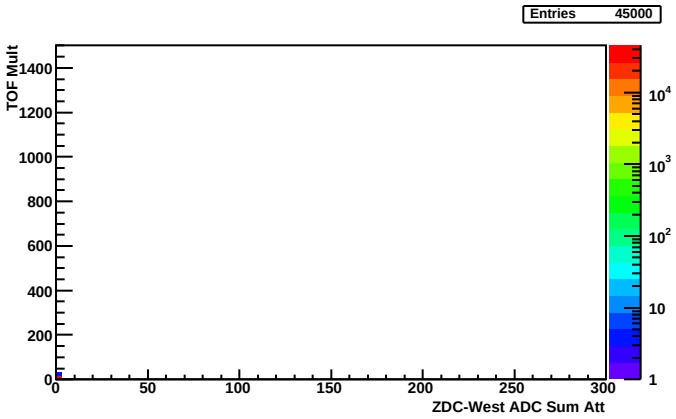

Entries 45000
TOF Mult
BBC-L-East ADC Sum

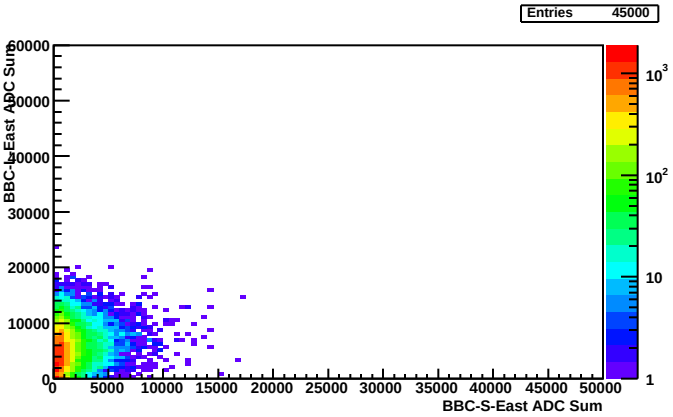

Entries 45000
TOF Mult
BBC-L-West ADC Sum

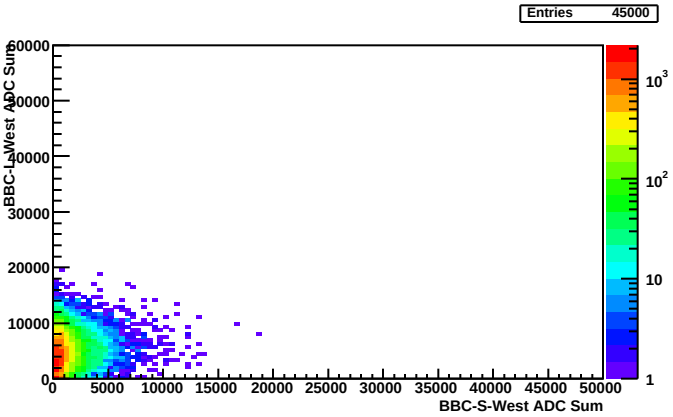

Entries 45000
TOF Mult
BBC-S-East ADC Sum

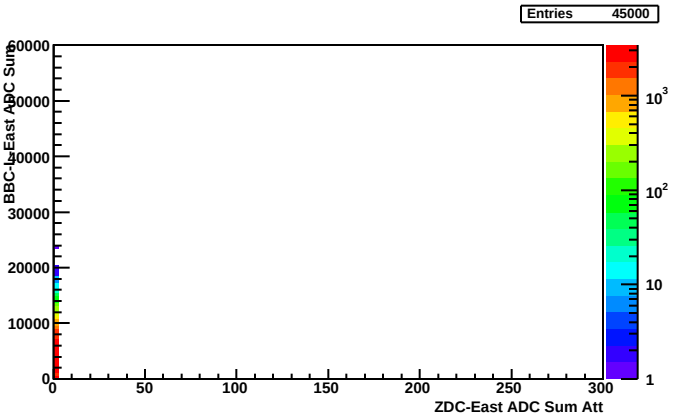

Entries 45000
TOF Mult
BBC-S-West ADC Sum

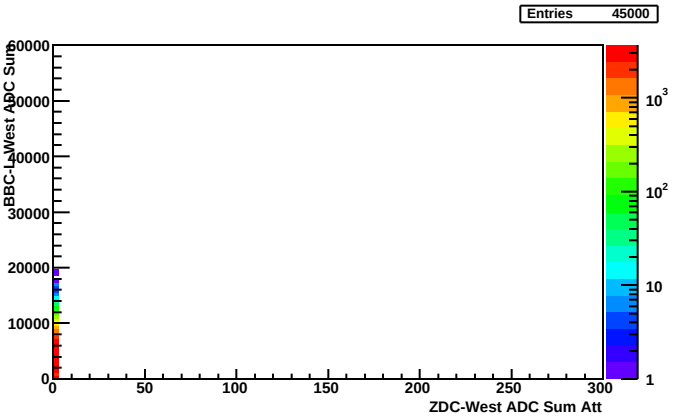

Entries 45000
TOF Mult
ZDC-East ADC Sum Att

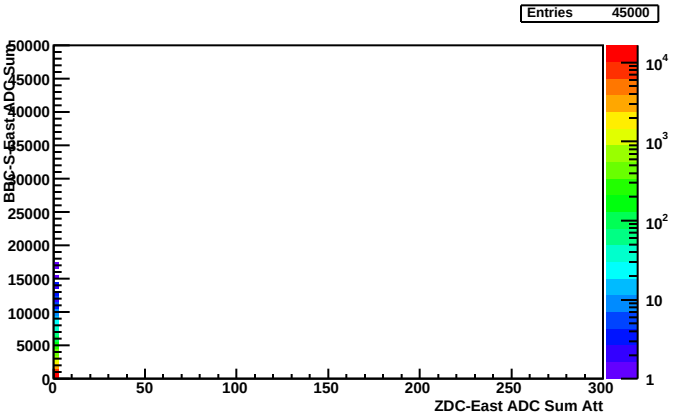

Entries 45000
TOF Mult
ZDC-West ADC Sum Att

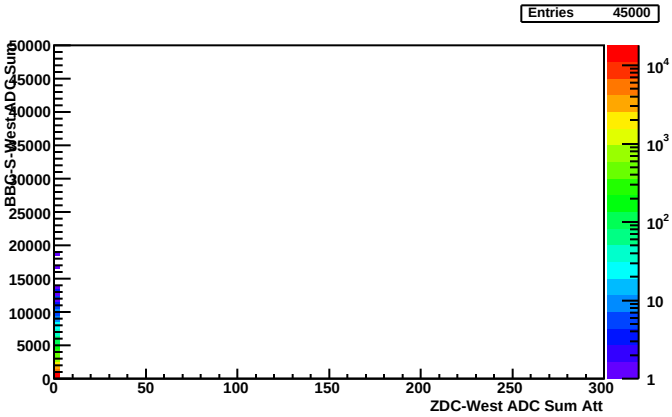

Entries 45000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

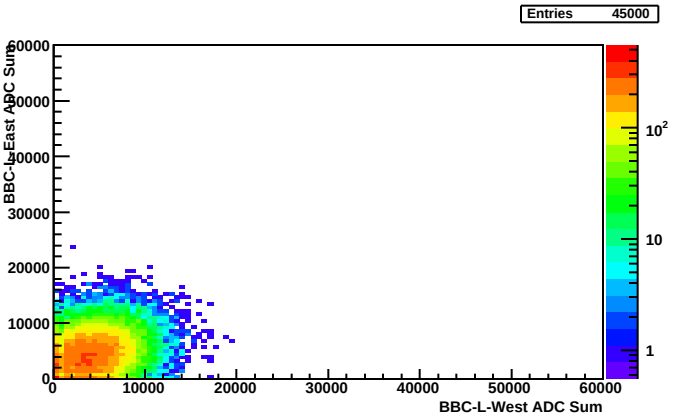

Entries 45000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

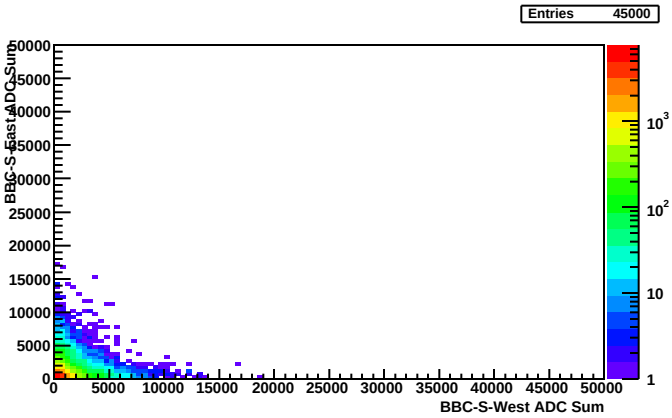

Entries 45000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

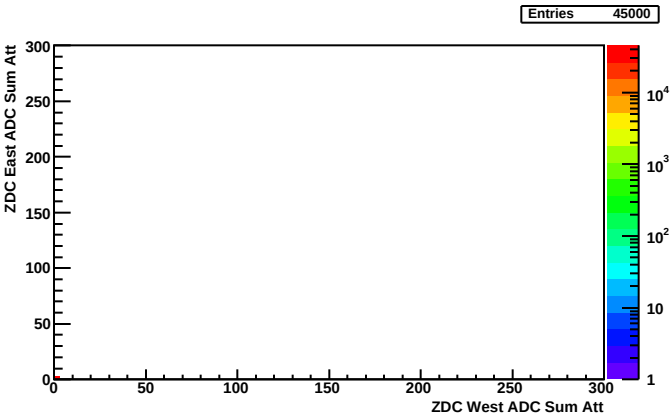

Entries 45000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

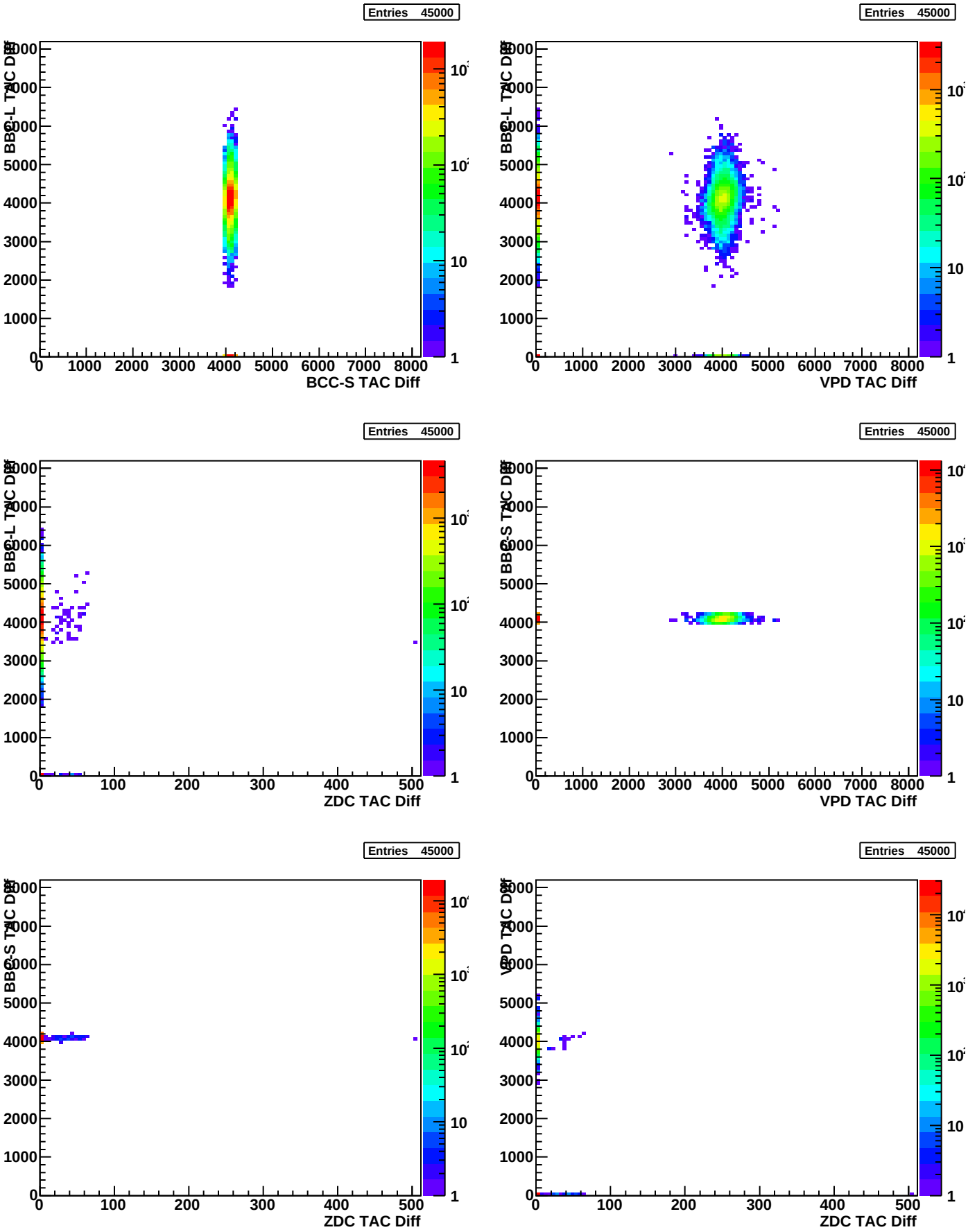

Entries 45000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

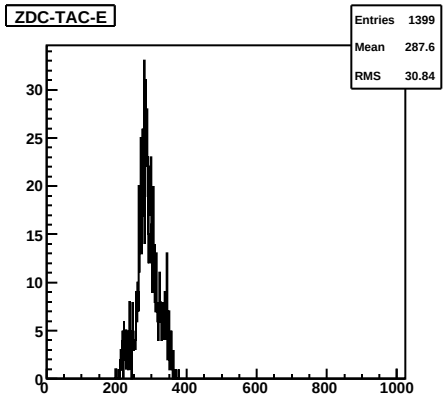

Entries 45000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

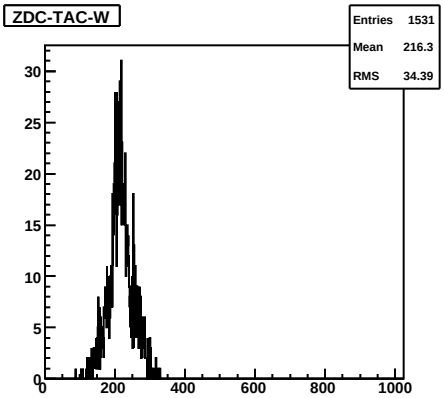

Entries 45000
BBC-L-East ADC Sum
BBC-L-West ADC Sum

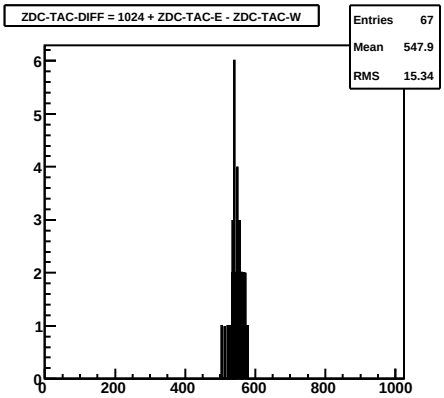

Entries 45000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

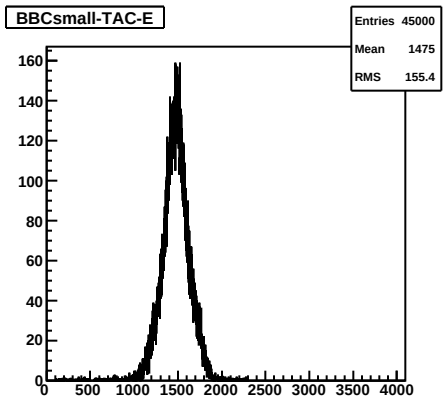

Entries 45000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

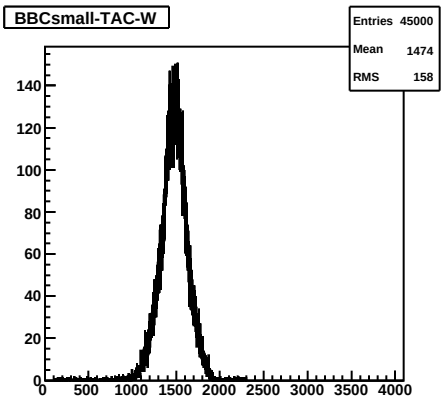

Entries 45000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 45000
BBC-L TAC Diff
VPD TAC Diff
Entries 45000
BBC-L TAC Diff
ZDC TAC Diff
Entries 45000
BBC-S TAC Diff
VPD TAC Diff
Entries 45000
BBC-S TAC Diff
ZDC TAC Diff
Entries 45000
VPD TAC Diff
ZDC TAC Diff

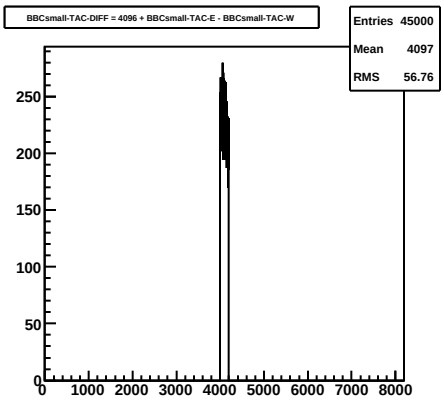

ZDC-TAC-E
Entries 1399
Mean 287.6
RMS 30.84

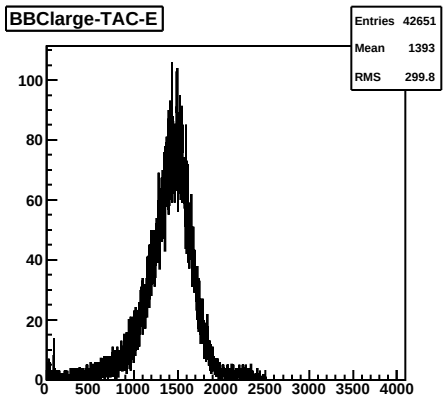

ZDC-TAC-W
Entries 1531
Mean 216.3
RMS 34.39

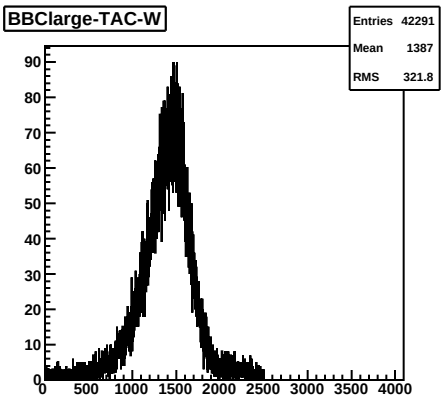

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 67
Mean 547.9
RMS 15.34

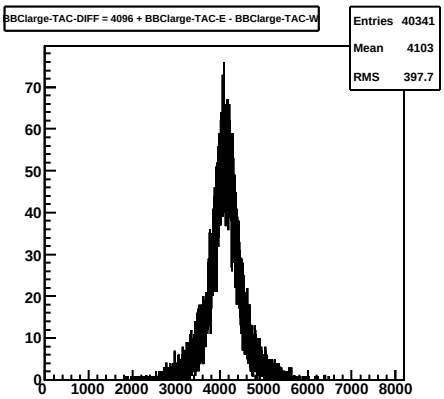

BBCsmall-TAC-E
Entries 45000
Mean 1475
RMS 155.4

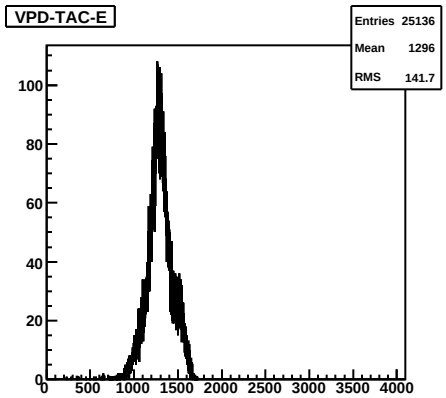

BBCsmall-TAC-W
Entries 45000
Mean 1474
RMS 158

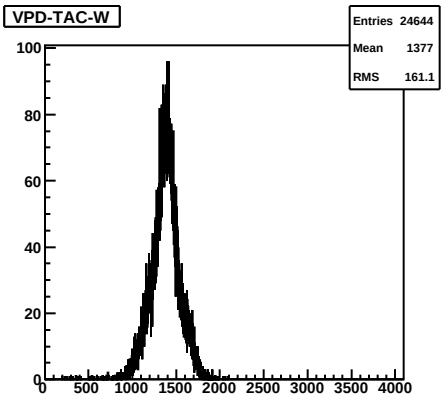

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 45000
Mean 4097
RMS 56.76

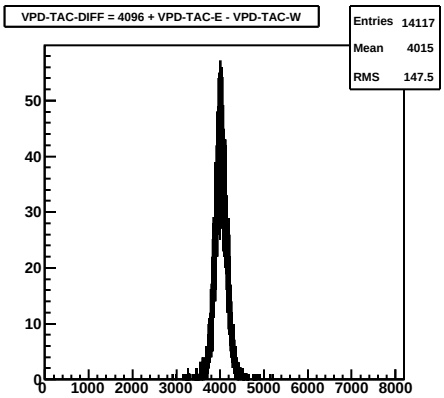

BBClarge-TAC-E
Entries 42651
Mean 1393
RMS 299.8

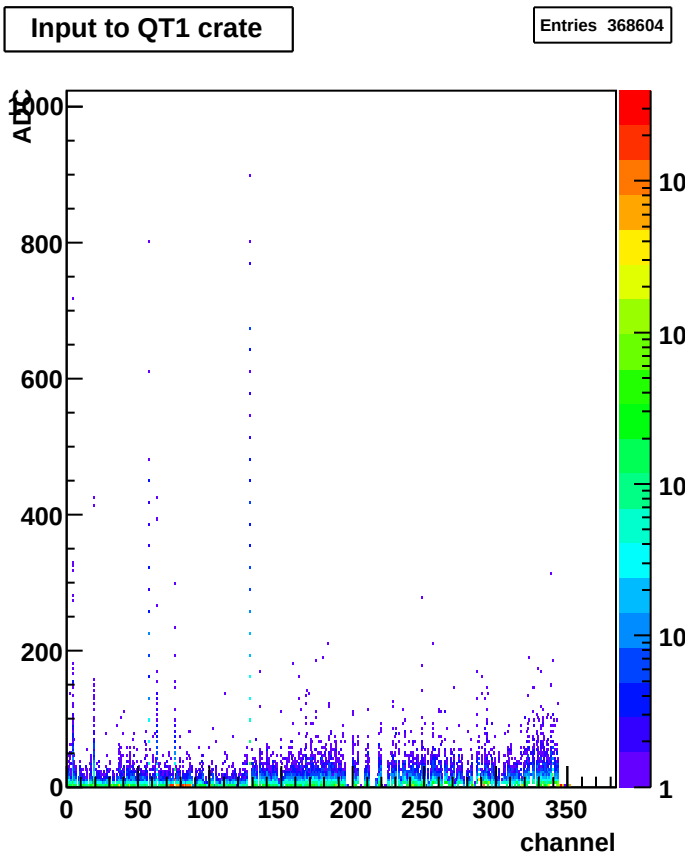

BBClarge-TAC-W
Entries 42291
Mean 1387
RMS 321.8

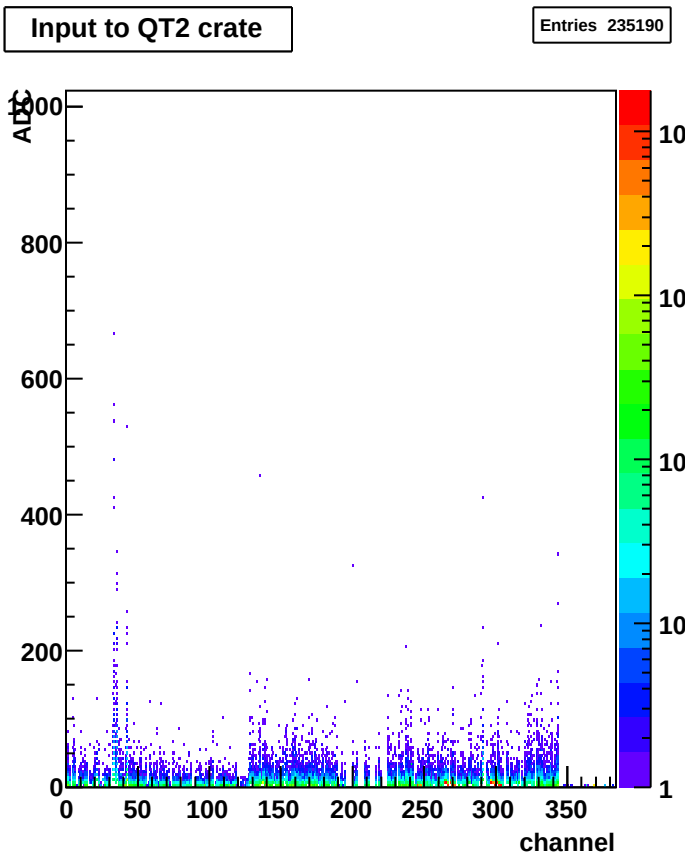

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 40341
Mean 4103
RMS 397.7

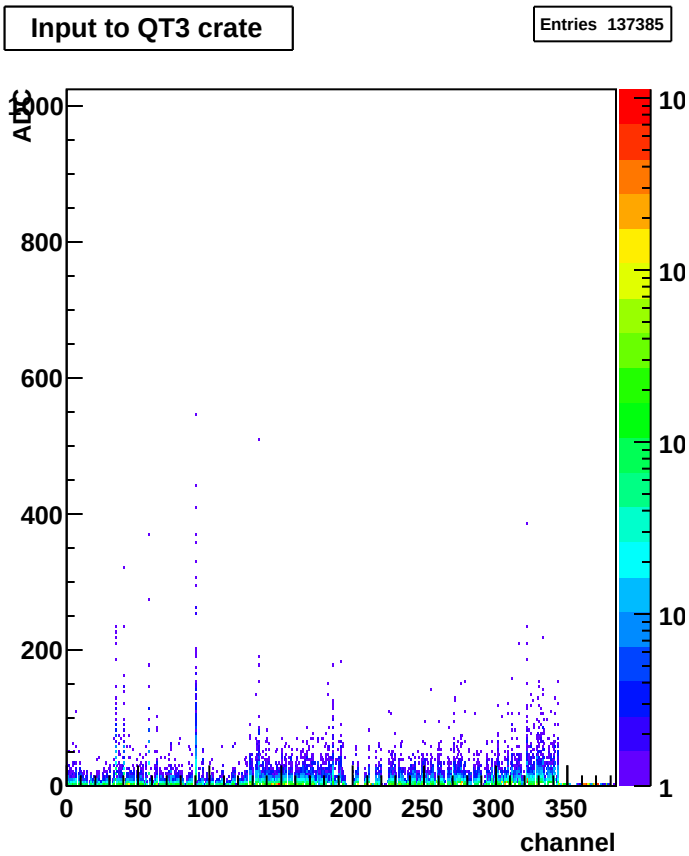

VPD-TAC-E
Entries 25136
Mean 1296
RMS 141.7

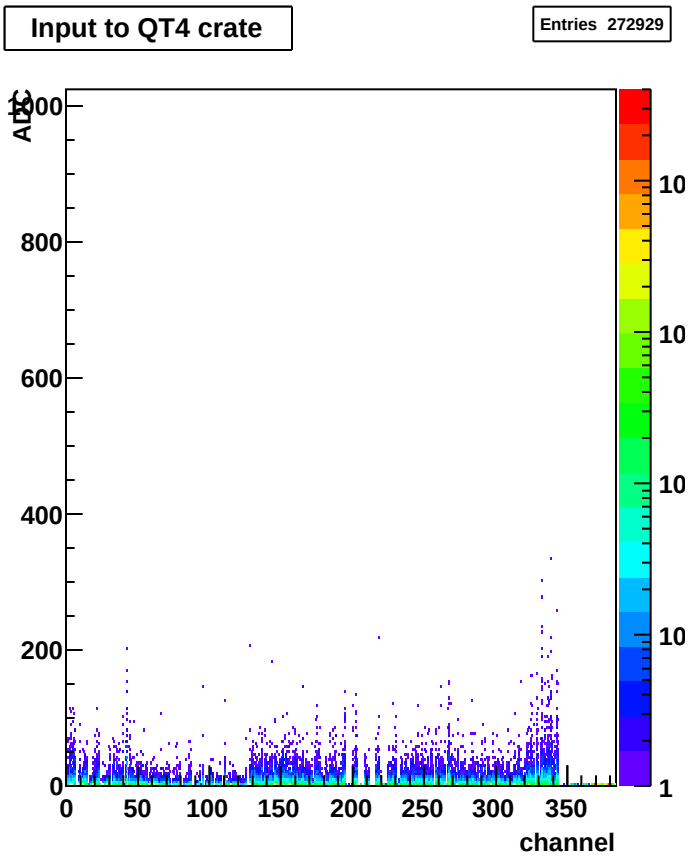

VPD-TAC-W
Entries 24644
Mean 1377
RMS 161.1

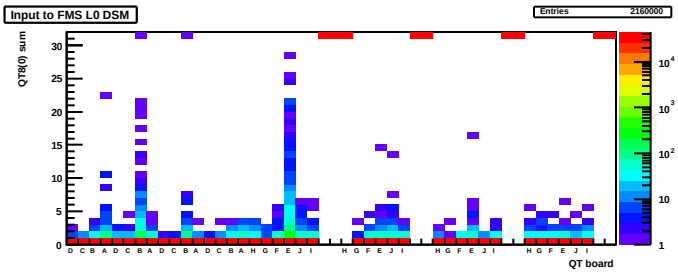

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 14117
Mean 4015
RMS 147.5

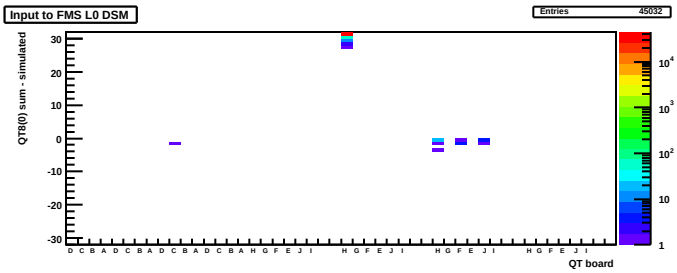

Input to QT1 crate
Entries 368604
ADC
channel

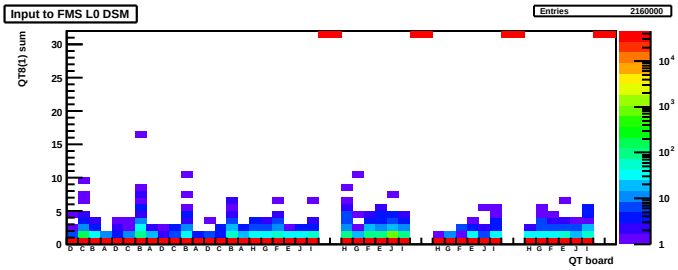

Input to QT2 crate
Entries 235190
ADC
channel

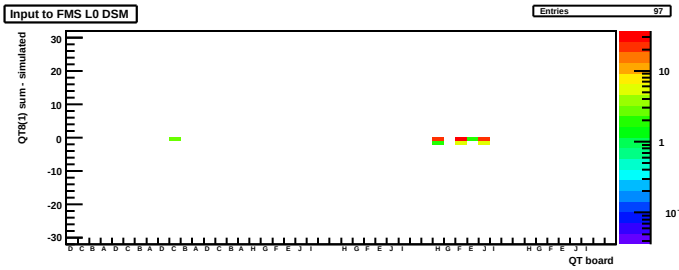

Input to QT3 crate
Entries 137385
ADC
channel

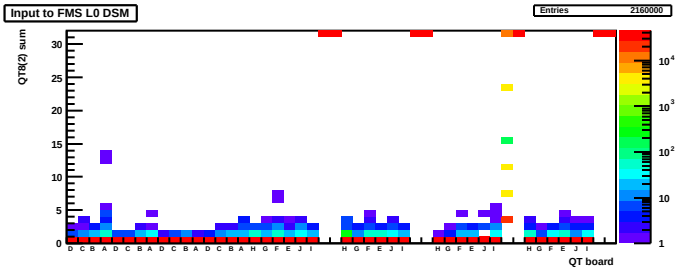

Input to QT4 crate
Entries 272929
ADC
channel

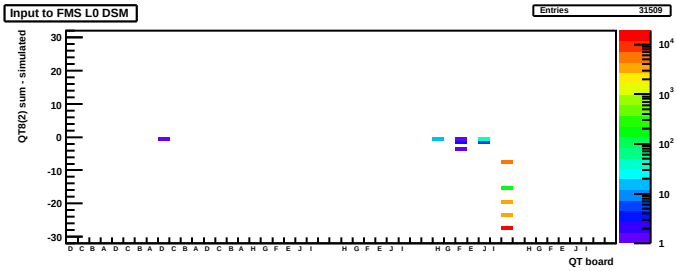

Input to FMS L0 DSM
Entries 2160000
QT8(0) sum
QT board

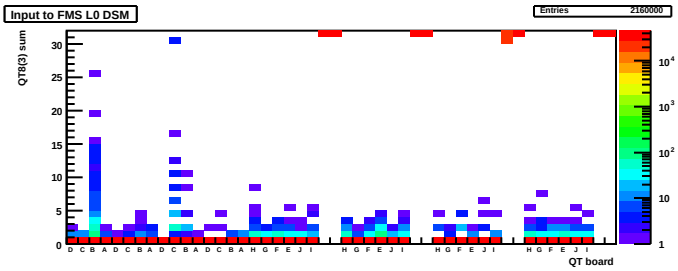

Input to FMS L0 DSM
Entries 45032
QT8(0) sum - simulated
QT board

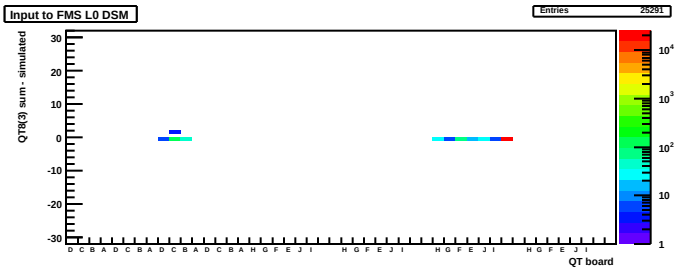

Input to FMS L0 DSM
Entries 2160000
QT8(1) sum
QT board

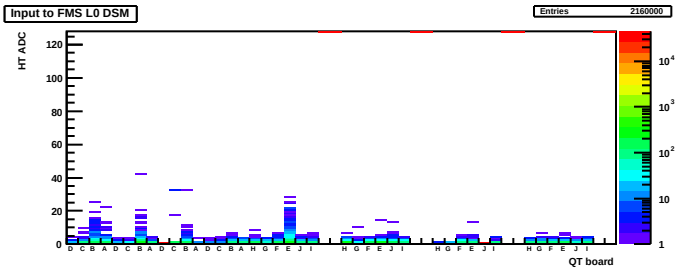

Input to FMS L0 DSM
Entries 97
QT8(1) sum - simulated
QT board

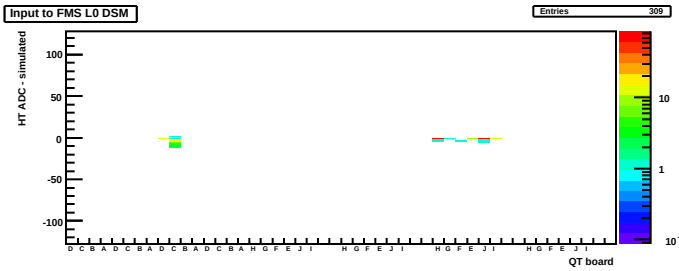

Input to FMS L0 DSM
Entries 2160000
QT8(2) sum
QT board

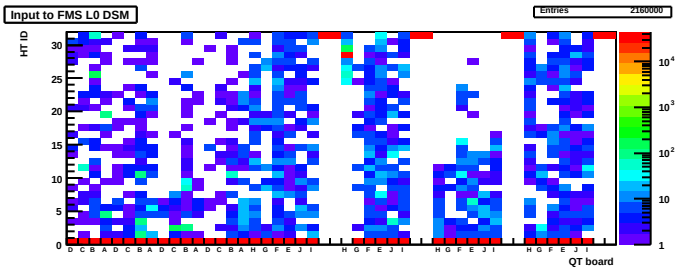

Input to FMS L0 DSM
Entries 31509
QT8(2) sum - simulated
QT board

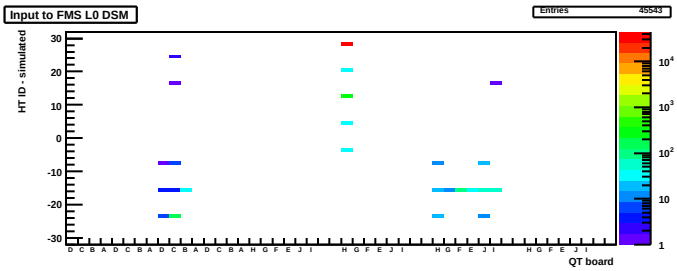

Input to FMS L0 DSM
Entries 2160000
QT8(3) sum
QT board

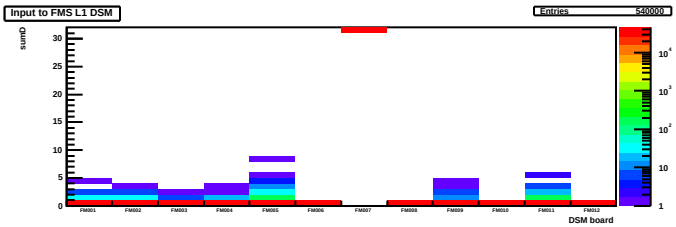

Input to FMS L0 DSM
Entries 25291
QT8(3) sum - simulated
QT board

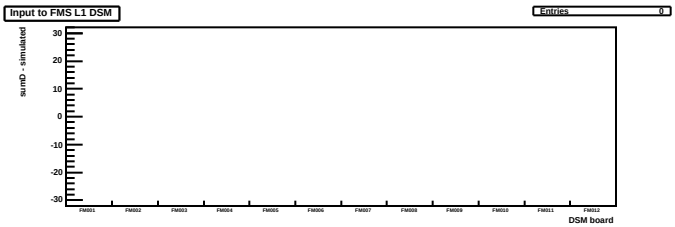

Input to FMS L0 DSM
Entries 2160000
HT ADC
QT board

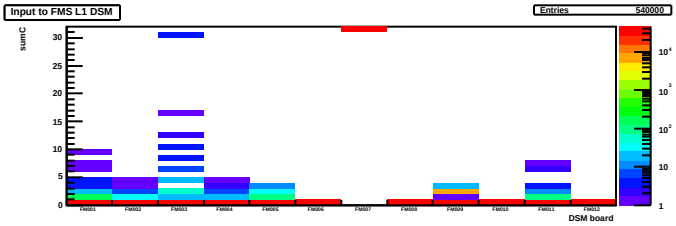

Input to FMS L0 DSM
Entries 309
HT ADC - simulated
QT board

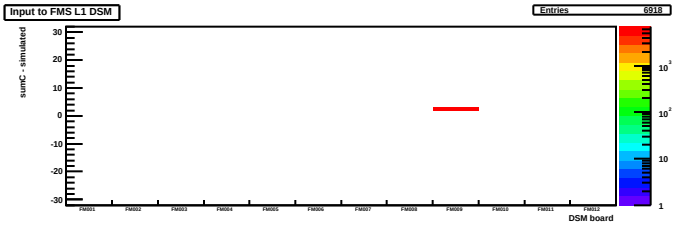

Input to FMS L0 DSM
Entries 2160000
HT ID
QT board

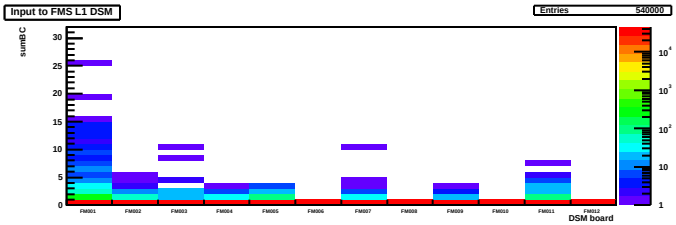

Input to FMS L0 DSM
Entries 45543
HT ID - simulated
QT board

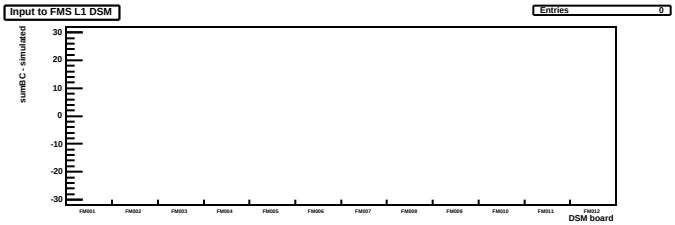

Input to FMS L1 DSM
Entries 540000
sumD
DSM board

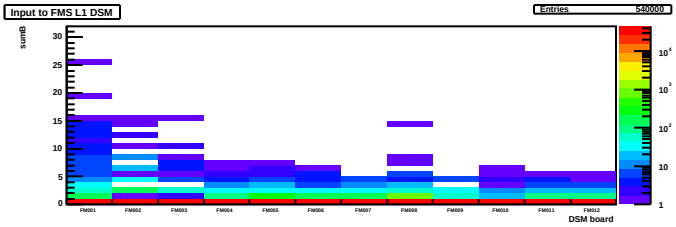

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

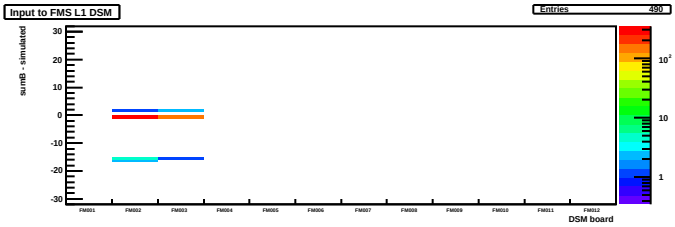

Input to FMS L1 DSM
Entries 540000
sumC
DSM board

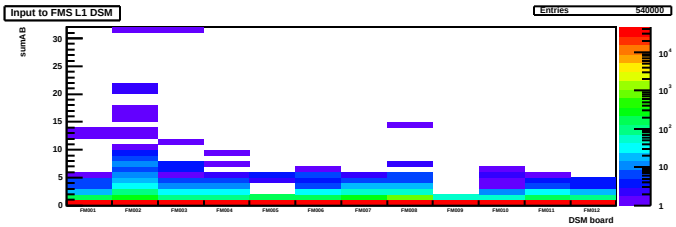

Input to FMS L1 DSM
Entries 6918
sumC - simulated
DSM board

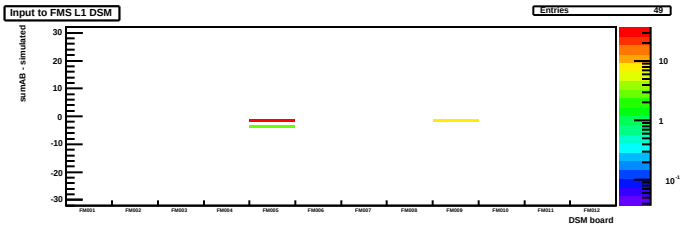

Input to FMS L1 DSM
Entries 540000
sumBC
DSM board

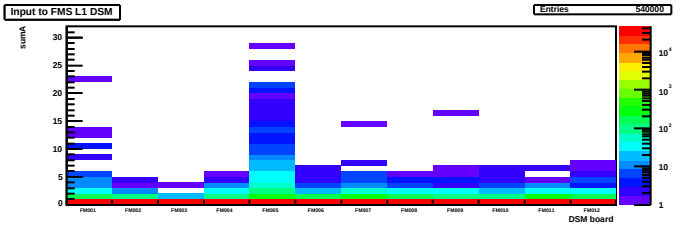

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

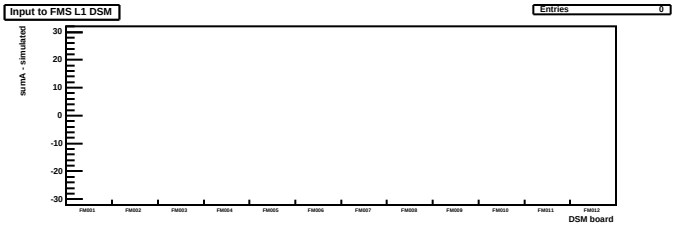

Input to FMS L1 DSM
Entries 540000
sumB
DSM board

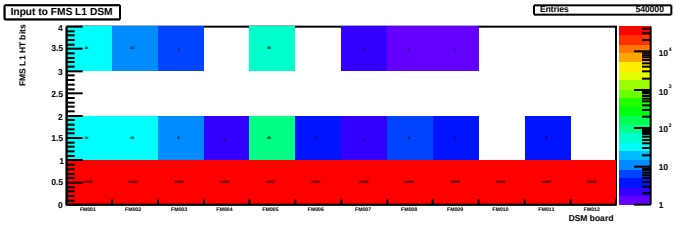

Input to FMS L1 DSM
Entries 490
sumB - simulated
DSM board

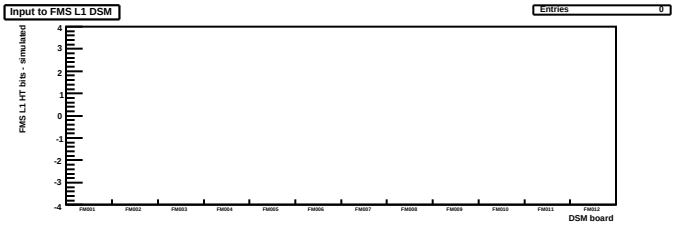

Input to FMS L1 DSM
Entries 540000
sumAB
DSM board

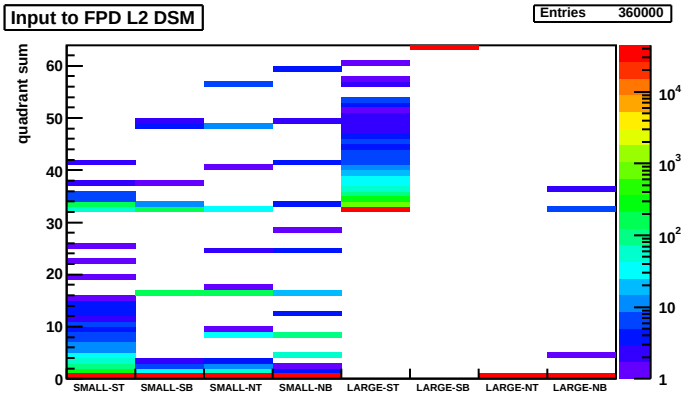

Input to FMS L1 DSM
Entries 49
sumAB - simulated
DSM board

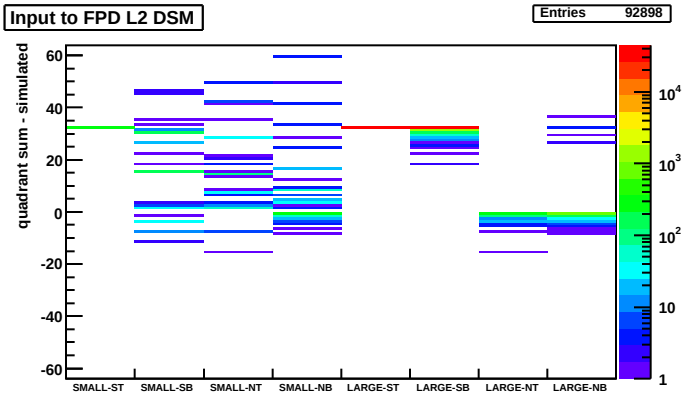

Input to FMS L1 DSM
Entries 540000
sumA
DSM board

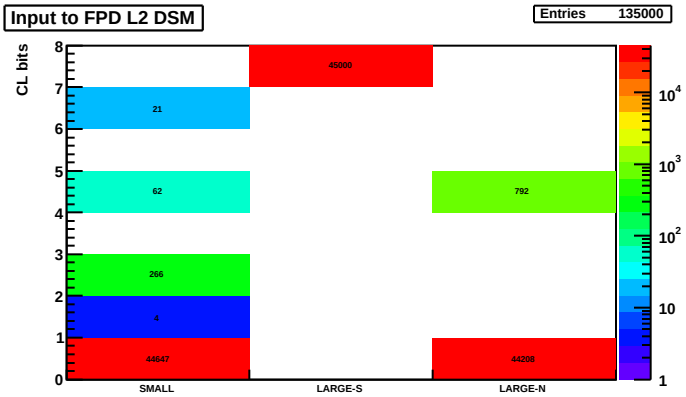

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

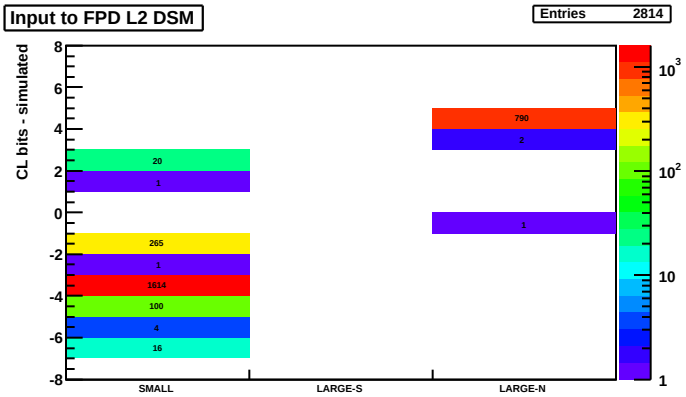

Input to FMS L1 DSM
Entries 540000
FMS L1 HT bits
DSM board

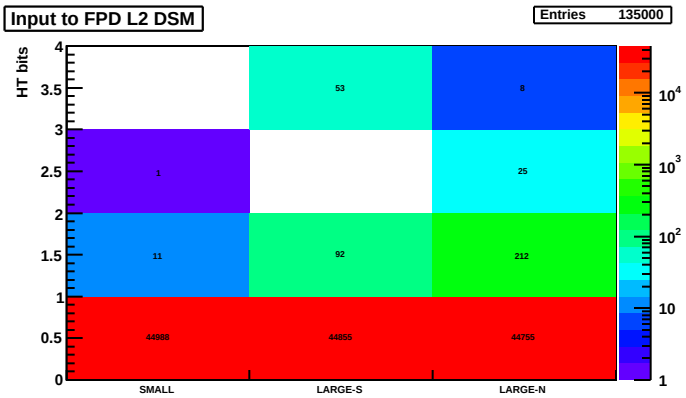

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

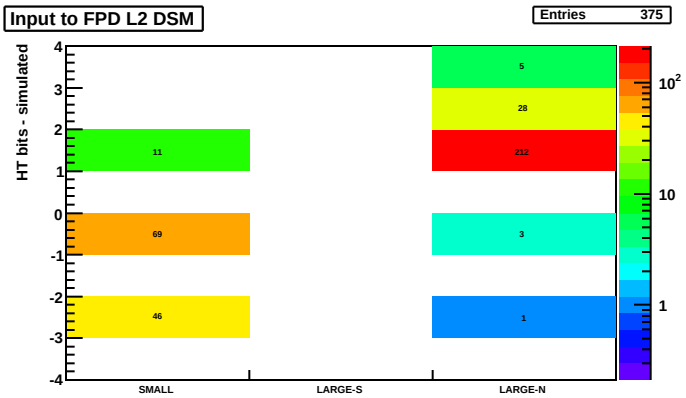

Input to FPD L2 DSM
Entries 360000
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

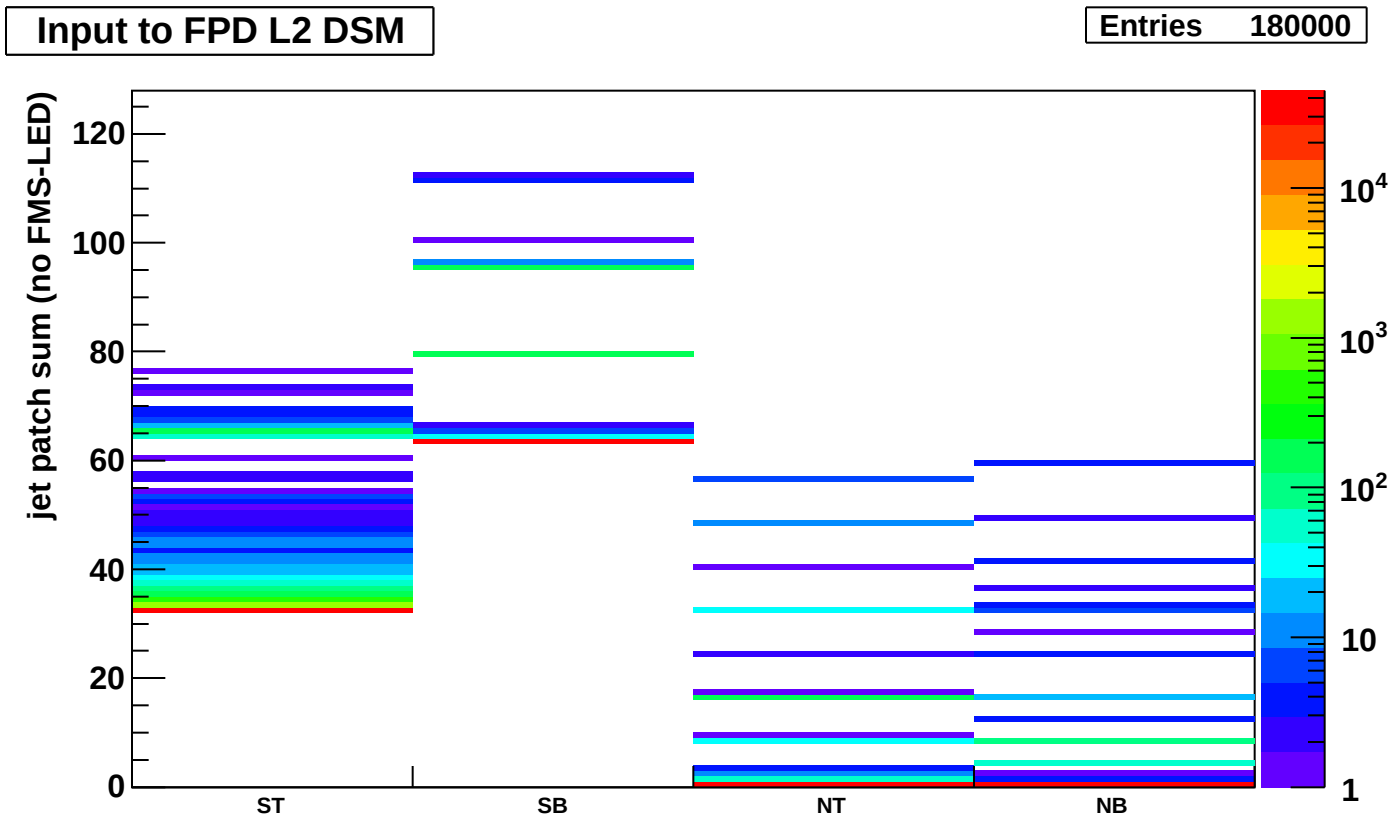

Input to FPD L2 DSM
Entries 92898
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

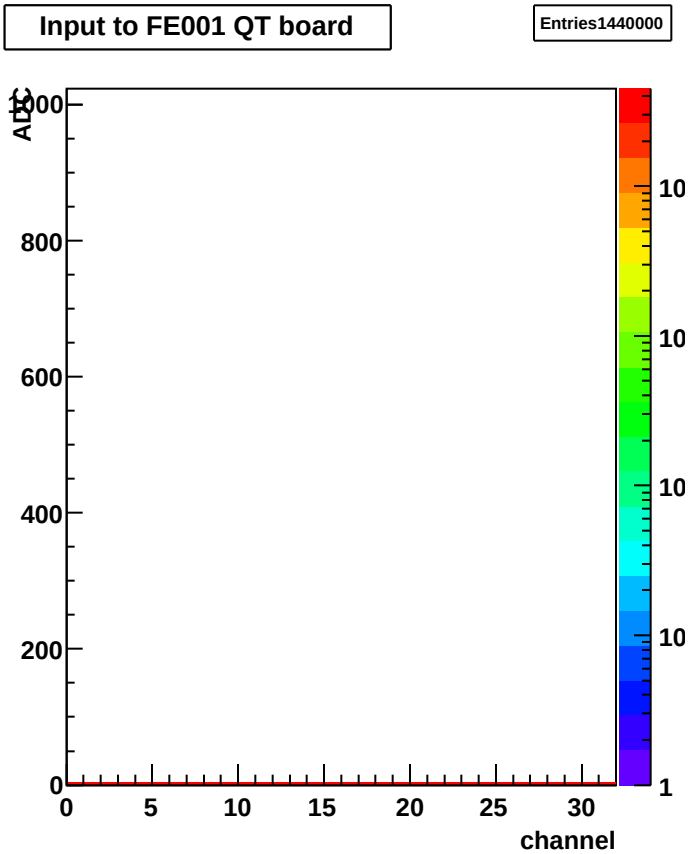

Input to FPD L2 DSM
Entries 135000
CL bits
45000
21
62
792
266
4
44647
44208
SMALL
LARGE-S
LARGE-N

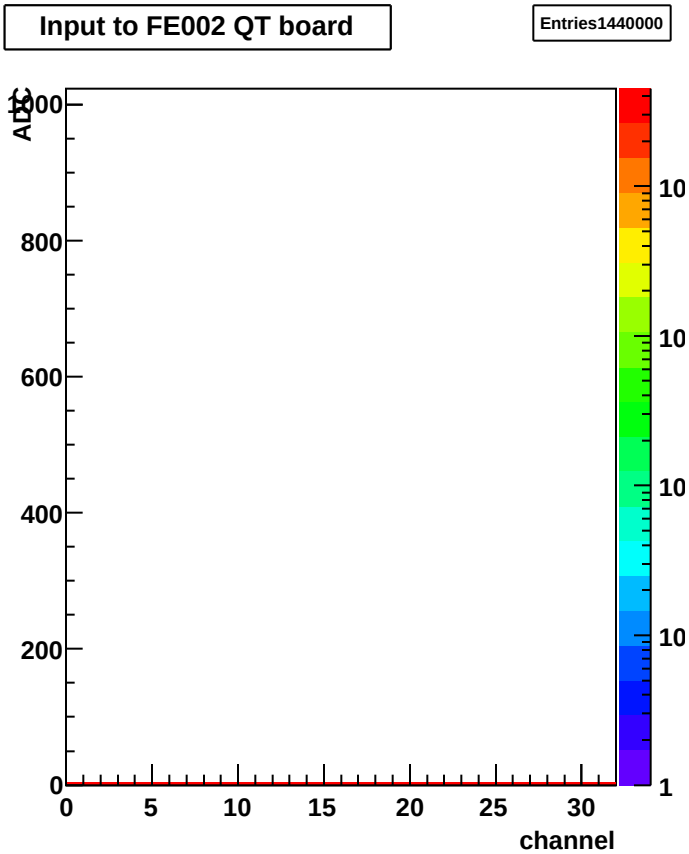

Input to FPD L2 DSM
Entries 2814
CL bits - simulated
790
2
20
1
1
265
1
1614
100
4
16
SMALL
LARGE-S
LARGE-N

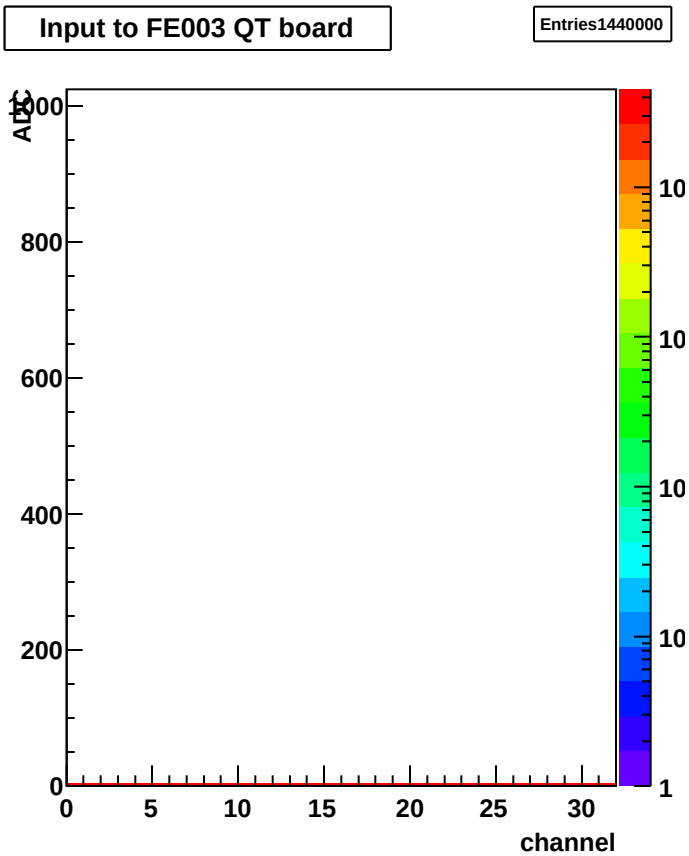

Input to FPD L2 DSM
Entries 135000
HT bits
53
8
1
25
11
92
212
44988
44855
44755
SMALL
LARGE-S
LARGE-N

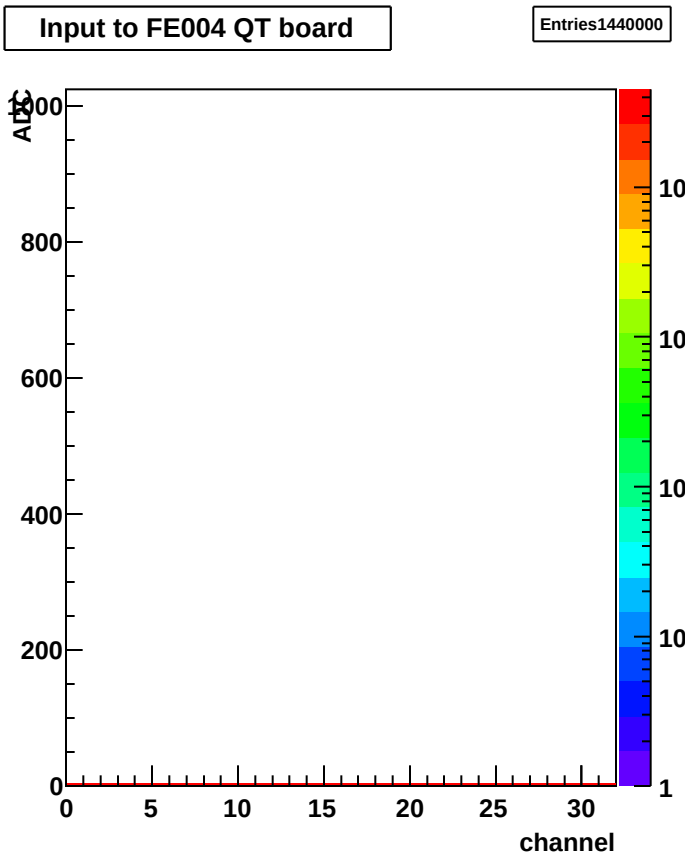

Input to FPD L2 DSM
Entries 375
HT bits - simulated
5
28
11
212
69
3
46
1
SMALL
LARGE-S
LARGE-N

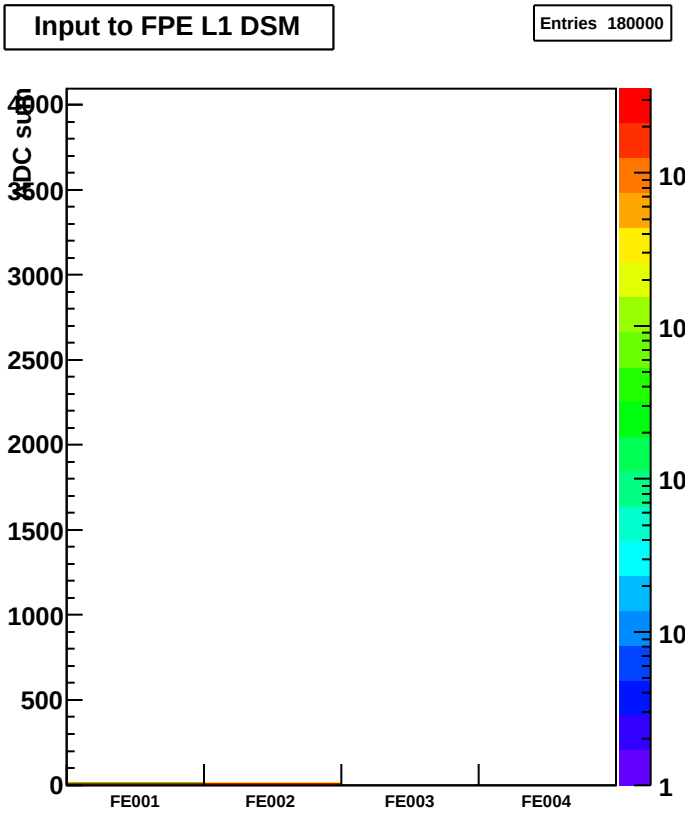

Input to FPD L2 DSM
Entries 180000
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

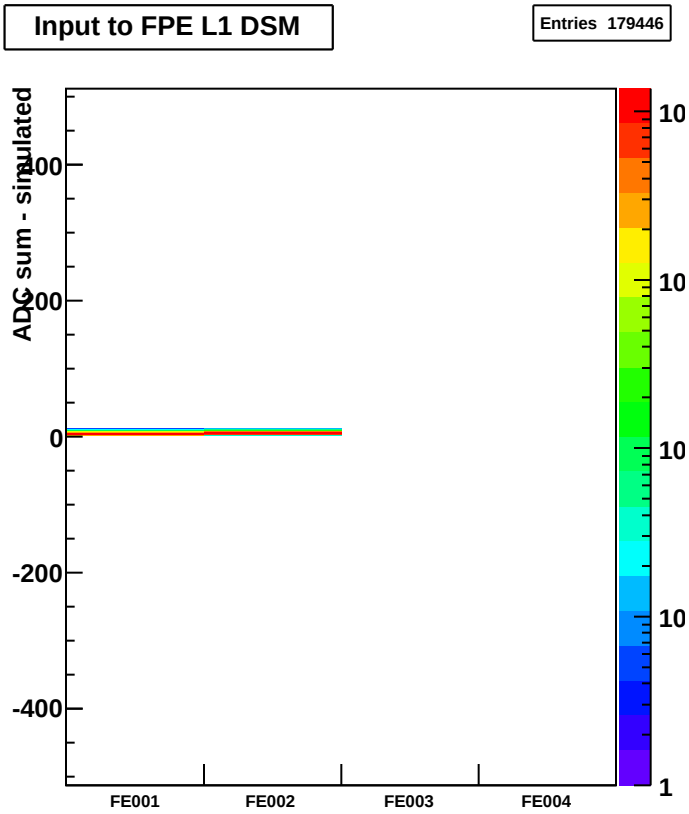

Input to FE001 QT board
Entries1440000
ADC
channel

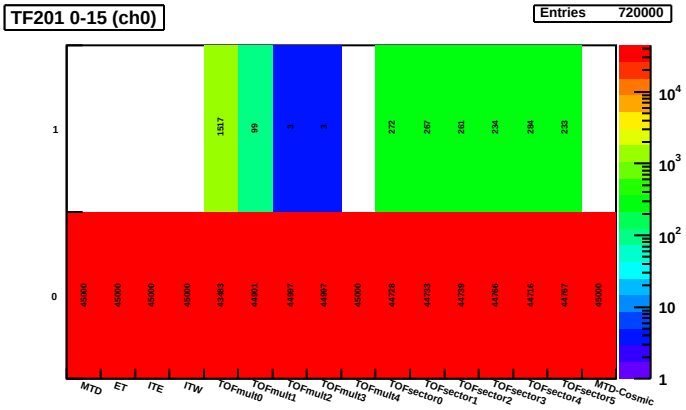

Input to FE002 QT board
Entries1440000
ADC
channel

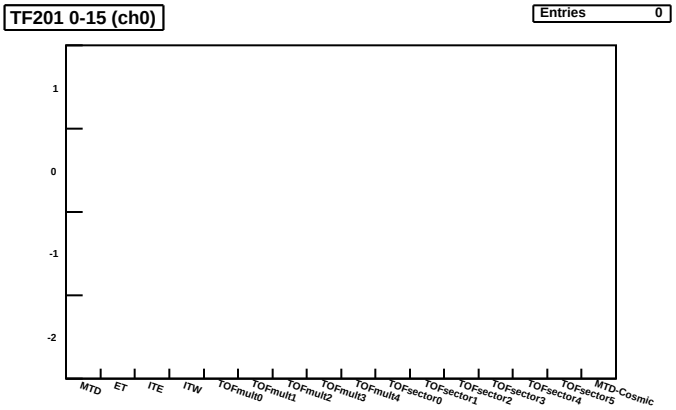

Input to FE003 QT board
Entries1440000
ADC
channel

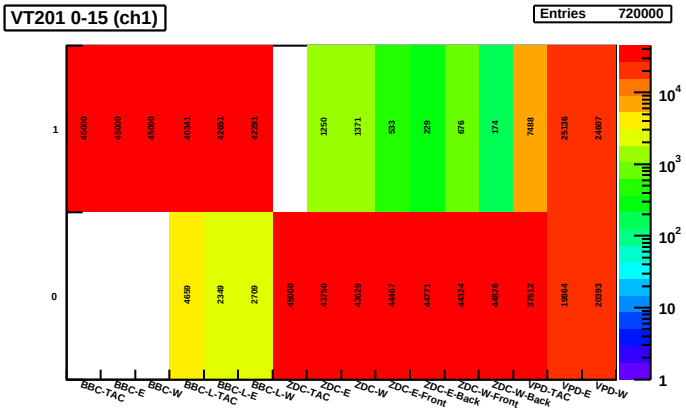

Input to FE004 QT board
Entries1440000
ADC
channel

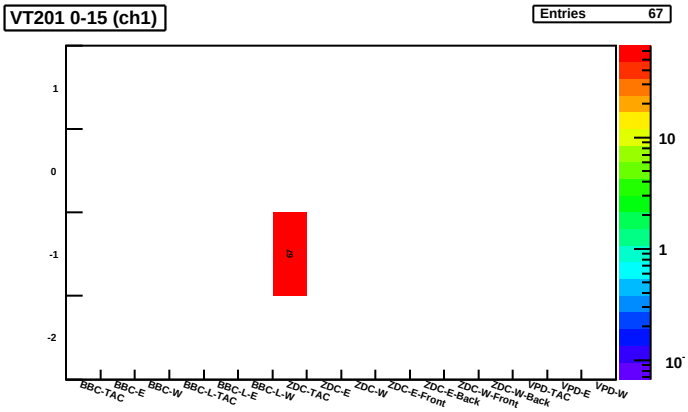

Input to FPE L1 DSM
Entries 180000
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

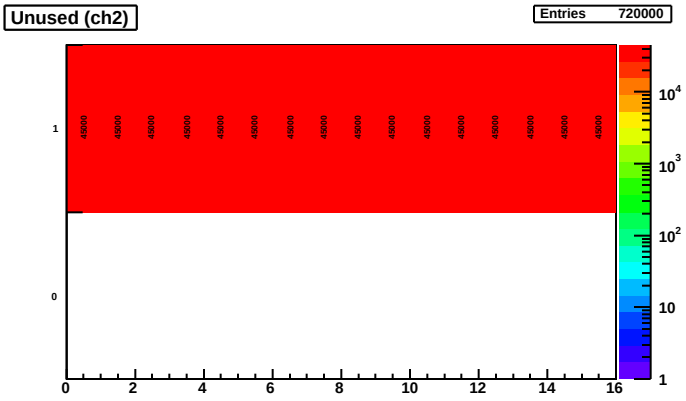

Input to FPE L1 DSM
Entries 179446
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1

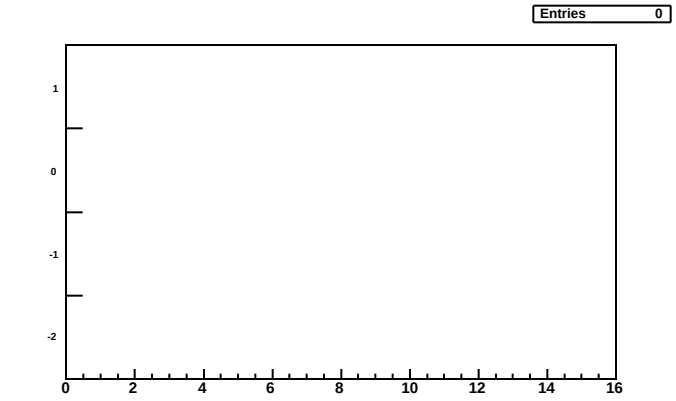

TF201 0-15 (ch0)
Entries 720000
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

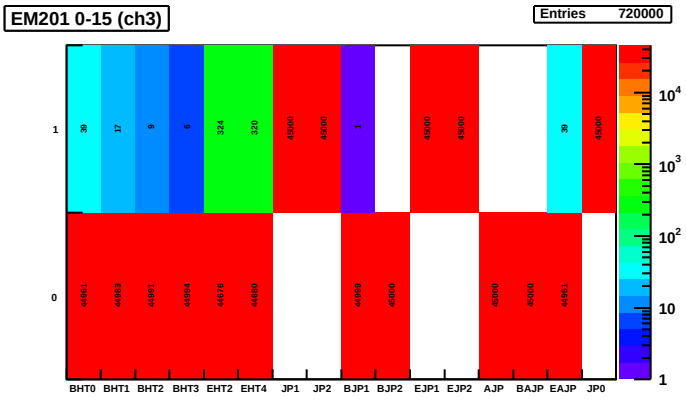

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

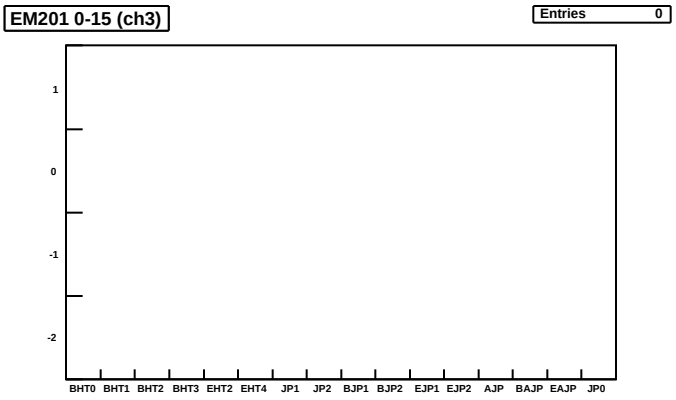

VT201 0-15 (ch1)
Entries 720000
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 67
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

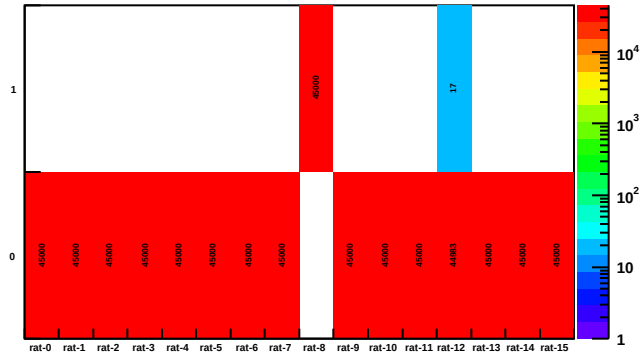

Unused (ch2)
Entries 720000

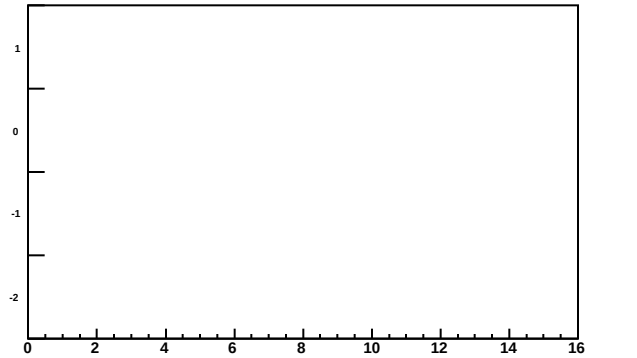

Entries 0


EM201 0-15 (ch3)
Entries 720000
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

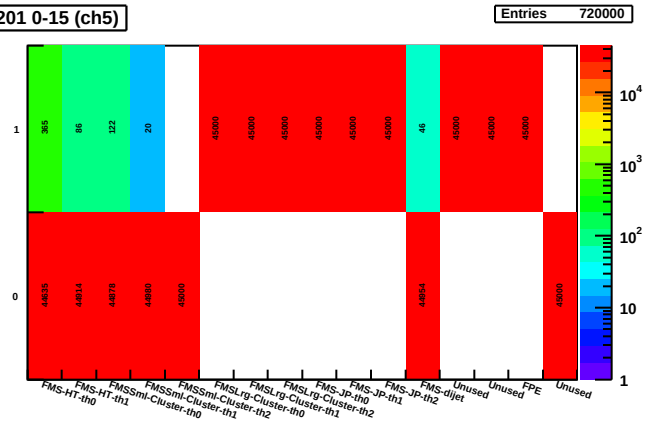

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

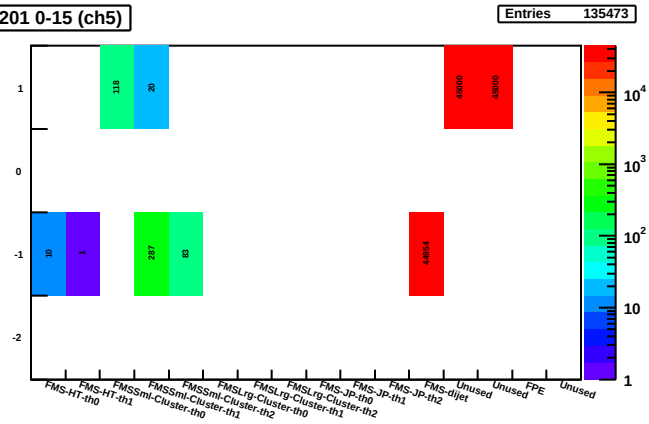




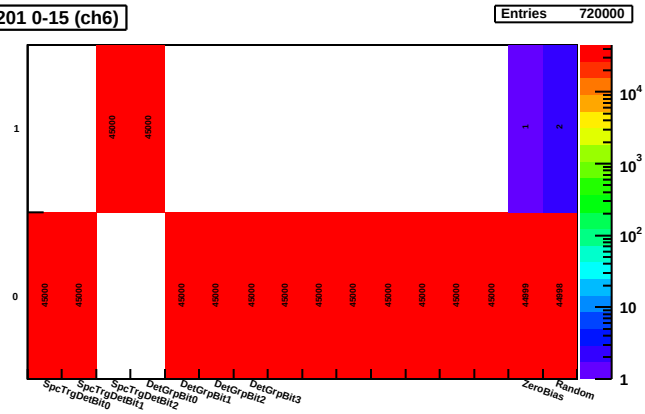
FP201 0-15 (ch5)

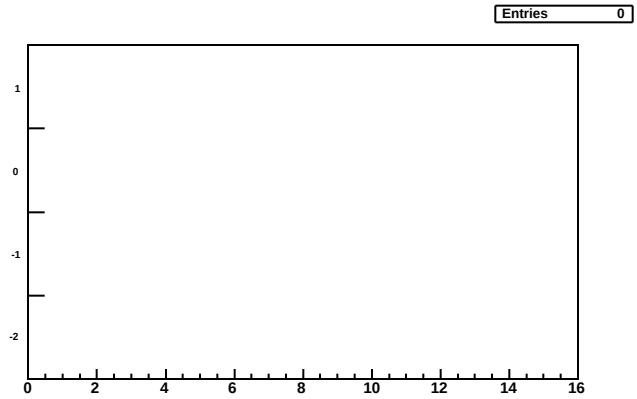
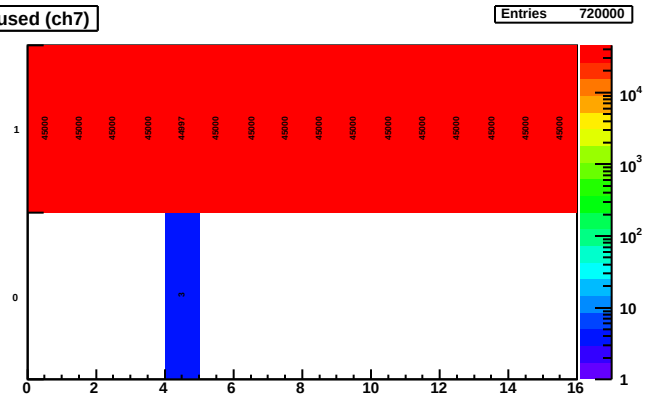
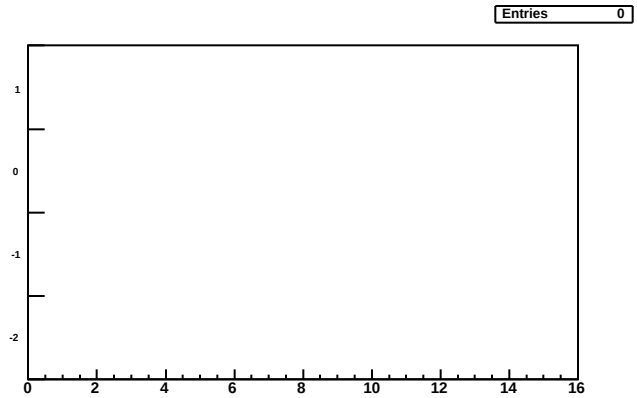


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)