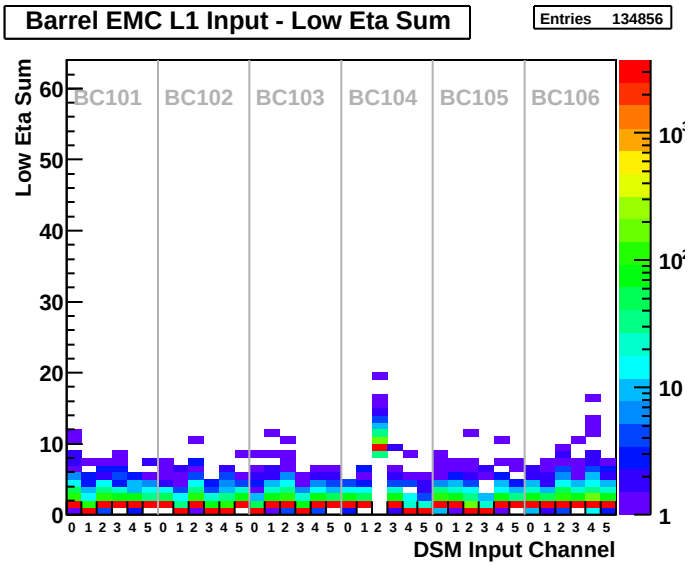
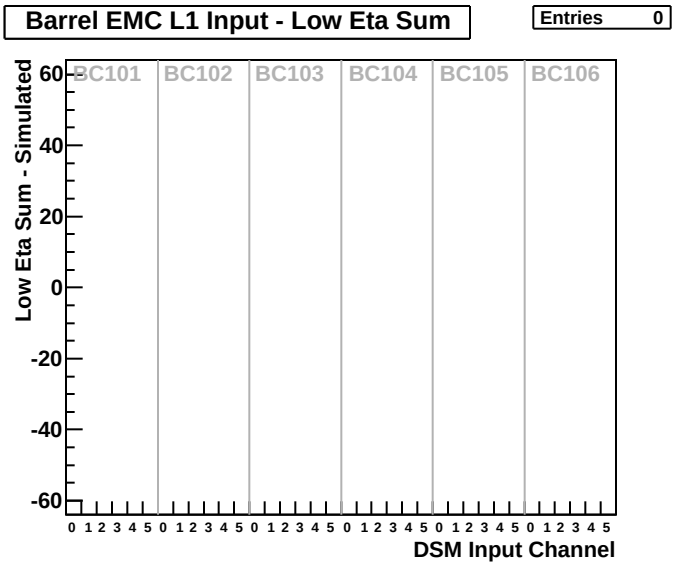
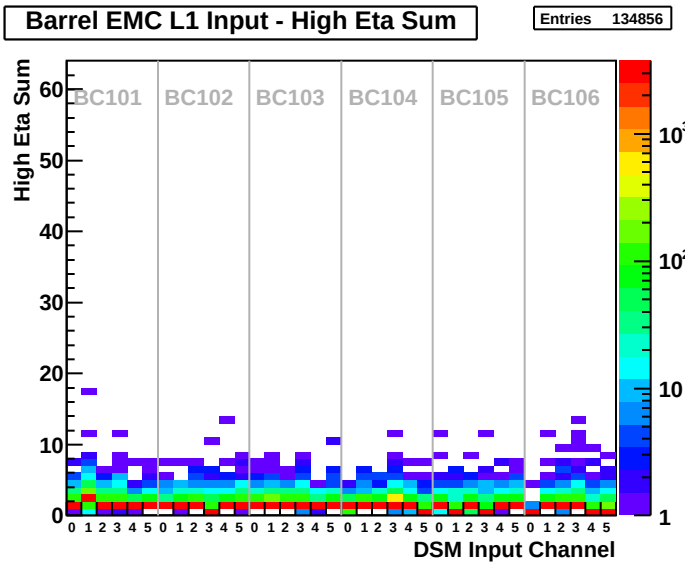
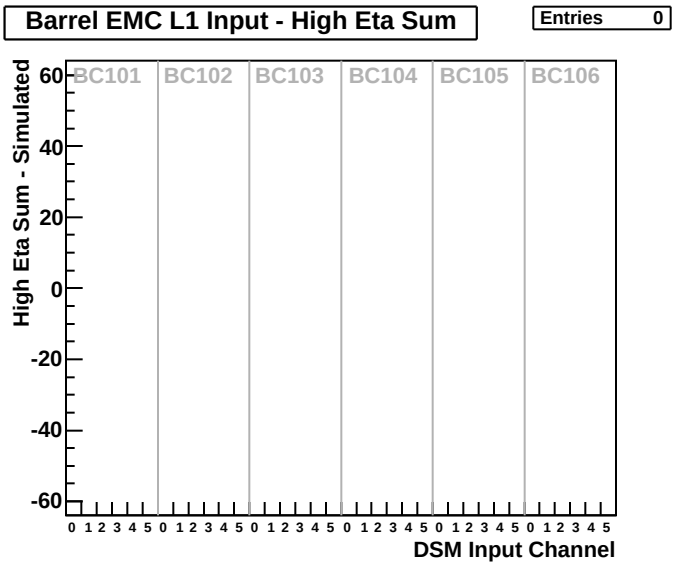
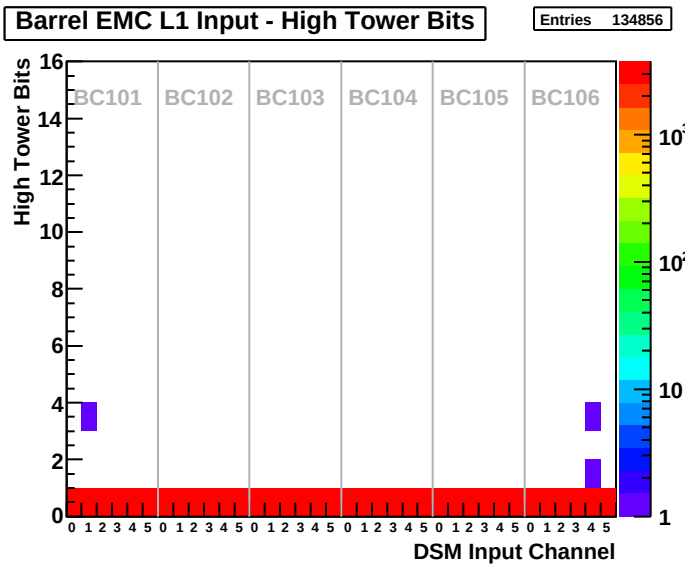
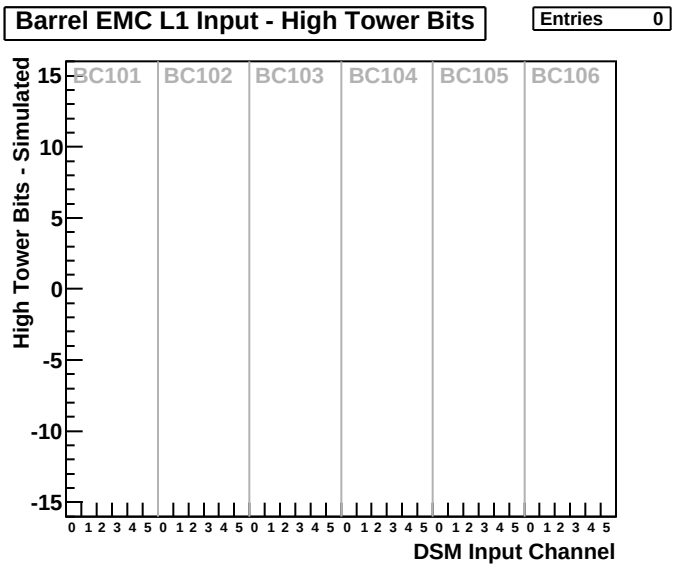

Barrel EMC L1 Input - Low Eta Sum
Entries 134856
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

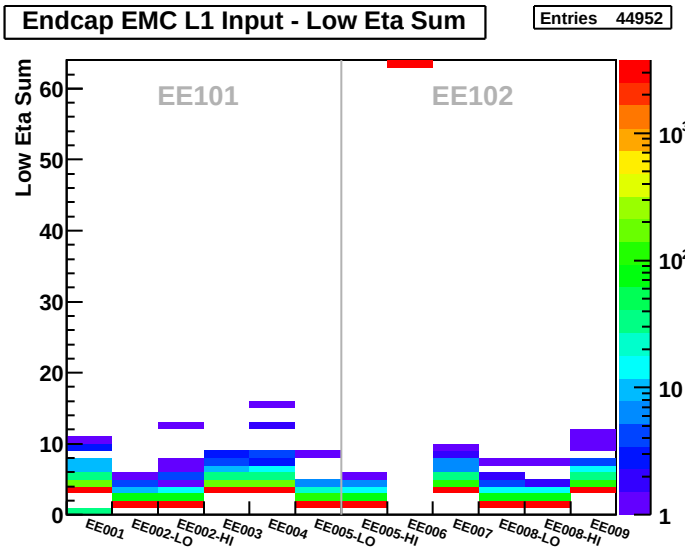

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

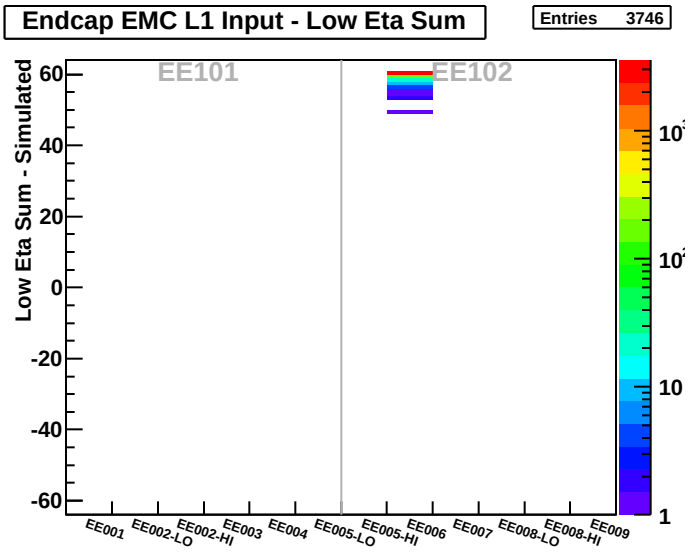

Barrel EMC L1 Input - High Eta Sum
Entries 134856
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

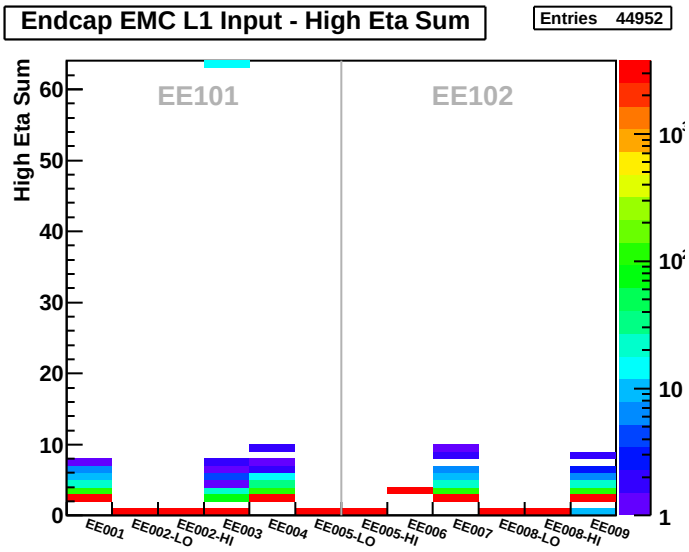

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

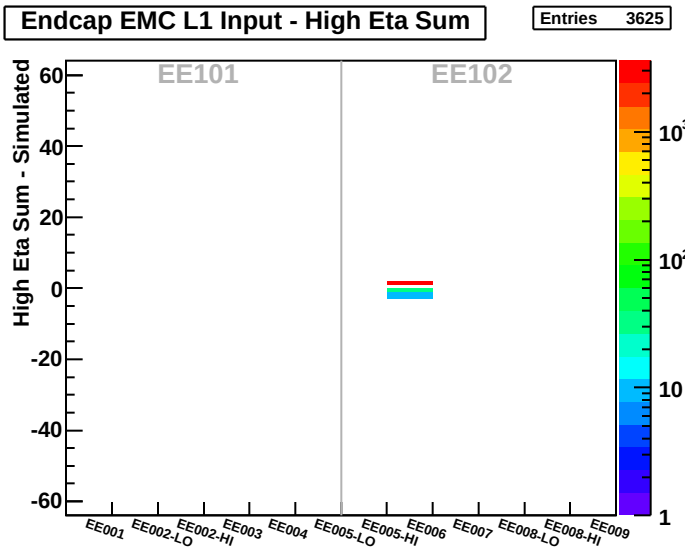

Barrel EMC L1 Input - High Tower Bits
Entries 134856
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

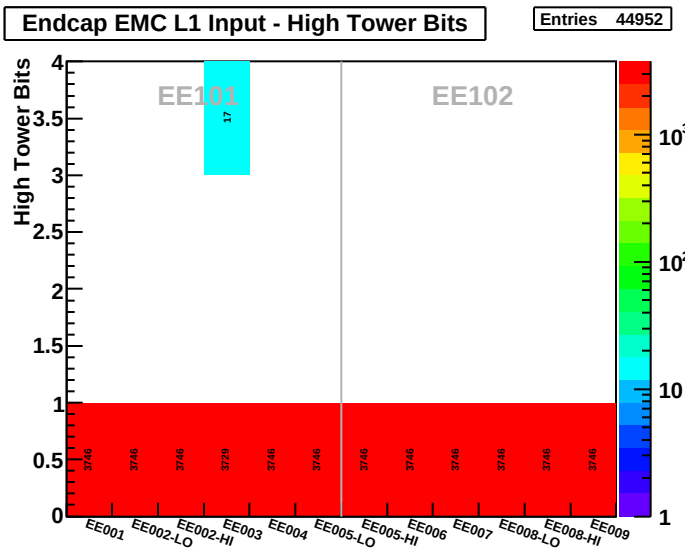

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

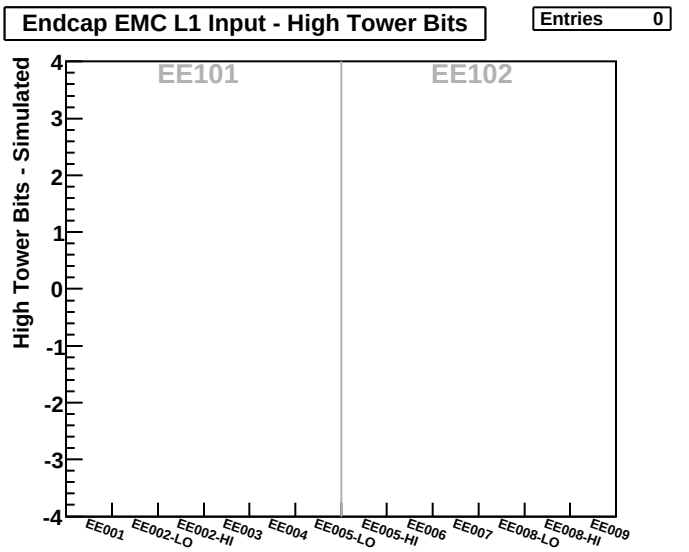

Endcap EMC L1 Input - Low Eta Sum
Entries 44952
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

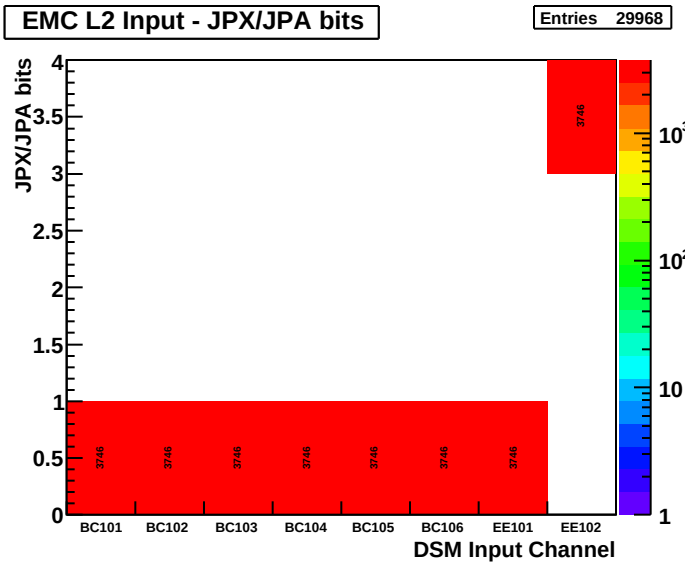

Endcap EMC L1 Input - Low Eta Sum
Entries 3746
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

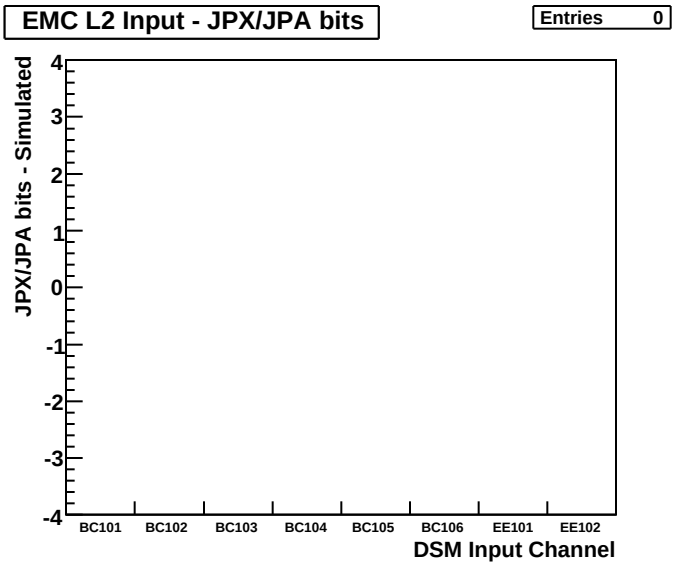

Endcap EMC L1 Input - High Eta Sum
Entries 44952
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

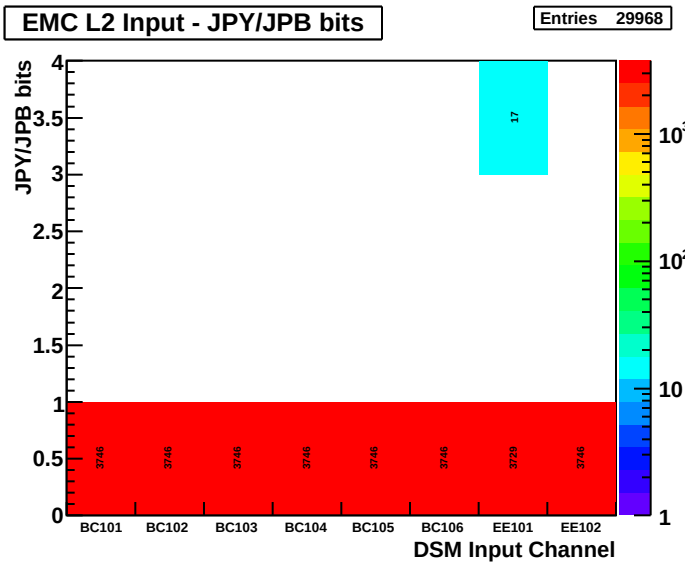

Endcap EMC L1 Input - High Eta Sum
Entries 3625
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

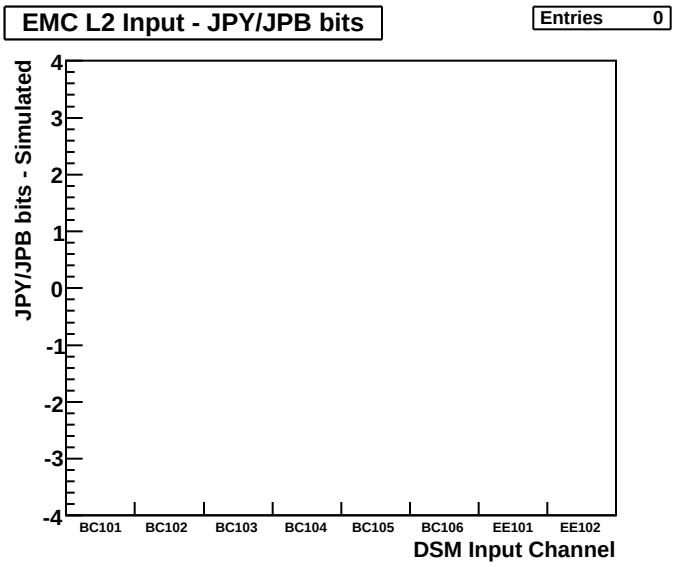

Endcap EMC L1 Input - High Tower Bits
Entries 44952
High Tower Bits
EE101
EE102
17
3746 3746 3746 3729 3746 3746 3746 3746 3746 3746 3746 3746
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

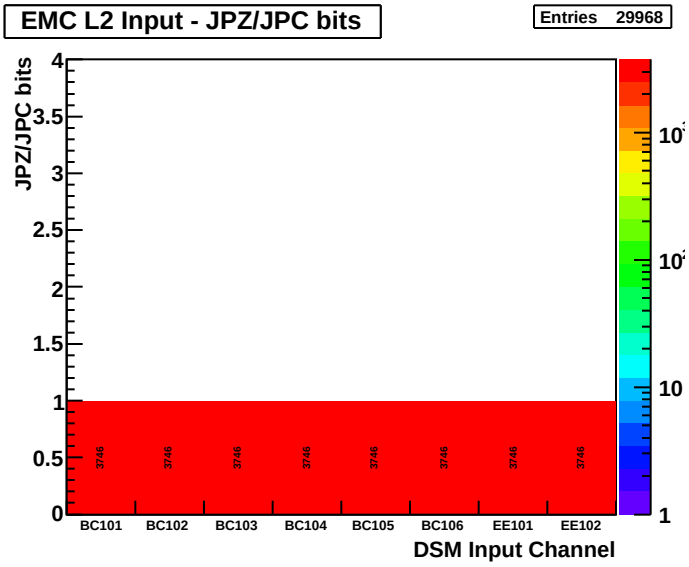

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

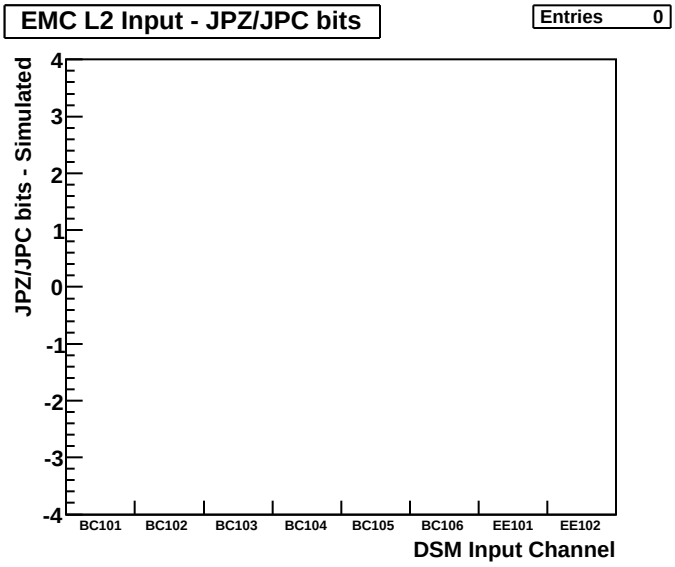

EMC L2 Input - JPX/JPA bits
Entries 29968
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3746

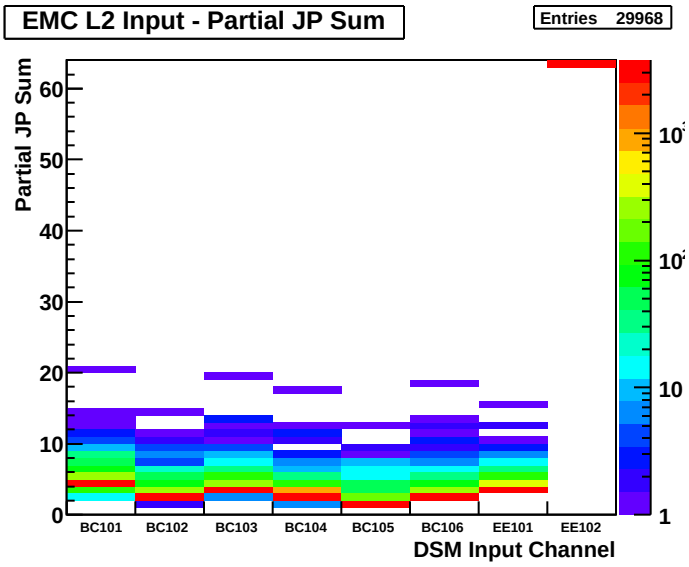

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

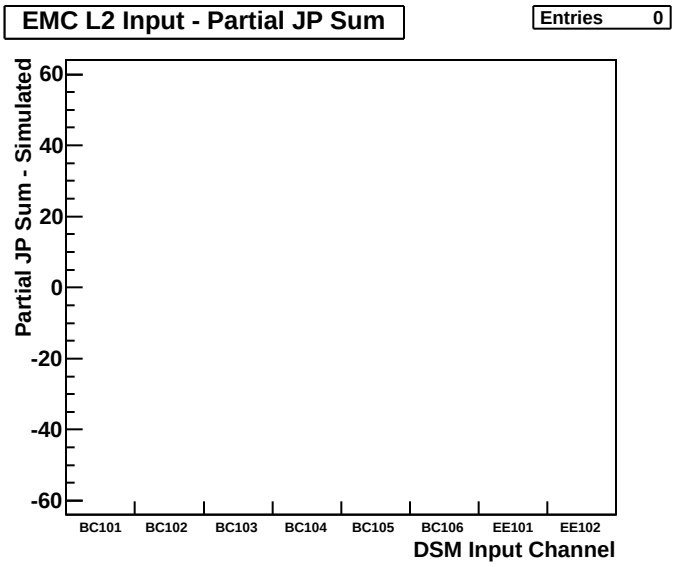

EMC L2 Input - JPY/JPB bits
Entries 29968
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3746 3746 3746 3746 3746 3746 3729 3746
17

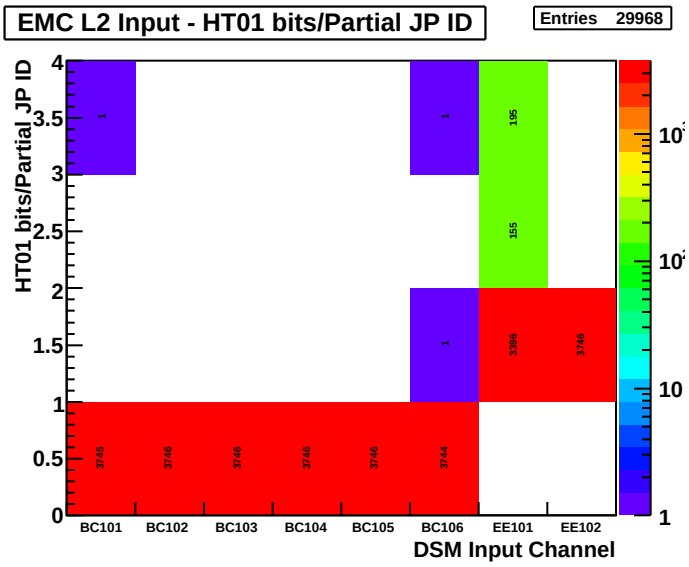

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

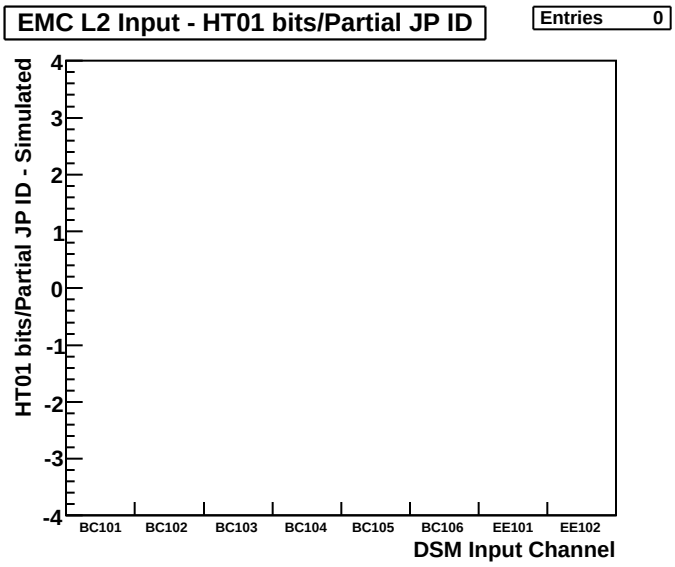

EMC L2 Input - JPZ/JPC bits
Entries 29968
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3746

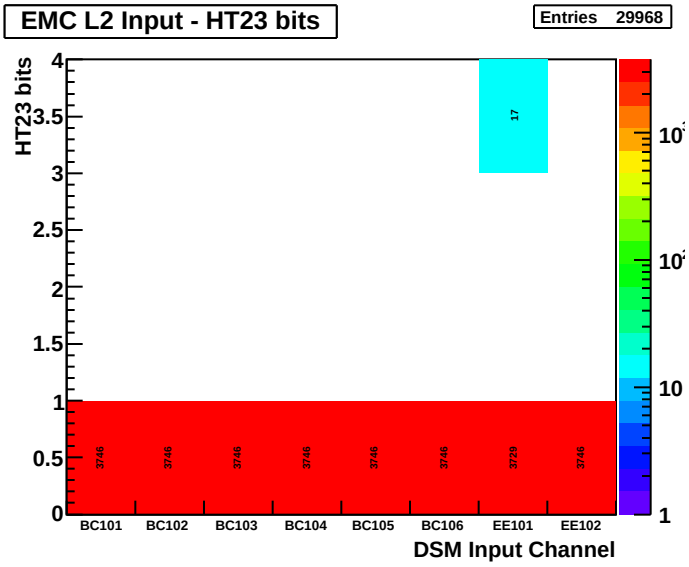

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

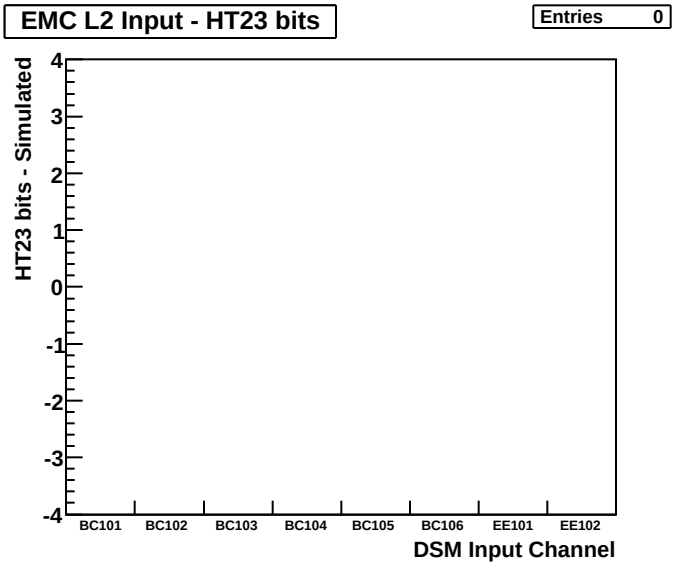

EMC L2 Input - Partial JP Sum
Entries 29968
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

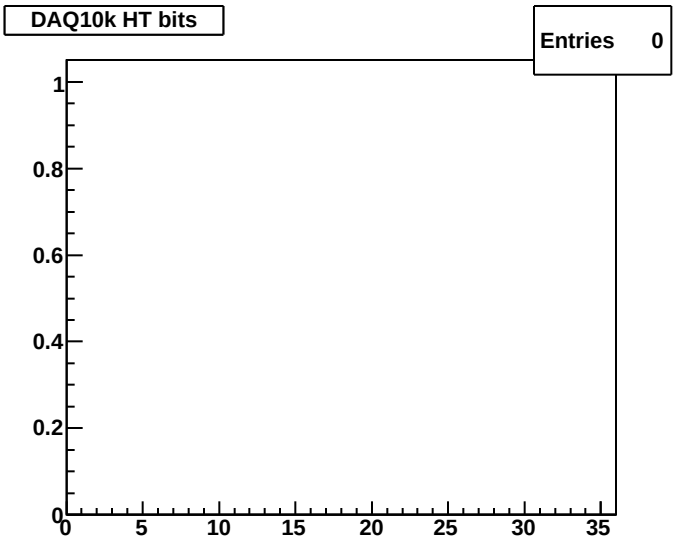

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

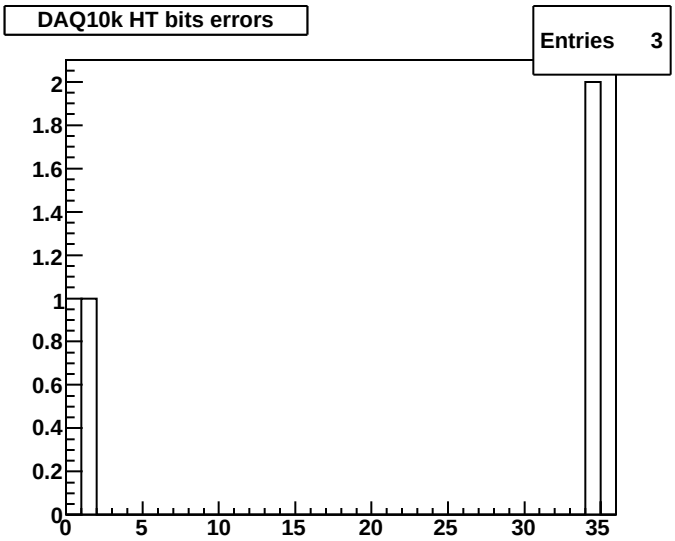

EMC L2 Input - HT01 bits/Partial JP ID
Entries 29968
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

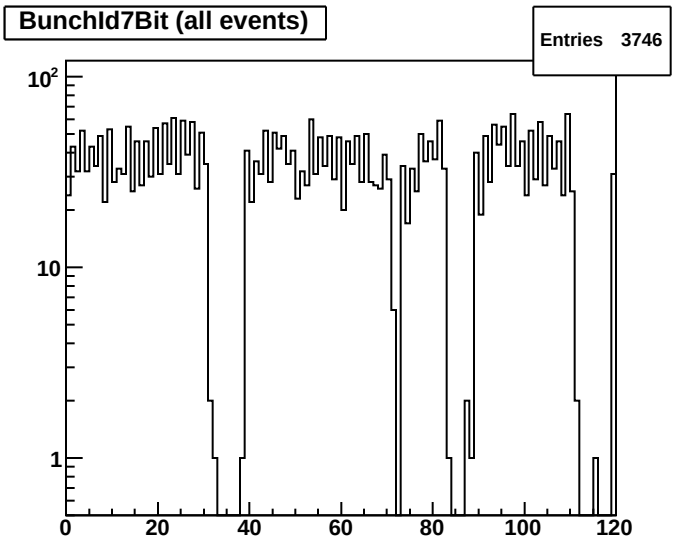

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

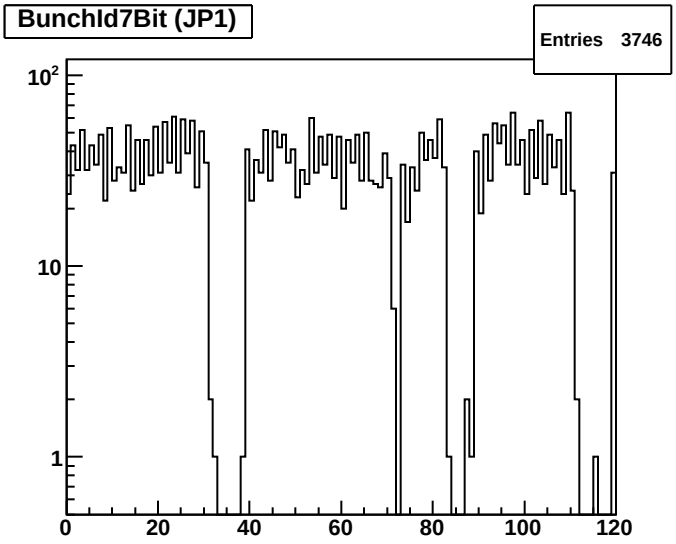

EMC L2 Input - HT23 bits
Entries 29968
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

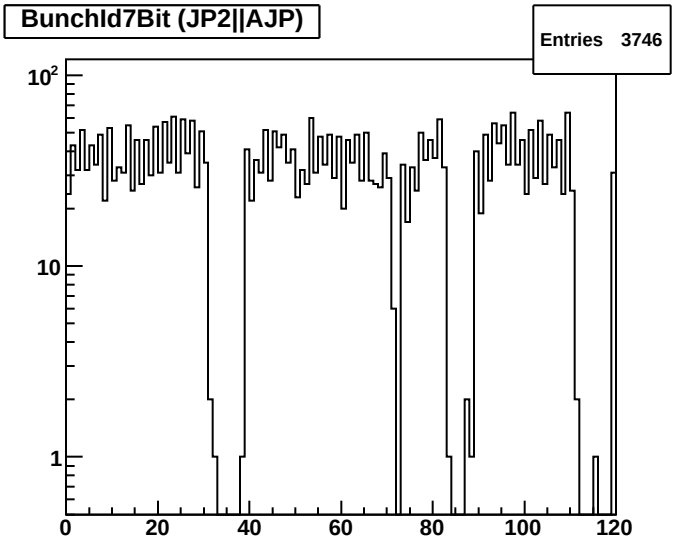

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

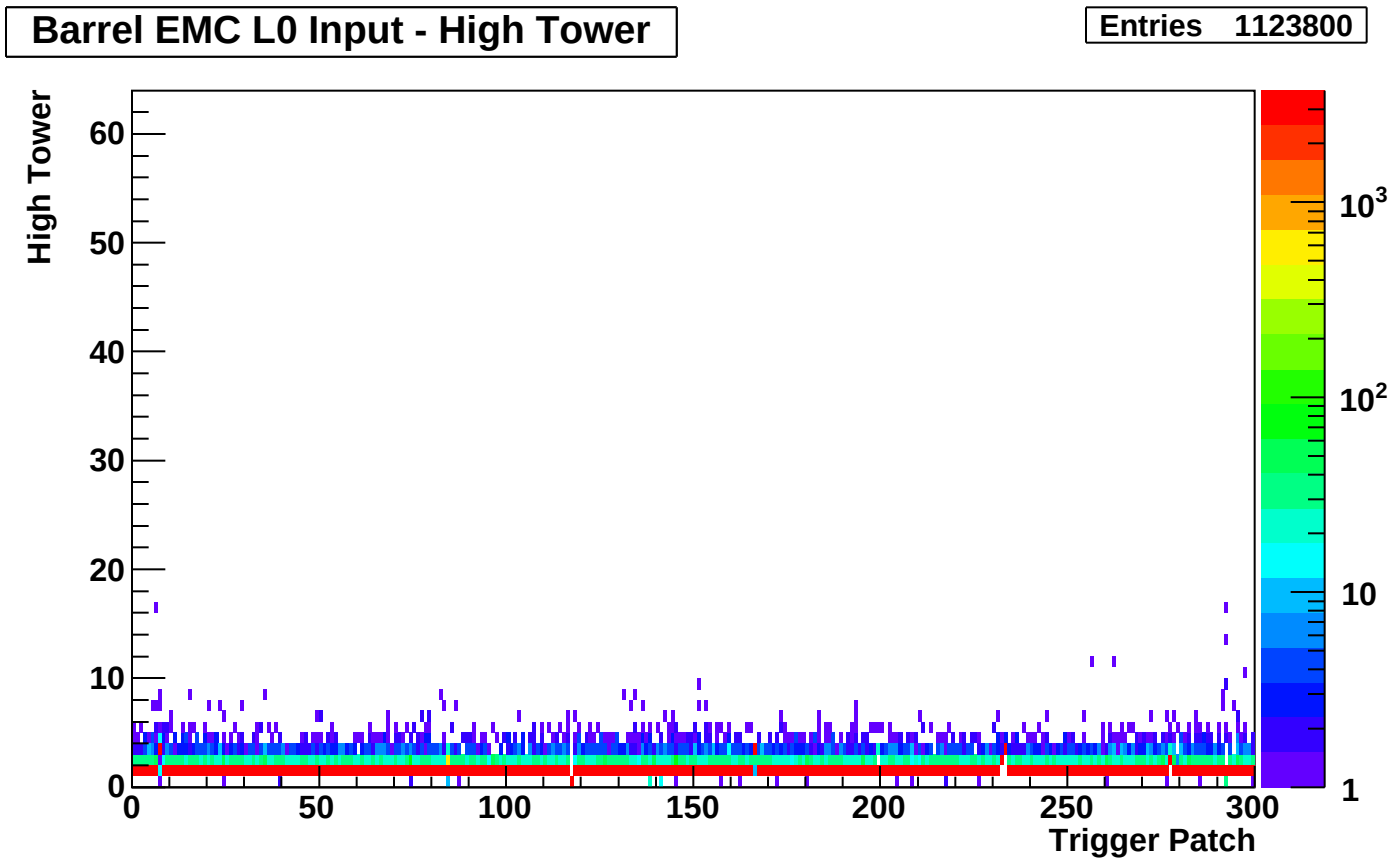

DAQ10k HT bits
Entries 0

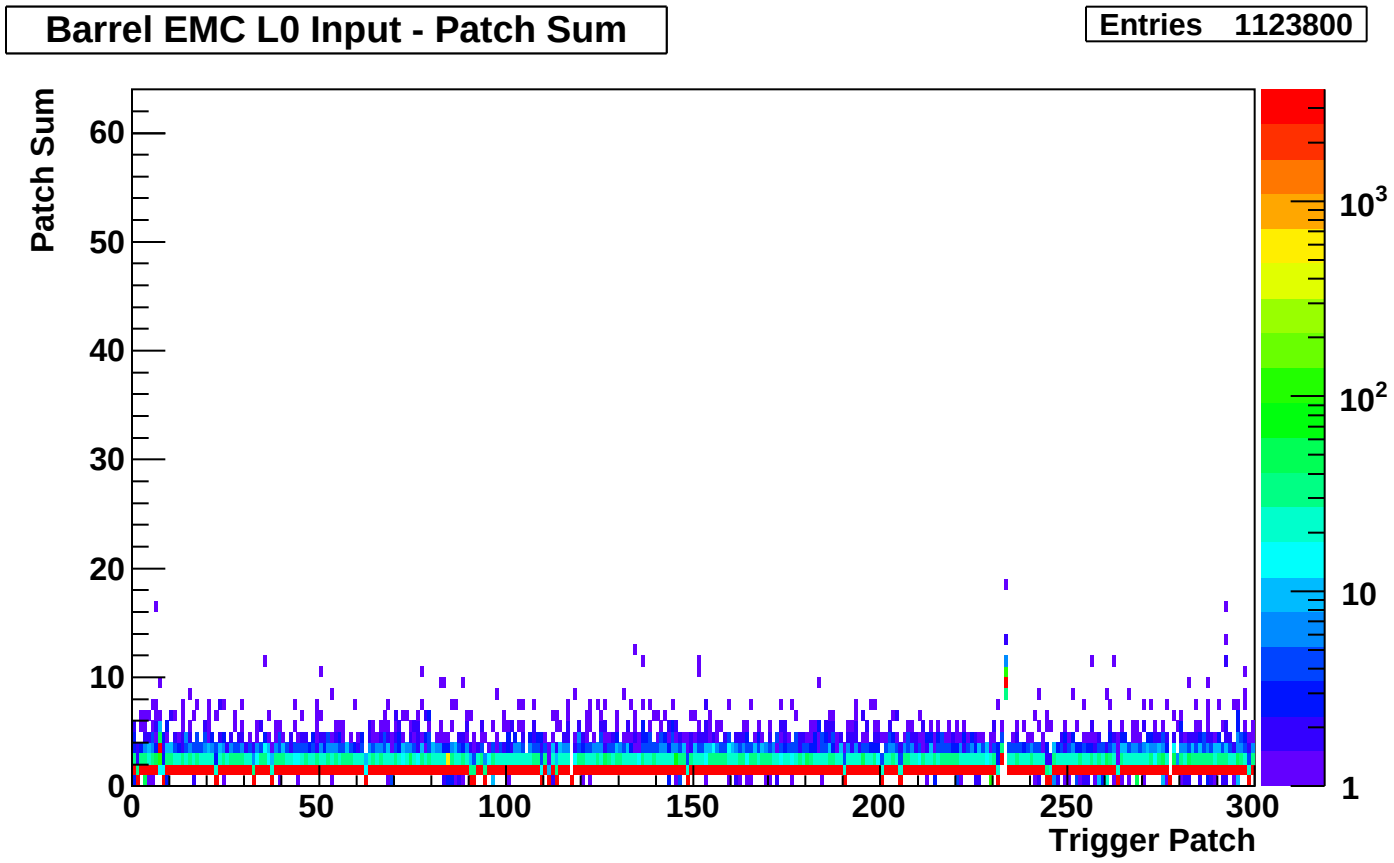

DAQ10k HT bits errors
Entries 3

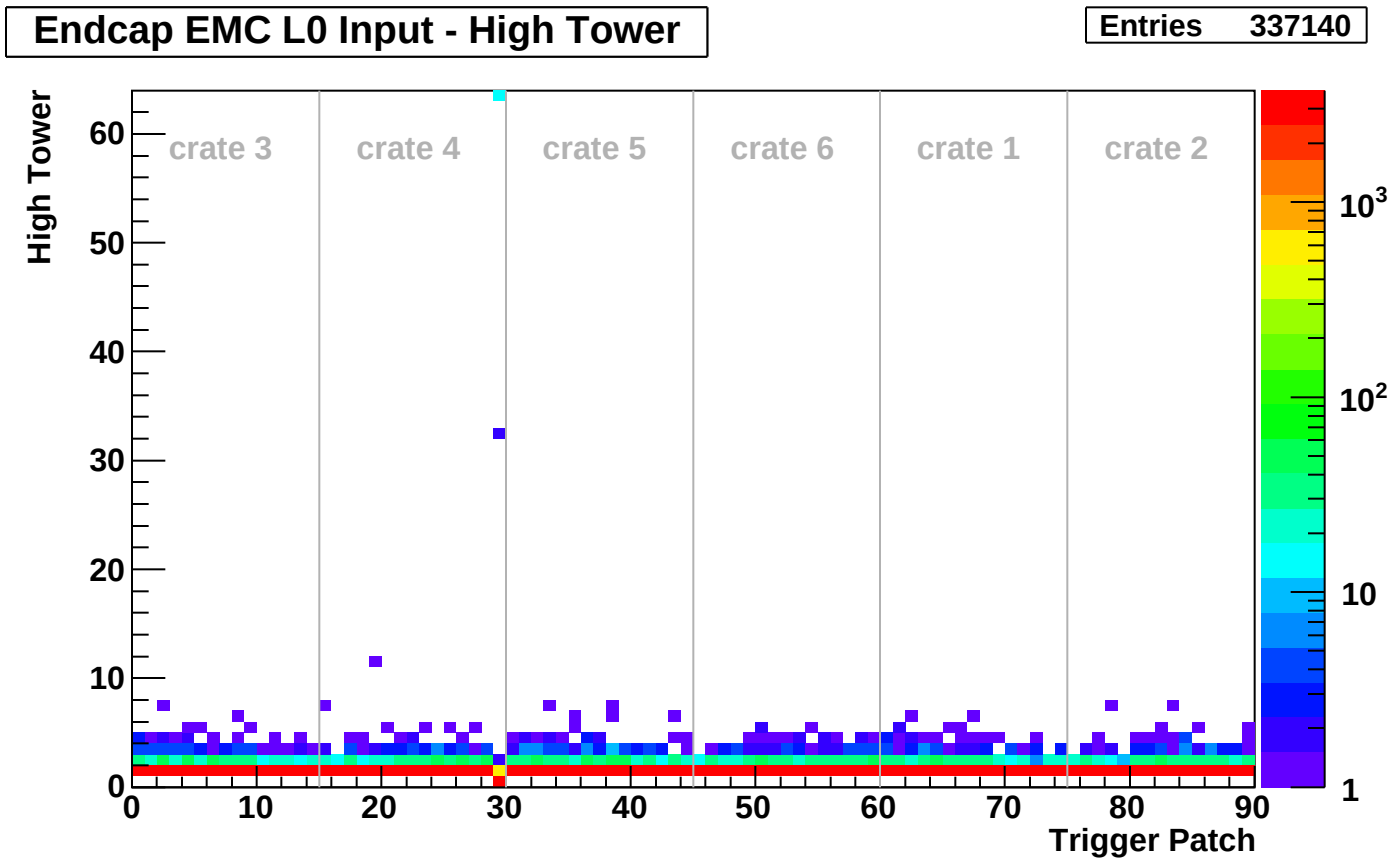

BunchId7Bit (all events)
Entries 3746

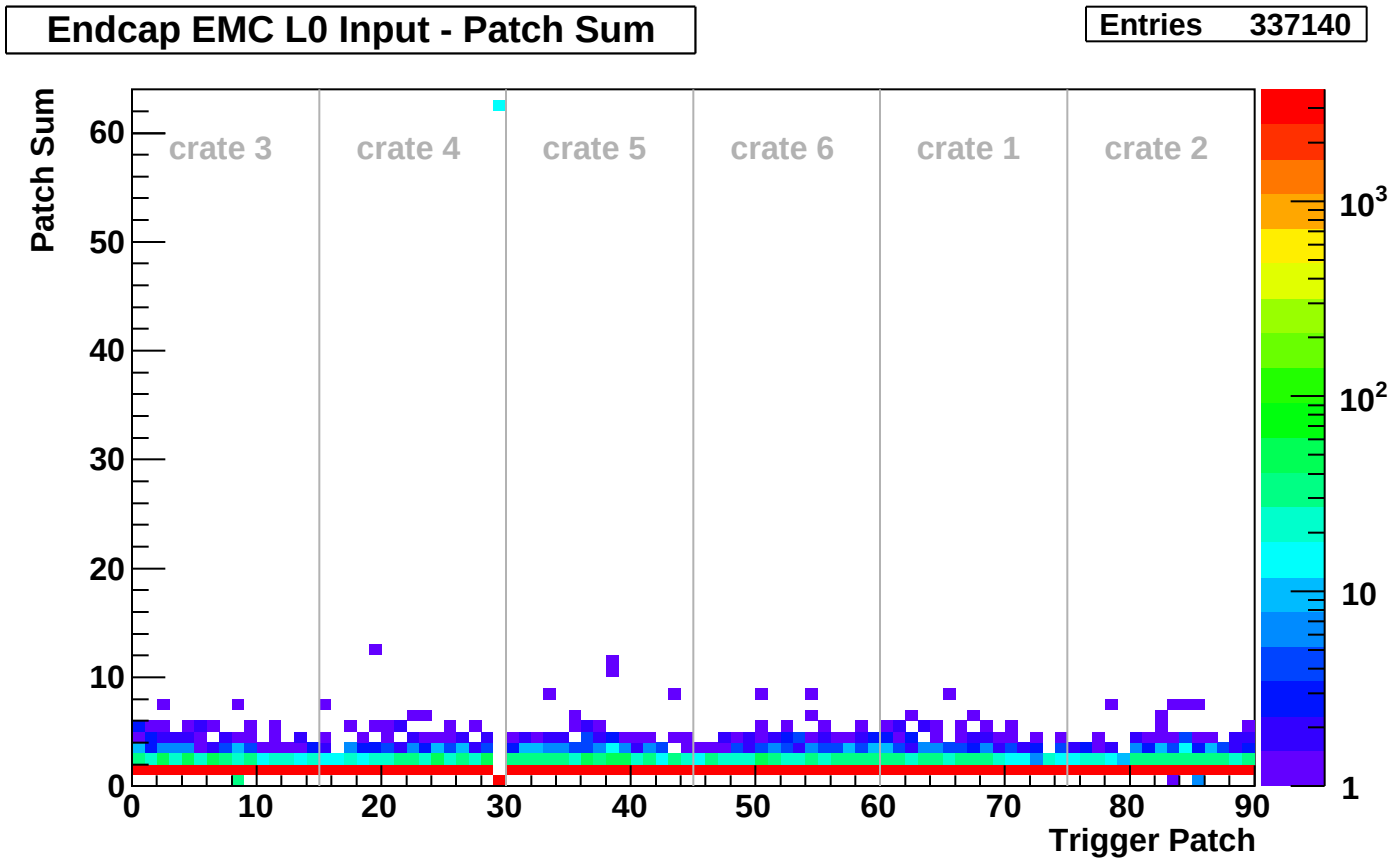

BunchId7Bit (JP1)
Entries 3746

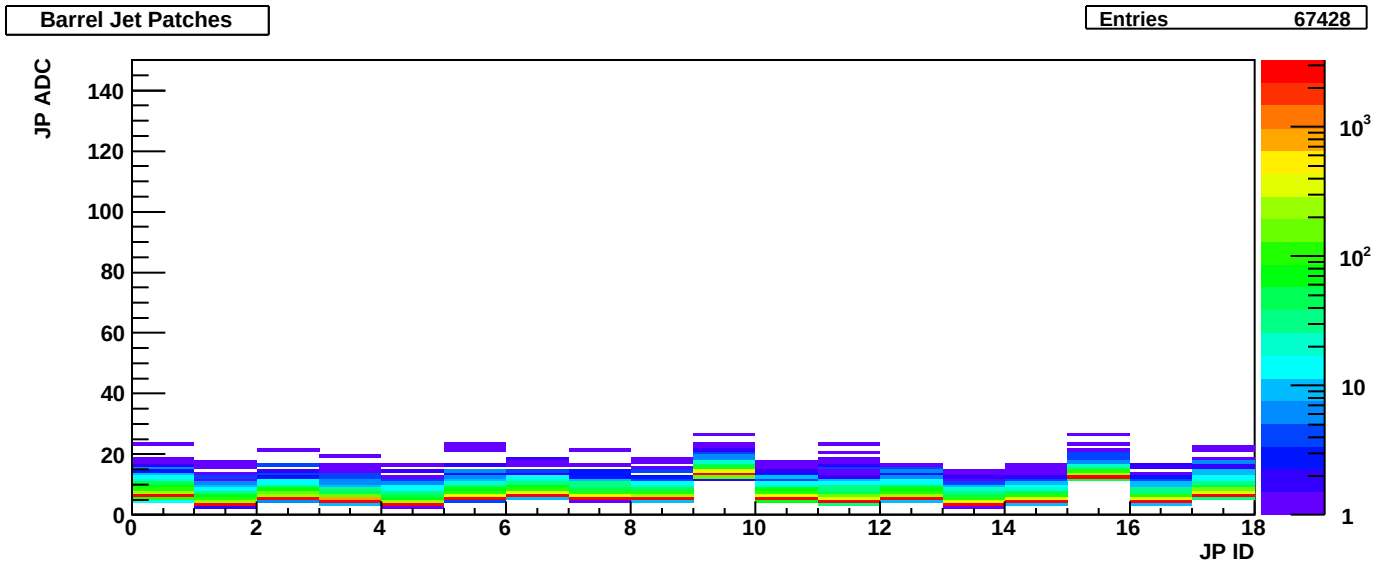

BunchId7Bit (JP2||AJP)
Entries 3746

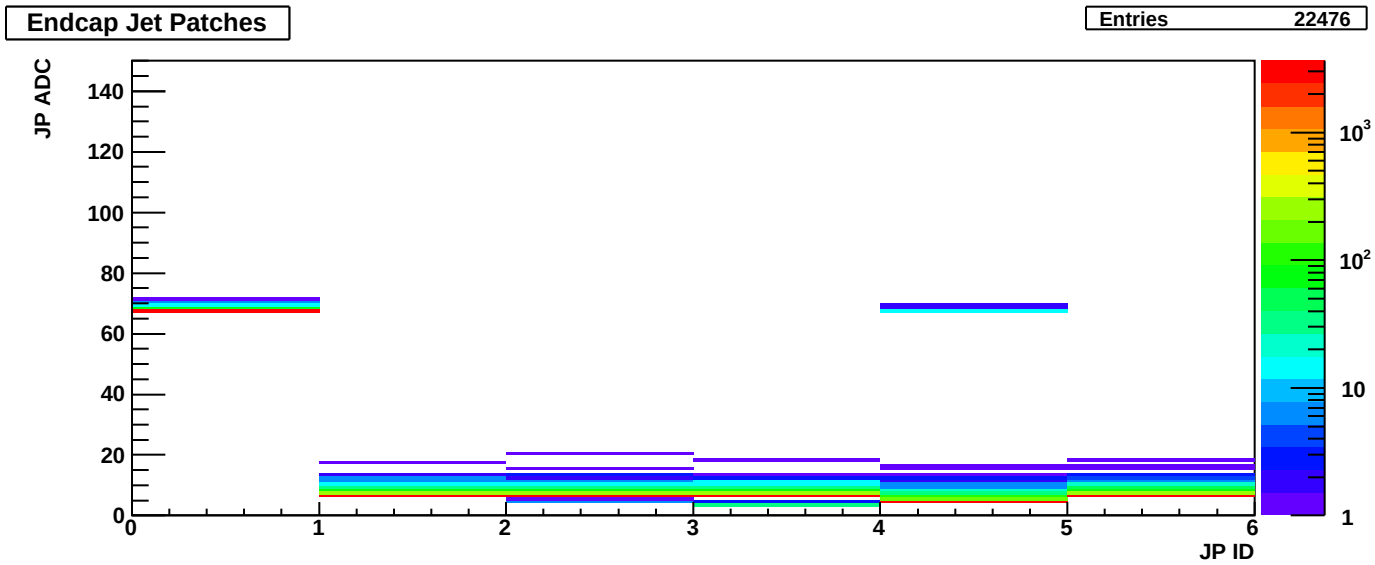

Barrel EMC L0 Input - High Tower
Entries 1123800
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

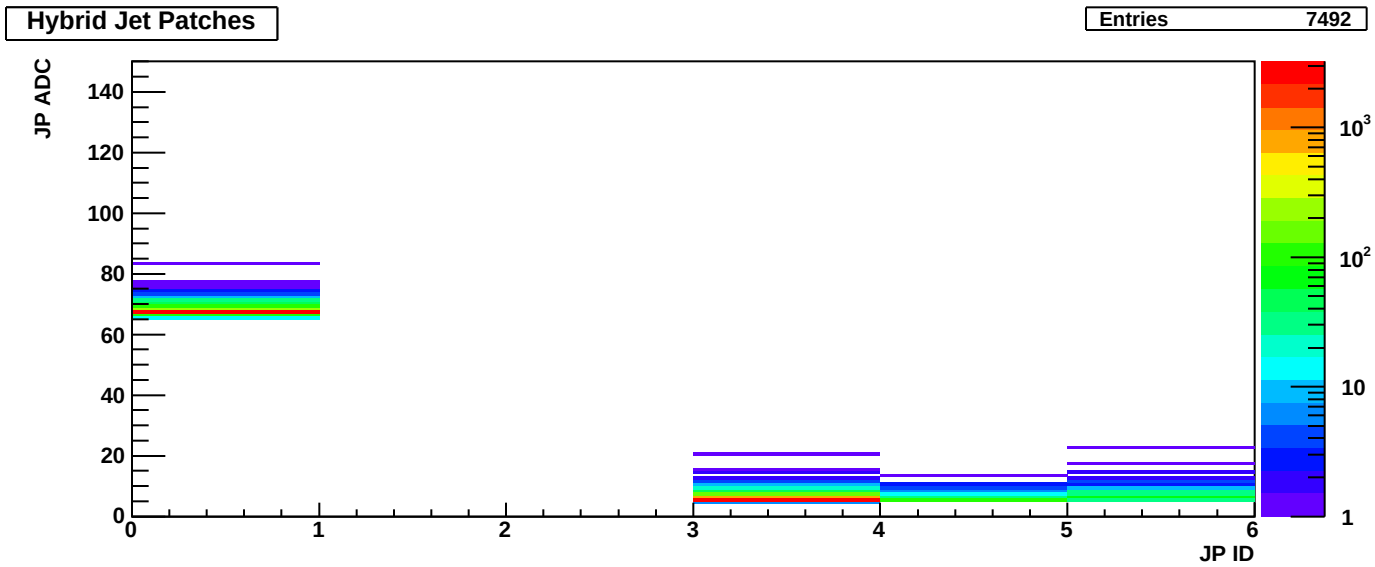

Barrel EMC L0 Input - Patch Sum
Entries 1123800
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

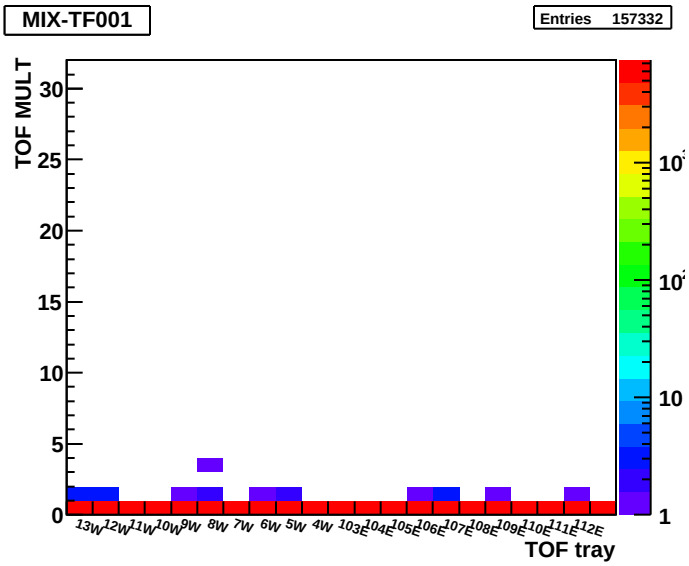

Endcap EMC L0 Input - High Tower
Entries 337140
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

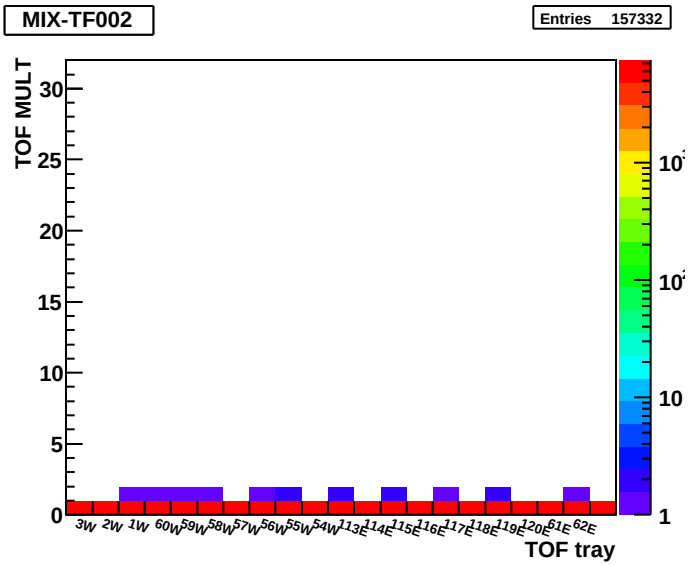

Endcap EMC L0 Input - Patch Sum
Entries 337140
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

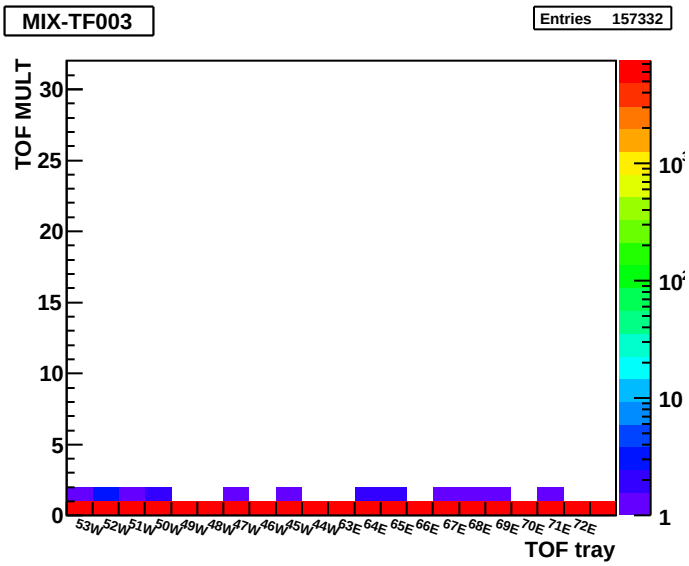

Barrel Jet Patches
Entries 67428
JP ADC
JP ID

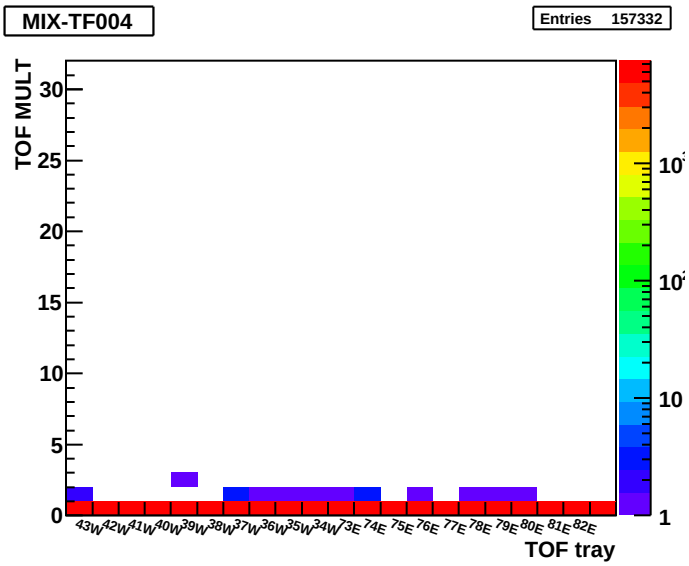

Endcap Jet Patches
Entries 22476
JP ADC
JP ID

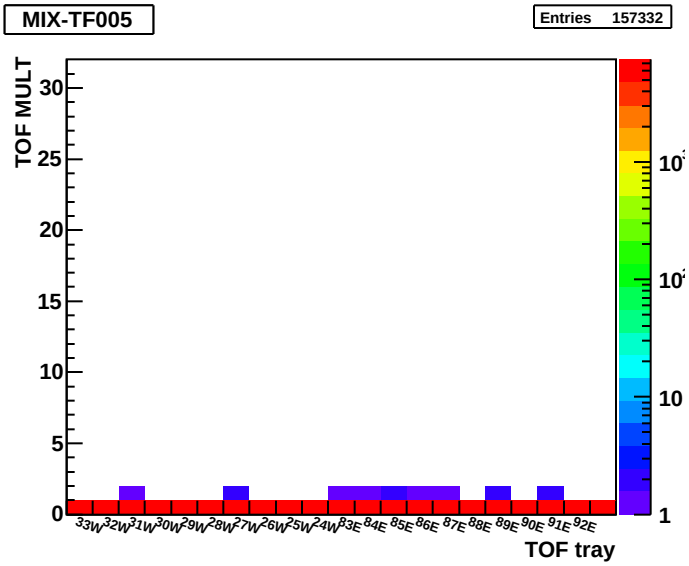

Hybrid Jet Patches
Entries 7492
JP ADC
JP ID

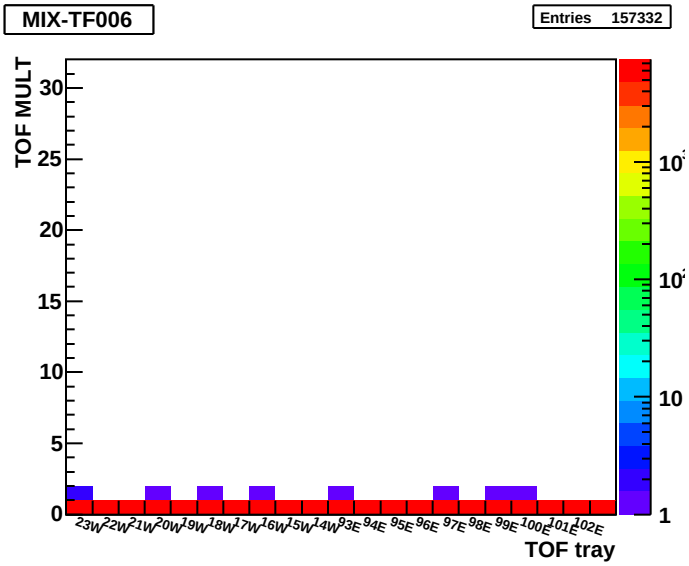

MIX-TF001
Entries 157332
TOF MULT
TOF tray

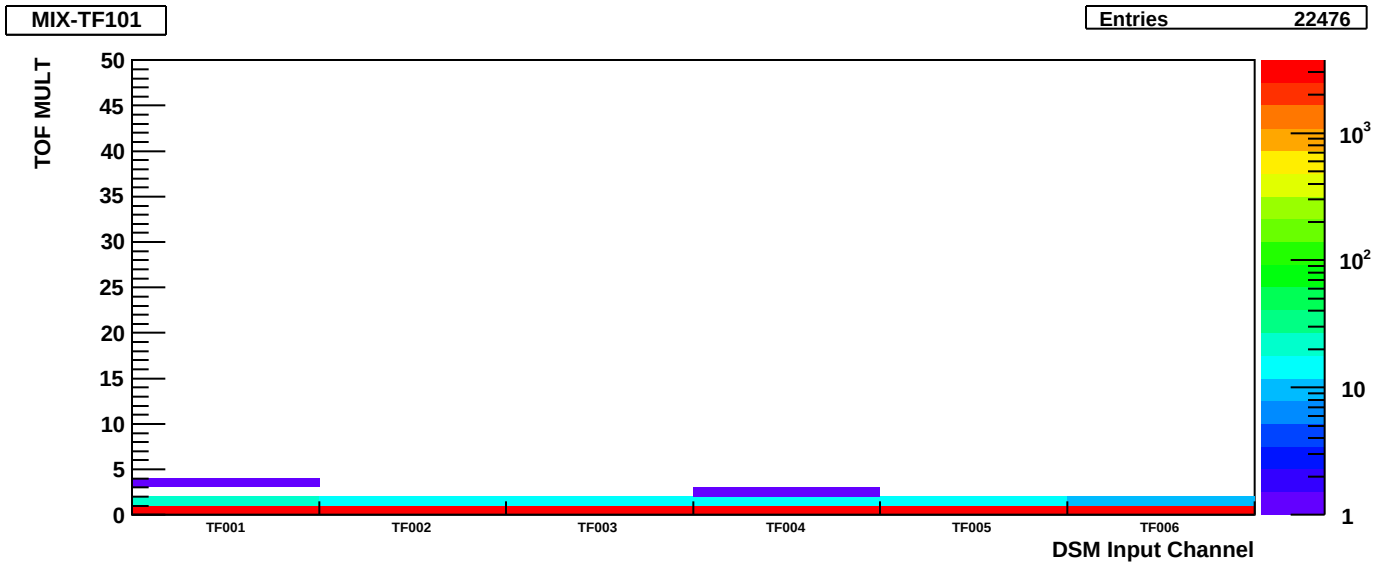

MIX-TF002
Entries 157332
TOF MULT
TOF tray

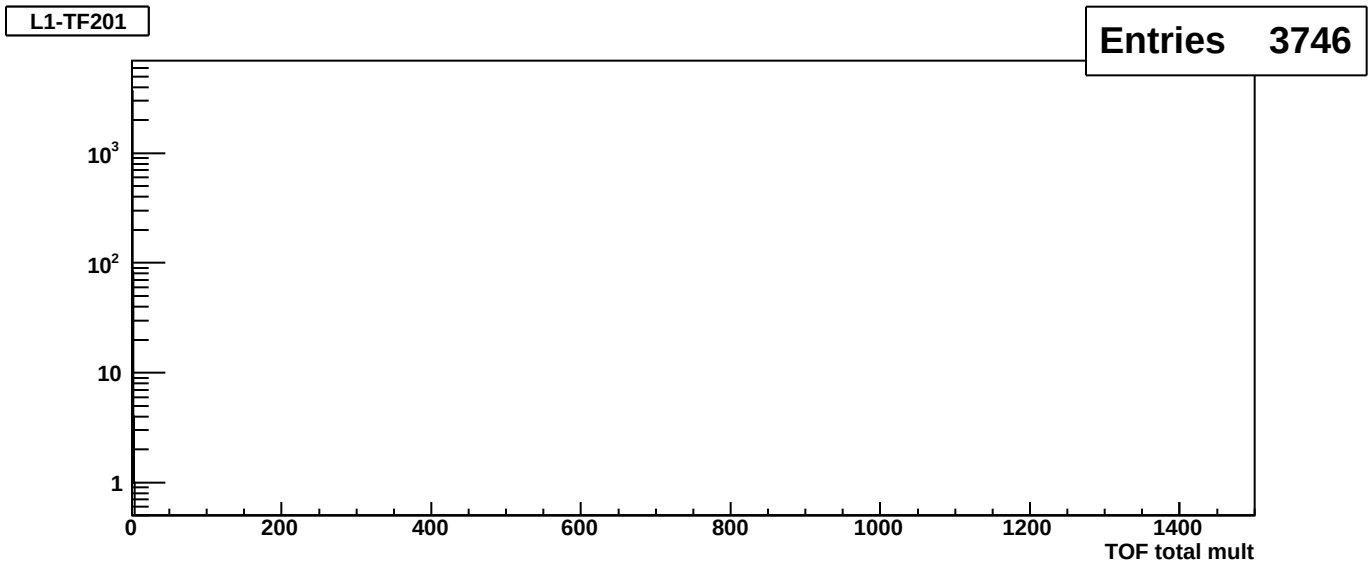

MIX-TF003
Entries 157332
TOF MULT
TOF tray

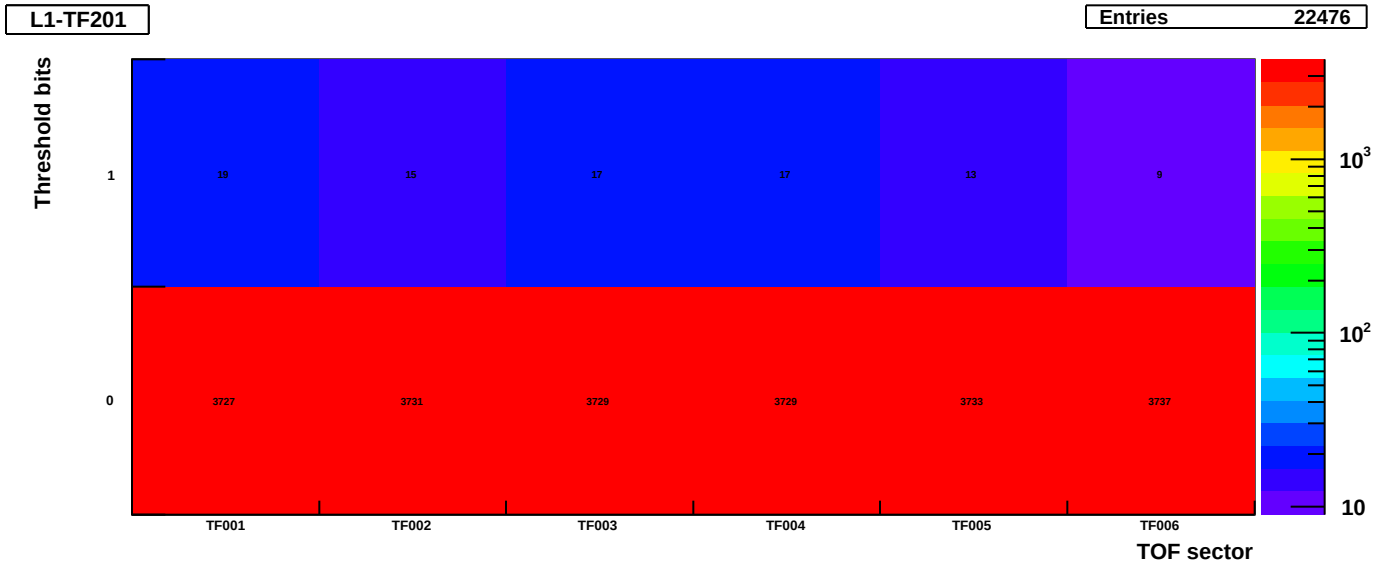

MIX-TF004
Entries 157332
TOF MULT
TOF tray

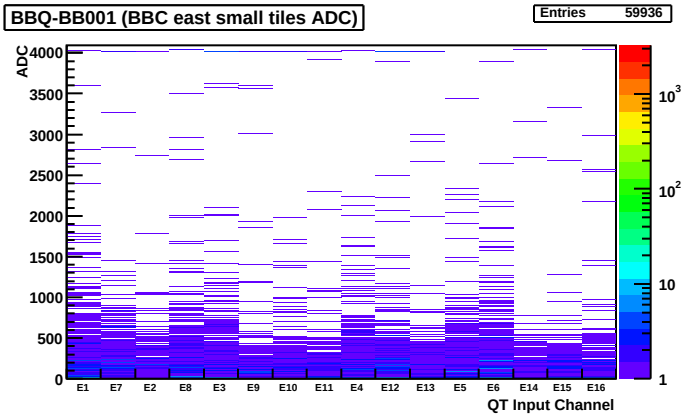

MIX-TF005
Entries 157332
TOF MULT
TOF tray

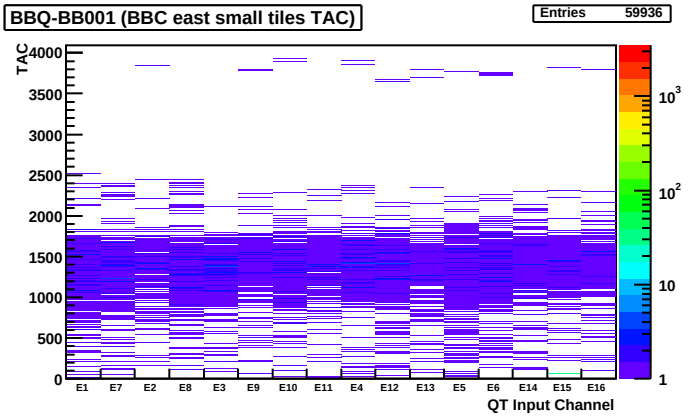

MIX-TF006
Entries 157332
TOF MULT
TOF tray

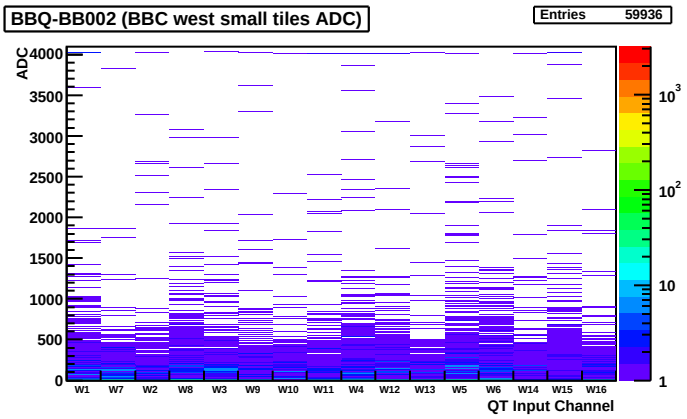

MIX-TF101
Entries 22476
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10
1

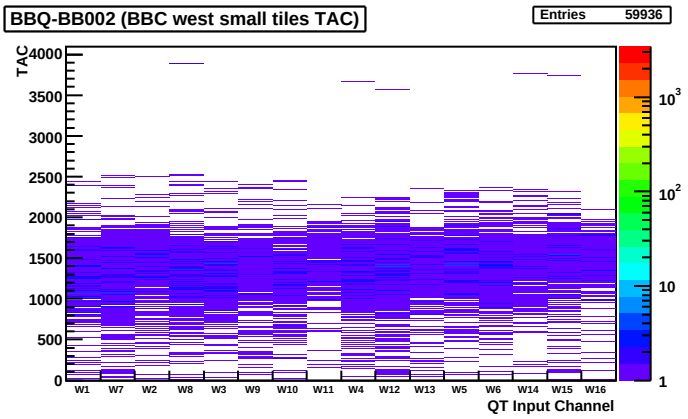

L1-TF201
Entries 3746
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

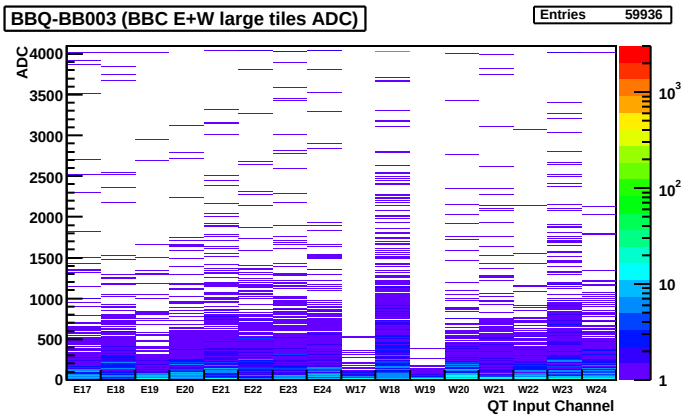

L1-TF201
Entries 22476
Threshold bits
1
0
19
15
17
17
13
9
3727
3731
3729
3729
3733
3737
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^3
10^2
10

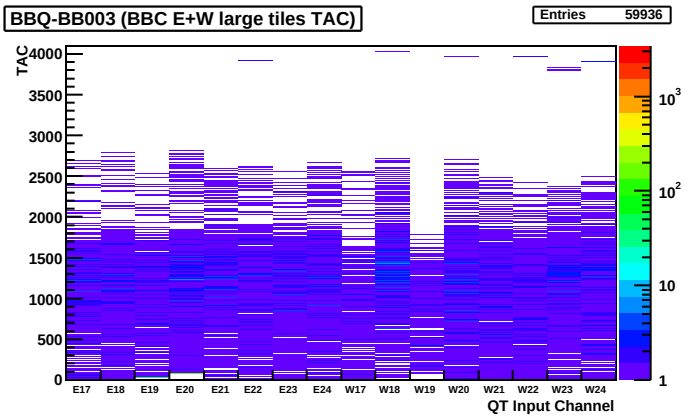

BBQ-BB001 (BBC east small tiles ADC)
Entries 59936
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

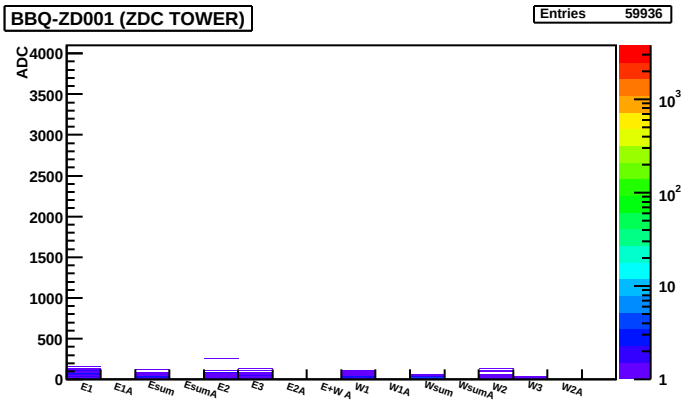

BBQ-BB001 (BBC east small tiles TAC)
Entries 59936
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

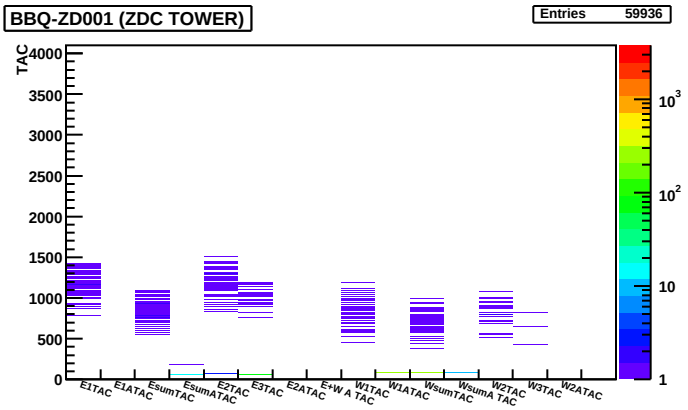

BBQ-BB002 (BBC west small tiles ADC)
Entries 59936
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

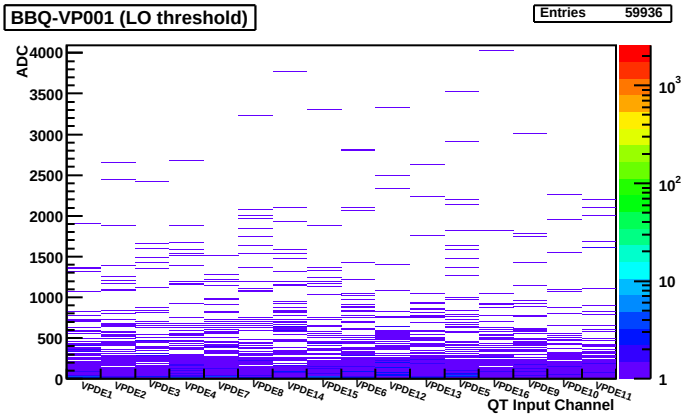

BBQ-BB002 (BBC west small tiles TAC)
Entries 59936
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

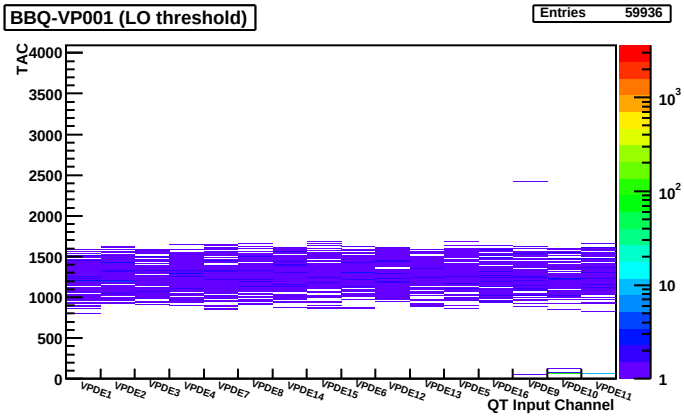

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 59936
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

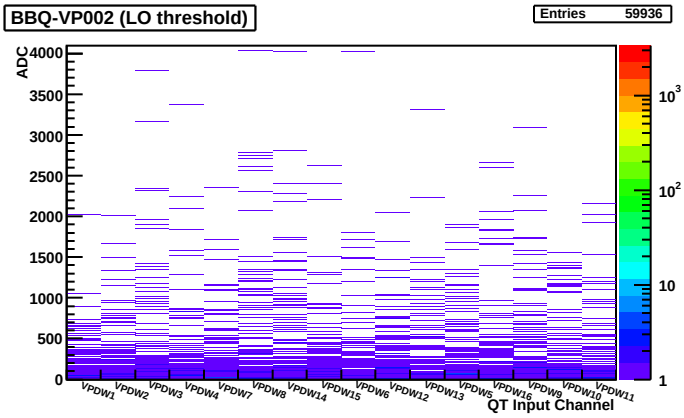

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 59936
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

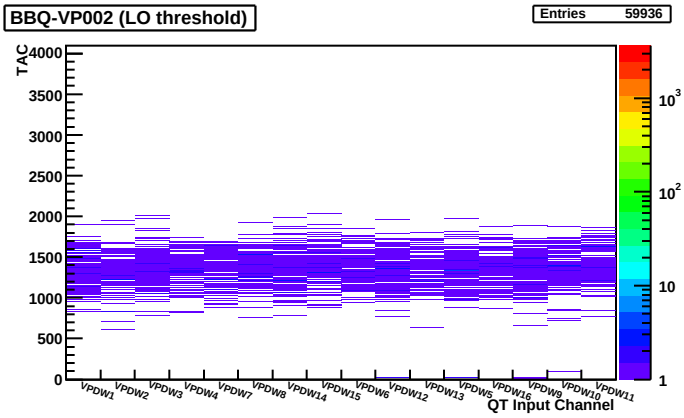

BBQ-ZD001 (ZDC TOWER)
Entries 59936
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

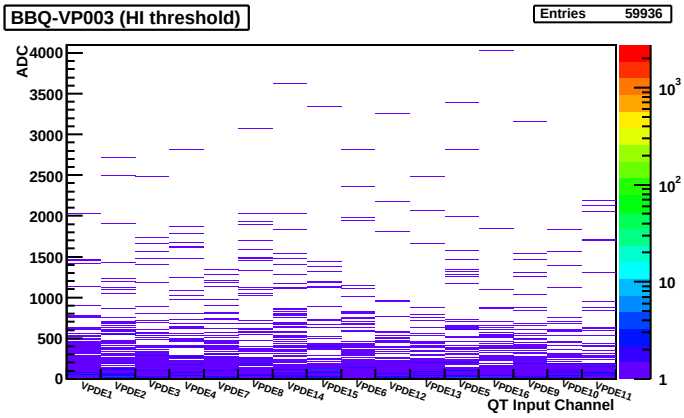

BBQ-ZD001 (ZDC TOWER)
Entries 59936
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

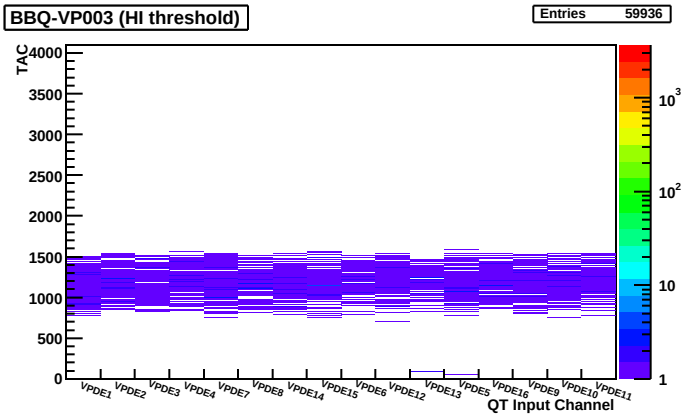

BBQ-VP001 (LO threshold)
Entries 59936
ADC
QT Input Channel

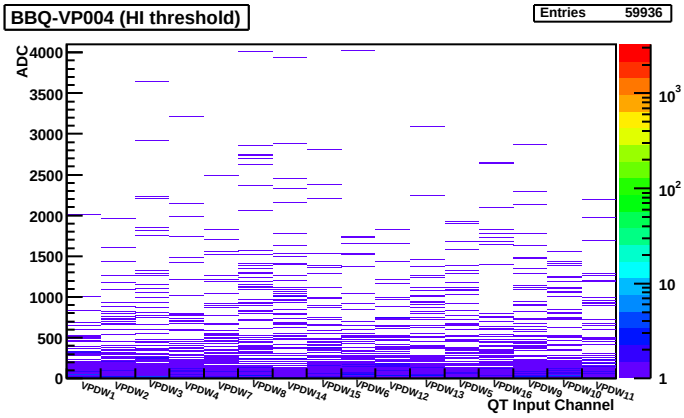

BBQ-VP001 (LO threshold)
Entries 59936
TAC
QT Input Channel

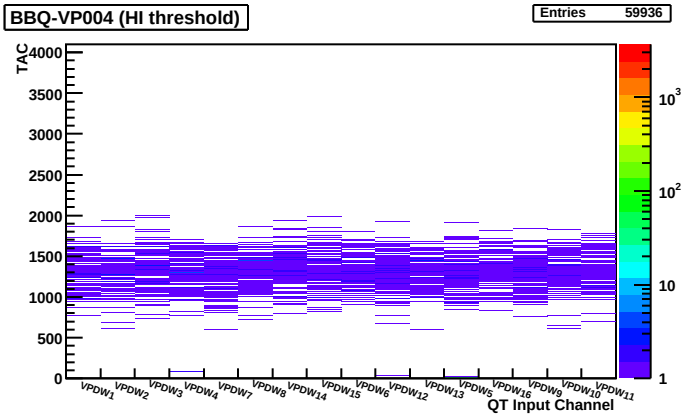

BBQ-VP002 (LO threshold)
Entries 59936
ADC
QT Input Channel

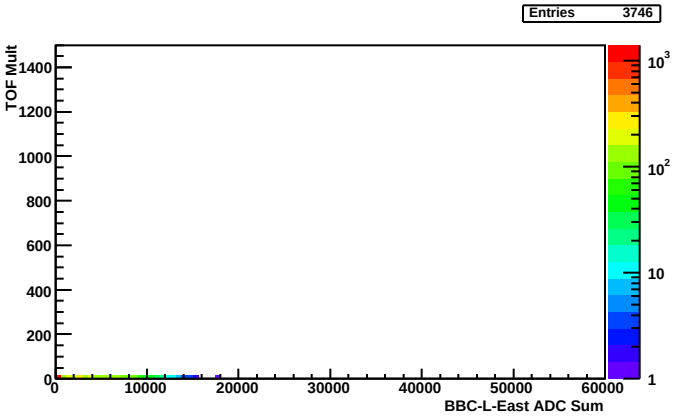

BBQ-VP002 (LO threshold)
Entries 59936
TAC
QT Input Channel

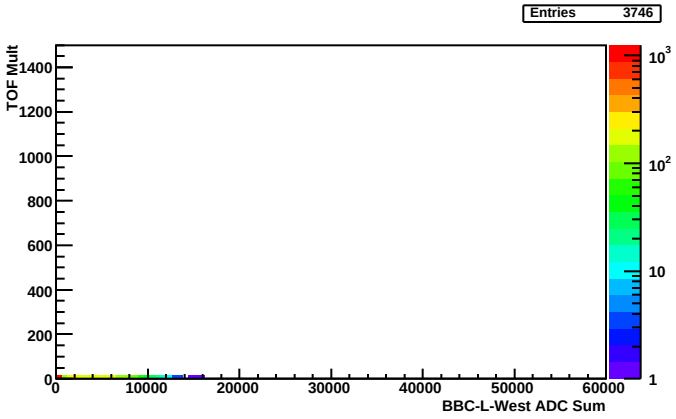

BBQ-VP003 (HI threshold)
Entries 59936
ADC
QT Input Channel

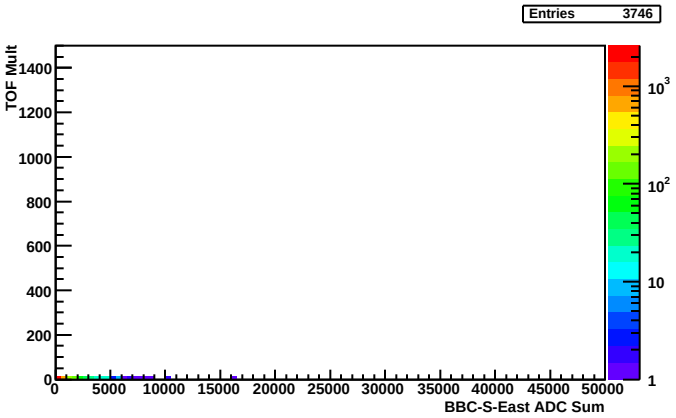

BBQ-VP003 (HI threshold)
Entries 59936
TAC
QT Input Channel

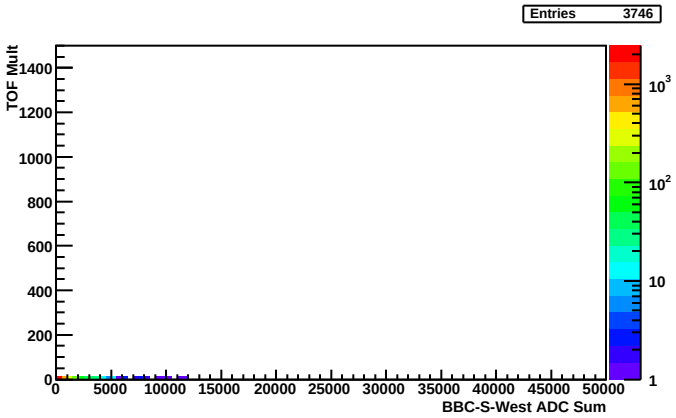

BBQ-VP004 (HI threshold)
Entries 59936
ADC
QT Input Channel

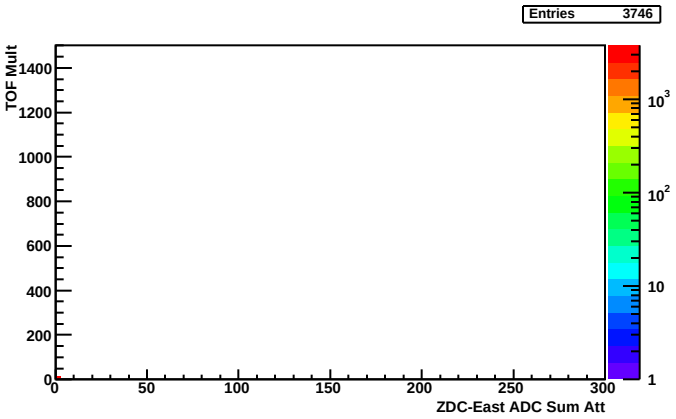

BBQ-VP004 (HI threshold)
Entries 59936
TAC
QT Input Channel

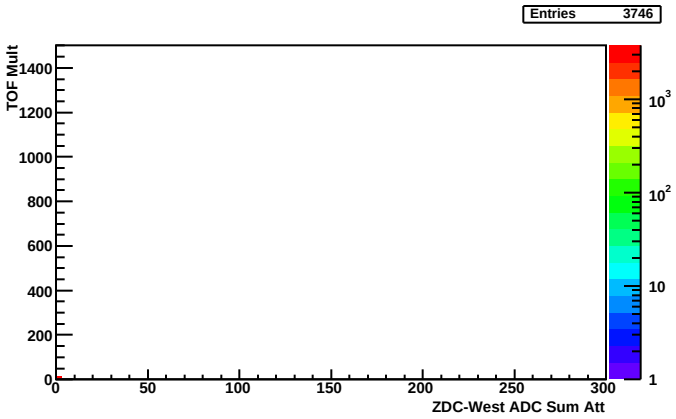

Entries 3746
TOF Mult
BBC-L-East ADC Sum

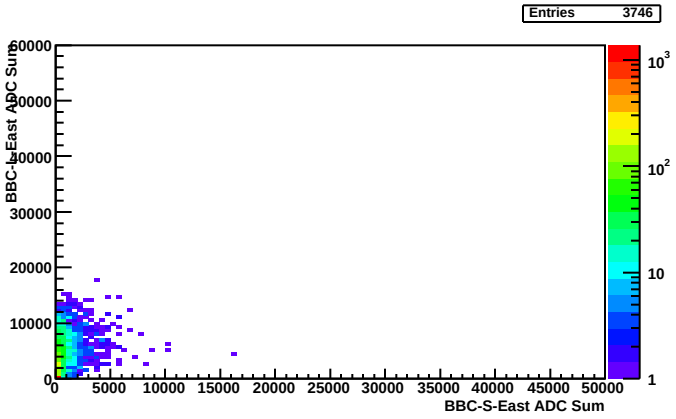

Entries 3746
TOF Mult
BBC-L-West ADC Sum

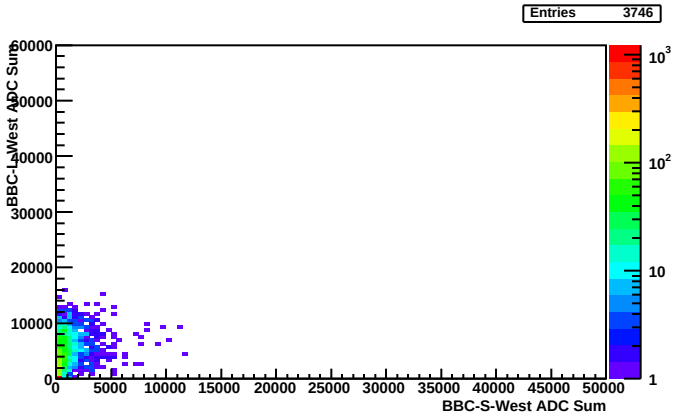

Entries 3746
TOF Mult
BBC-S-East ADC Sum

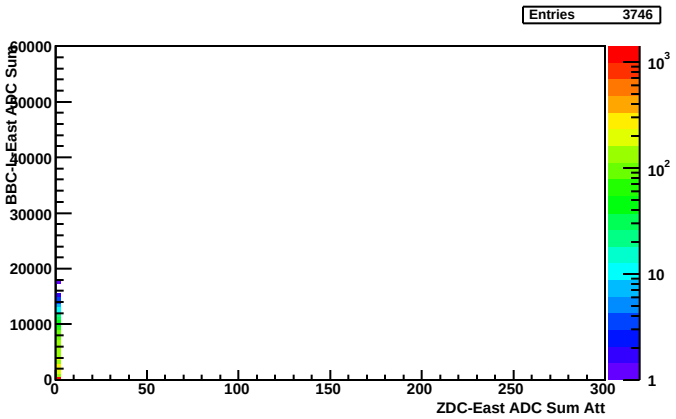

Entries 3746
TOF Mult
BBC-S-West ADC Sum

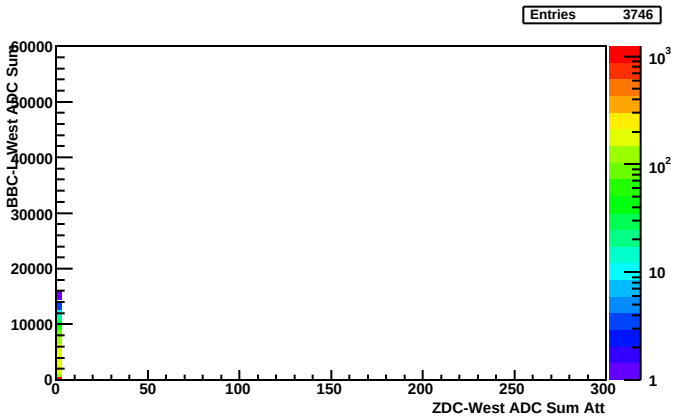

Entries 3746
TOF Mult
ZDC-East ADC Sum Att

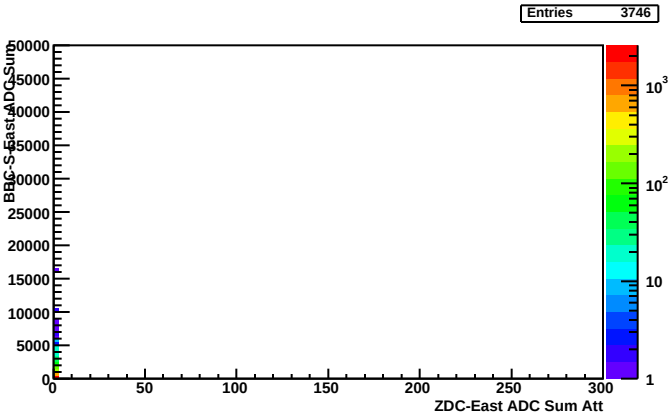

Entries 3746
TOF Mult
ZDC-West ADC Sum Att

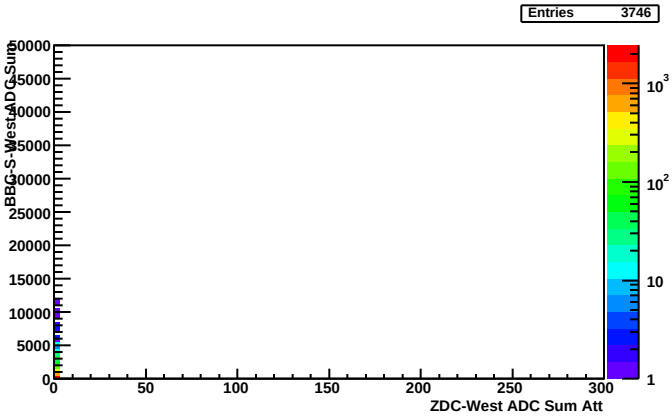

Entries 3746
BBC-L-East ADC Sum
BBC-S-East ADC Sum

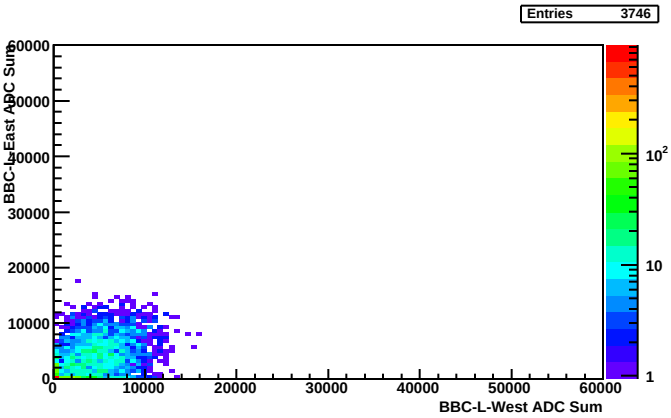

Entries 3746
BBC-L-West ADC Sum
BBC-S-West ADC Sum

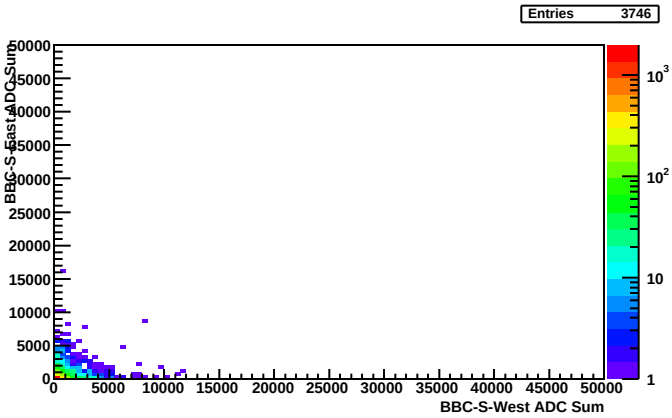

Entries 3746
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

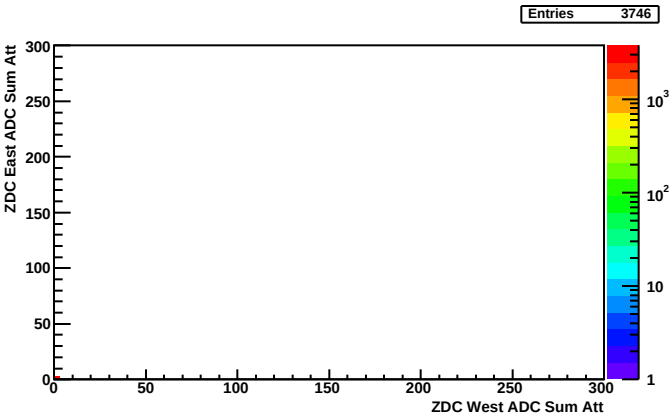

Entries 3746
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

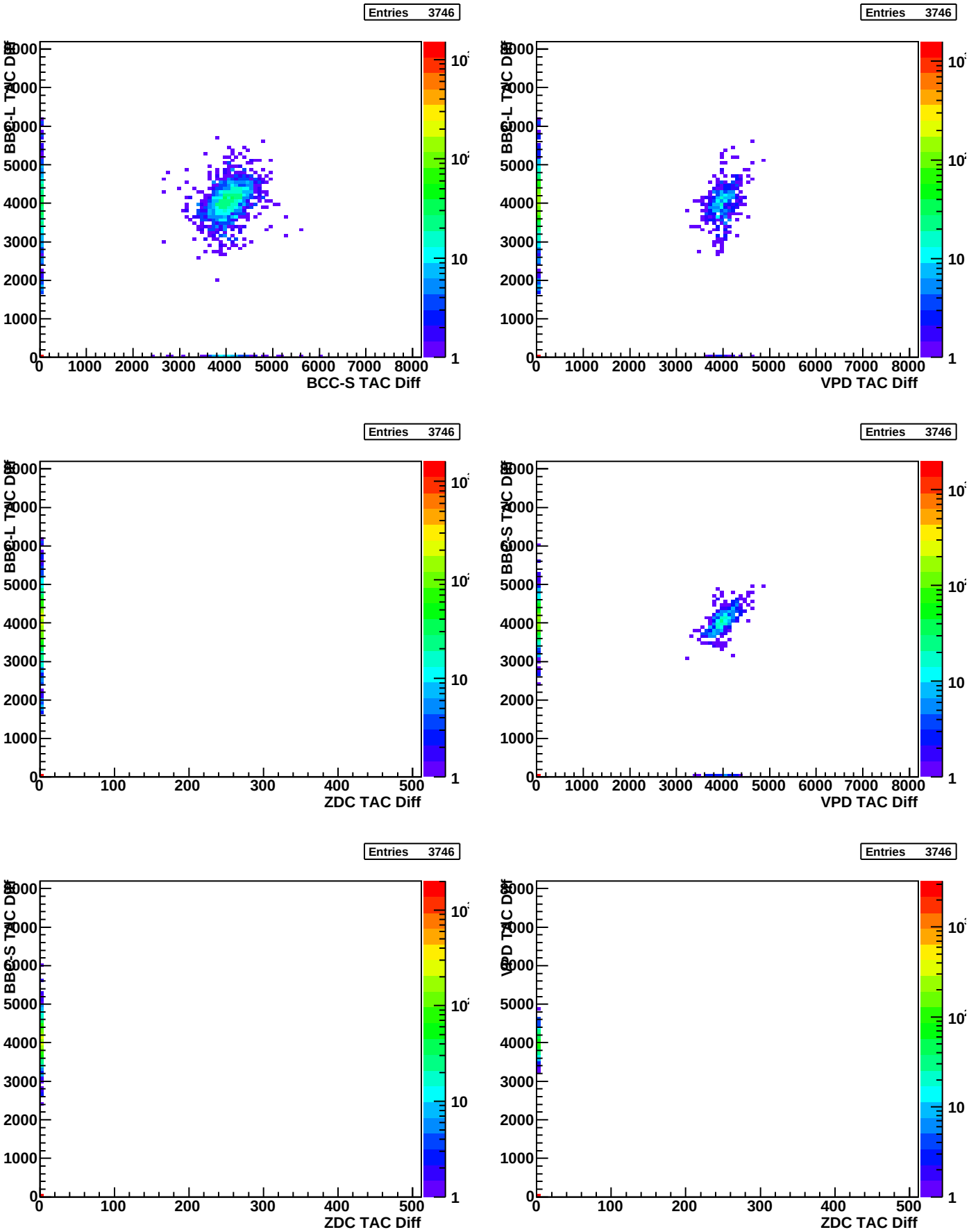

Entries 3746
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

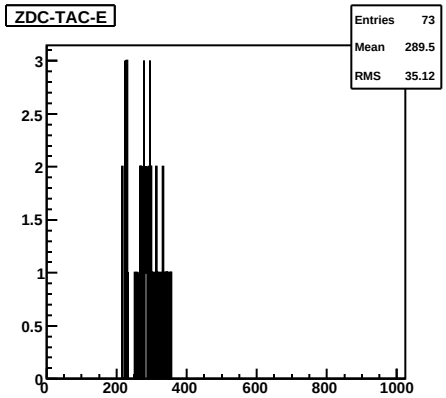

Entries 3746
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

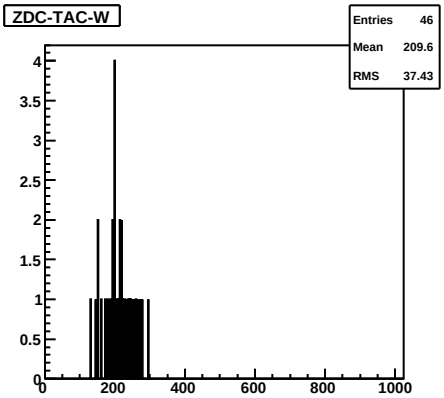

Entries 3746
BBC-L-East ADC Sum
BBC-L-West ADC Sum

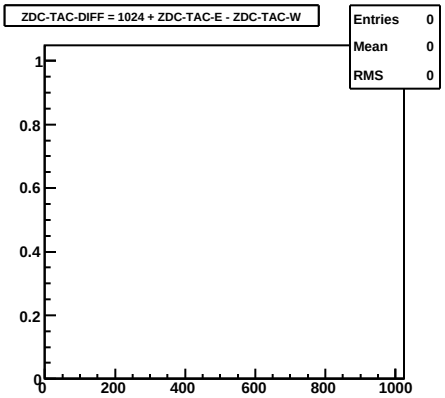

Entries 3746
BBC-S-East ADC Sum
BBC-S-West ADC Sum

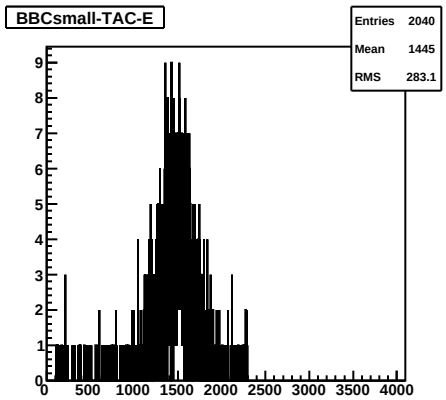

Entries 3746
ZDC East ADC Sum Att
ZDC West ADC Sum Att

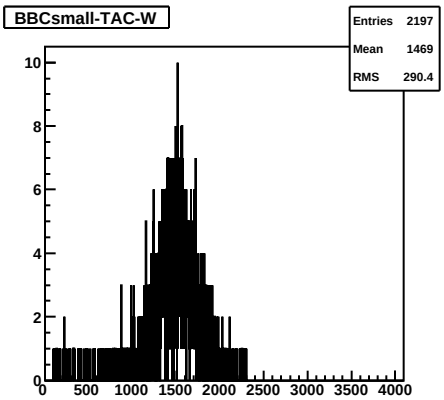

Entries 3746
BBC-L TAC Diff
BCC-S TAC Diff
Entries 3746
BBC-L TAC Diff
VPD TAC Diff
Entries 3746
BBC-L TAC Diff
ZDC TAC Diff
Entries 3746
BBC-S TAC Diff
VPD TAC Diff
Entries 3746
BBC-S TAC Diff
ZDC TAC Diff
Entries 3746
VPD TAC Diff
ZDC TAC Diff

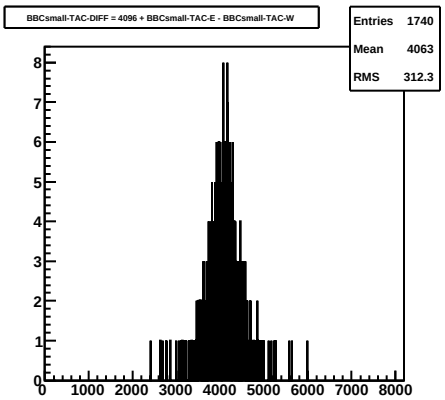

ZDC-TAC-E
Entries 73
Mean 289.5
RMS 35.12

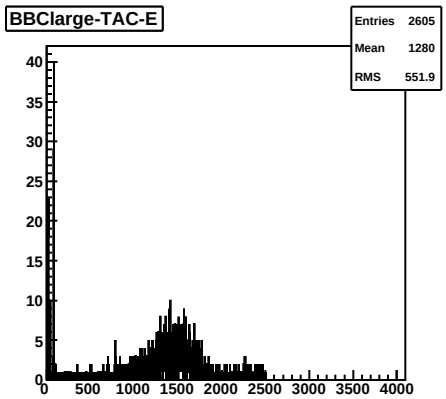

ZDC-TAC-W
Entries 46
Mean 209.6
RMS 37.43

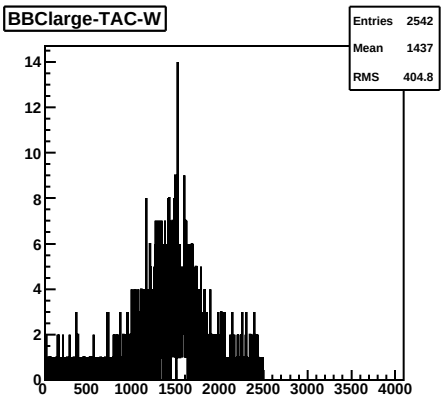

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 0
Mean 0
RMS 0

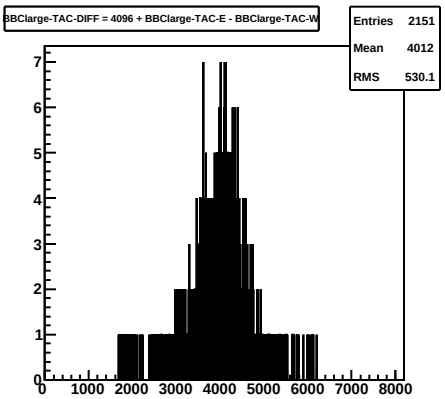

BBCsmall-TAC-E
Entries 2040
Mean 1445
RMS 283.1

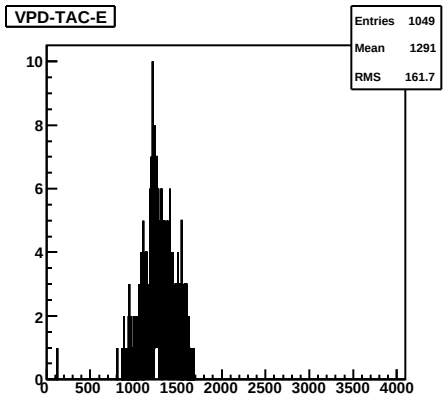

BBCsmall-TAC-W
Entries 2197
Mean 1469
RMS 290.4

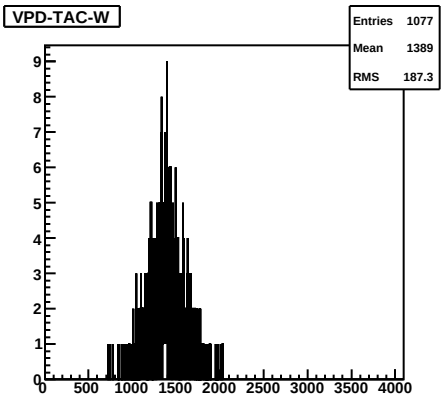

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1740
Mean 4063
RMS 312.3

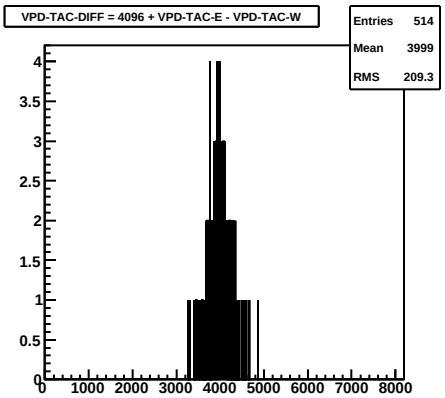

BBClarge-TAC-E
Entries 2605
Mean 1280
RMS 551.9

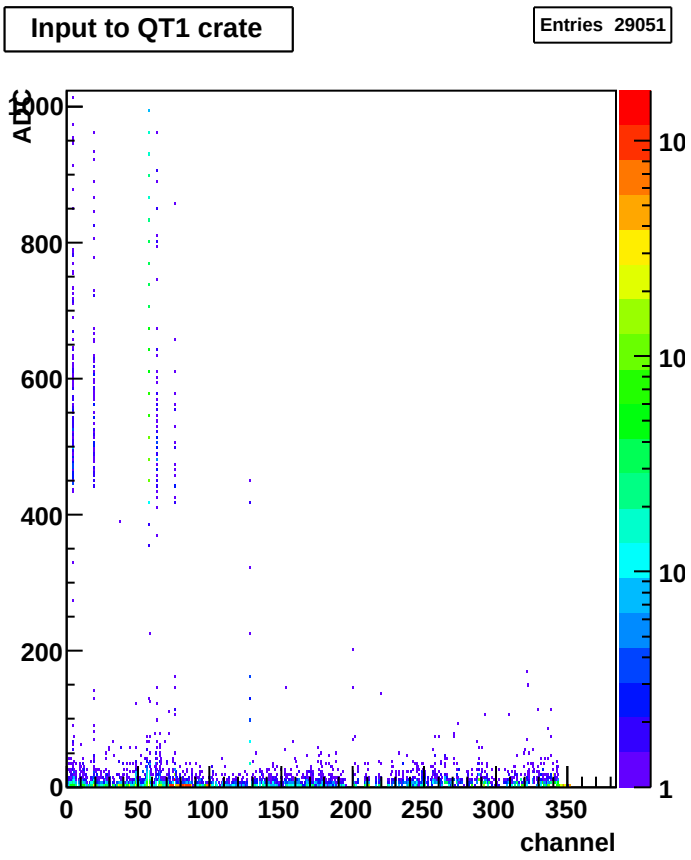

BBClarge-TAC-W
Entries 2542
Mean 1437
RMS 404.8

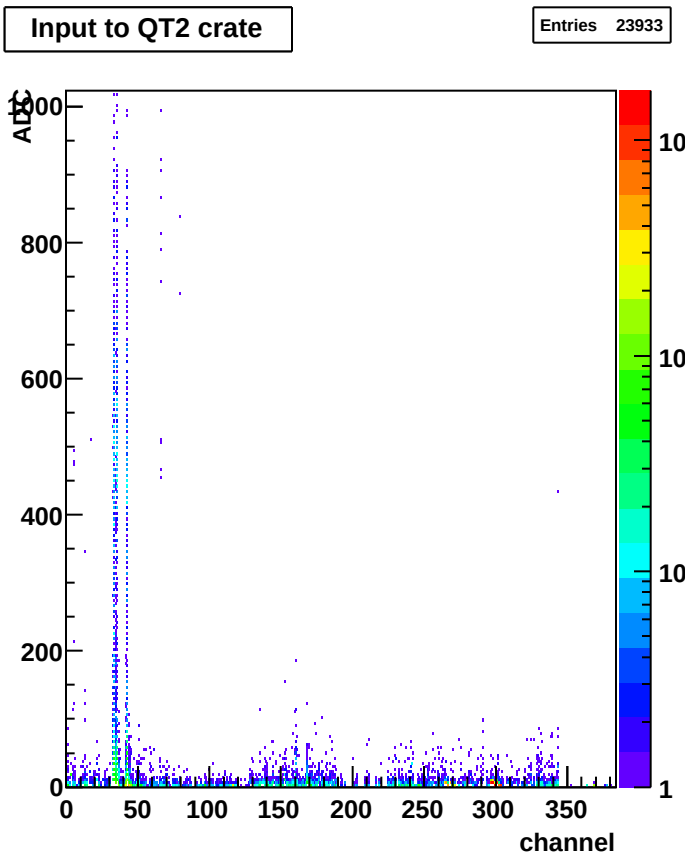

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2151
Mean 4012
RMS 530.1

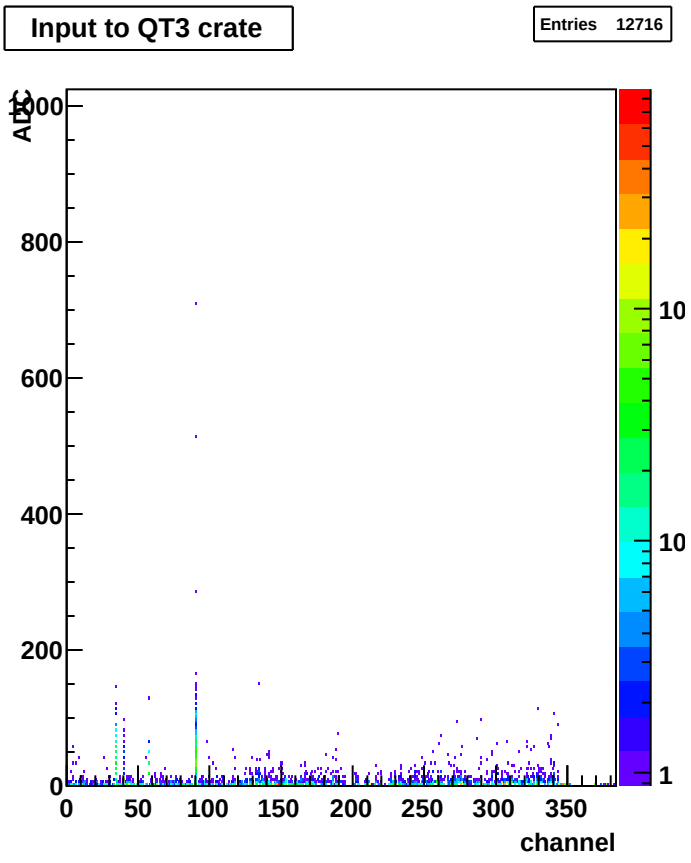

VPD-TAC-E
Entries 1049
Mean 1291
RMS 161.7

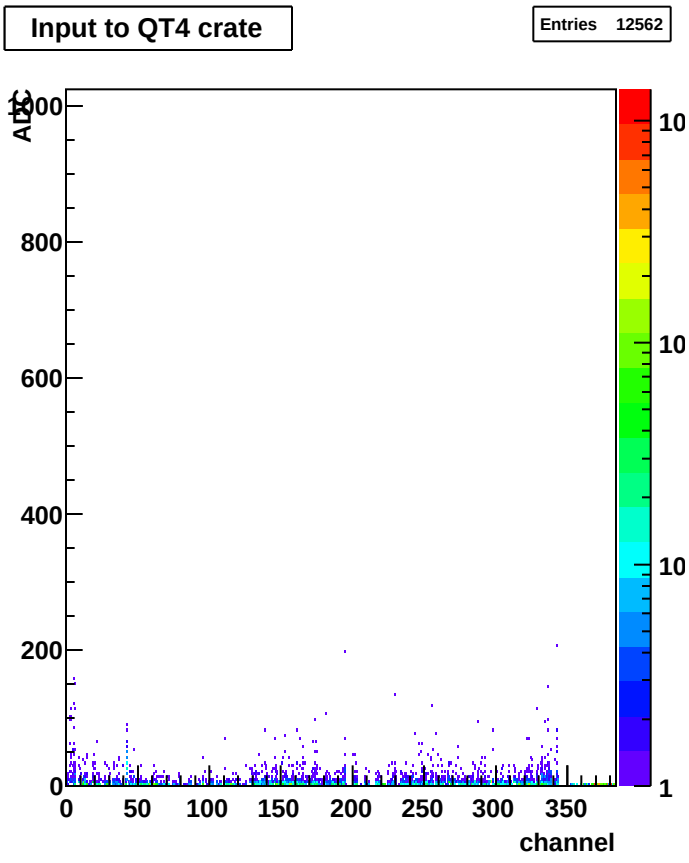

VPD-TAC-W
Entries 1077
Mean 1389
RMS 187.3

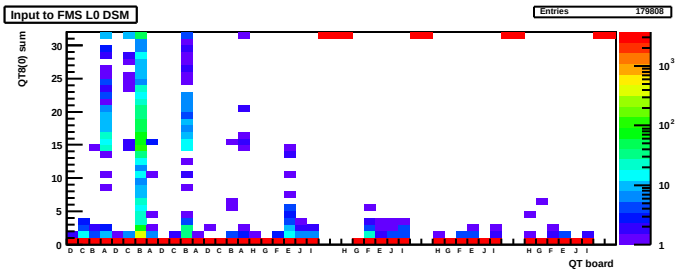

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 514
Mean 3999
RMS 209.3

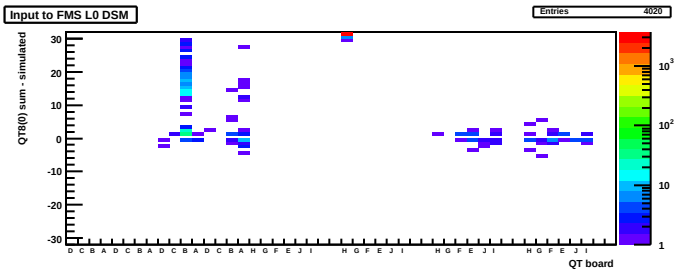

Input to QT1 crate
Entries 29051
ADC
channel

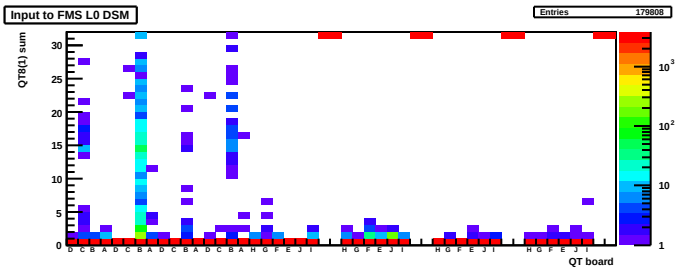

Input to QT2 crate
Entries 23933
ADC
channel

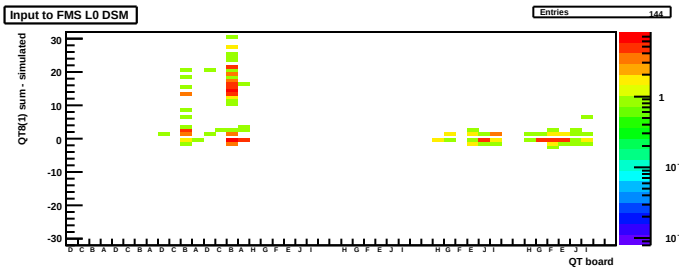

Input to QT3 crate
Entries 12716
ADC
channel

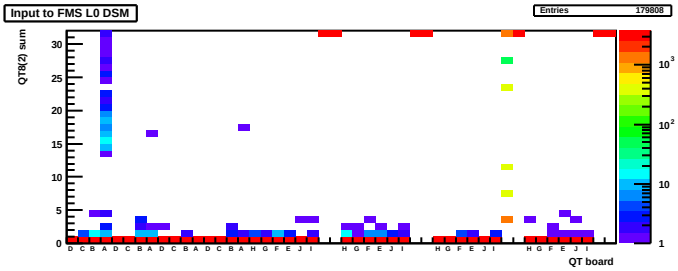

Input to QT4 crate
Entries 12562
ADC
channel

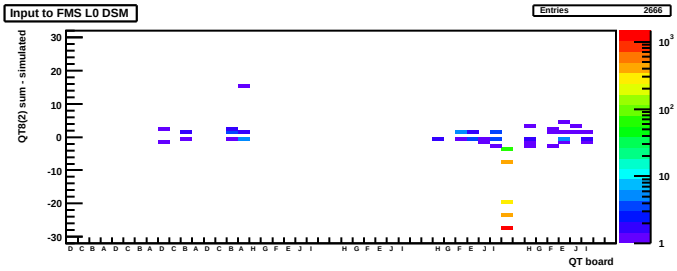

Input to FMS L0 DSM
Entries 179808
QT8(0) sum
QT board

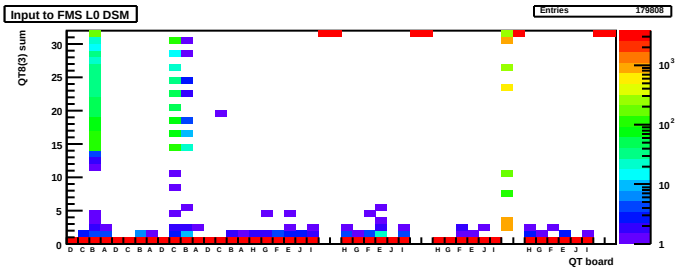

Input to FMS L0 DSM
Entries 4020
QT8(0) sum - simulated
QT board

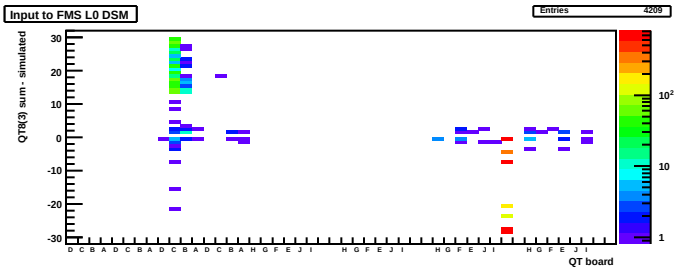

Input to FMS L0 DSM
Entries 179808
QT8(1) sum
QT board

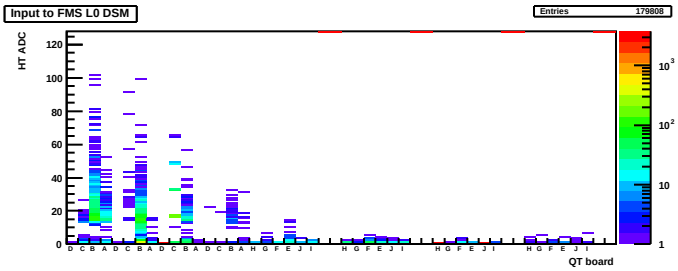

Input to FMS L0 DSM
Entries 144
QT8(1) sum - simulated
QT board

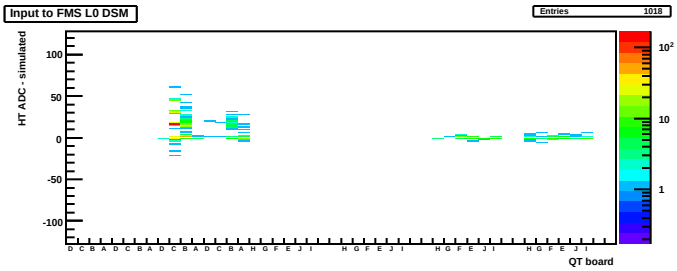

Input to FMS L0 DSM
Entries 179808
QT8(2) sum
QT board

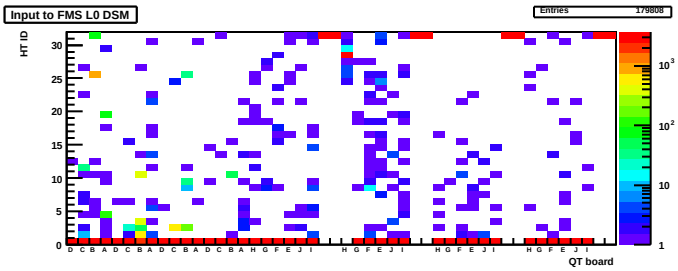

Input to FMS L0 DSM
Entries 2666
QT8(2) sum - simulated
QT board

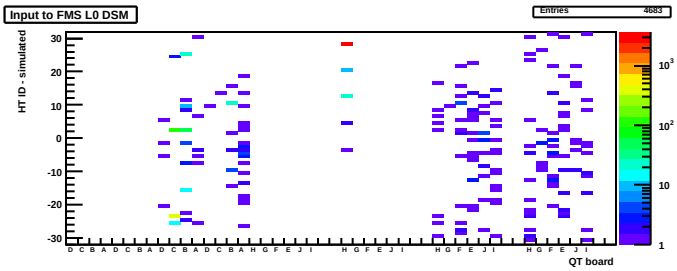

Input to FMS L0 DSM
Entries 179808
QT8(3) sum
QT board

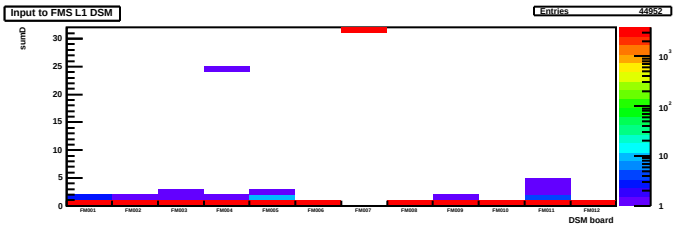

Input to FMS L0 DSM
Entries 4209
QT8(3) sum - simulated
QT board

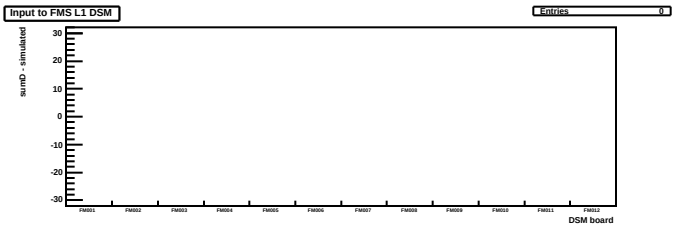

Input to FMS L0 DSM
Entries 179808
HT ADC
QT board

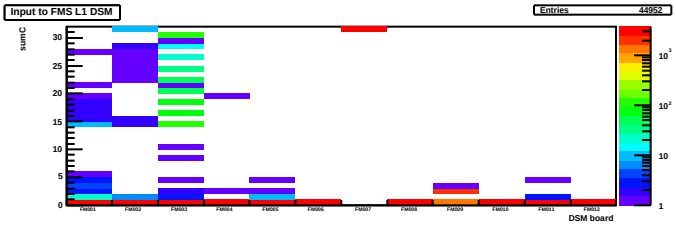

Input to FMS L0 DSM
Entries 1018
HT ADC - simulated
QT board

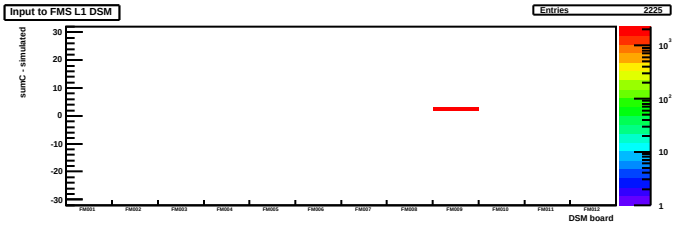

Input to FMS L0 DSM
Entries 179808
HT ID
QT board

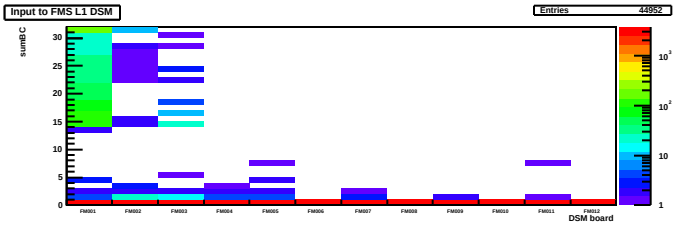

Input to FMS L0 DSM
Entries 4683
HT ID - simulated
QT board

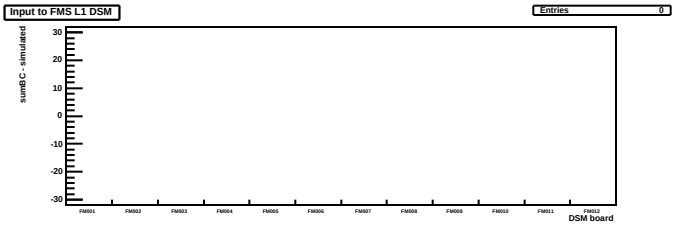

Input to FMS L1 DSM
Entries 44952
sumD
DSM board

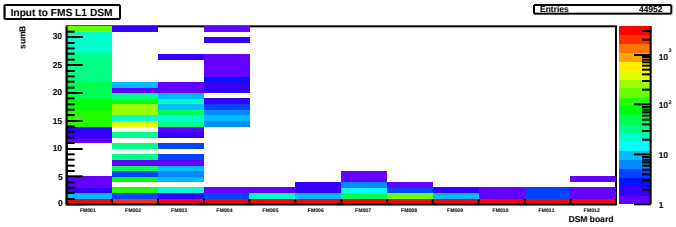

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

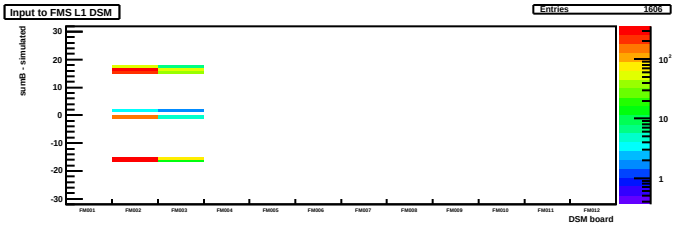

Input to FMS L1 DSM
Entries 44952
sumC
DSM board

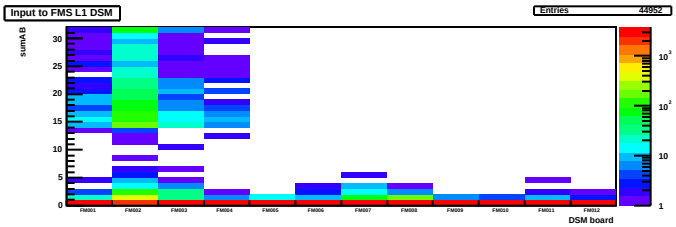

Input to FMS L1 DSM
Entries 2225
sumC - simulated
DSM board

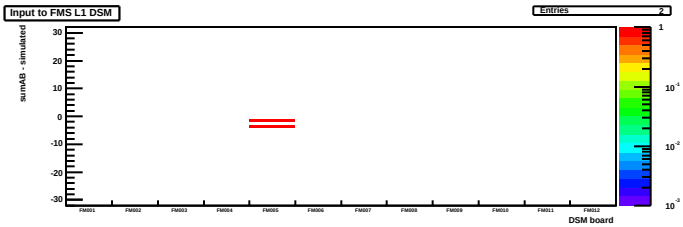

Input to FMS L1 DSM
Entries 44952
sumBC
DSM board

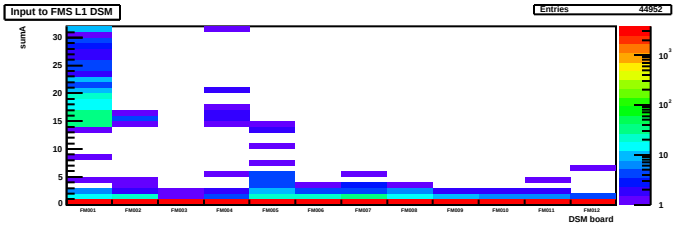

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

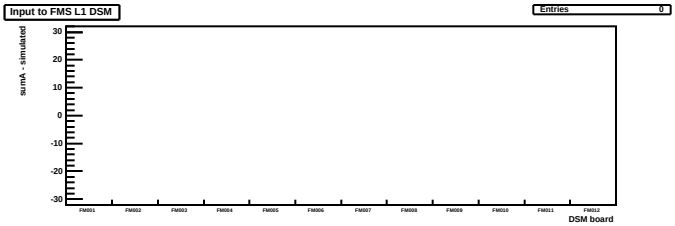

Input to FMS L1 DSM
Entries 44952
sumB
DSM board

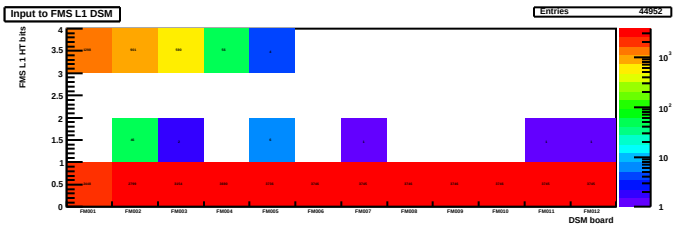

Input to FMS L1 DSM
Entries 1606
sumB - simulated
DSM board

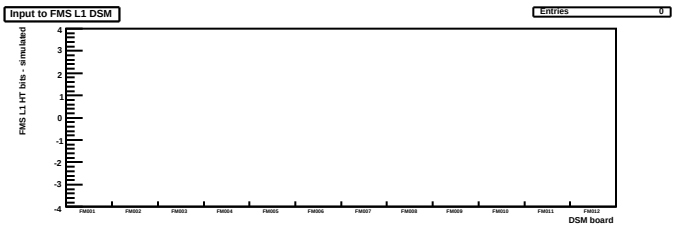

Input to FMS L1 DSM
Entries 44952
sumAB
DSM board

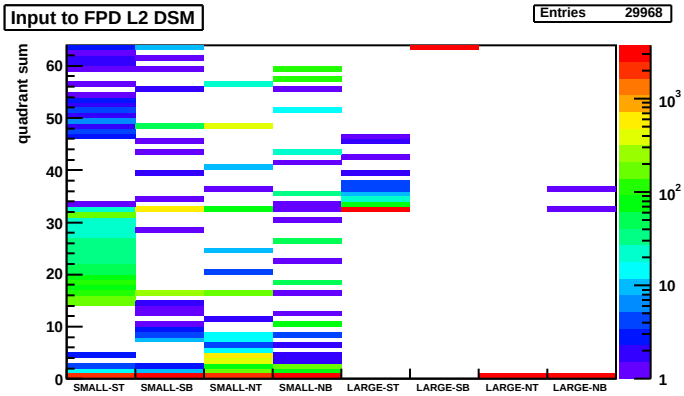

Input to FMS L1 DSM
Entries 2
sumAB - simulated
DSM board

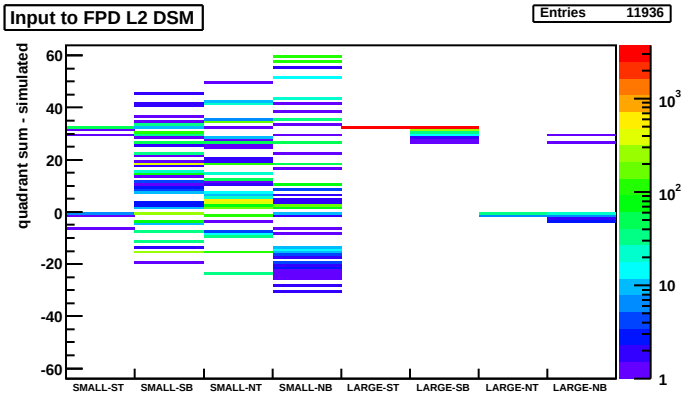

Input to FMS L1 DSM
Entries 44952
sumA
DSM board

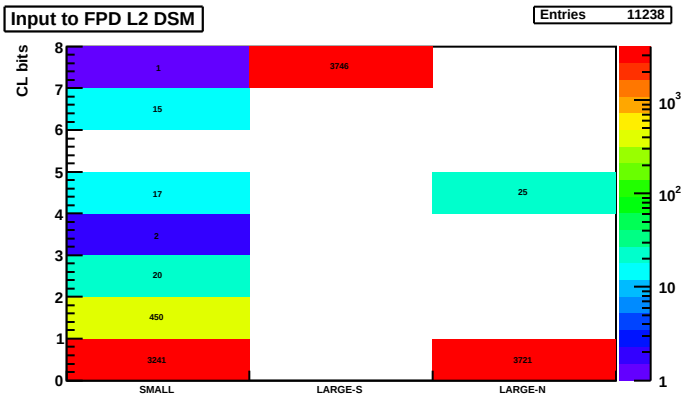

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

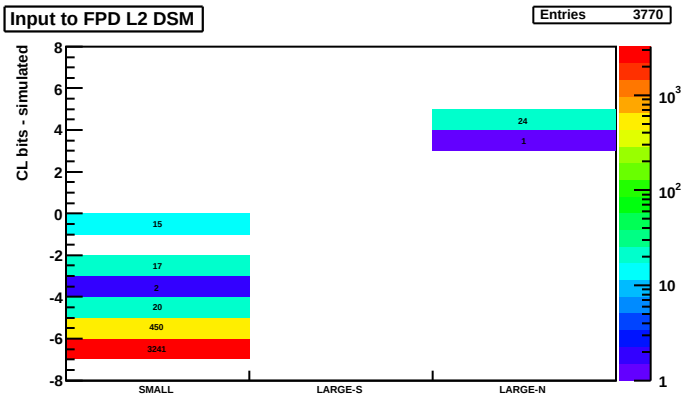

Input to FMS L1 DSM
Entries 44952
FMS L1 HT bits
DSM board

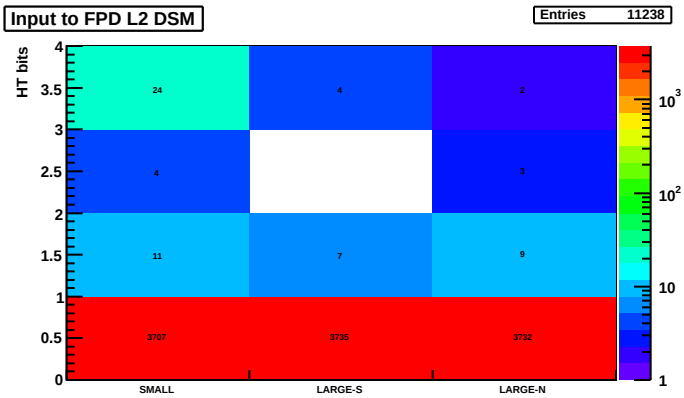

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

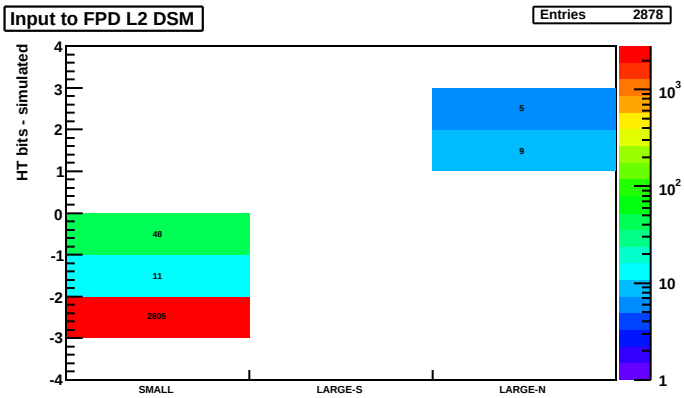

Input to FPD L2 DSM
Entries 29968
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

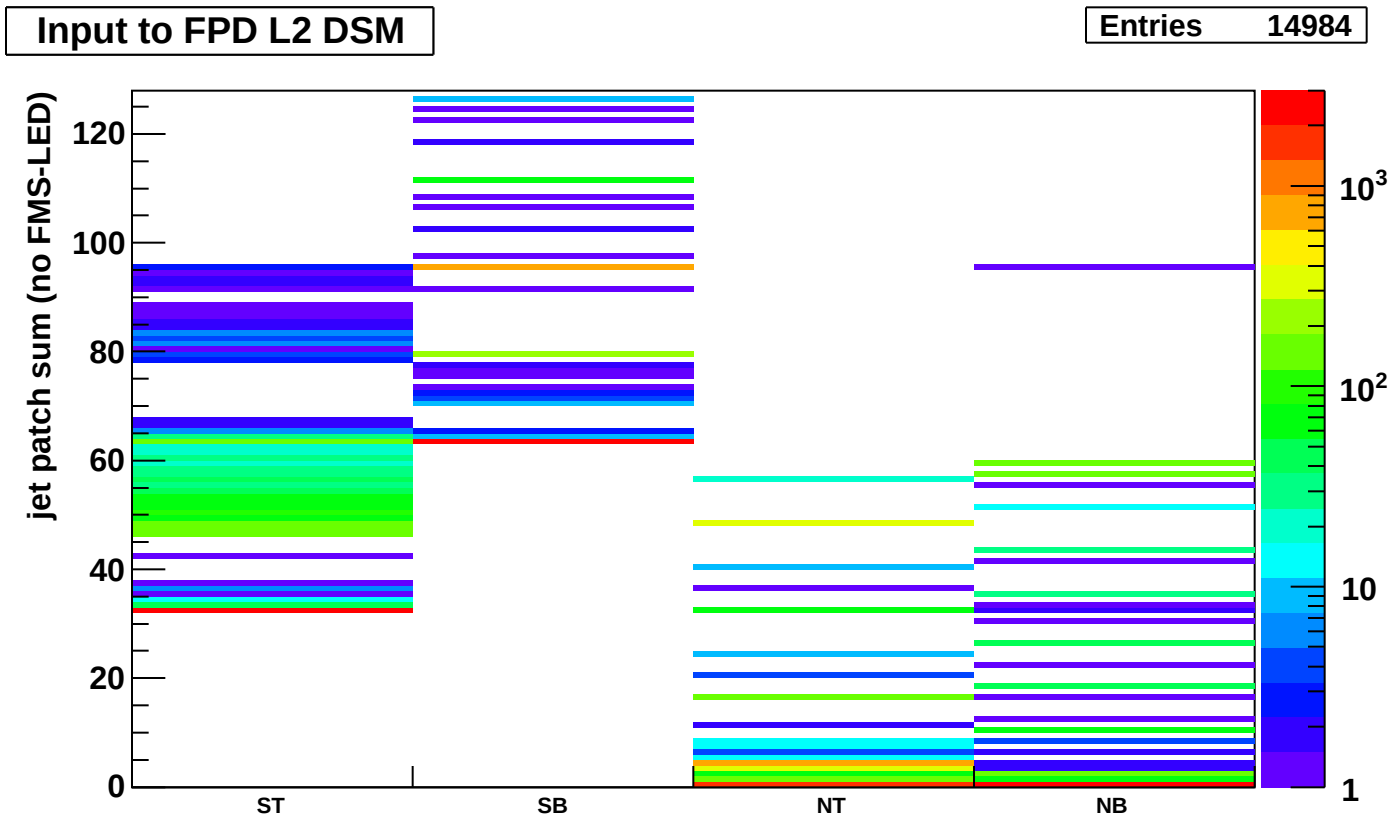

Input to FPD L2 DSM
Entries 11936
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

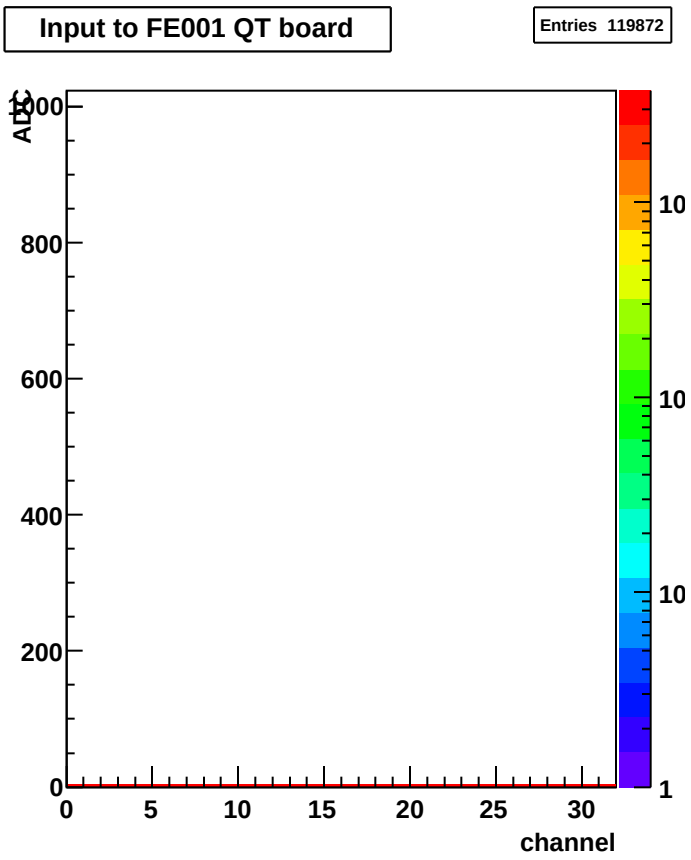

Input to FPD L2 DSM
Entries 11238
CL bits
SMALL
LARGE-S
LARGE-N

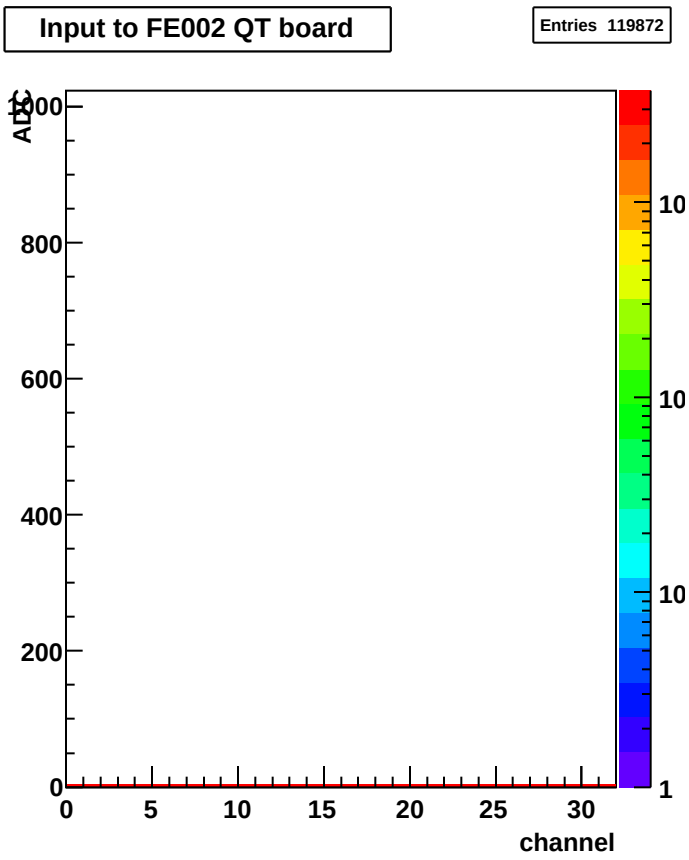

Input to FPD L2 DSM
Entries 3770
CL bits - simulated
SMALL
LARGE-S
LARGE-N

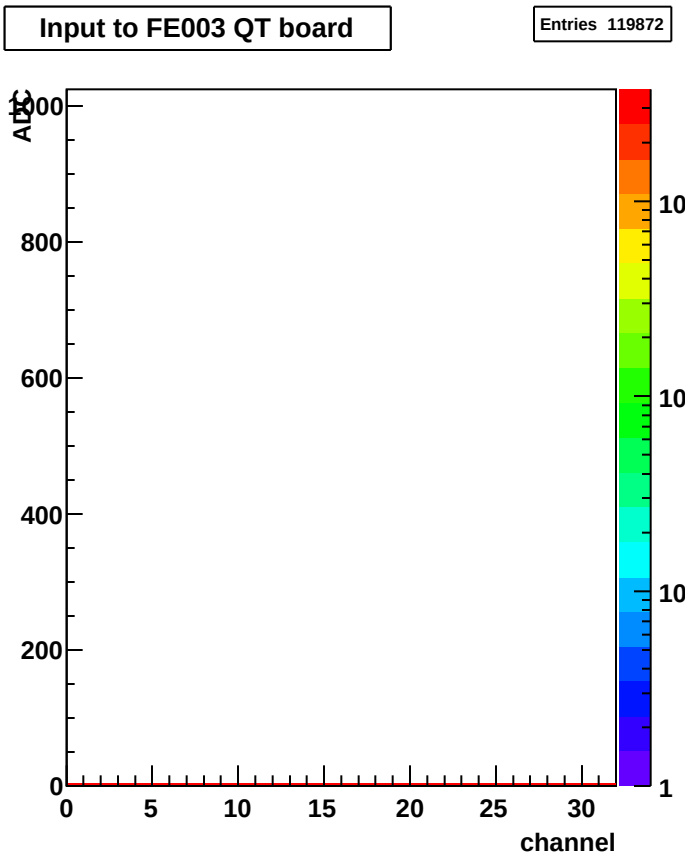

Input to FPD L2 DSM
Entries 11238
HT bits
SMALL
LARGE-S
LARGE-N

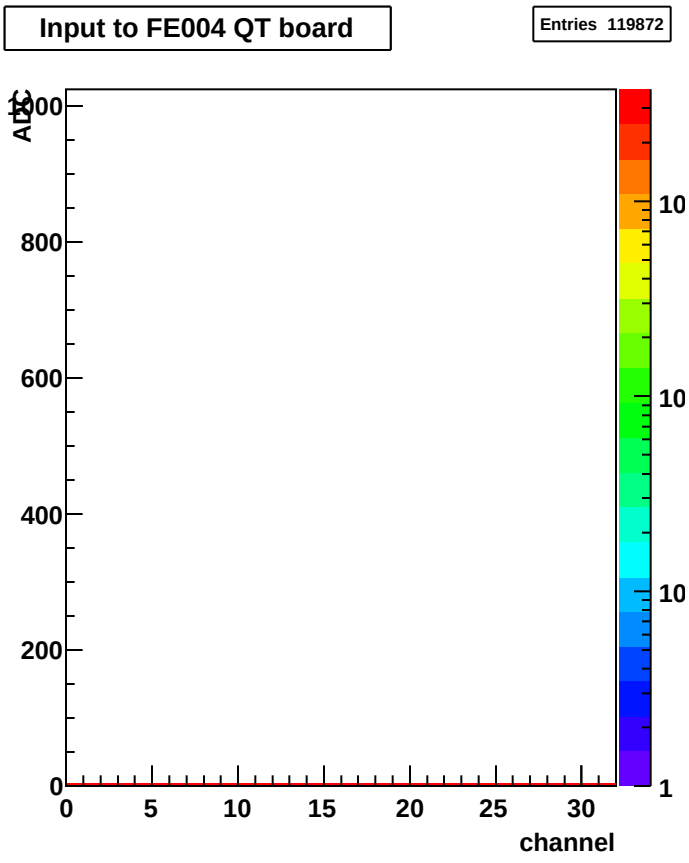

Input to FPD L2 DSM
Entries 2878
HT bits - simulated
SMALL
LARGE-S
LARGE-N

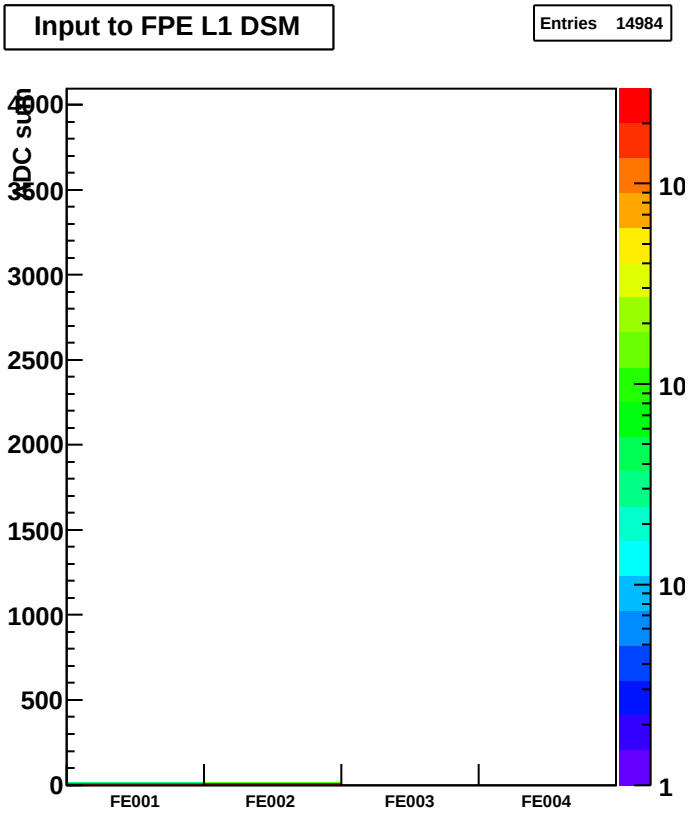

Input to FPD L2 DSM
Entries 14984
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3

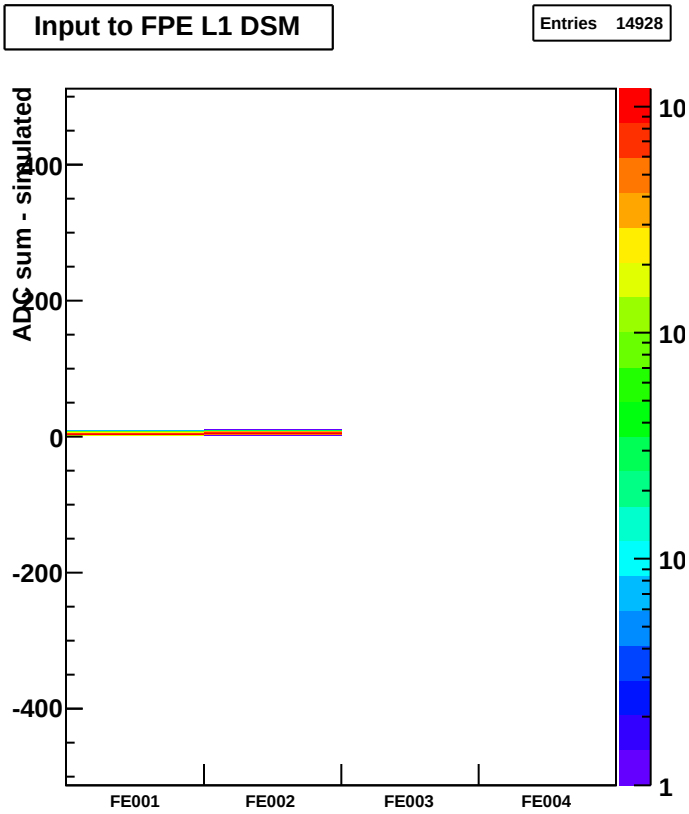

Input to FE001 QT board
Entries 119872
ADC
channel

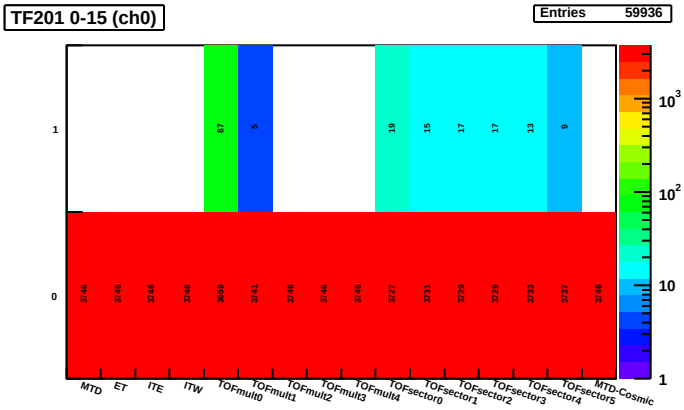

Input to FE002 QT board
Entries 119872
ADC
channel

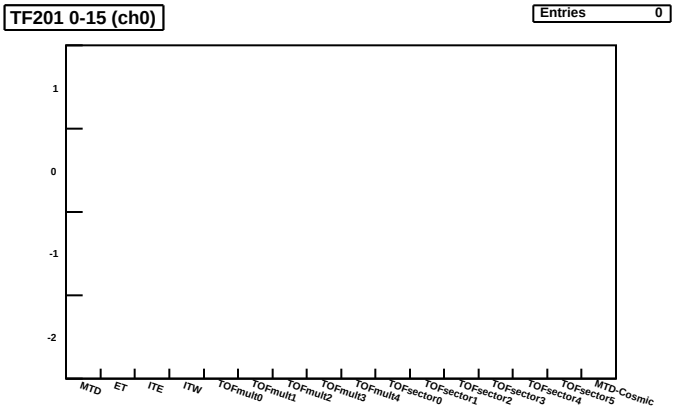

Input to FE003 QT board
Entries 119872
ADC
channel

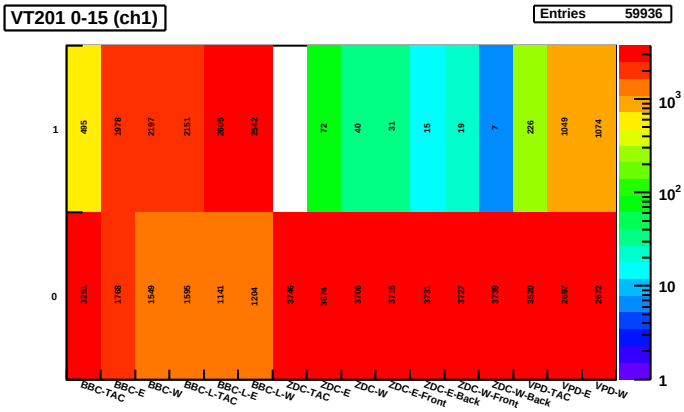

Input to FE004 QT board
Entries 119872
ADC
channel

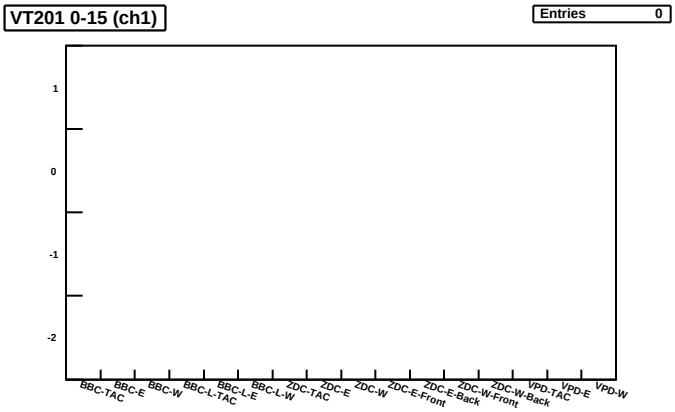

Input to FPE L1 DSM
Entries 14984
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
1

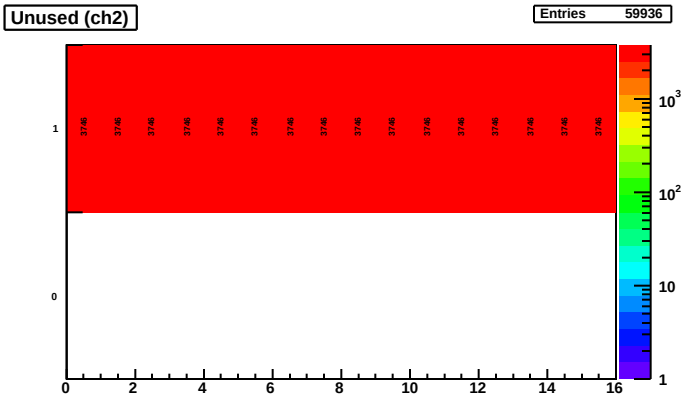

Input to FPE L1 DSM
Entries 14928
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
1

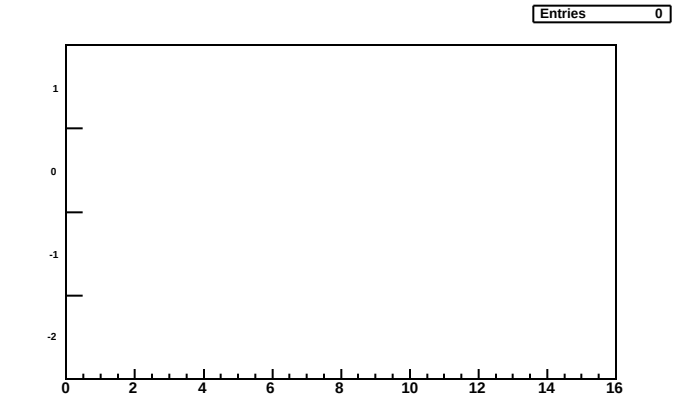

TF201 0-15 (ch0)
Entries 59936
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

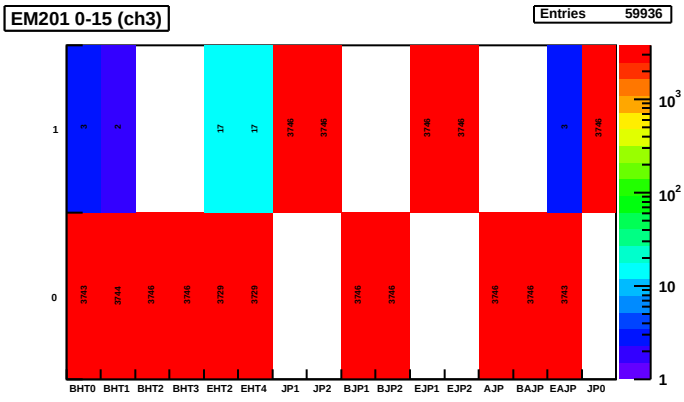

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

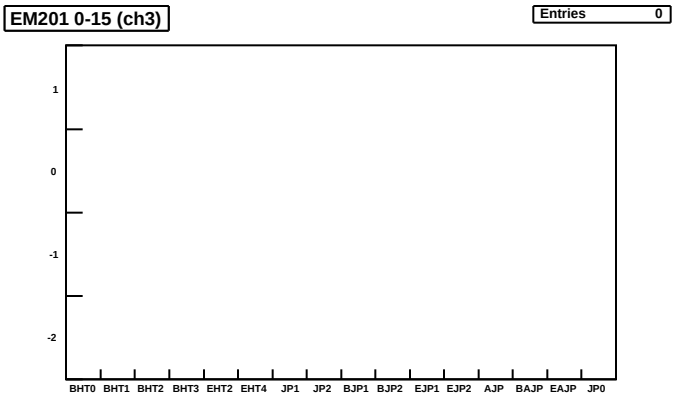

VT201 0-15 (ch1)
Entries 59936
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

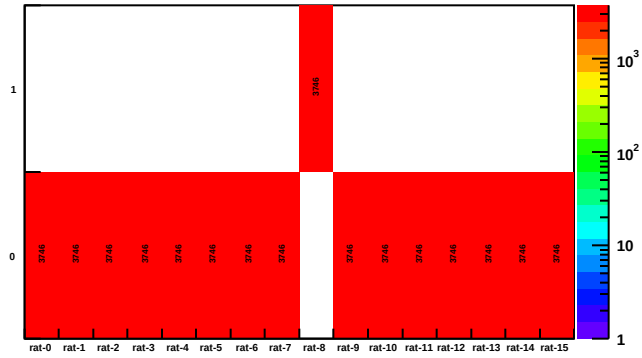

Unused (ch2)
Entries 59936

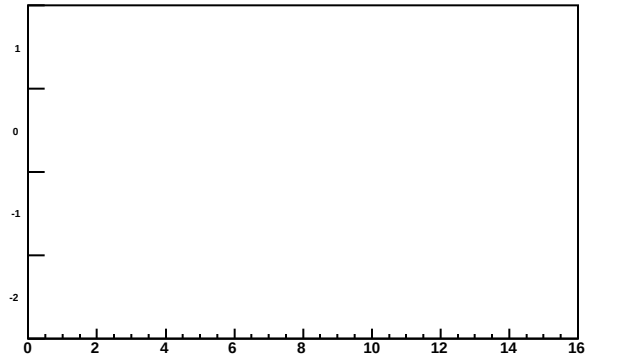

Entries 0


EM201 0-15 (ch3)
Entries 59936
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

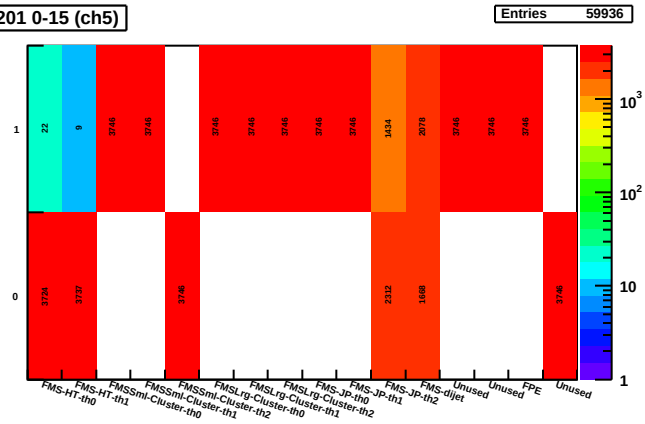

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

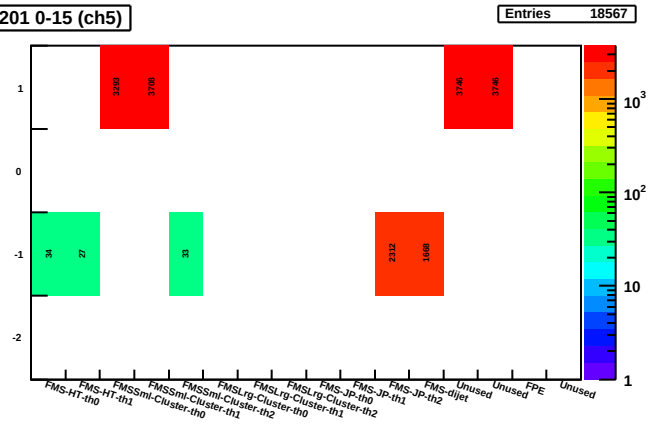




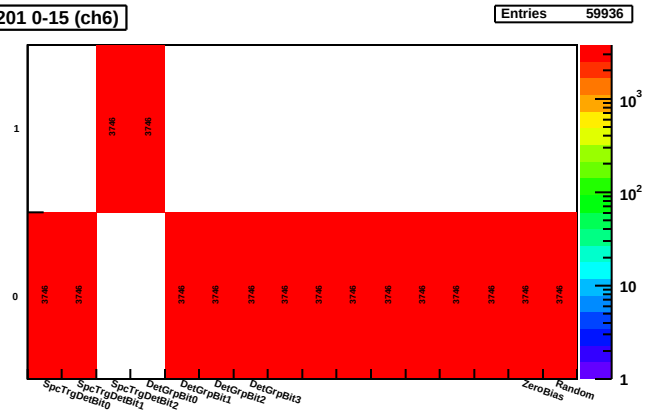
FP201 0-15 (ch5)

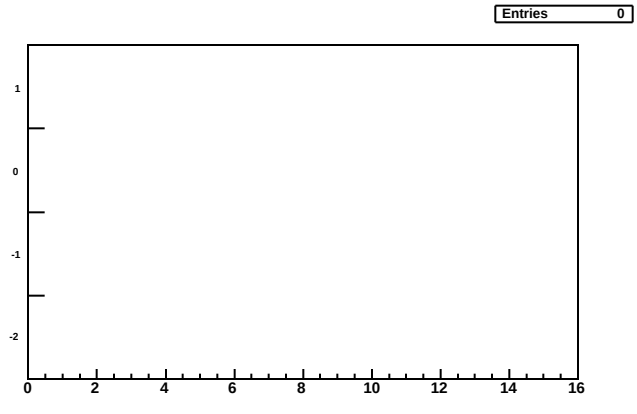
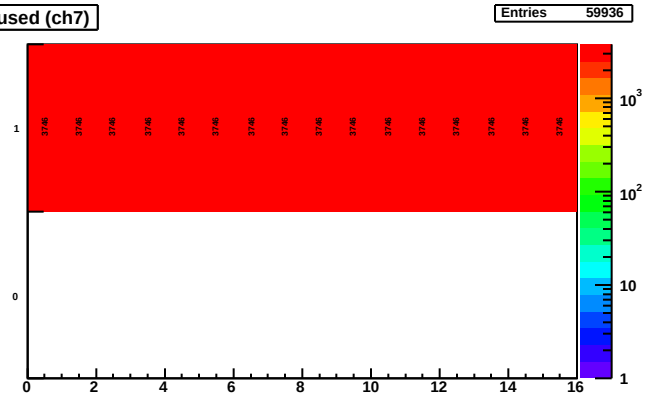
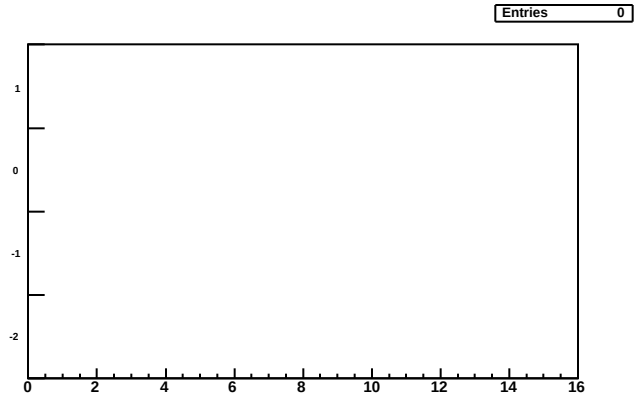


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)