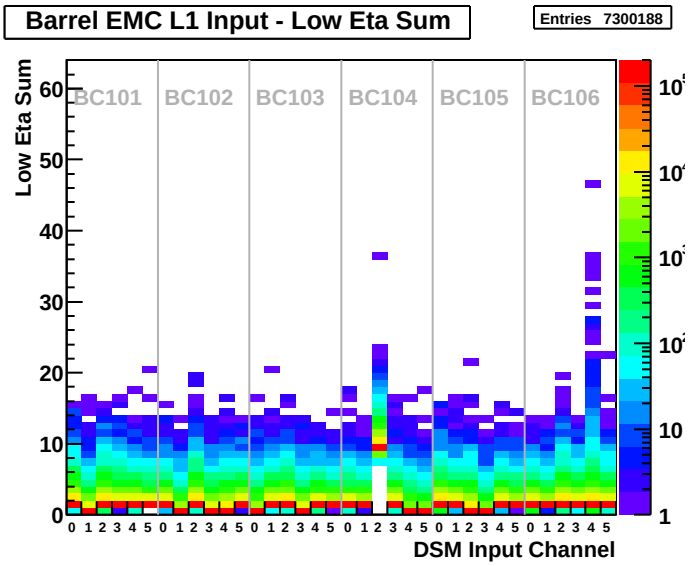
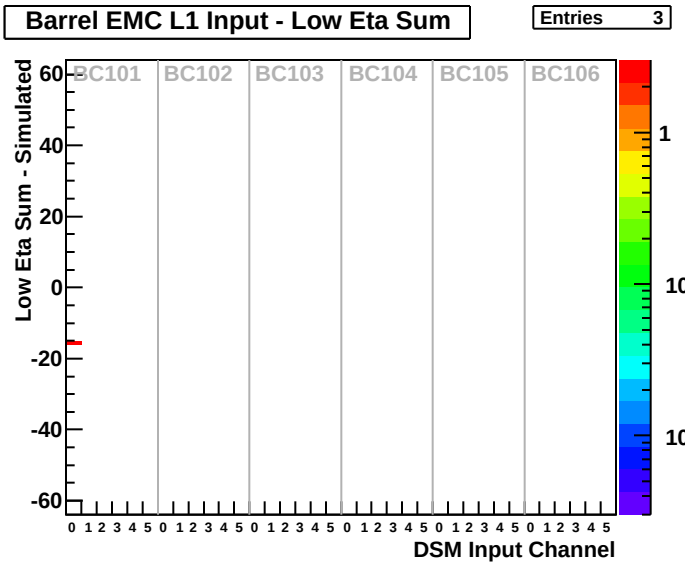
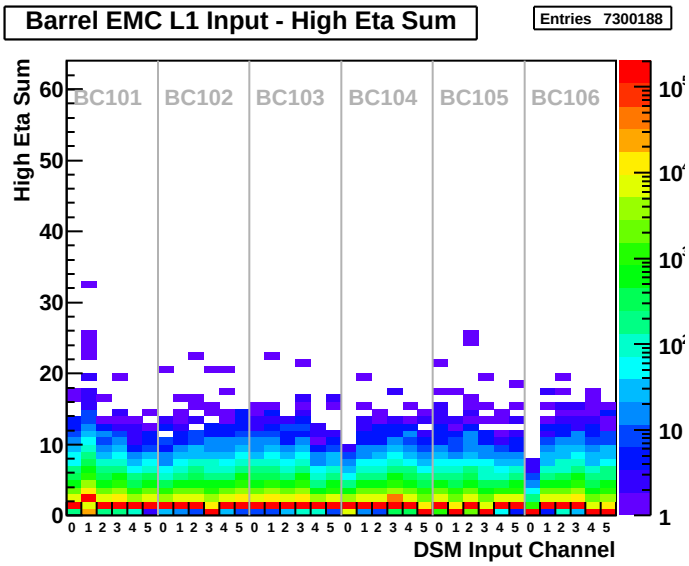
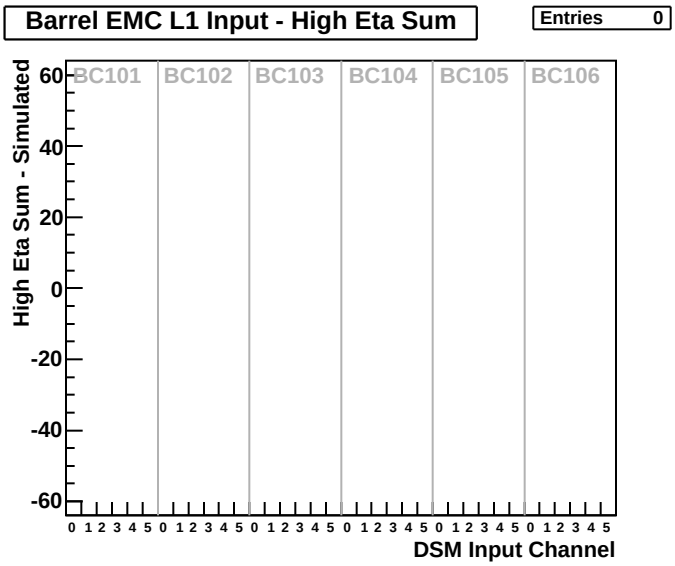
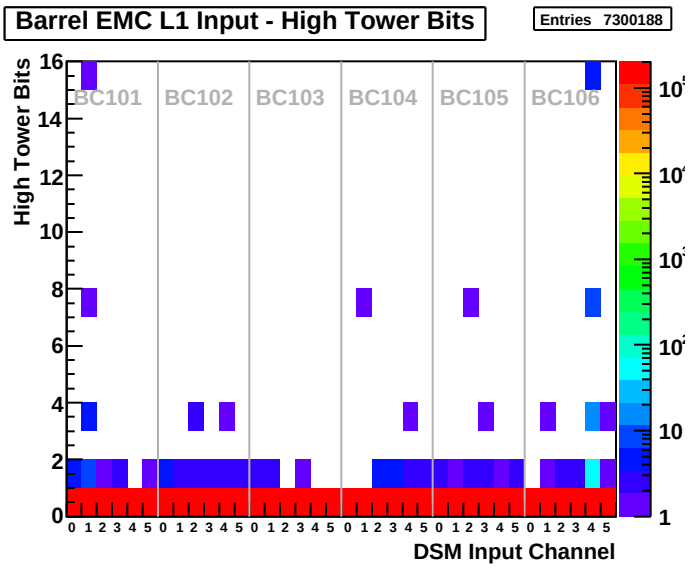
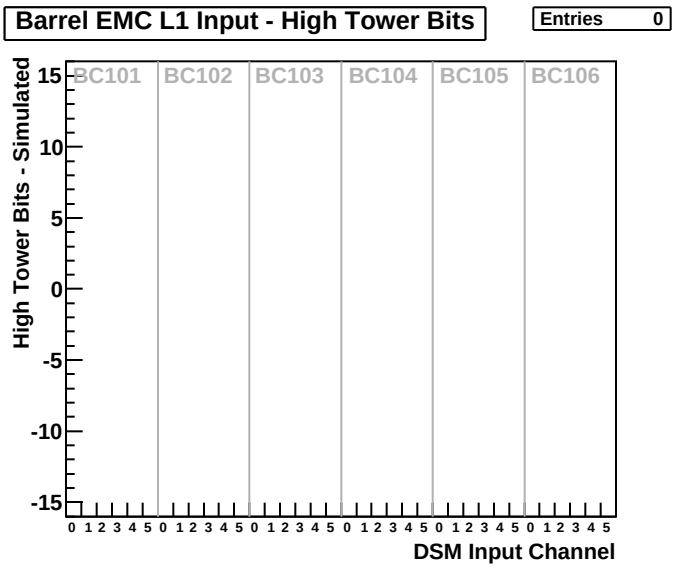

Barrel EMC L1 Input - Low Eta Sum
Entries 7300188
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

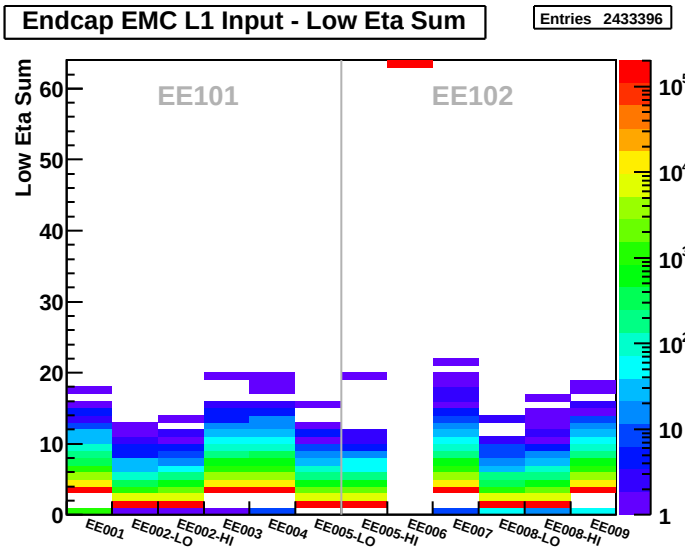

Barrel EMC L1 Input - Low Eta Sum
Entries 3
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

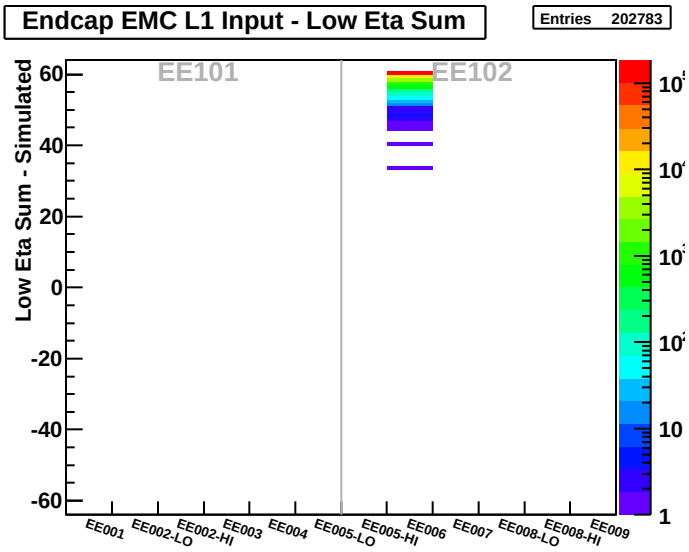

Barrel EMC L1 Input - High Eta Sum
Entries 7300188
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

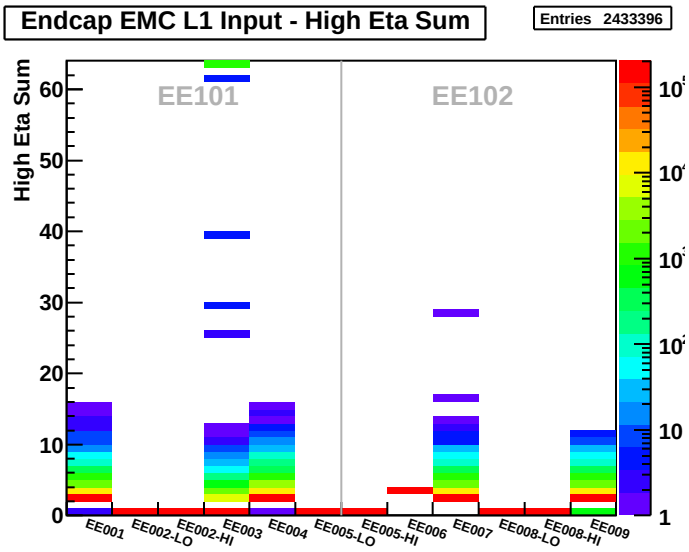

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

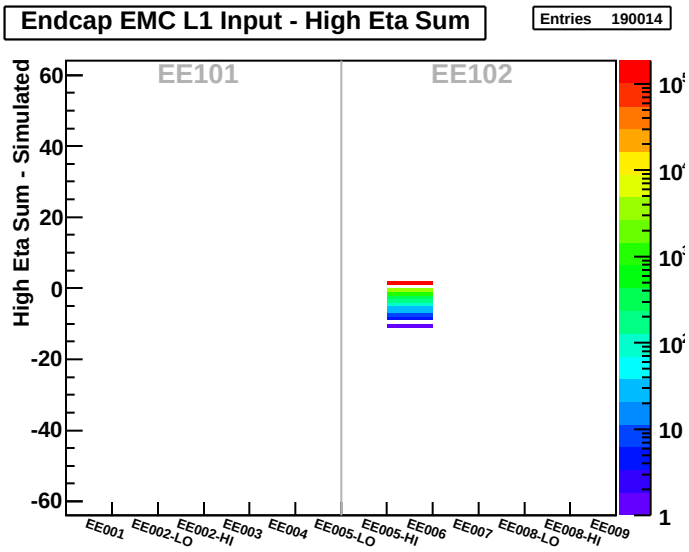

Barrel EMC L1 Input - High Tower Bits
Entries 7300188
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

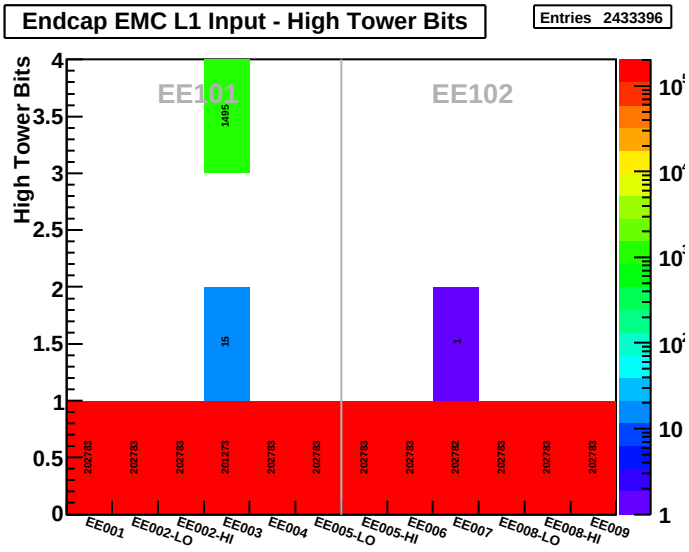

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

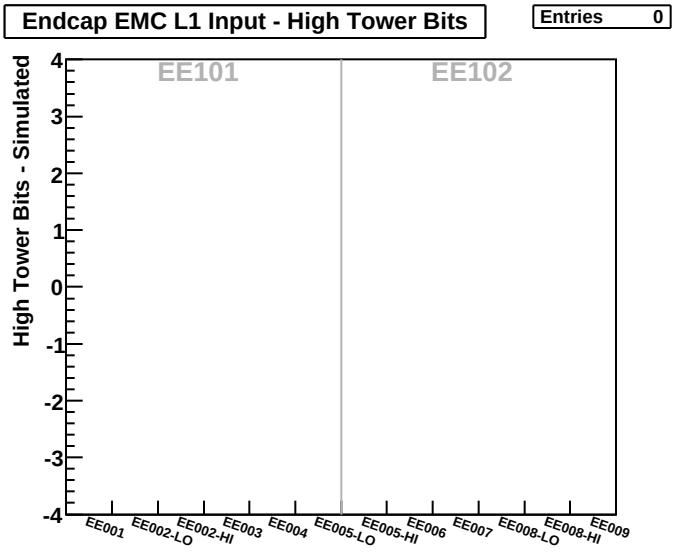

Endcap EMC L1 Input - Low Eta Sum
Entries 2433396
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

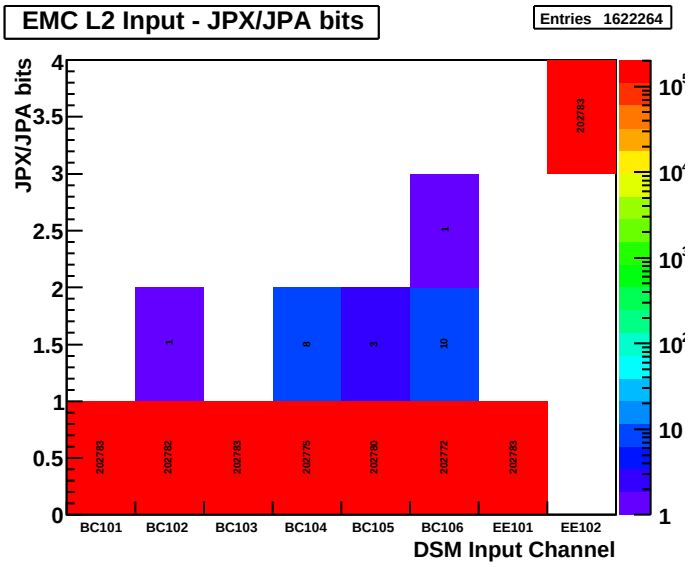

Endcap EMC L1 Input - Low Eta Sum
Entries 202783
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

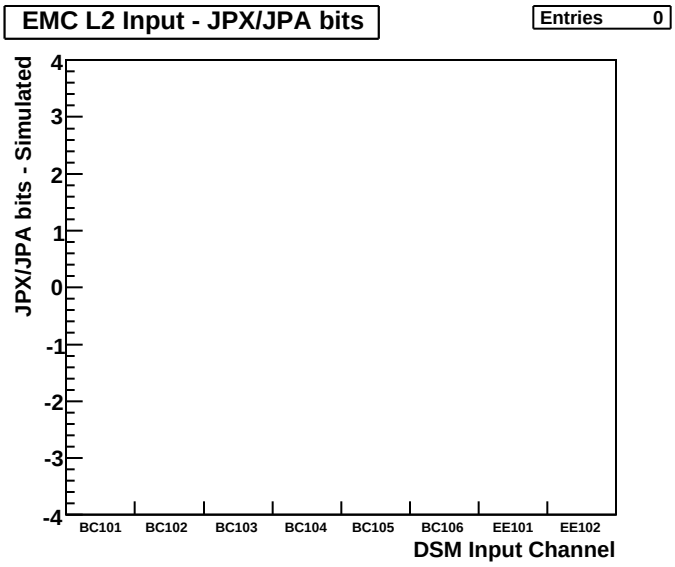

Endcap EMC L1 Input - High Eta Sum
Entries 2433396
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

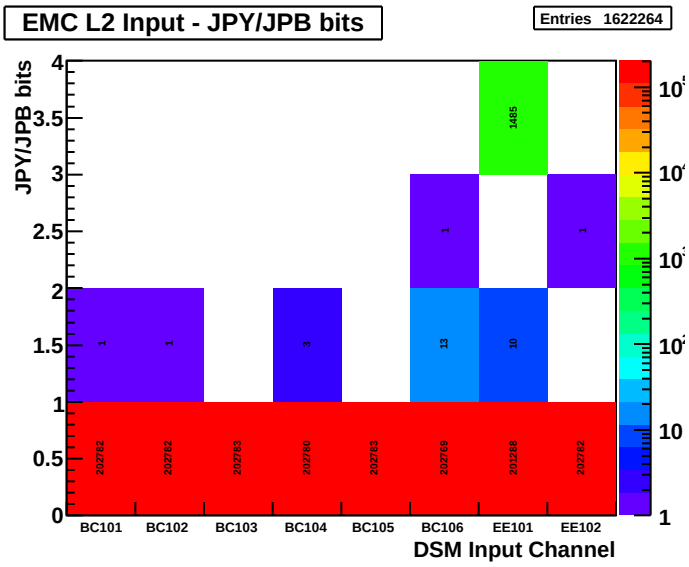

Endcap EMC L1 Input - High Eta Sum
Entries 190014
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

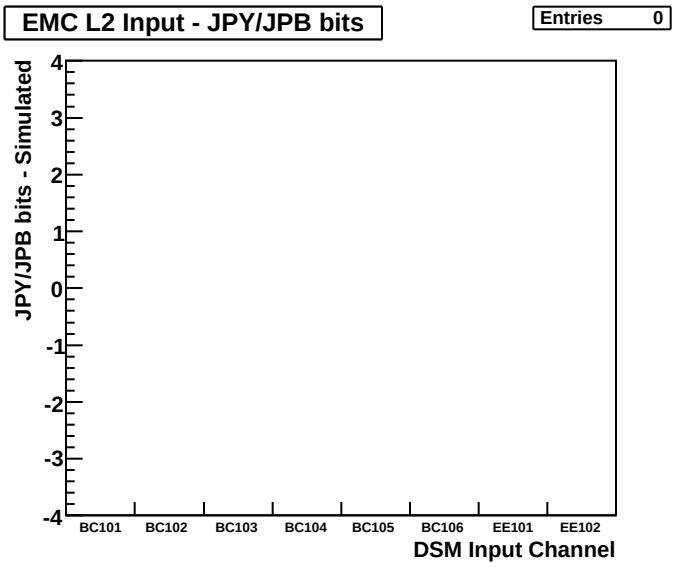

Endcap EMC L1 Input - High Tower Bits
Entries 2433396
High Tower Bits
EE101
EE102
1495
15
1
202783 202783 202783 201273 202783 202783 202783 202783 202782 202783 202783 202783
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

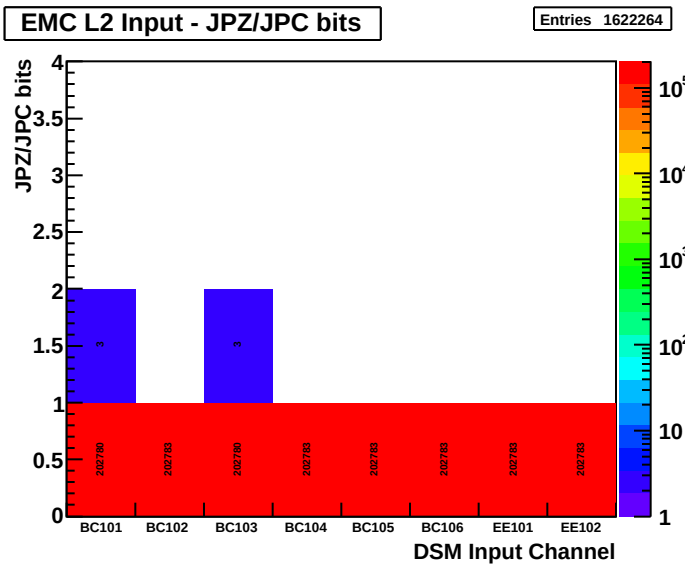

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

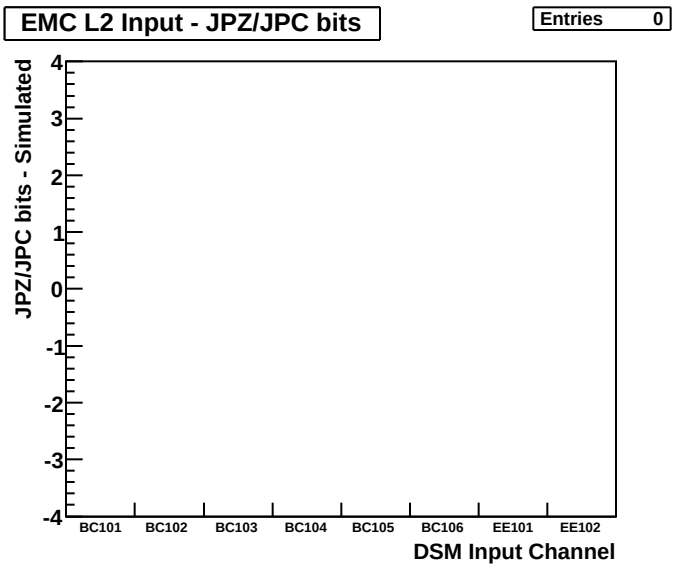

EMC L2 Input - JPX/JPA bits
Entries 1622264
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

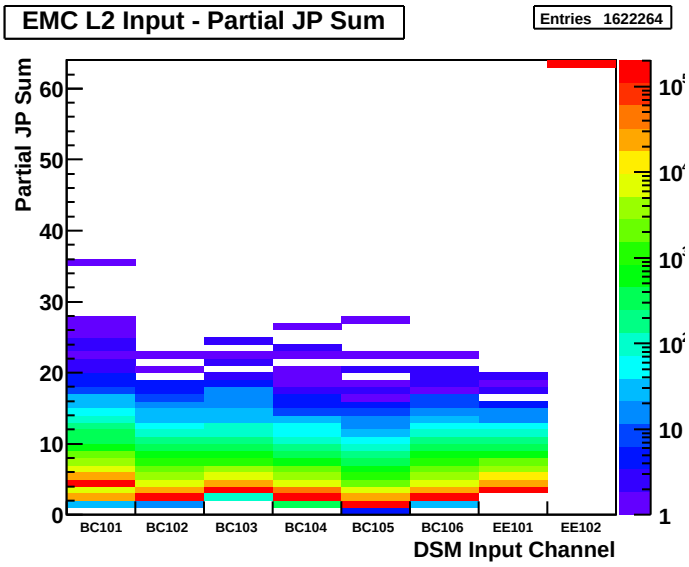

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

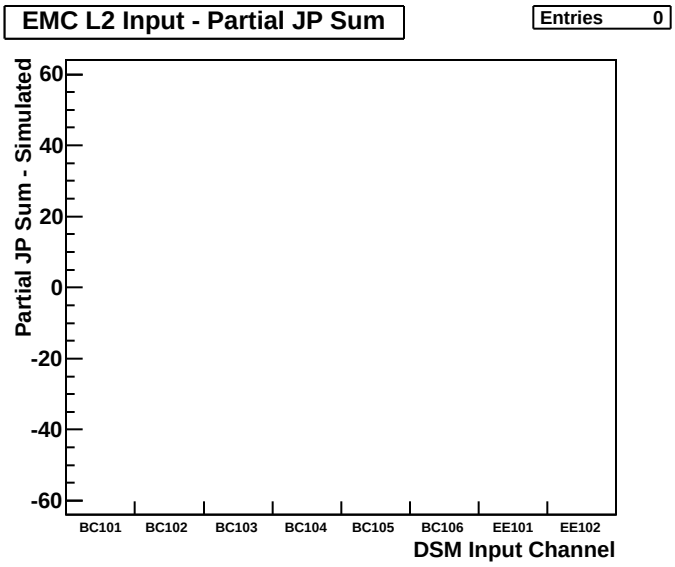

EMC L2 Input - JPY/JPB bits
Entries 1622264
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

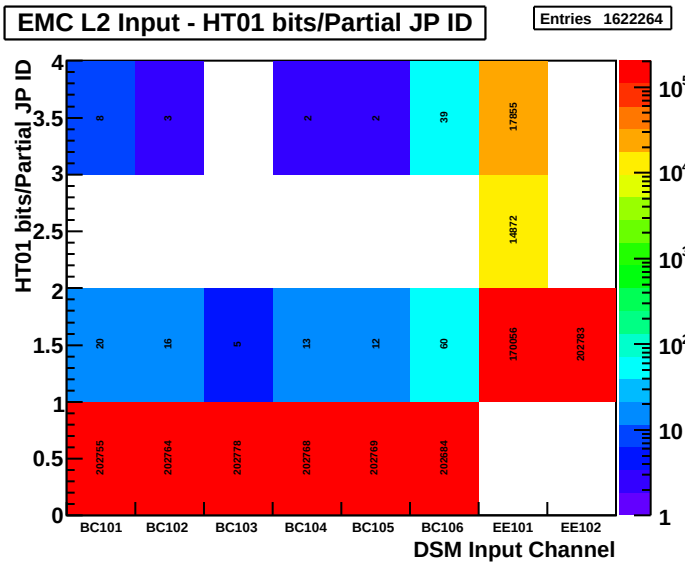

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

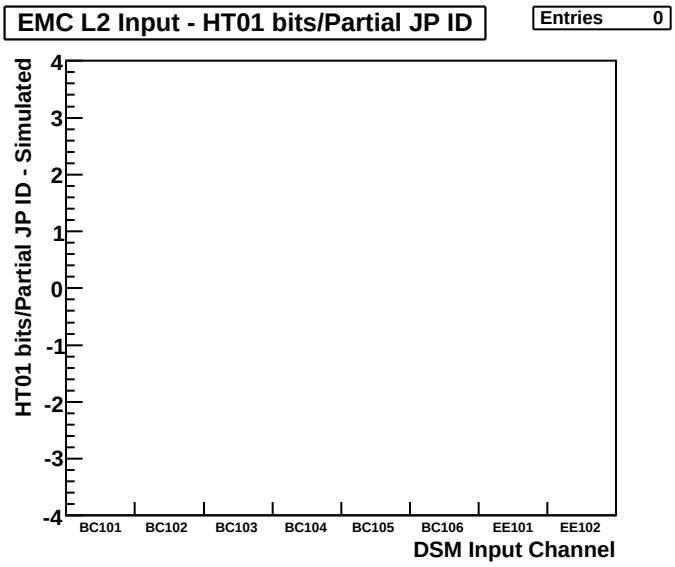

EMC L2 Input - JPZ/JPC bits
Entries 1622264
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

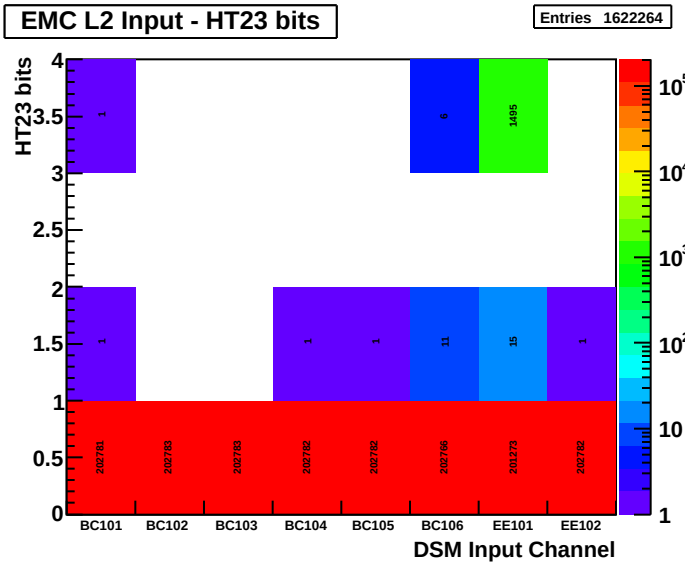

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

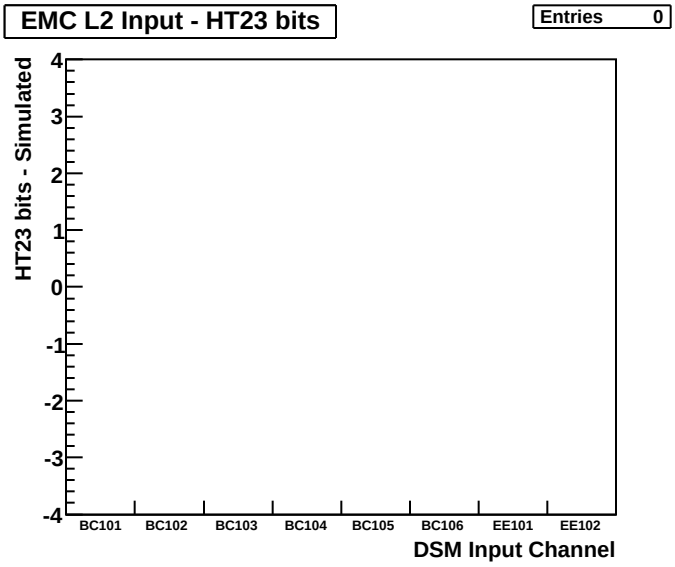

EMC L2 Input - Partial JP Sum
Entries 1622264
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

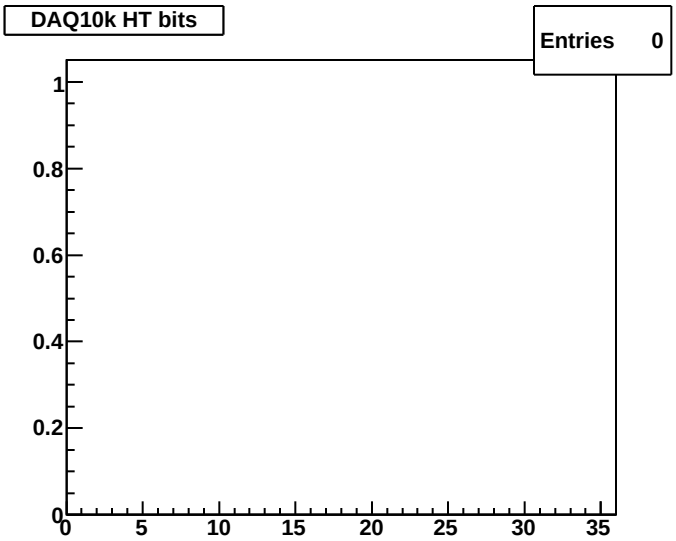

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

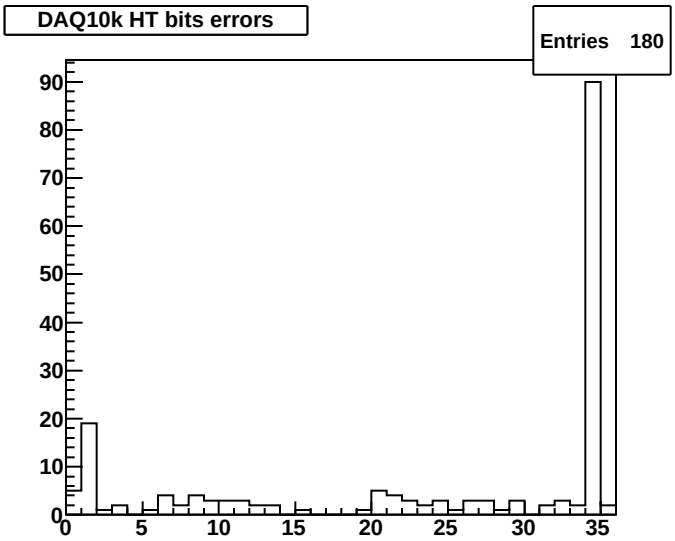

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1622264
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

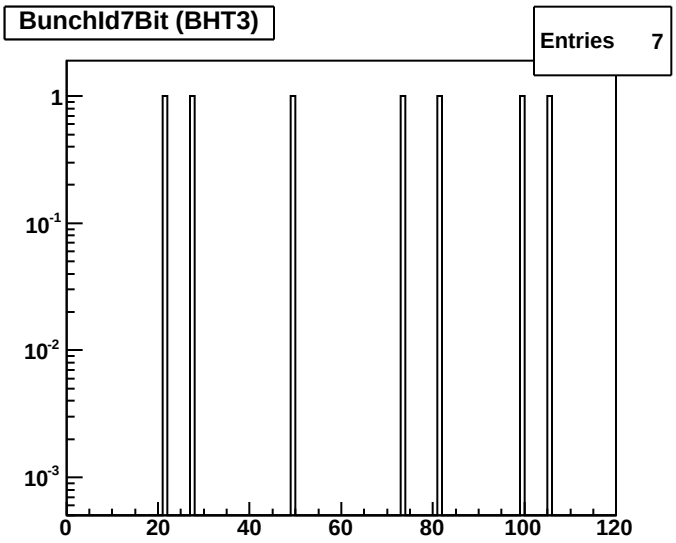

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

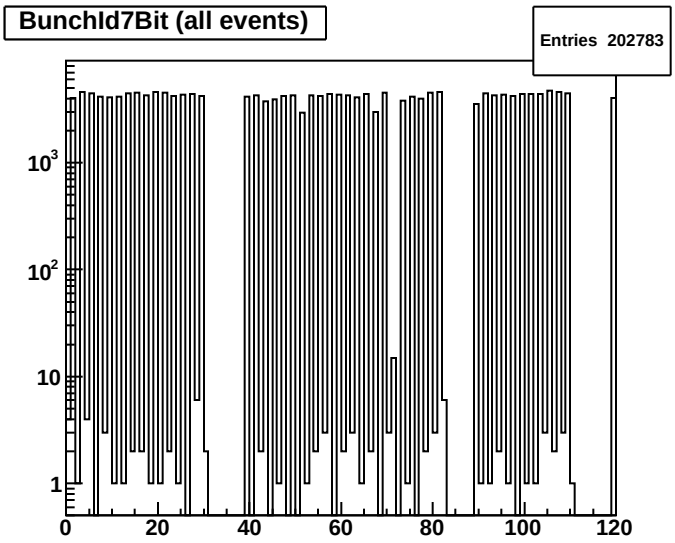

EMC L2 Input - HT23 bits
Entries 1622264
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

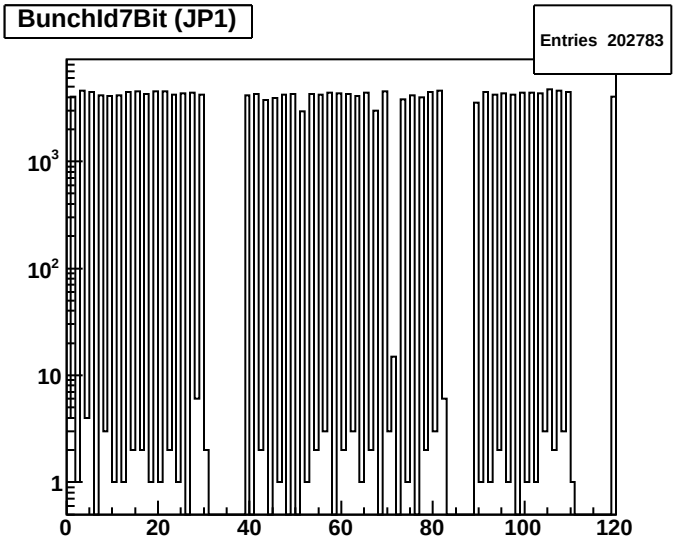

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

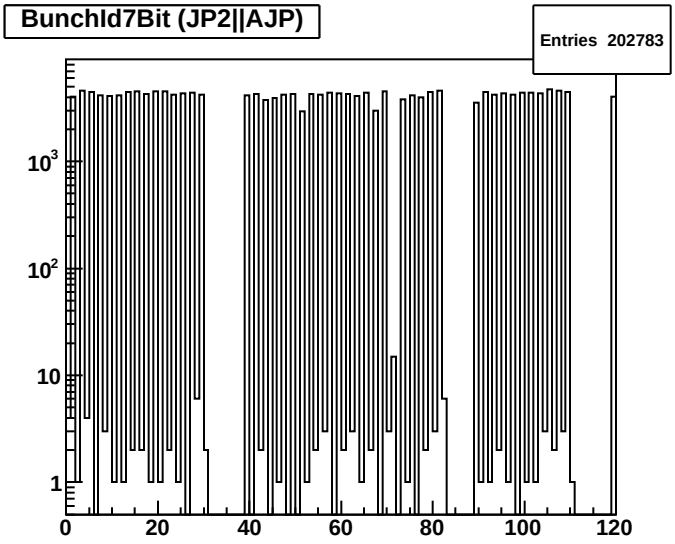

DAQ10k HT bits
Entries 0

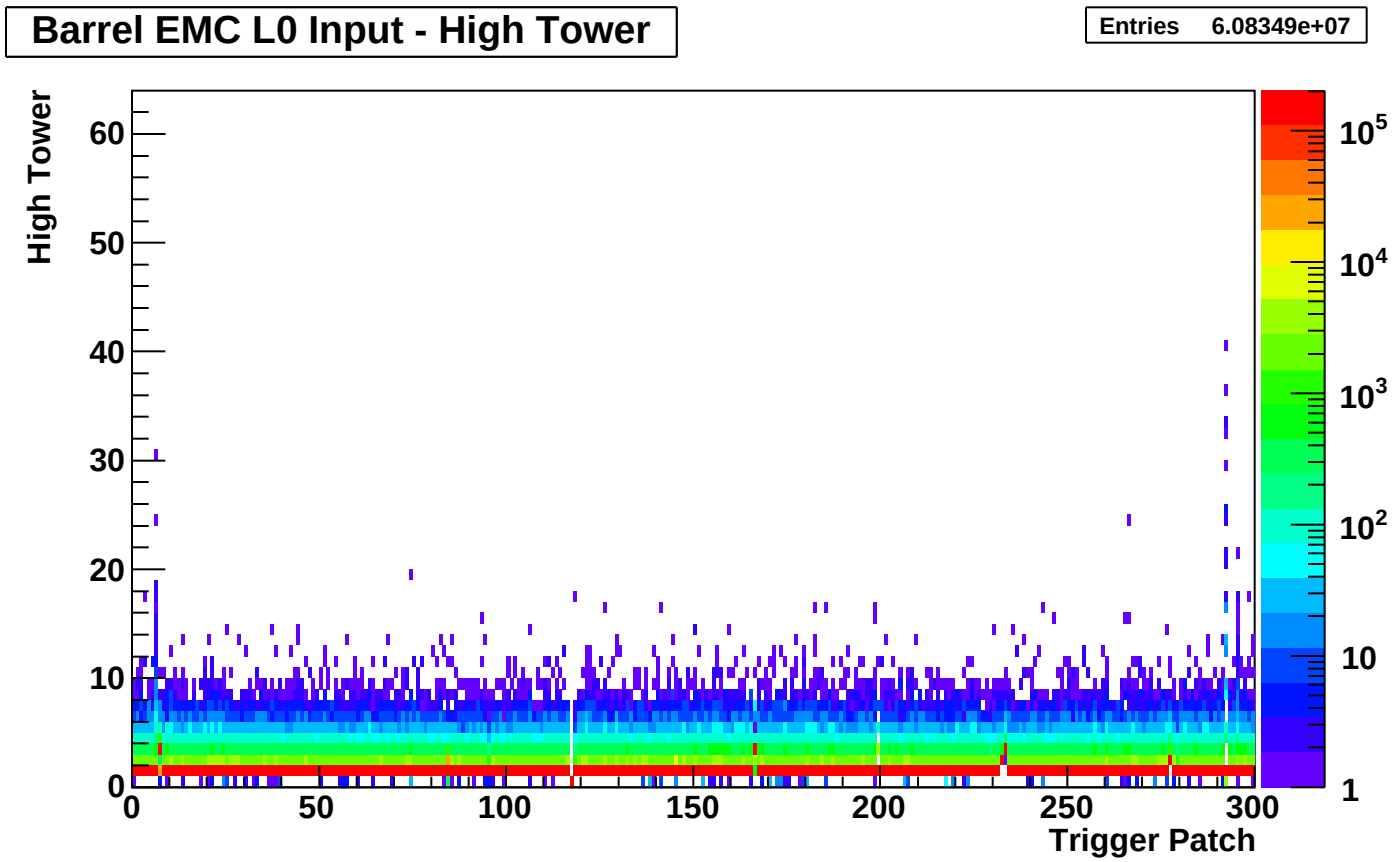

DAQ10k HT bits errors
Entries 180

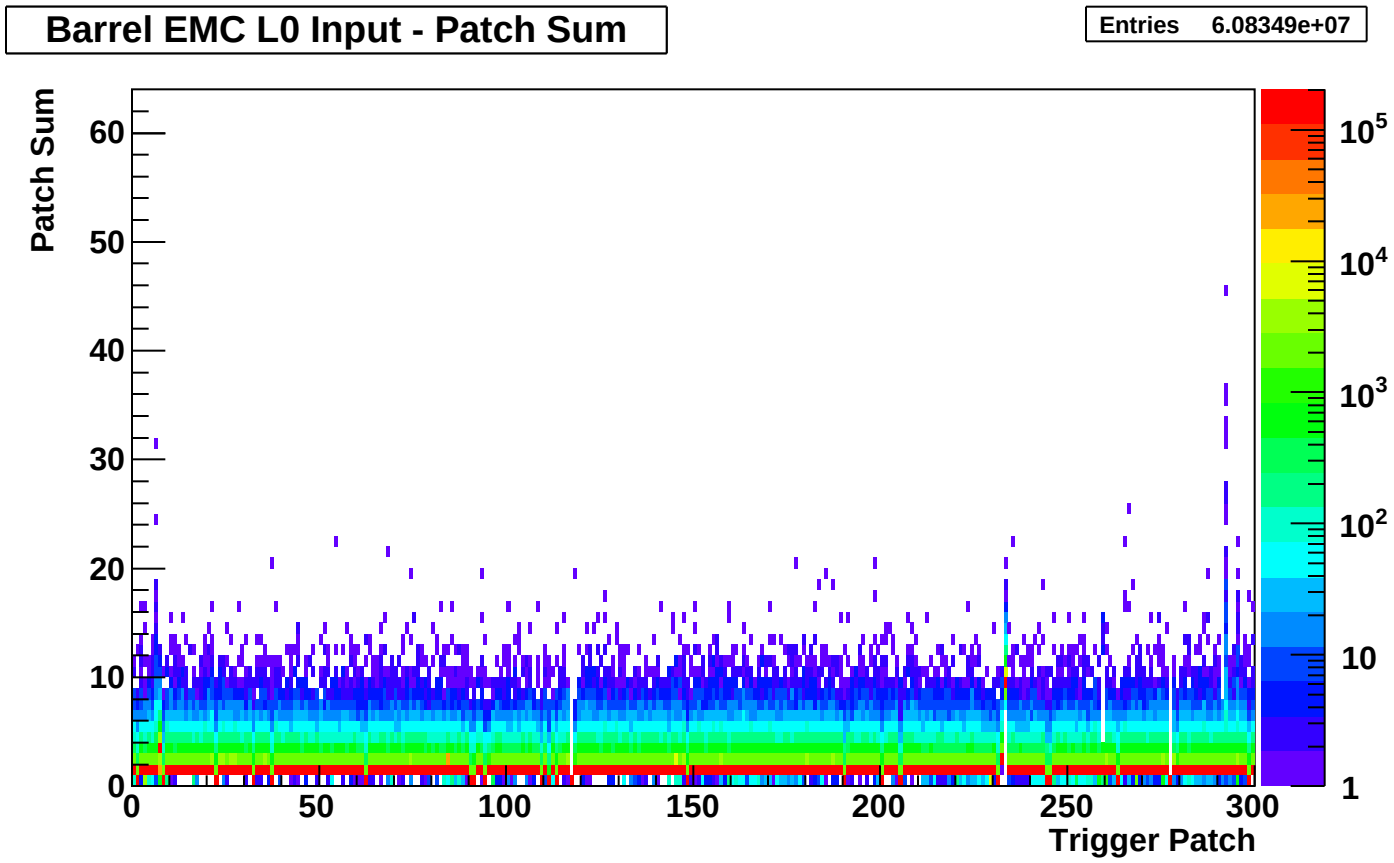

BunchId7Bit (BHT3)
Entries 7

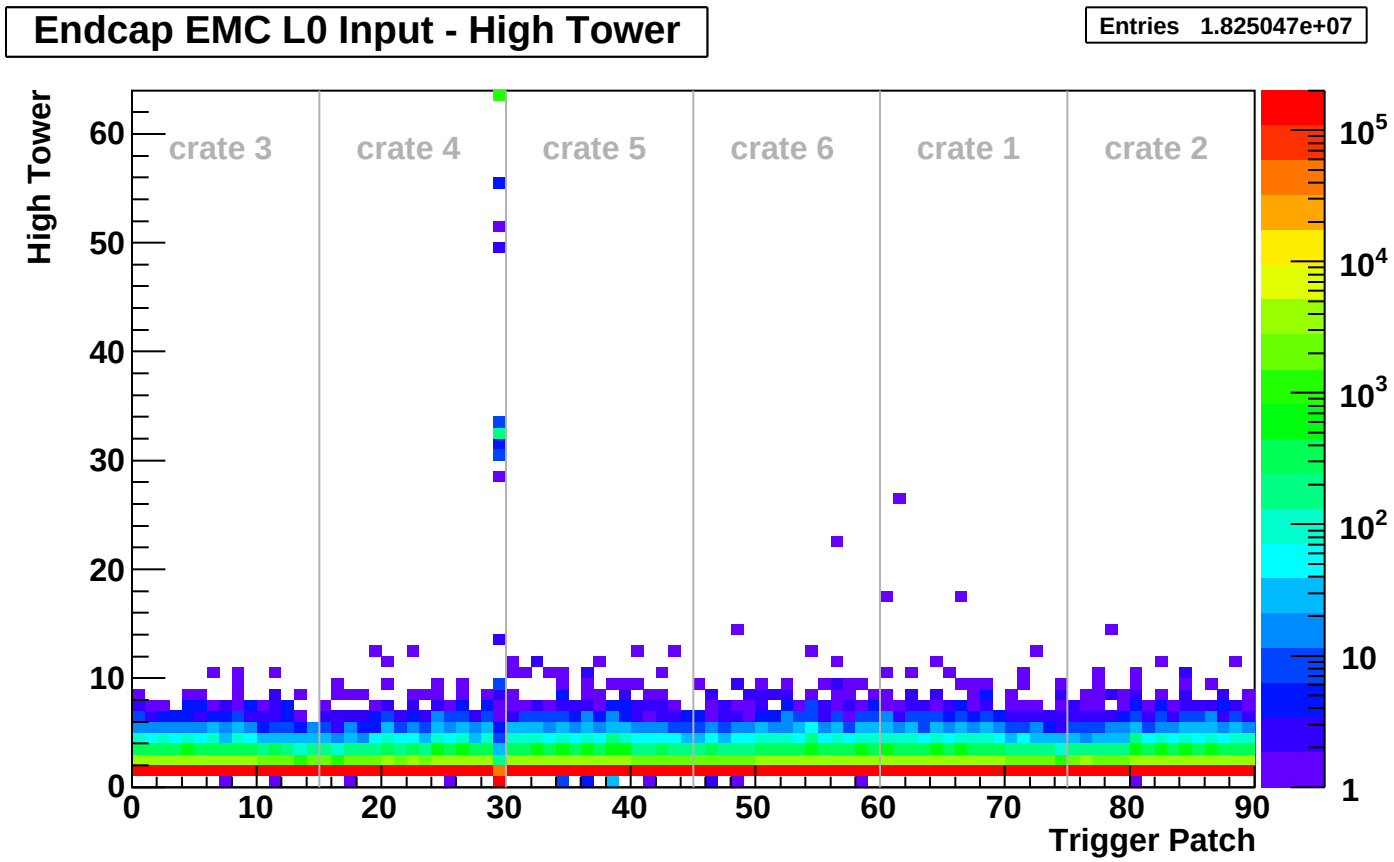

BunchId7Bit (all events)
Entries 202783

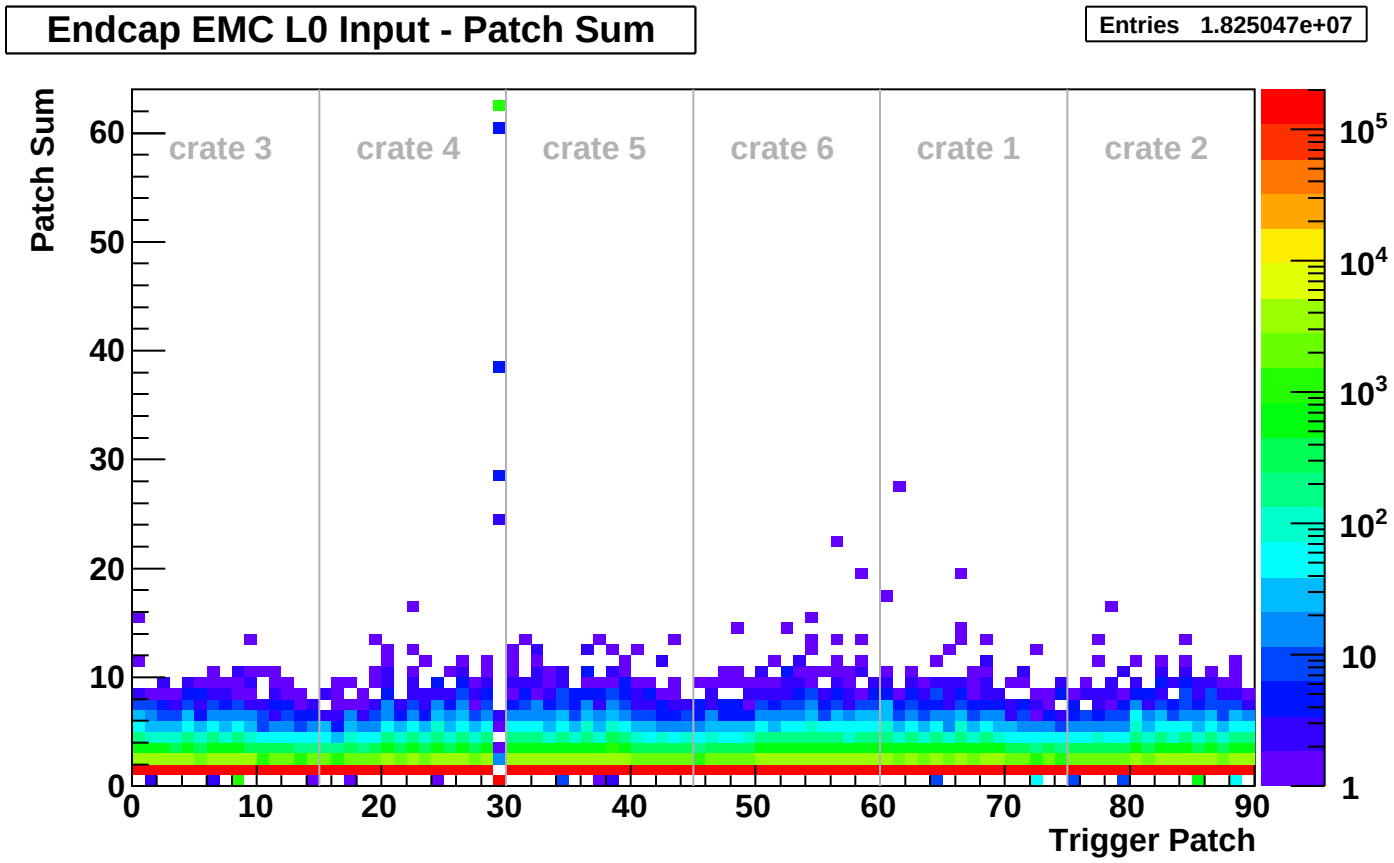

BunchId7Bit (JP1)
Entries 202783

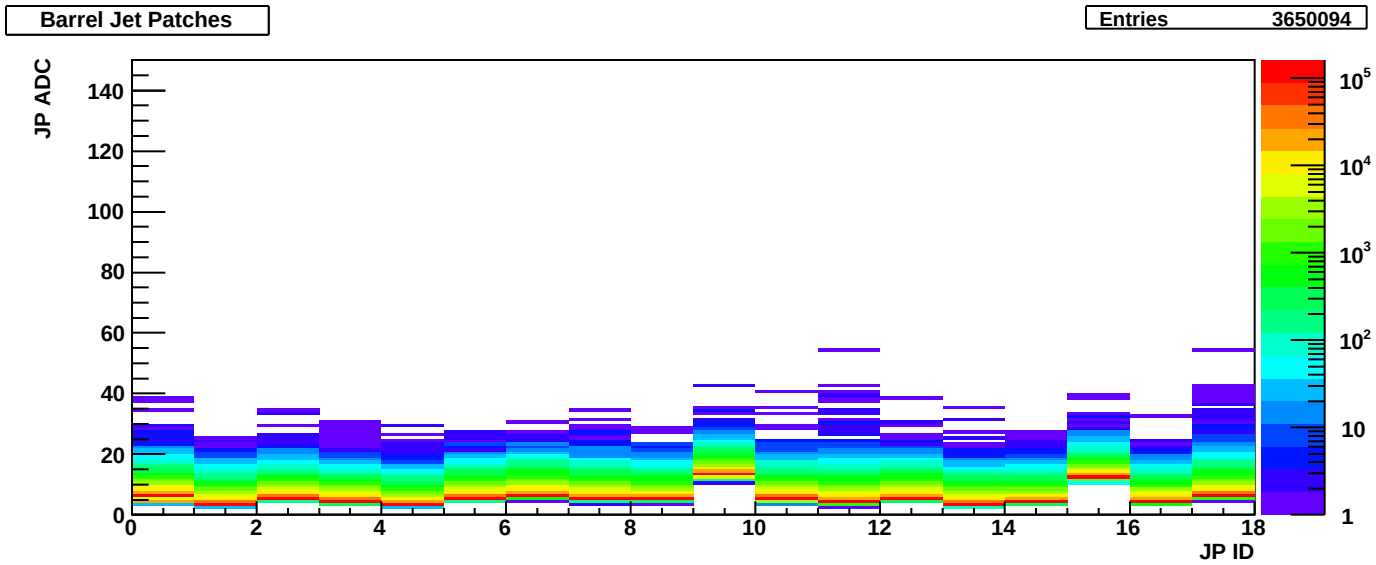

BunchId7Bit (JP2||AJP)
Entries 202783

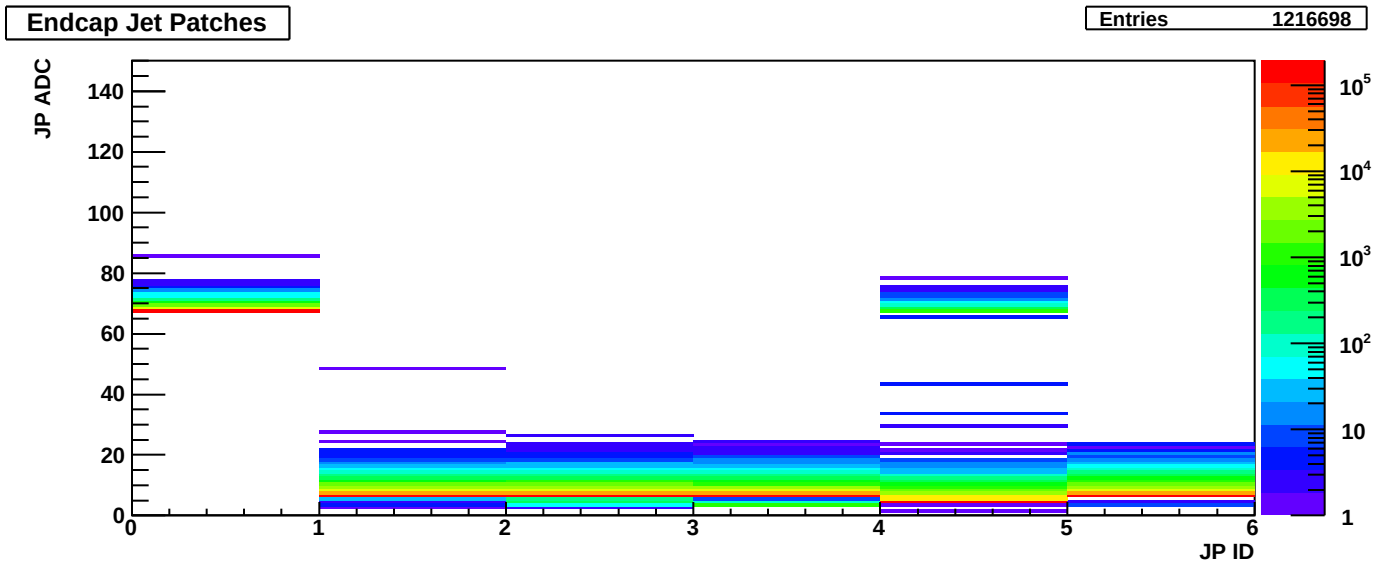

Barrel EMC L0 Input - High Tower
Entries 6.08349e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

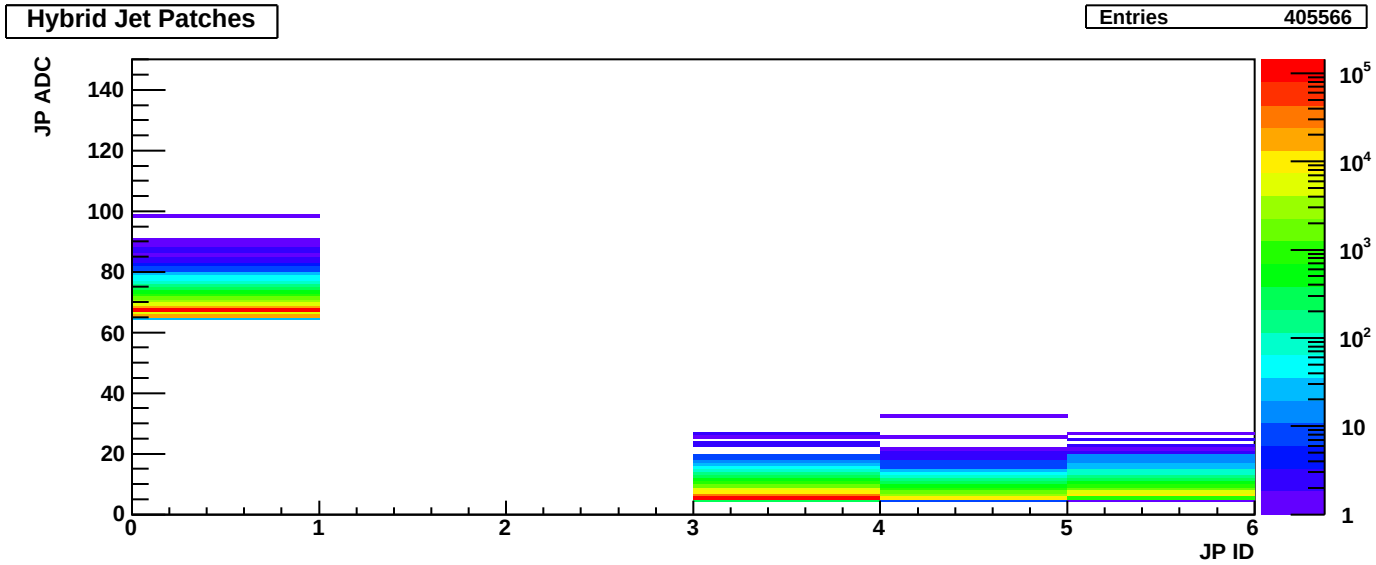

Barrel EMC L0 Input - Patch Sum
Entries 6.08349e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

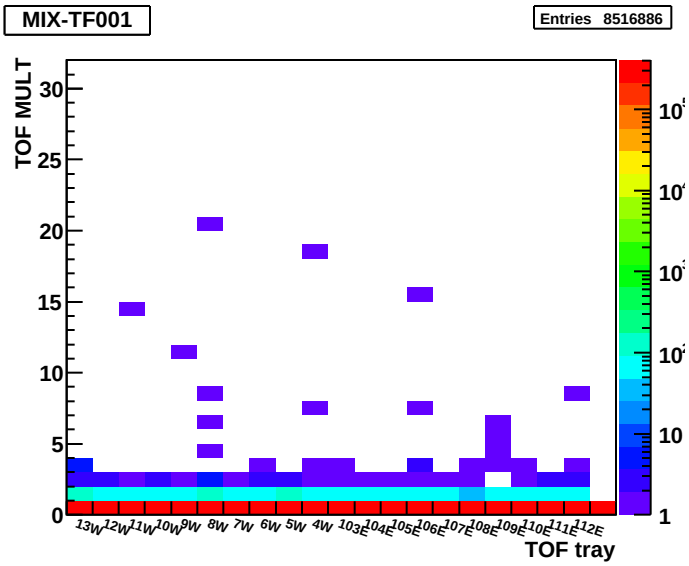

Endcap EMC L0 Input - High Tower
Entries 1.825047e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

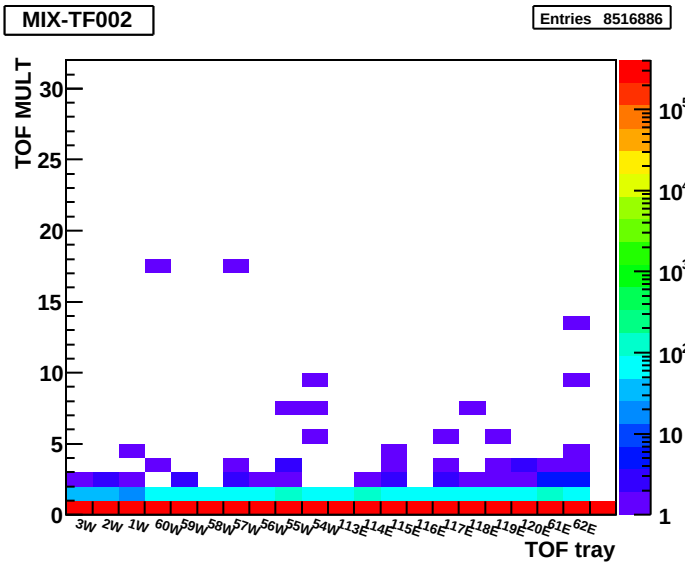

Endcap EMC L0 Input - Patch Sum
Entries 1.825047e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

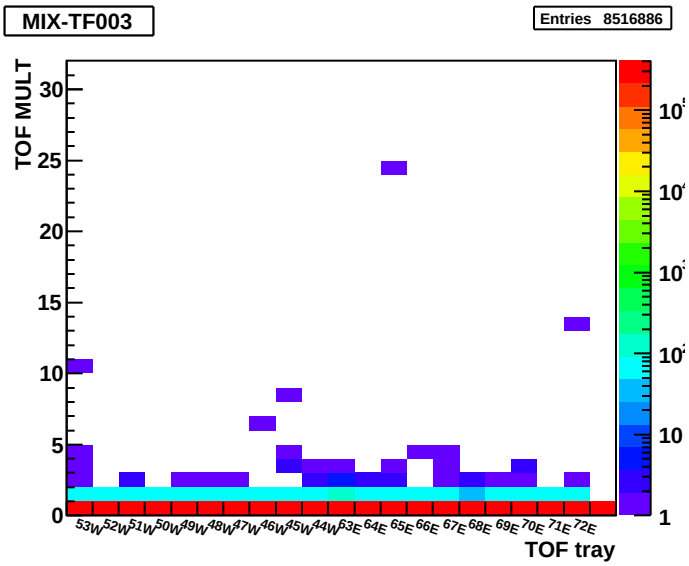

Barrel Jet Patches
Entries 3650094
JP ADC
JP ID

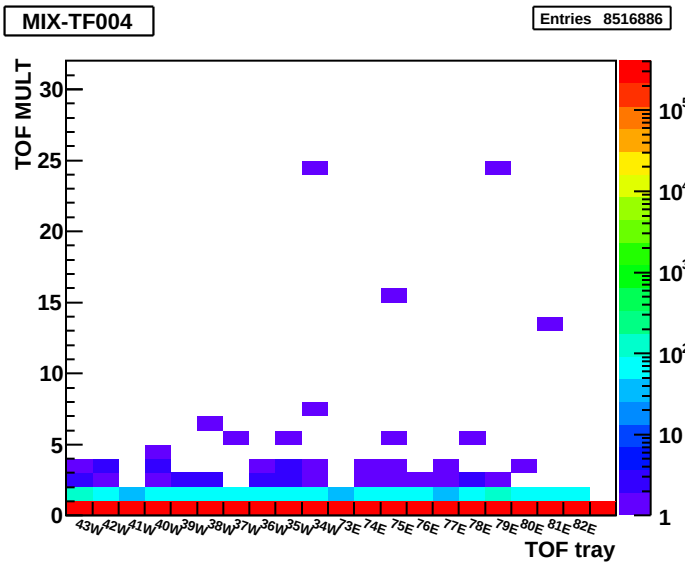

Endcap Jet Patches
Entries 1216698
JP ADC
JP ID

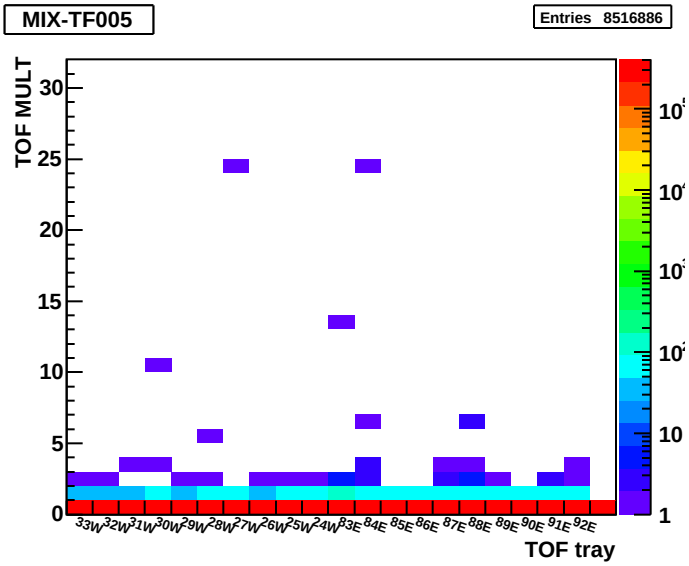

Hybrid Jet Patches
Entries 405566
JP ADC
JP ID

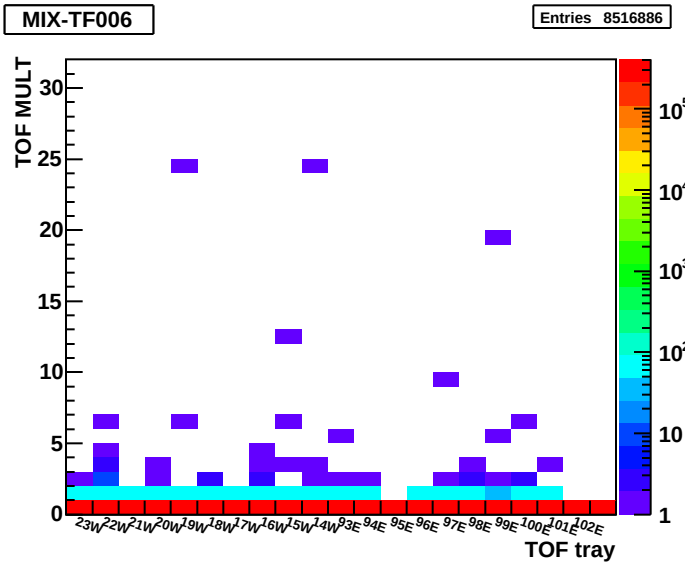

MIX-TF001
Entries 8516886
TOF MULT
TOF tray

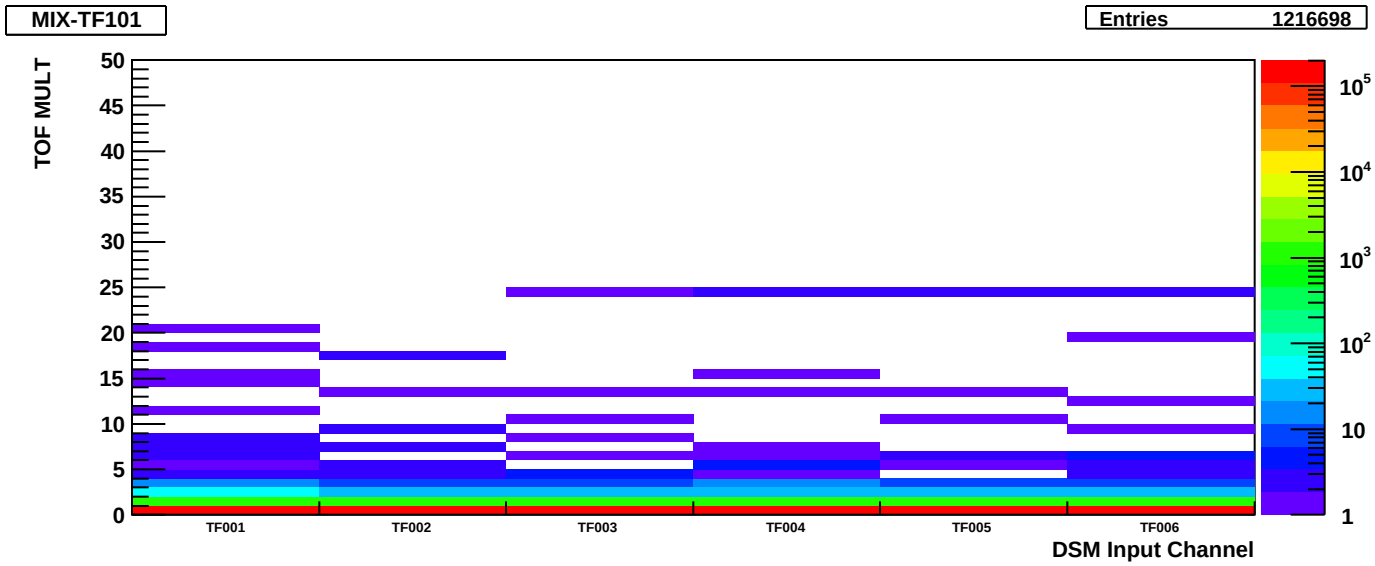

MIX-TF002
Entries 8516886
TOF MULT
TOF tray

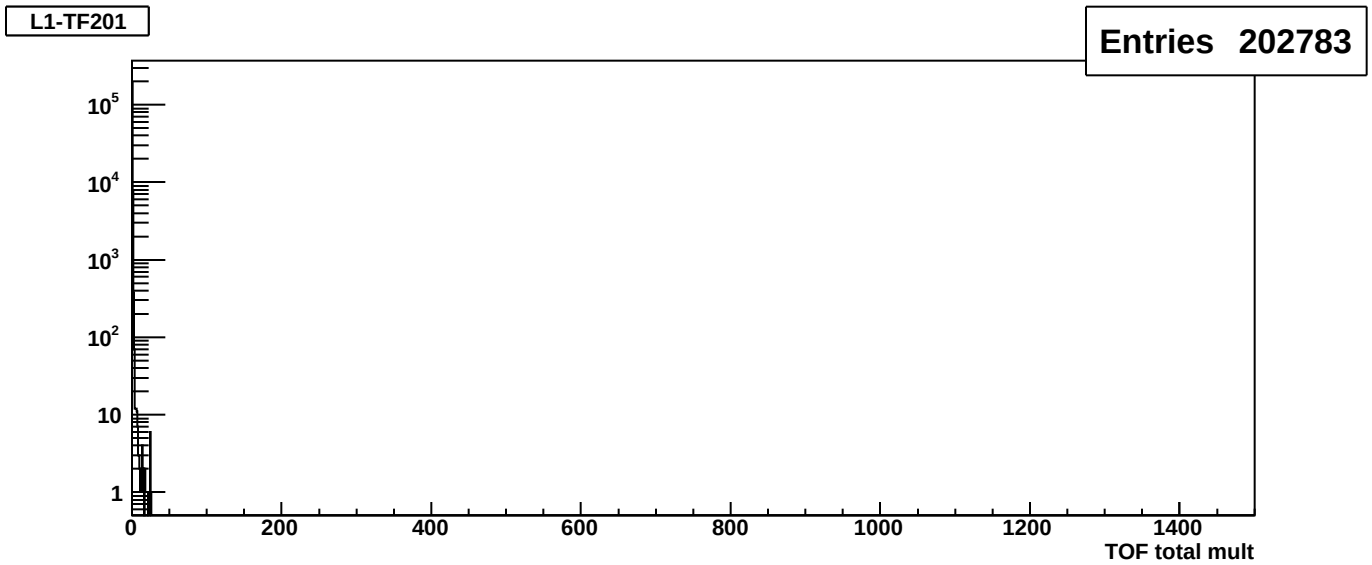

MIX-TF003
Entries 8516886
TOF MULT
TOF tray

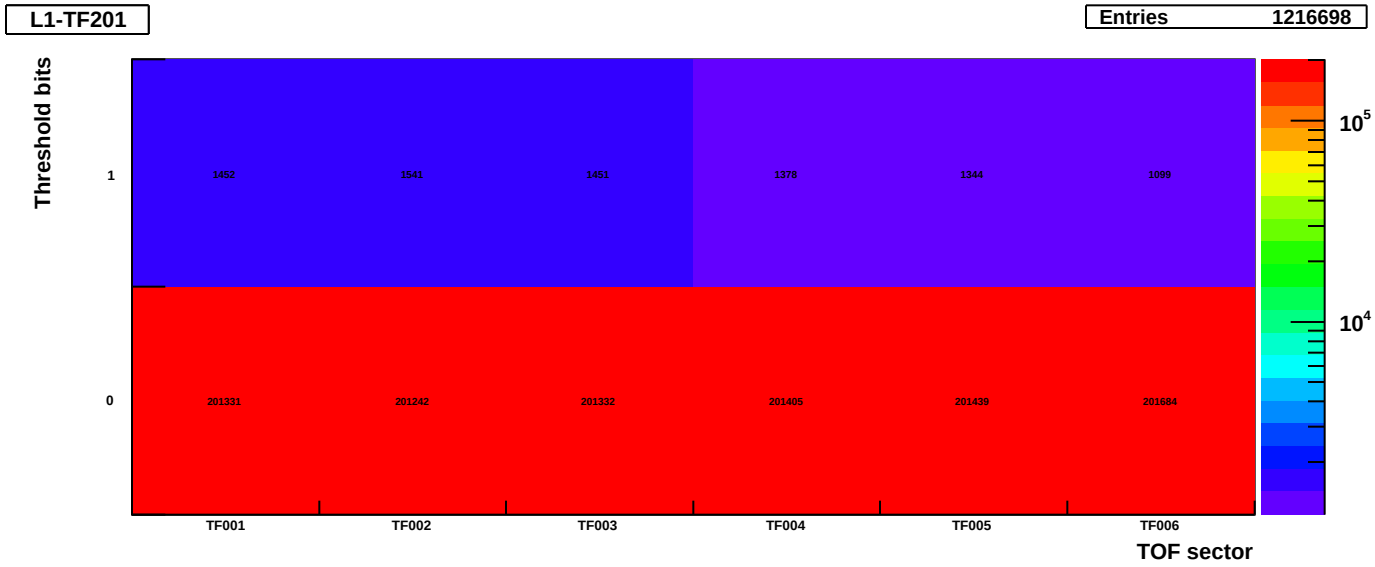

MIX-TF004
Entries 8516886
TOF MULT
TOF tray

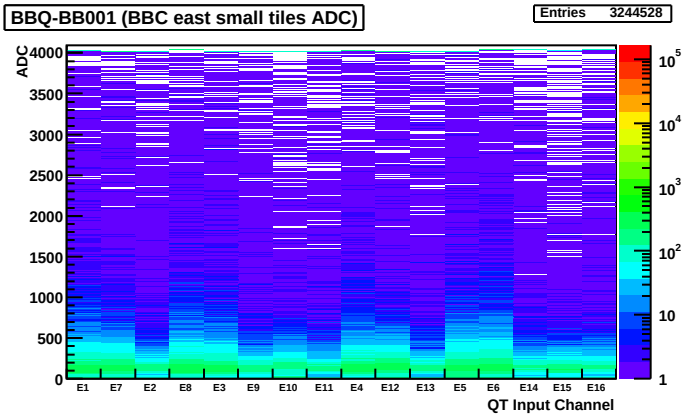

MIX-TF005
Entries 8516886
TOF MULT
TOF tray

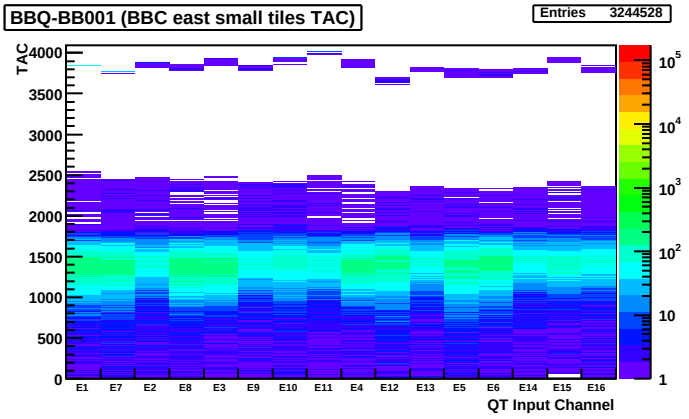

MIX-TF006
Entries 8516886
TOF MULT
TOF tray

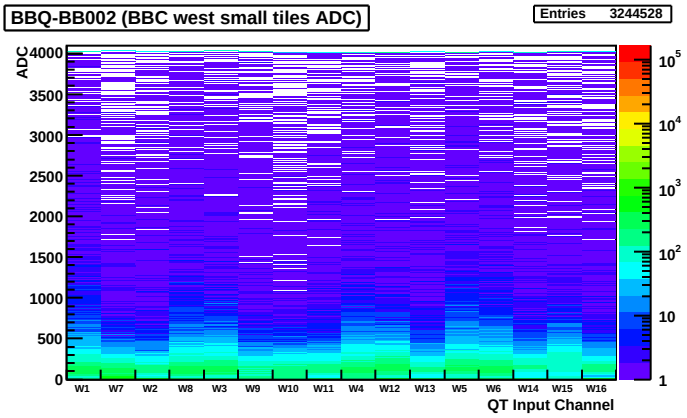

MIX-TF101
Entries 1216698
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

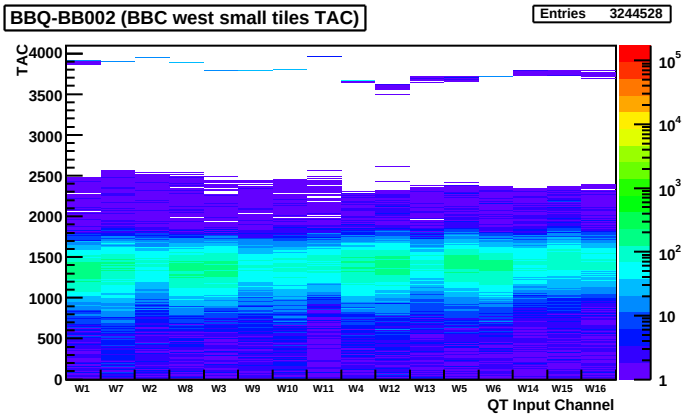

L1-TF201
Entries 202783
TOF total mult

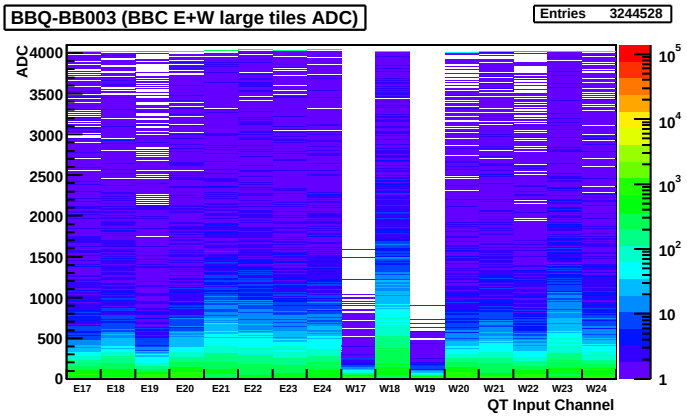

L1-TF201
Entries 1216698
Threshold bits
1452
1541
1451
1378
1344
1099
201331
201242
201332
201405
201439
201684
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

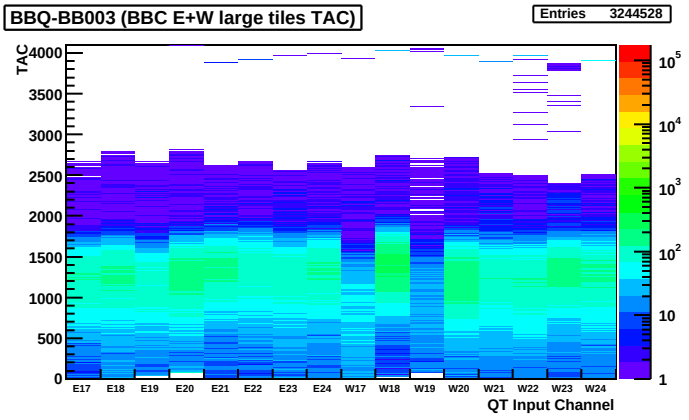

BBQ-BB001 (BBC east small tiles ADC)
Entries 3244528
ADC
QT Input Channel

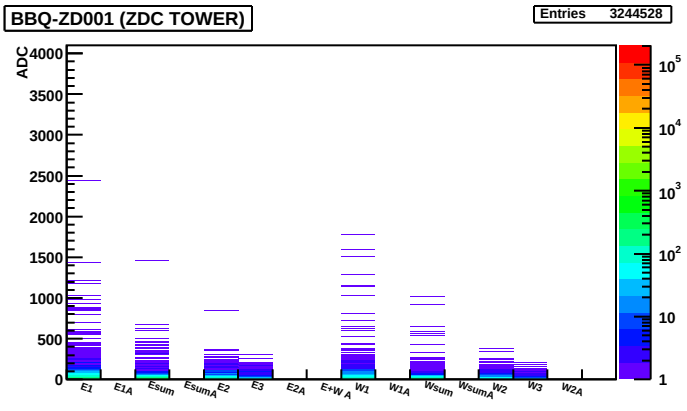

BBQ-BB001 (BBC east small tiles TAC)
Entries 3244528
TAC
QT Input Channel

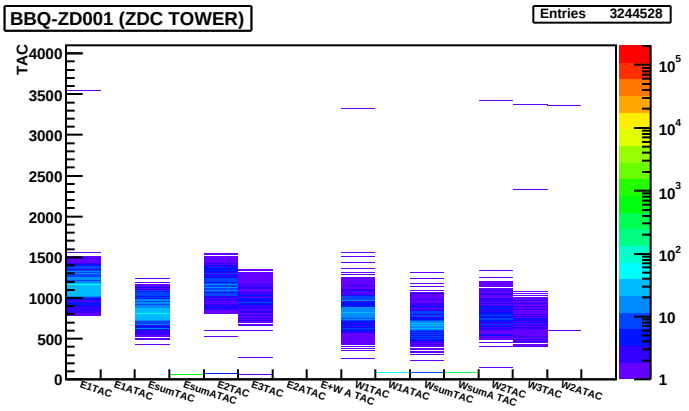

BBQ-BB002 (BBC west small tiles ADC)
Entries 3244528
ADC
QT Input Channel

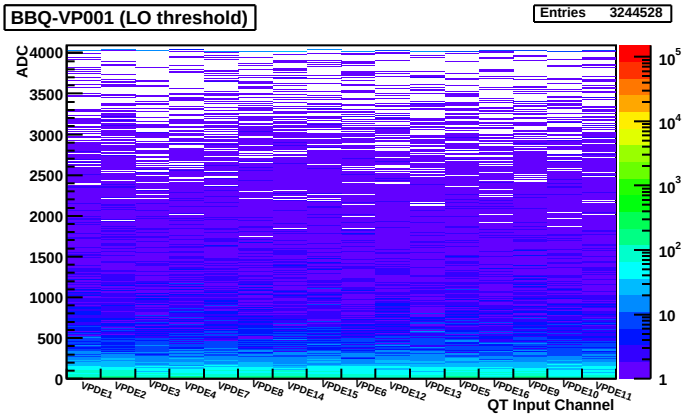

BBQ-BB002 (BBC west small tiles TAC)
Entries 3244528
TAC
QT Input Channel

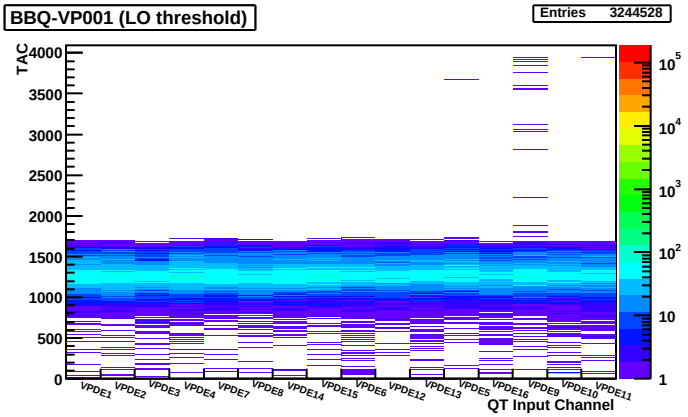

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3244528
ADC
QT Input Channel

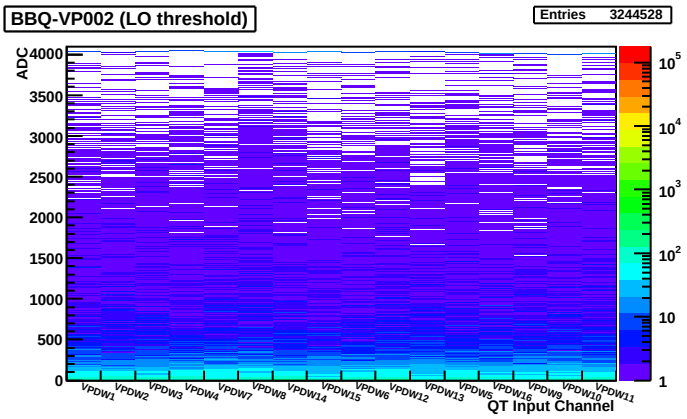

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3244528
TAC
QT Input Channel

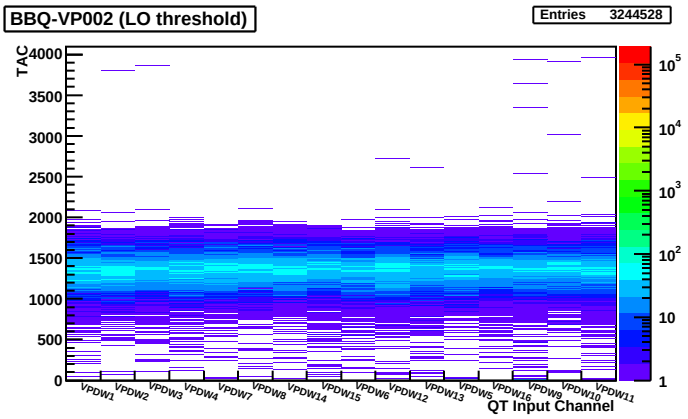

BBQ-ZD001 (ZDC TOWER)
Entries 3244528
ADC

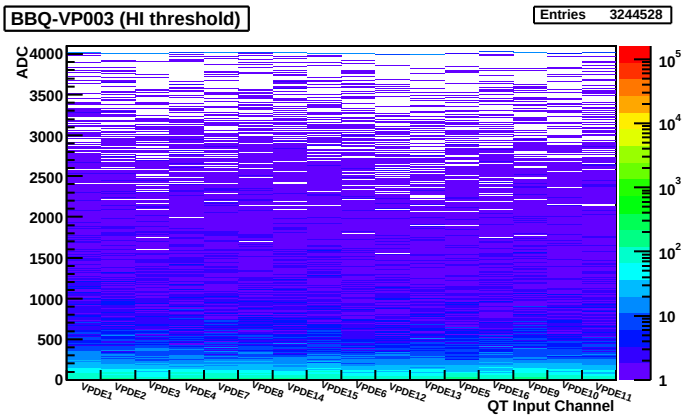

BBQ-ZD001 (ZDC TOWER)
Entries 3244528
TAC

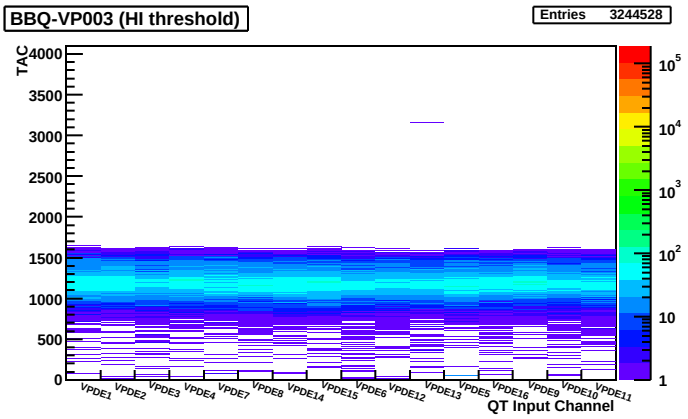

BBQ-VP001 (LO threshold)
Entries 3244528
ADC
QT Input Channel

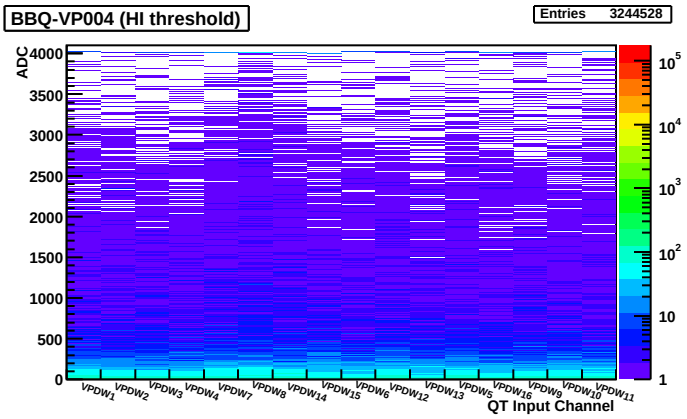

BBQ-VP001 (LO threshold)
Entries 3244528
TAC
QT Input Channel

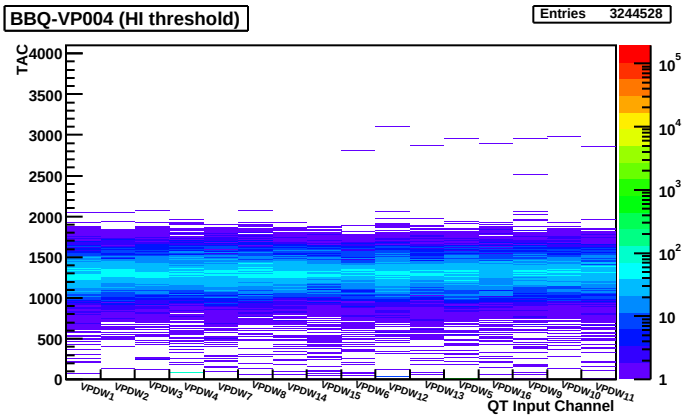

BBQ-VP002 (LO threshold)
Entries 3244528
ADC
QT Input Channel

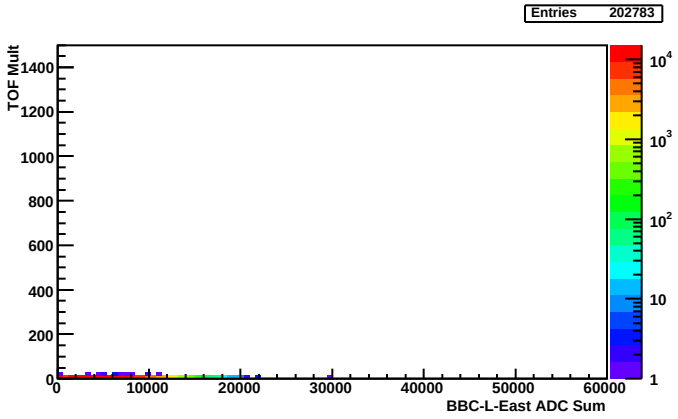

BBQ-VP002 (LO threshold)
Entries 3244528
TAC
QT Input Channel

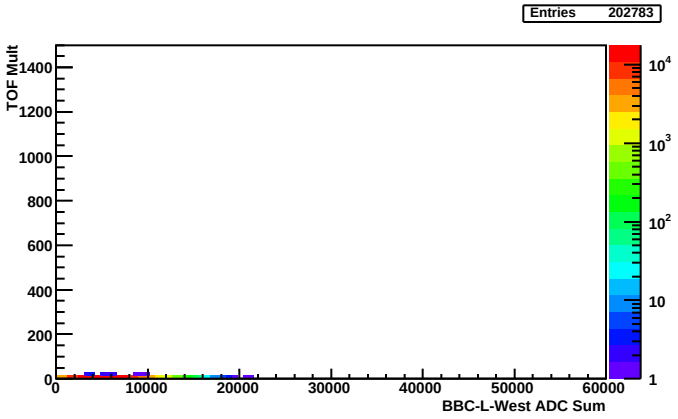

BBQ-VP003 (HI threshold)
Entries 3244528
ADC
QT Input Channel

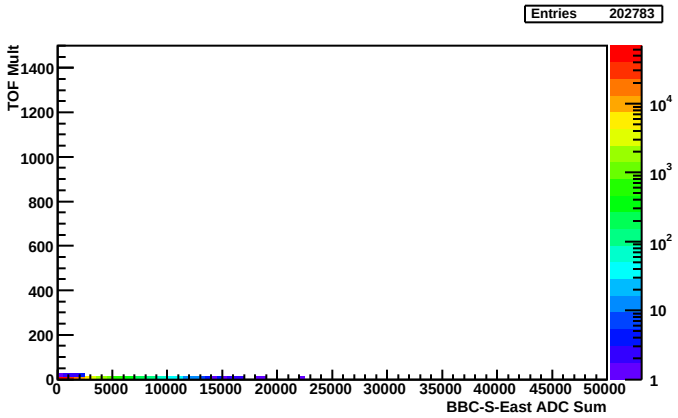

BBQ-VP003 (HI threshold)
Entries 3244528
TAC
QT Input Channel

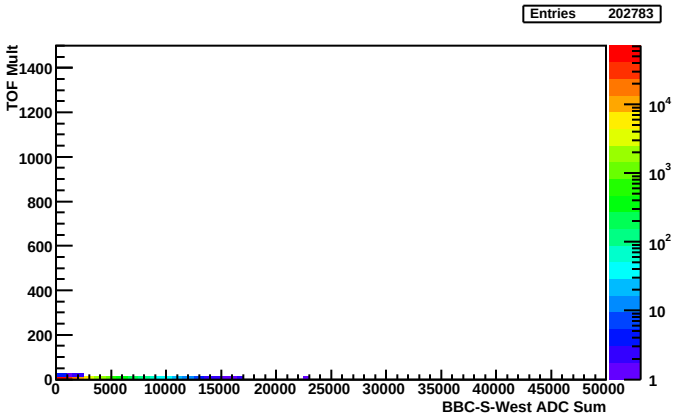

BBQ-VP004 (HI threshold)
Entries 3244528
ADC
QT Input Channel

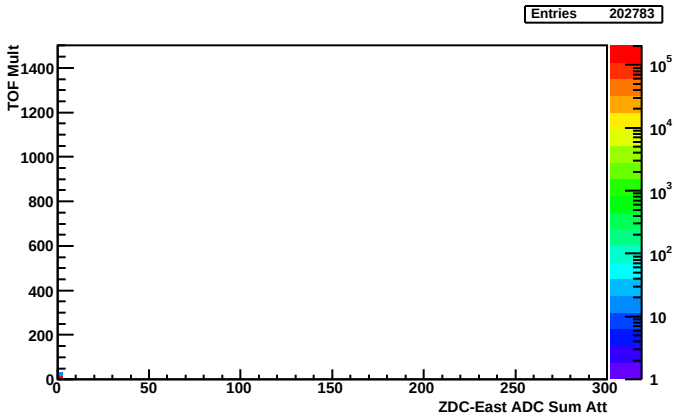

BBQ-VP004 (HI threshold)
Entries 3244528
TAC
QT Input Channel

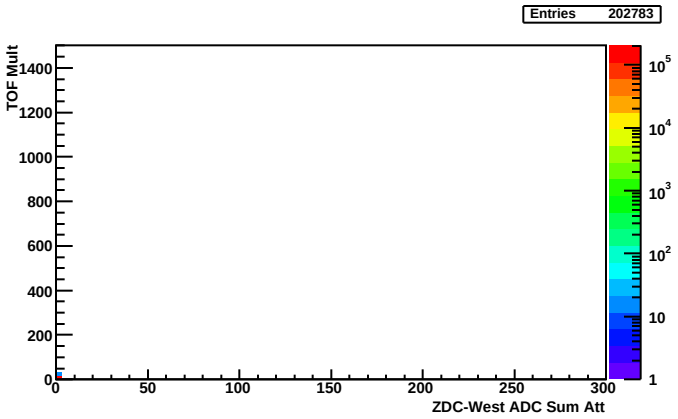

Entries 202783
TOF Mult
BBC-L-East ADC Sum

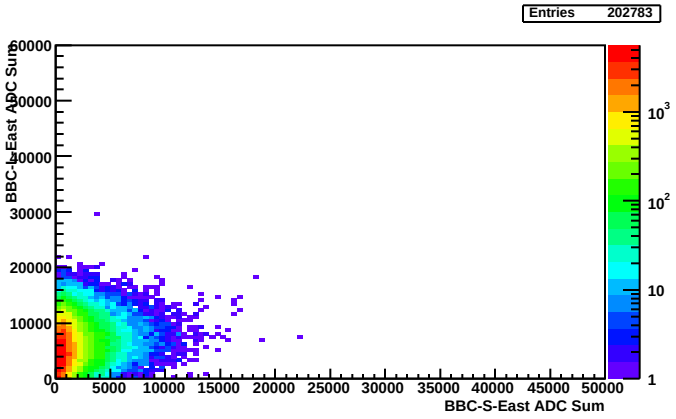

Entries 202783
TOF Mult
BBC-L-West ADC Sum

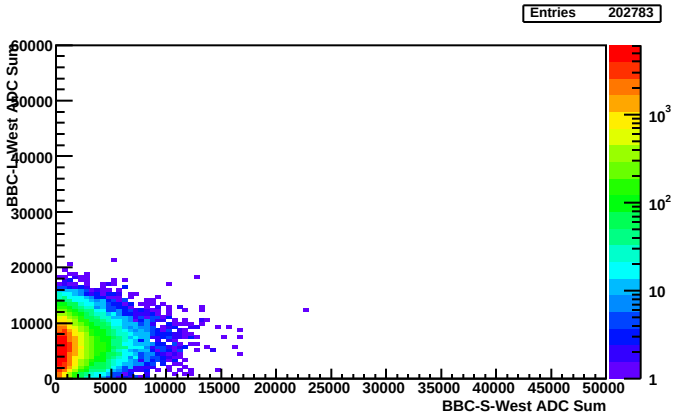

Entries 202783
TOF Mult
BBC-S-East ADC Sum

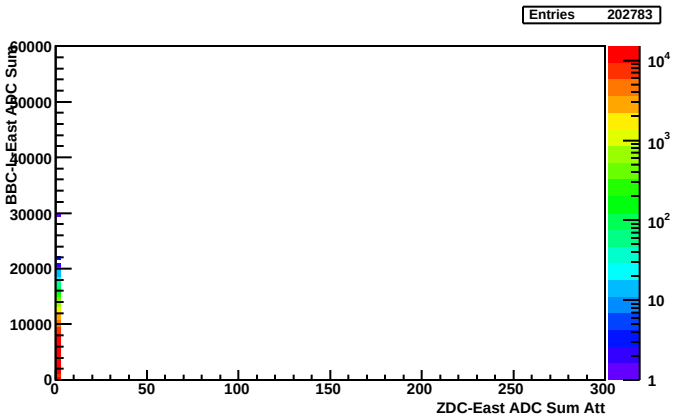

Entries 202783
TOF Mult
BBC-S-West ADC Sum

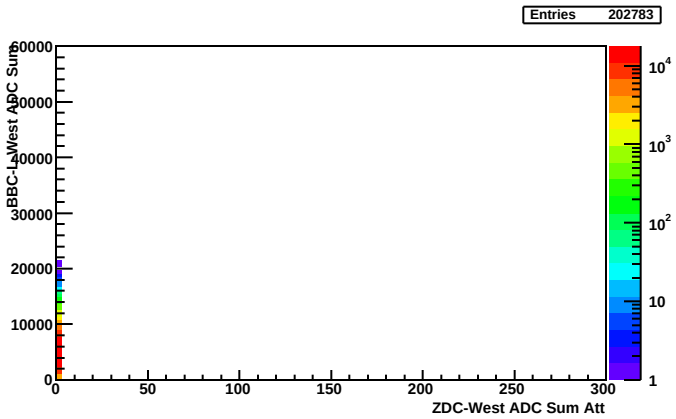

Entries 202783
TOF Mult
ZDC-East ADC Sum Att

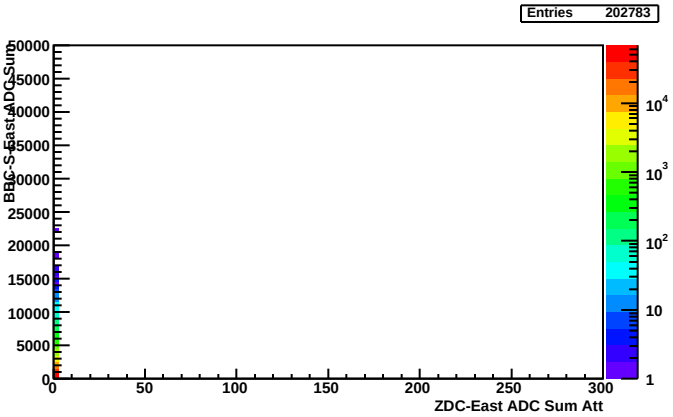

Entries 202783
TOF Mult
ZDC-West ADC Sum Att

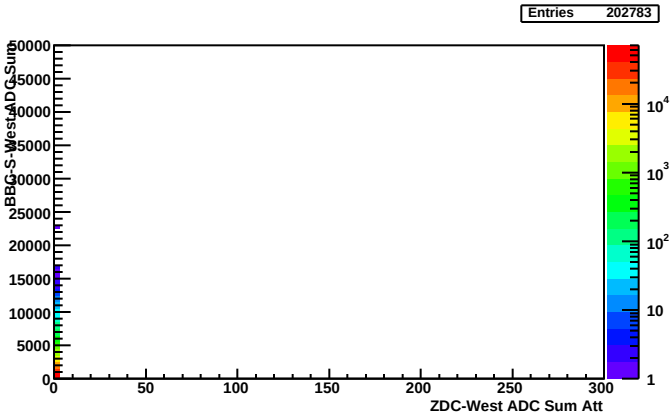

Entries 202783
BBC-L-East ADC Sum
BBC-S-East ADC Sum

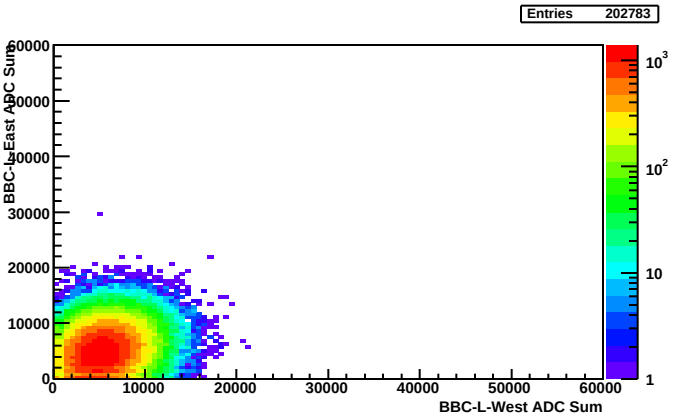

Entries 202783
BBC-L-West ADC Sum
BBC-S-West ADC Sum

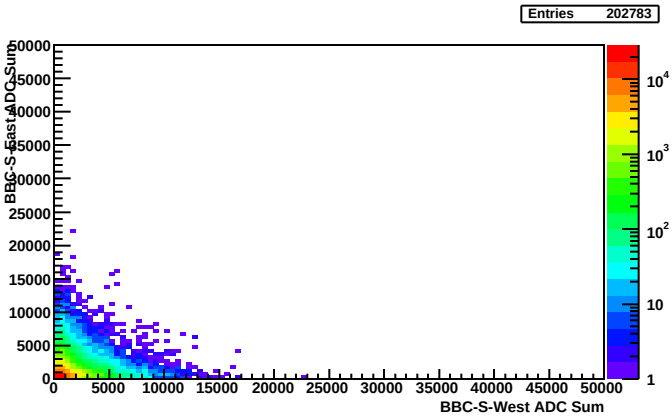

Entries 202783
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

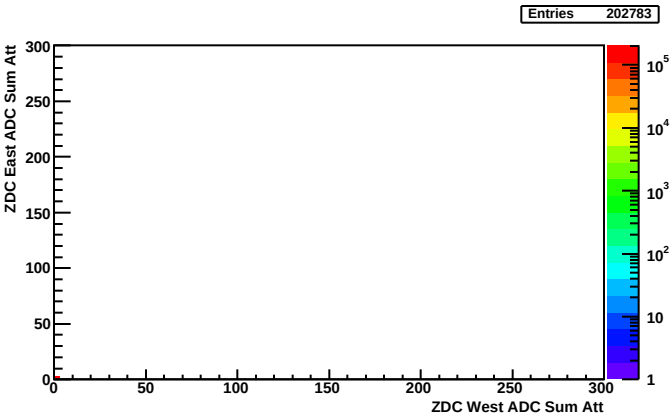

Entries 202783
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

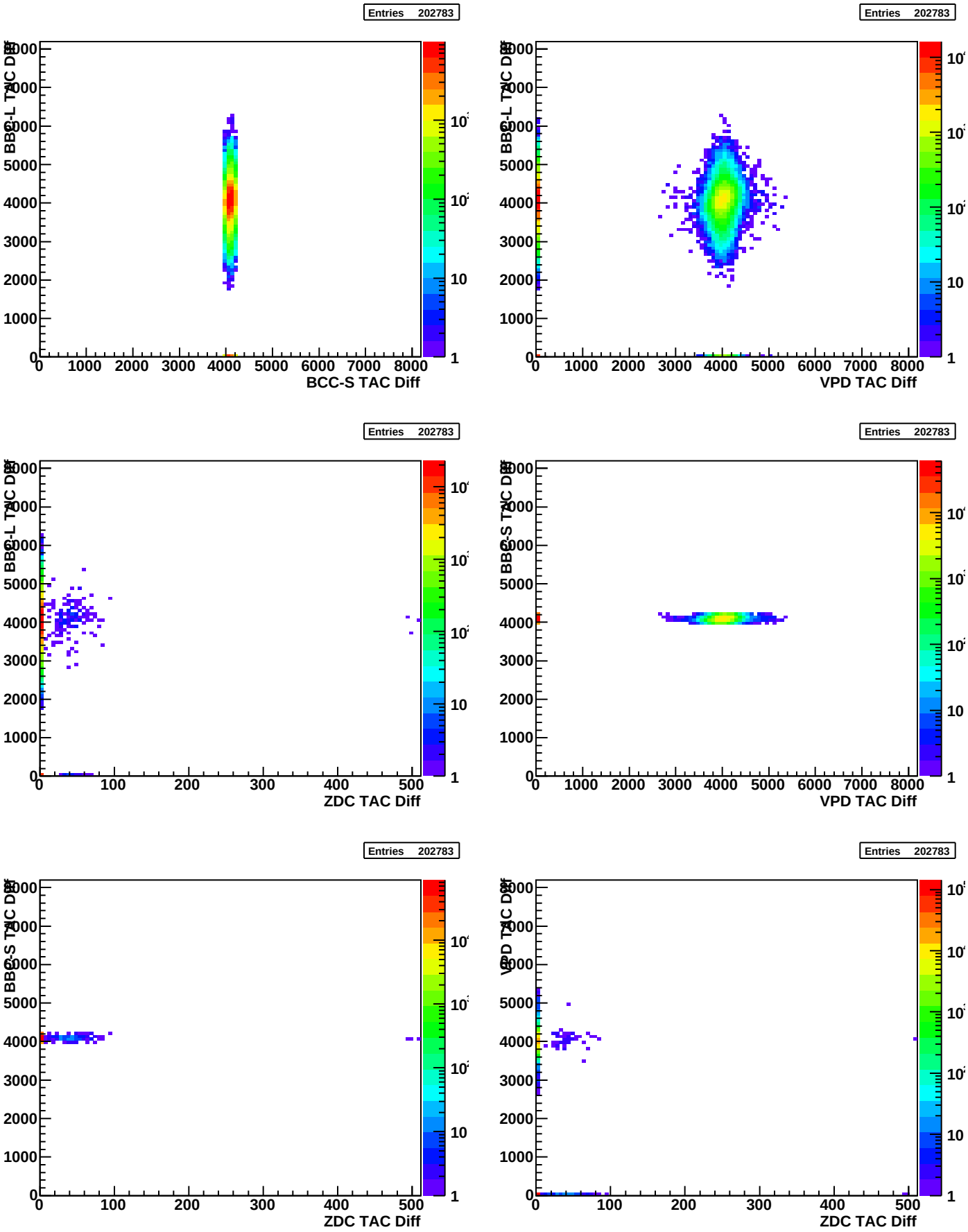

Entries 202783
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

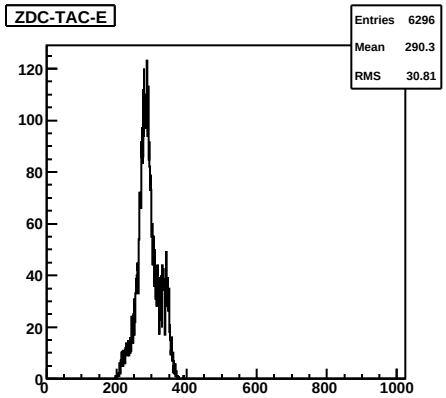

Entries 202783
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

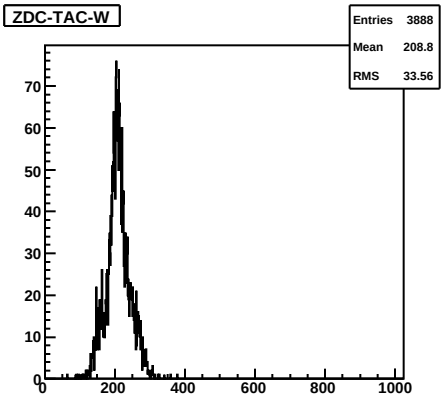

Entries 202783
BBC-L-East ADC Sum
BBC-L-West ADC Sum

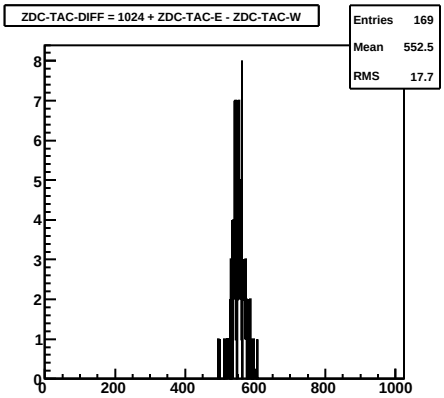

Entries 202783
BBC-S-East ADC Sum
BBC-S-West ADC Sum

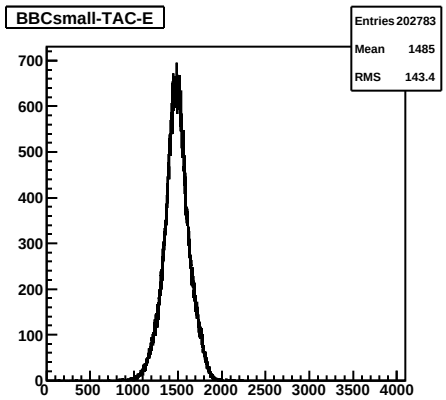

Entries 202783
ZDC East ADC Sum Att
ZDC West ADC Sum Att

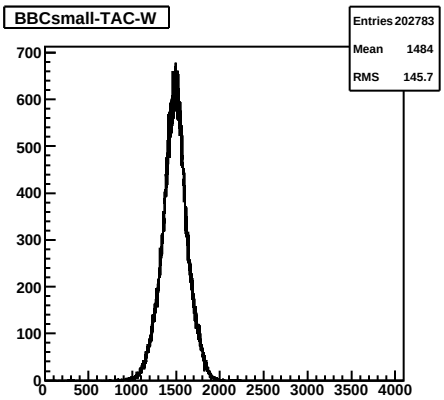

Entries 202783
BBC-L TAC Diff
BCC-S TAC Diff
Entries 202783
BBC-L TAC Diff
VPD TAC Diff
Entries 202783
BBC-L TAC Diff
ZDC TAC Diff
Entries 202783
BBC-S TAC Diff
VPD TAC Diff
Entries 202783
BBC-S TAC Diff
ZDC TAC Diff
Entries 202783
VPD TAC Diff
ZDC TAC Diff

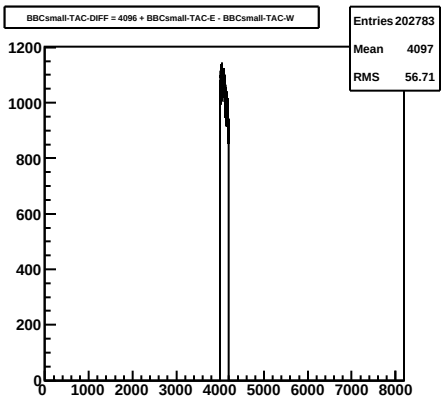

ZDC-TAC-E
Entries 6296
Mean 290.3
RMS 30.81

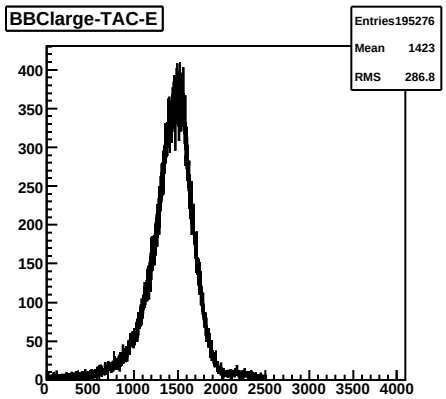

ZDC-TAC-W
Entries 3888
Mean 208.8
RMS 33.56

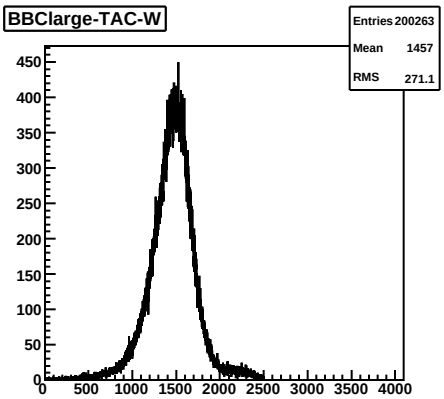

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 169
Mean 552.5
RMS 17.7

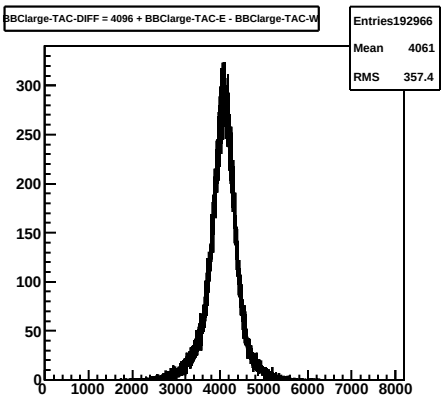

BBCsmall-TAC-E
Entries 202783
Mean 1485
RMS 143.4

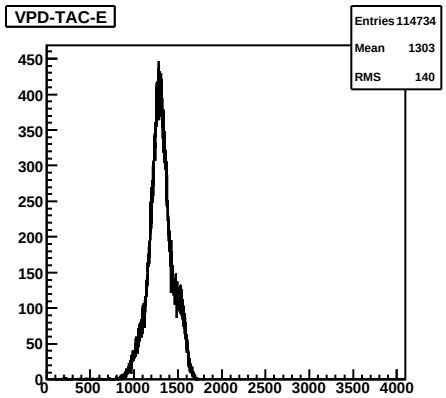

BBCsmall-TAC-W
Entries 202783
Mean 1484
RMS 145.7

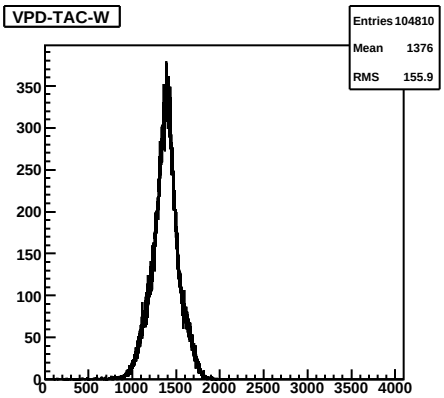

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 202783
Mean 4097
RMS 56.71

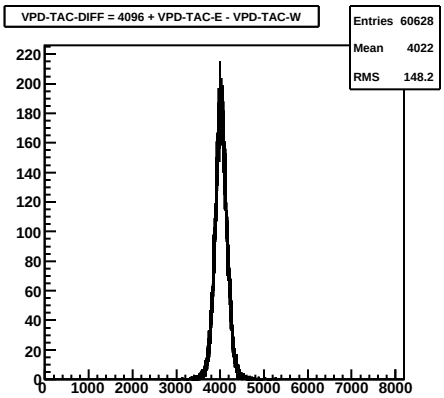

BBClarge-TAC-E
Entries 195276
Mean 1423
RMS 286.8

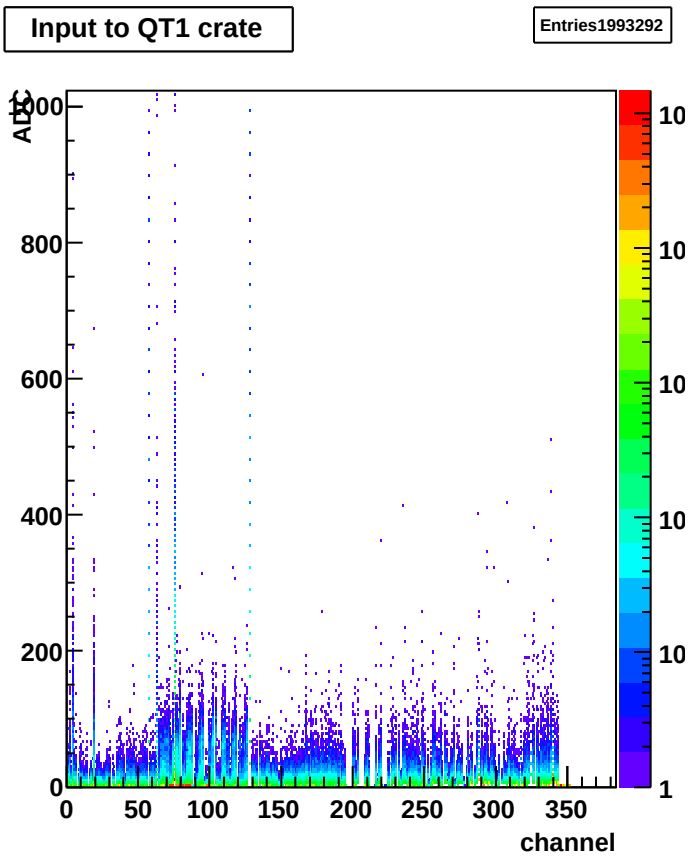

BBClarge-TAC-W
Entries 200263
Mean 1457
RMS 271.1

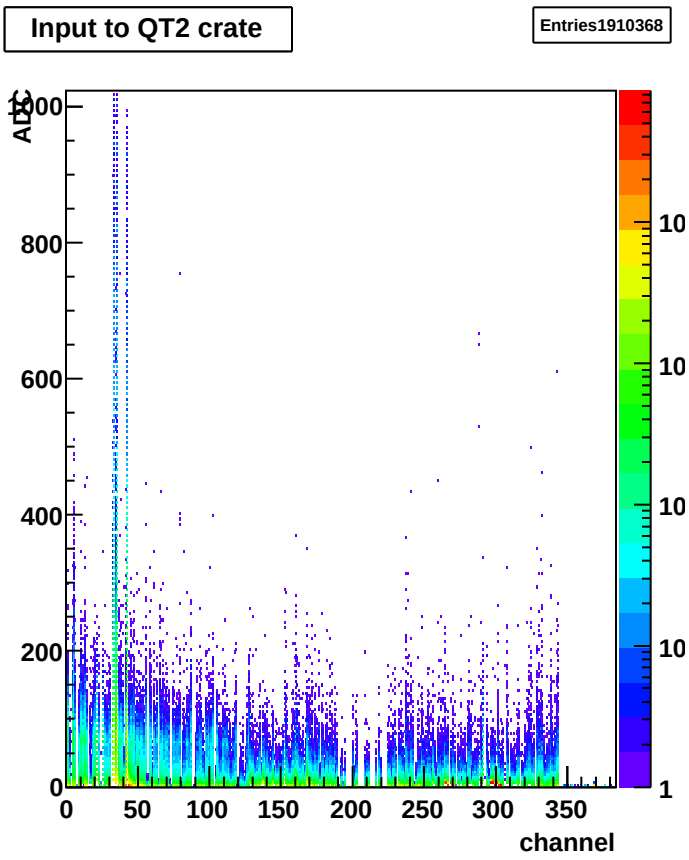

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 192966
Mean 4061
RMS 357.4

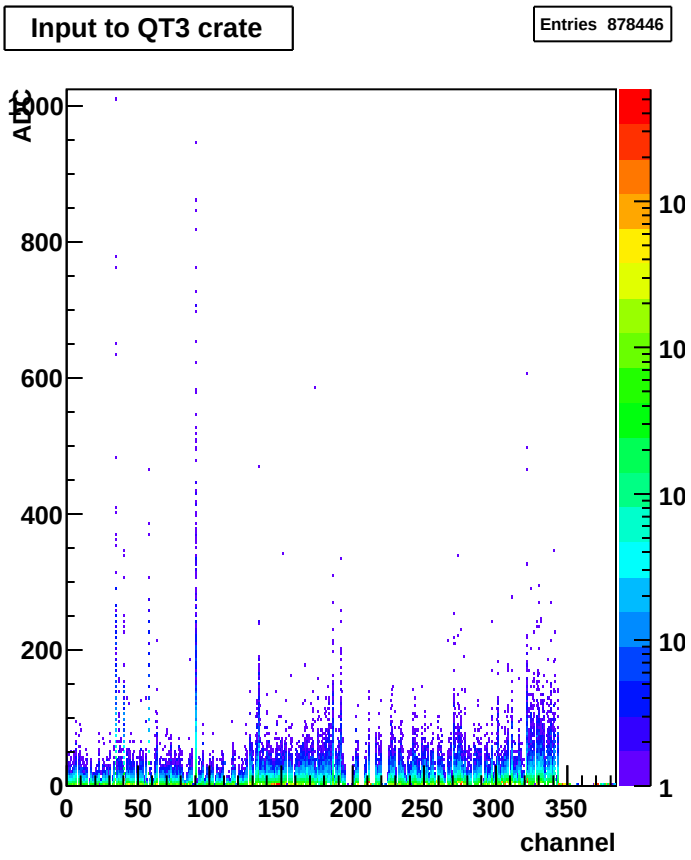

VPD-TAC-E
Entries 114734
Mean 1303
RMS 140

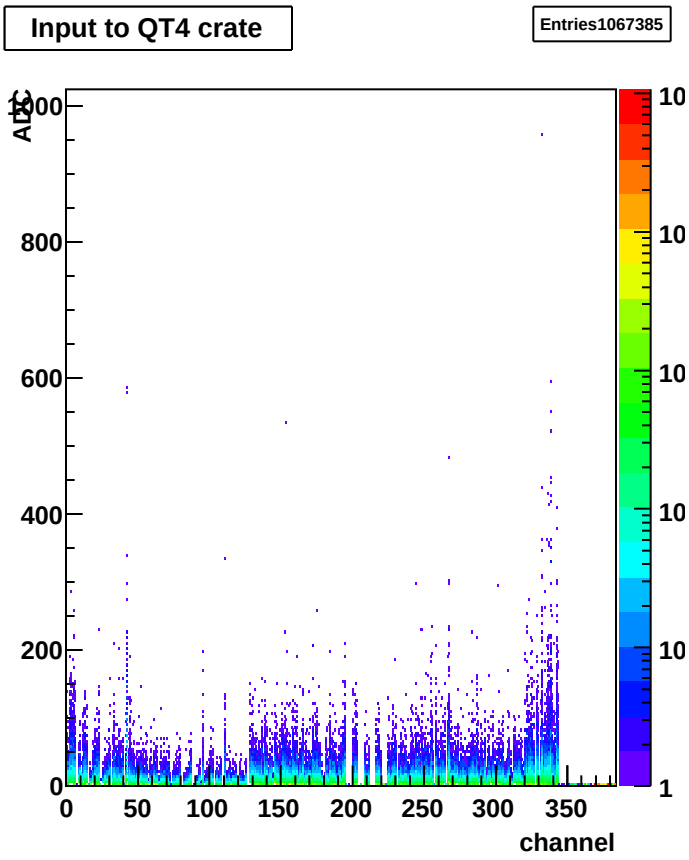

VPD-TAC-W
Entries 104810
Mean 1376
RMS 155.9

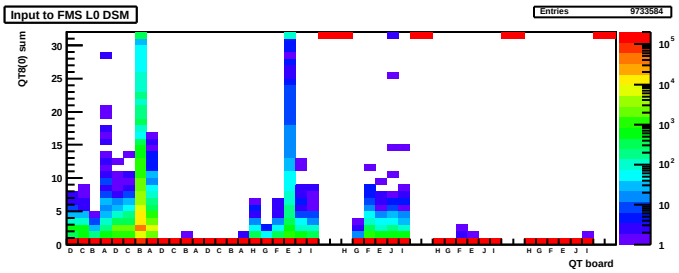

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 60628
Mean 4022
RMS 148.2

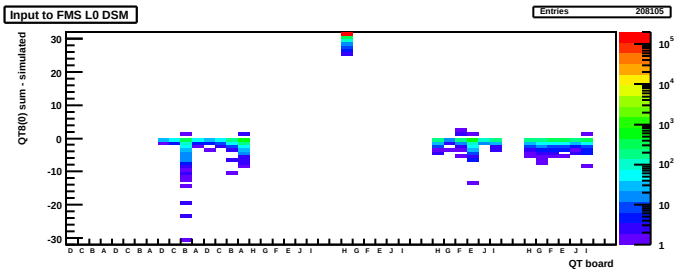

Input to QT1 crate
Entries1993292
ADC
channel

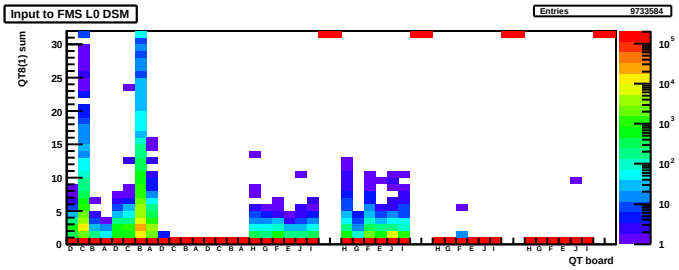

Input to QT2 crate
Entries1910368
ADC
channel

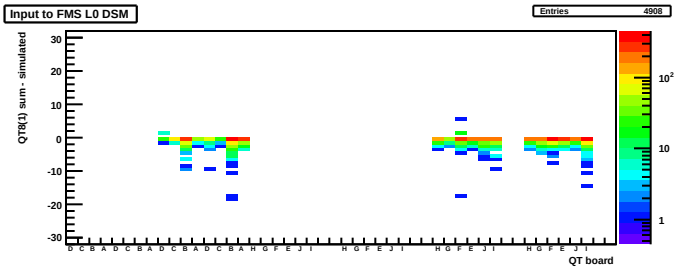

Input to QT3 crate
Entries 878446
ADC
channel

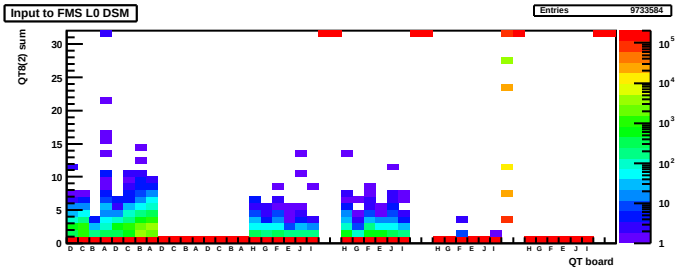

Input to QT4 crate
Entries1067385
ADC
channel

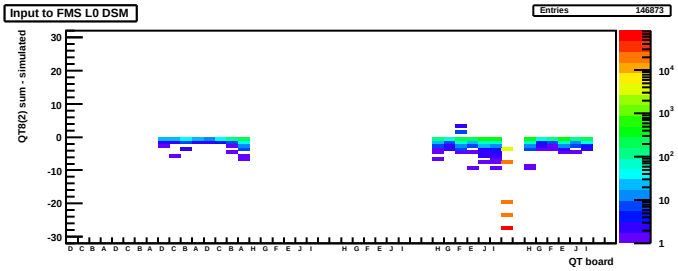

Input to FMS L0 DSM
Entries 9733584
QT8(0) sum
QT board

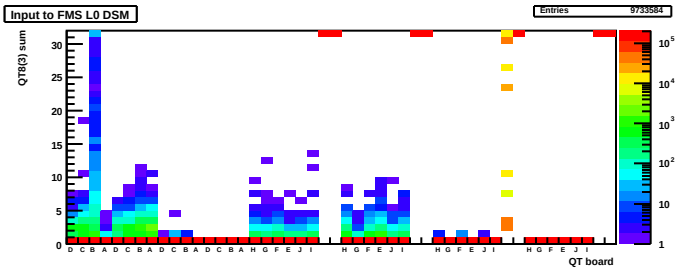

Input to FMS L0 DSM
Entries 208105
QT8(0) sum - simulated
QT board

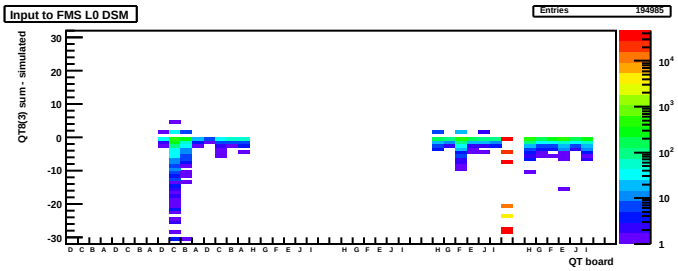

Input to FMS L0 DSM
Entries 9733584
QT8(1) sum
QT board

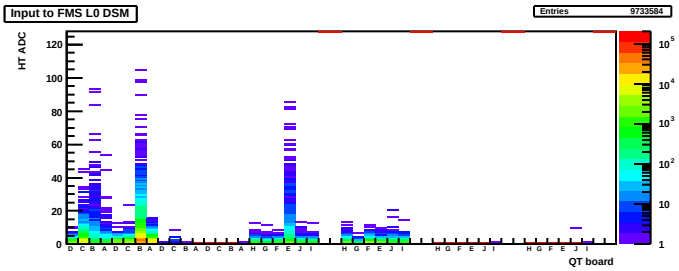

Input to FMS L0 DSM
Entries 4908
QT8(1) sum - simulated
QT board

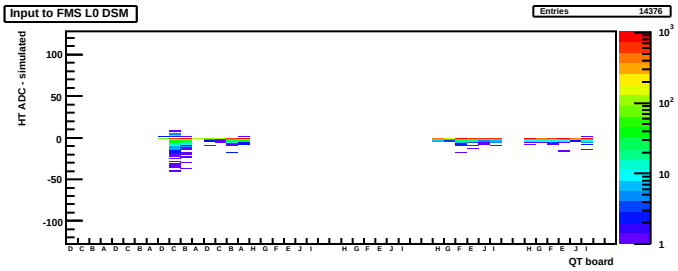

Input to FMS L0 DSM
Entries 9733584
QT8(2) sum
QT board

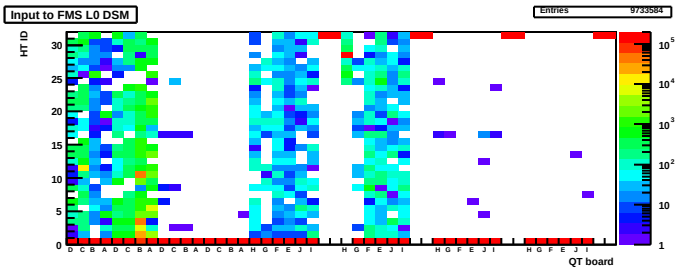

Input to FMS L0 DSM
Entries 146873
QT8(2) sum - simulated
QT board

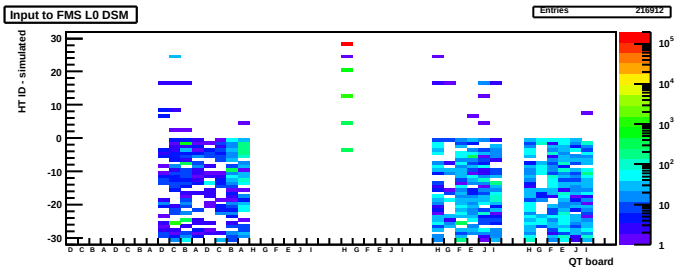

Input to FMS L0 DSM
Entries 9733584
QT8(3) sum
QT board

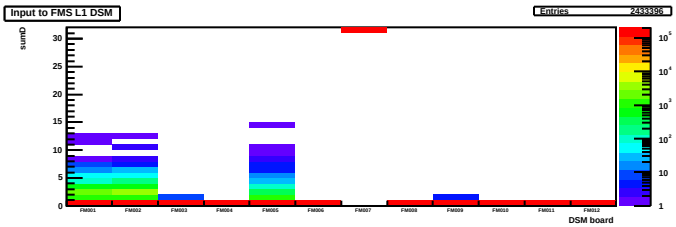

Input to FMS L0 DSM
Entries 194985
QT8(3) sum - simulated
QT board

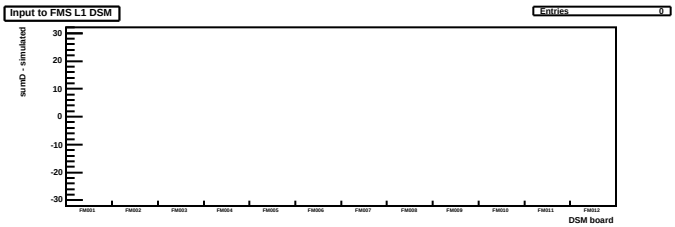

Input to FMS L0 DSM
Entries 9733584
HT ADC
QT board

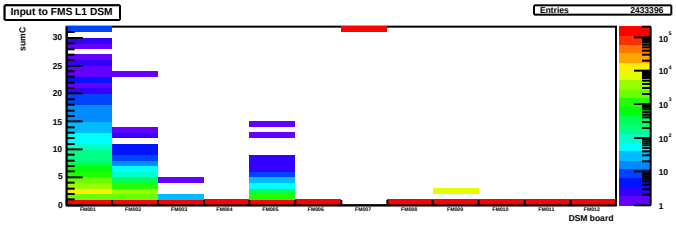

Input to FMS L0 DSM
Entries 14376
HT ADC - simulated
QT board

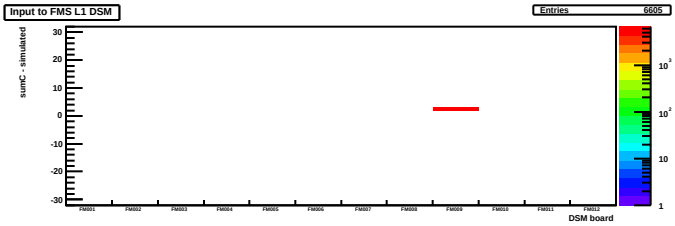

Input to FMS L0 DSM
Entries 9733584
HT ID
QT board

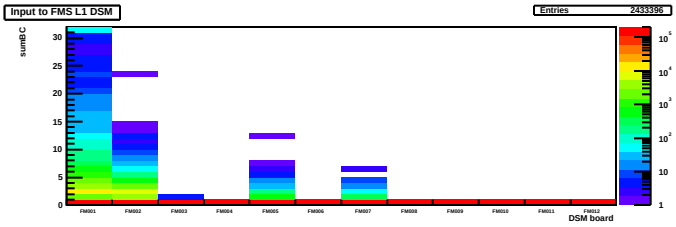

Input to FMS L0 DSM
Entries 216912
HT ID - simulated
QT board

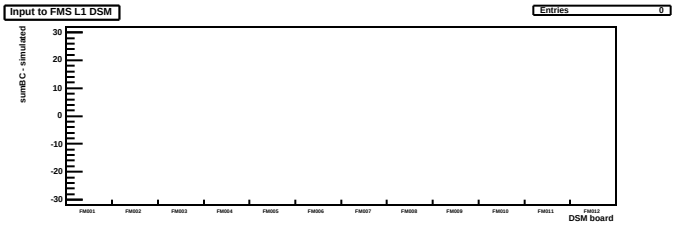

Input to FMS L1 DSM
Entries 2433396
sumD
DSM board

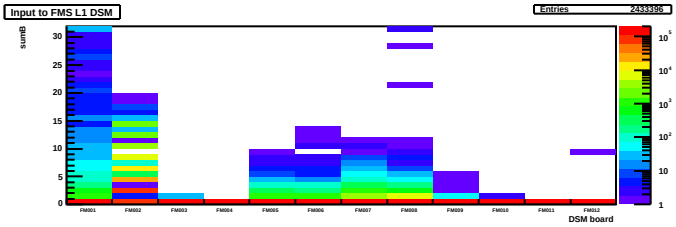

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

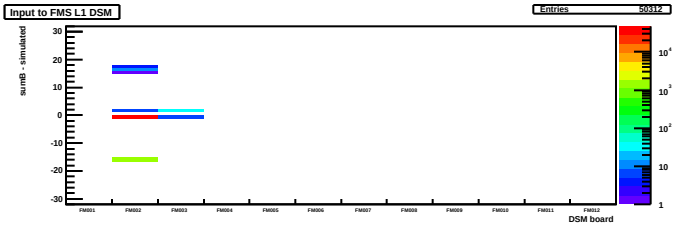

Input to FMS L1 DSM
Entries 2433396
sumC
DSM board

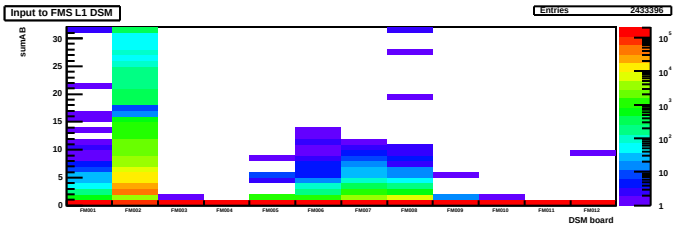

Input to FMS L1 DSM
Entries 6605
sumC - simulated
DSM board

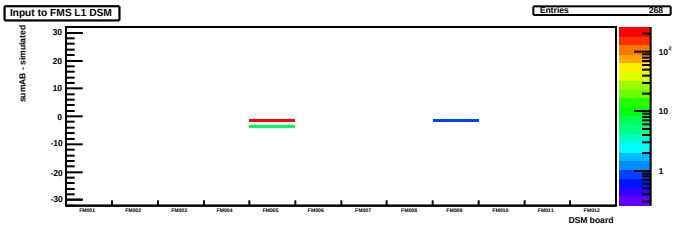

Input to FMS L1 DSM
Entries 2433396
sumBC
DSM board

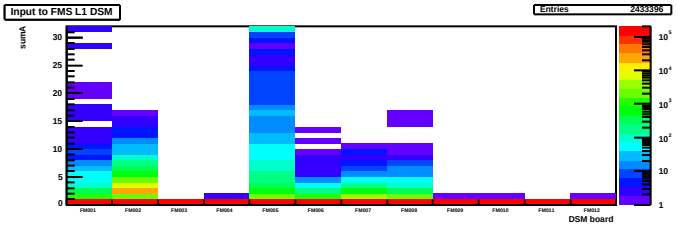

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

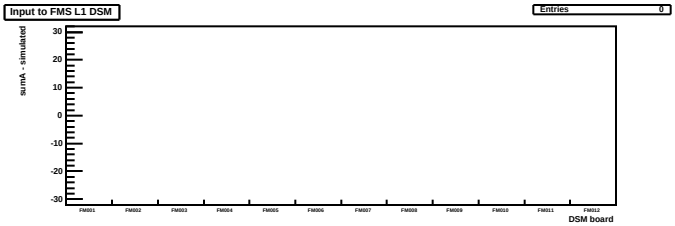

Input to FMS L1 DSM
Entries 2433396
sumB
DSM board

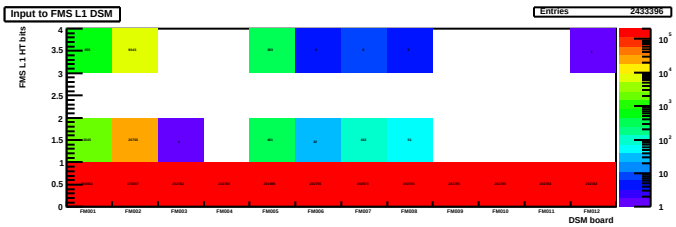

Input to FMS L1 DSM
Entries 50312
sumB - simulated
DSM board

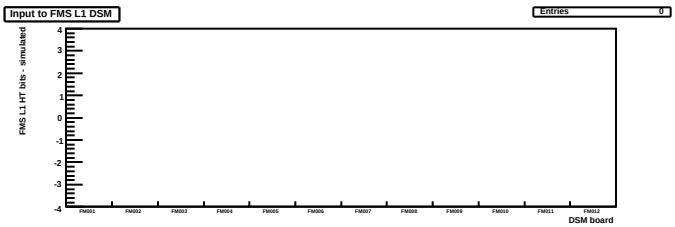

Input to FMS L1 DSM
Entries 2433396
sumAB
DSM board

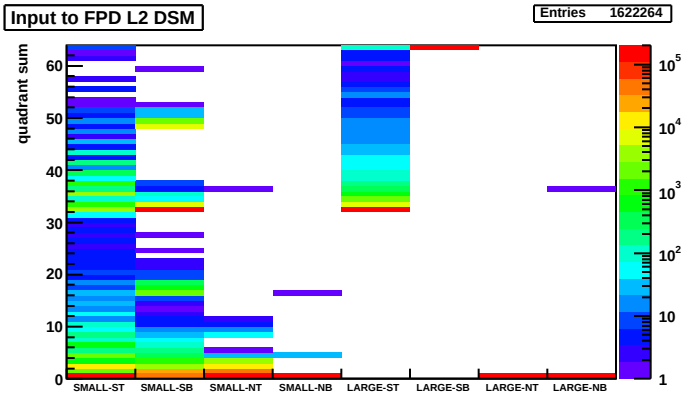

Input to FMS L1 DSM
Entries 268
sumAB - simulated
DSM board

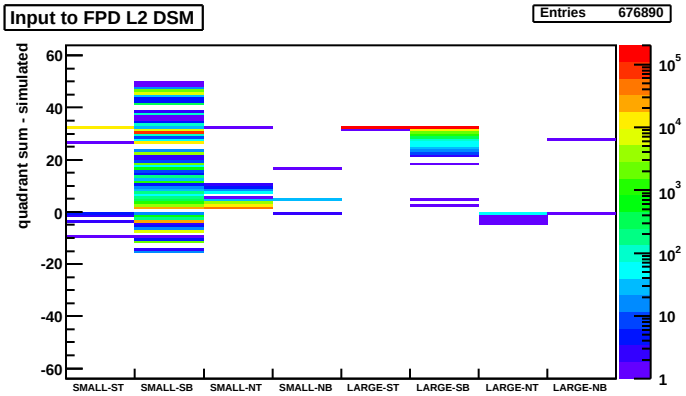

Input to FMS L1 DSM
Entries 2433396
sumA
DSM board

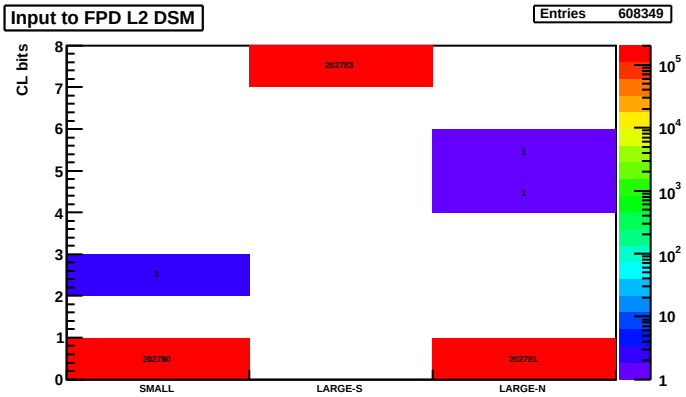

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

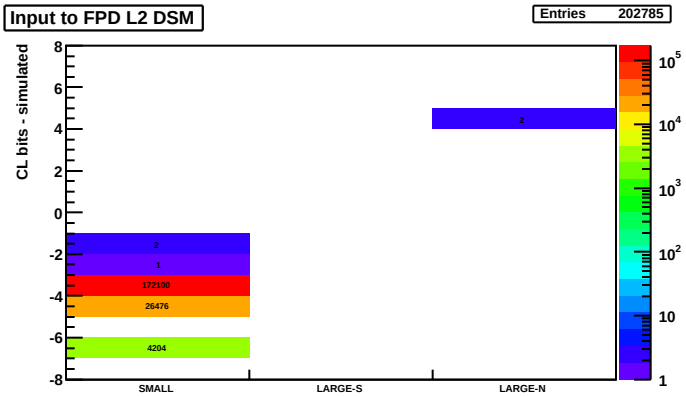

Input to FMS L1 DSM
Entries 2433396
FMS L1 HT bits
DSM board

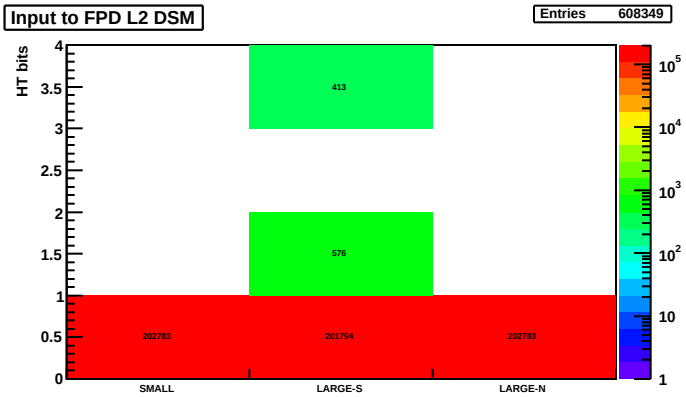

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

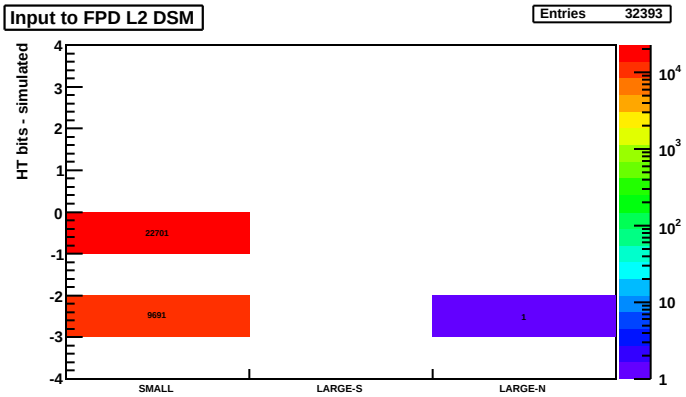

Input to FPD L2 DSM
Entries 1622264
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

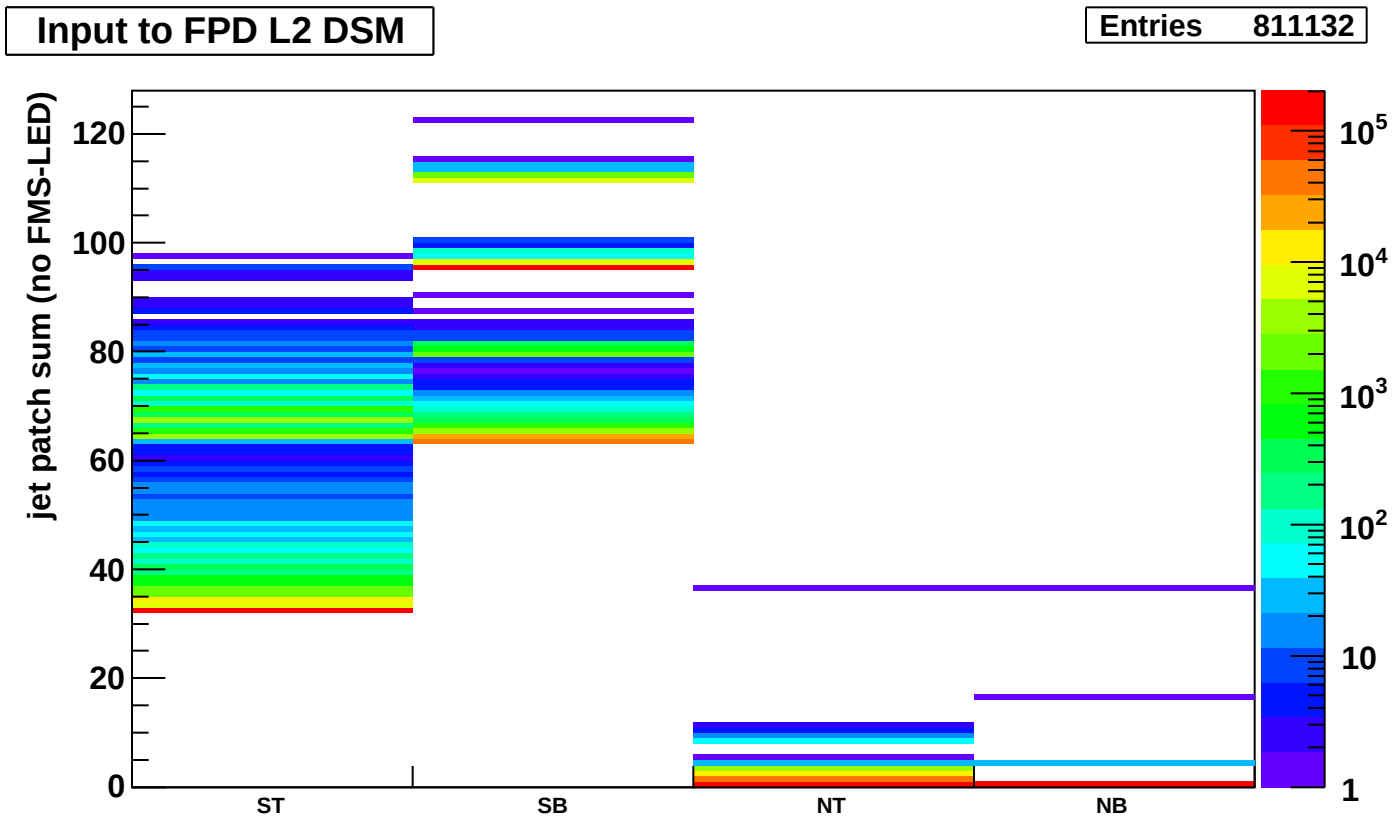

Input to FPD L2 DSM
Entries 676890
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

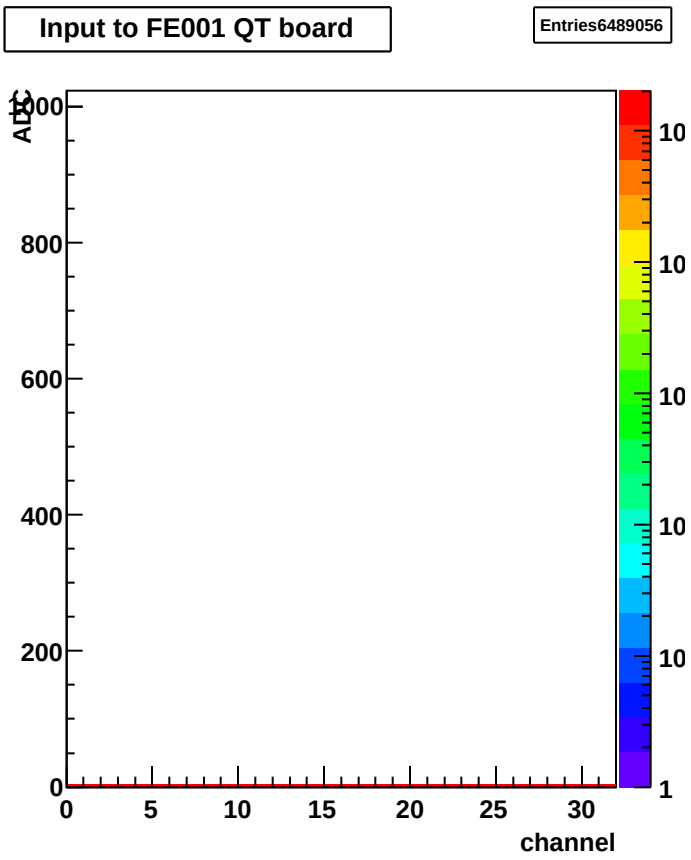

Input to FPD L2 DSM
Entries 608349
CL bits
202783
1
1
3
202780
202781
SMALL
LARGE-S
LARGE-N

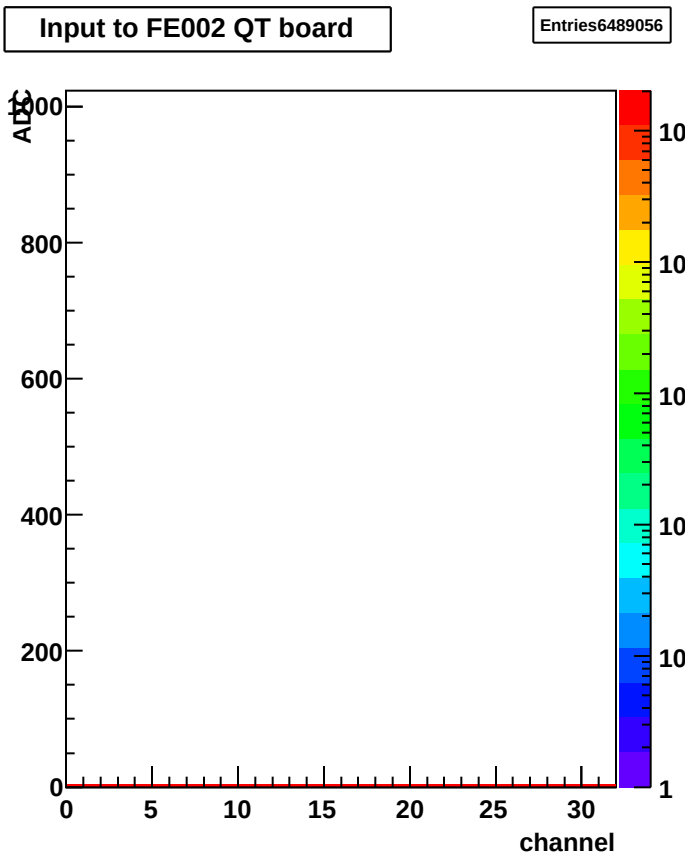

Input to FPD L2 DSM
Entries 202785
CL bits - simulated
2
2
1
172100
26476
4204
SMALL
LARGE-S
LARGE-N

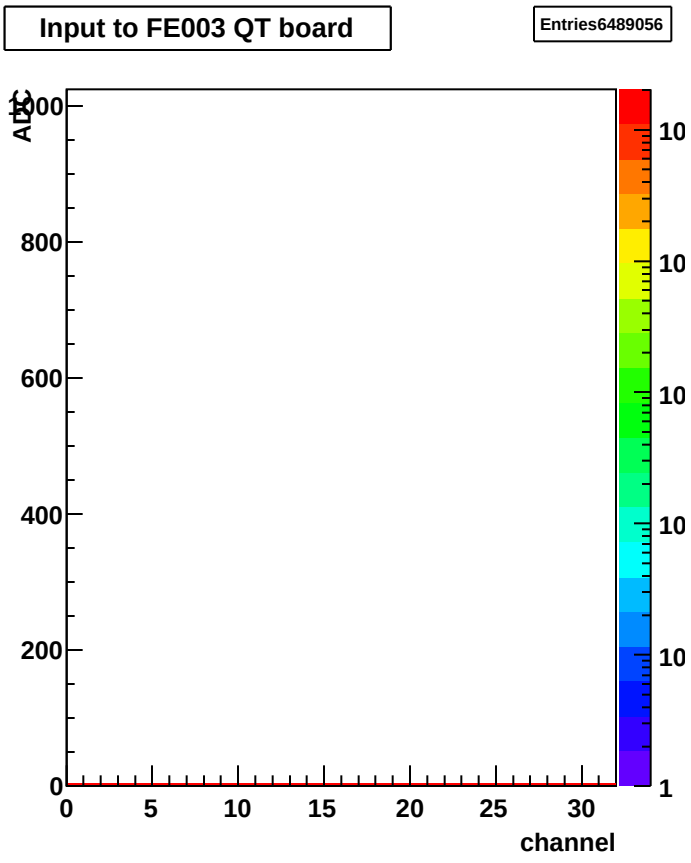

Input to FPD L2 DSM
Entries 608349
HT bits
413
576
202783
201794
202783
SMALL
LARGE-S
LARGE-N

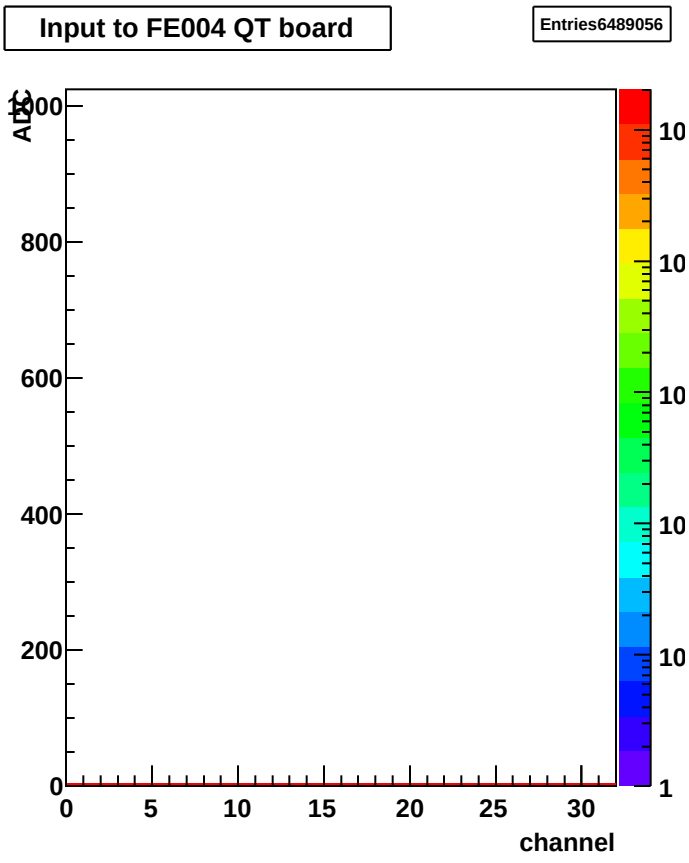

Input to FPD L2 DSM
Entries 32393
HT bits - simulated
22701
9691
1
SMALL
LARGE-S
LARGE-N

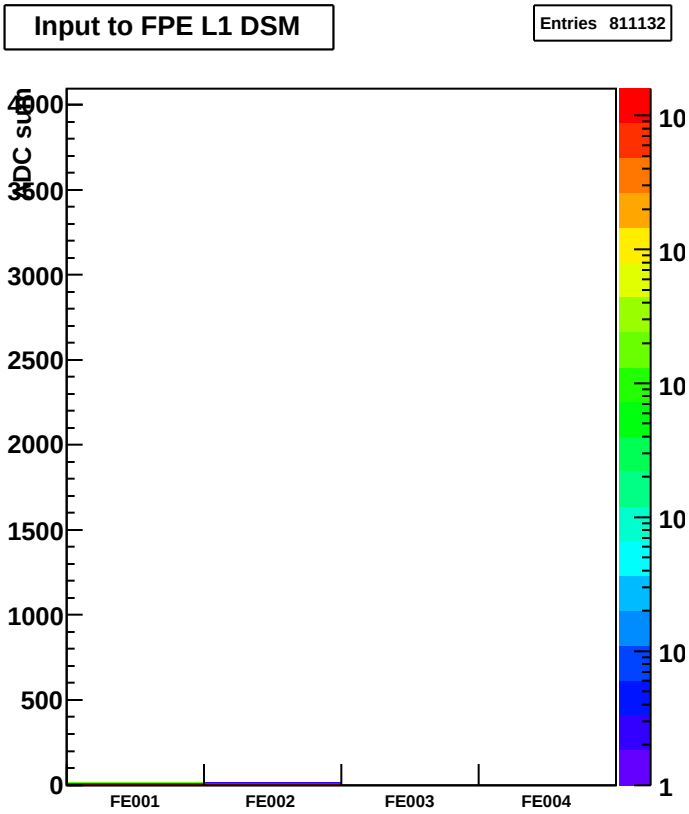

Input to FPD L2 DSM
Entries 811132
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

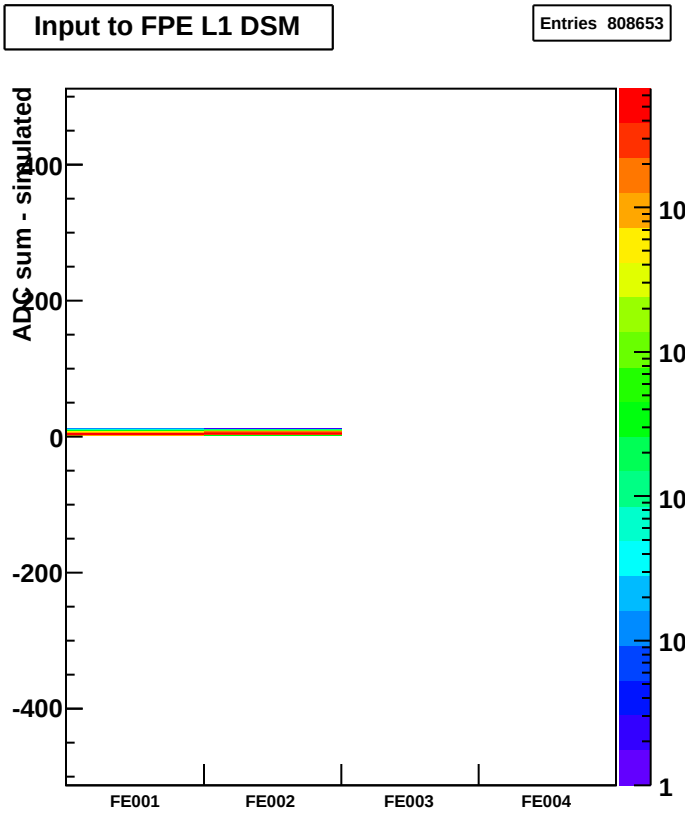

Input to FE001 QT board
Entries6489056
ADC
channel

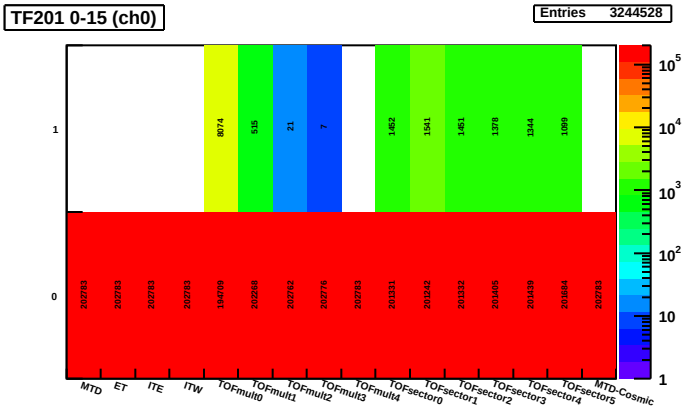

Input to FE002 QT board
Entries6489056
ADC
channel

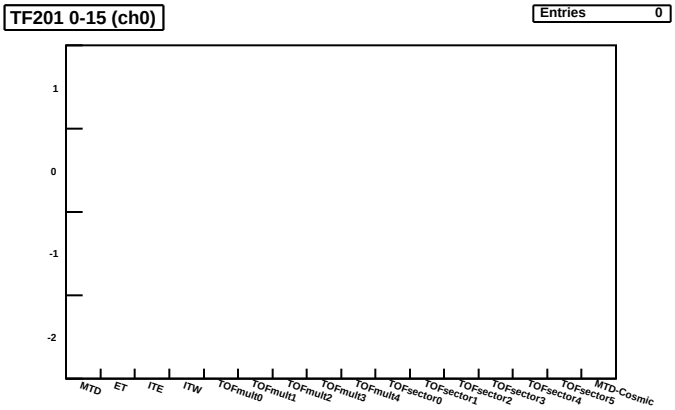

Input to FE003 QT board
Entries6489056
ADC
channel

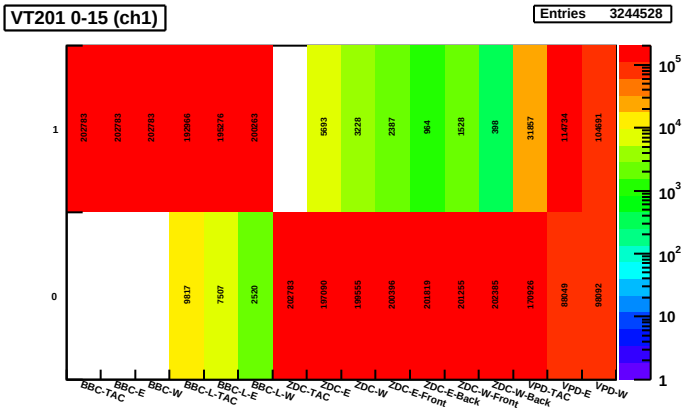

Input to FE004 QT board
Entries6489056
ADC
channel

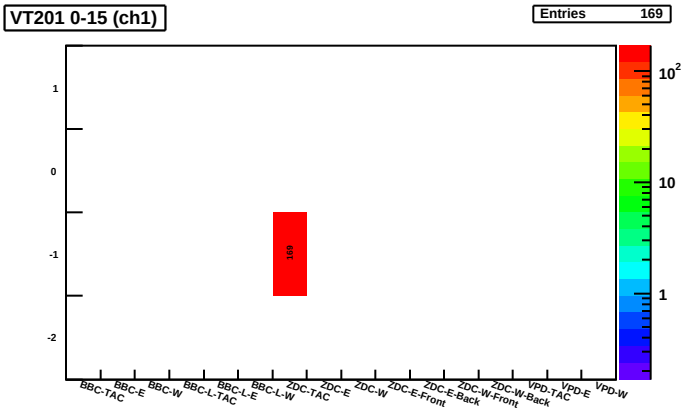

Input to FPE L1 DSM
Entries 811132
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

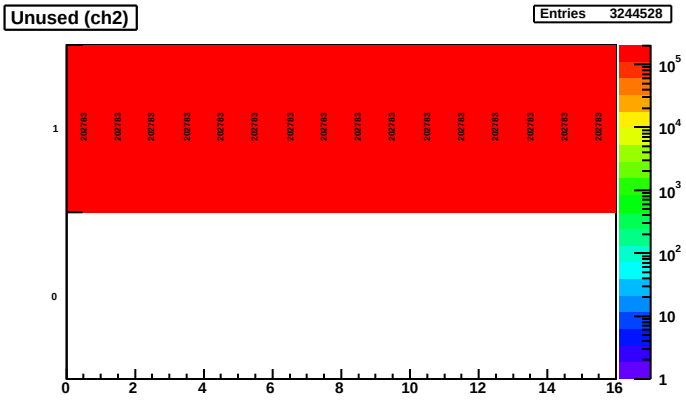

Input to FPE L1 DSM
Entries 808653
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1

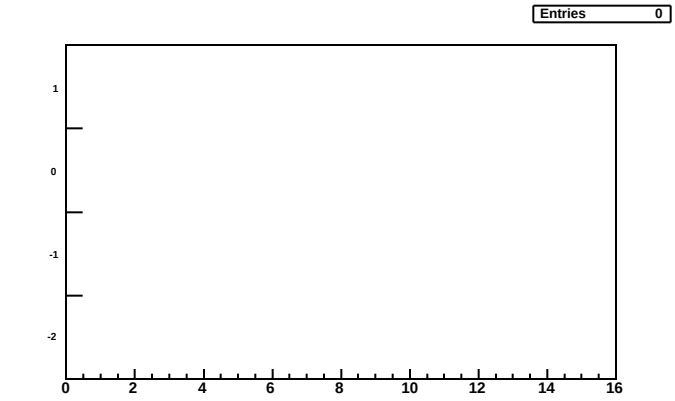

TF201 0-15 (ch0)
Entries 3244528
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

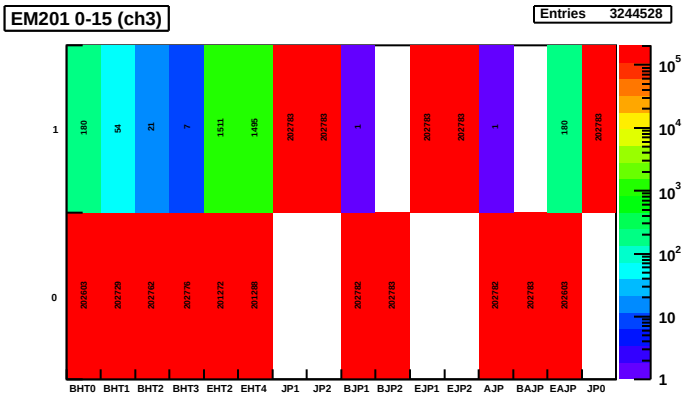

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

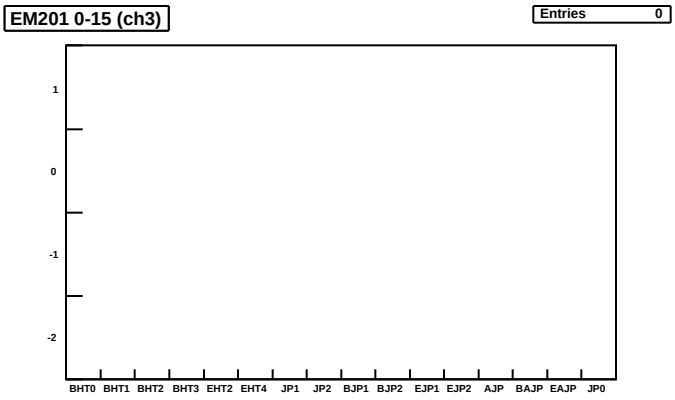

VT201 0-15 (ch1)
Entries 3244528
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 169
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

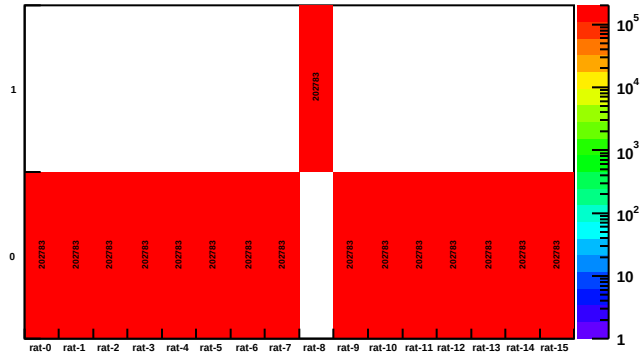

Unused (ch2)
Entries 3244528

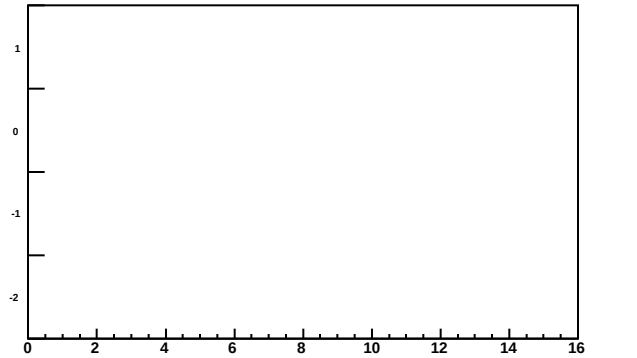

Entries 0


EM201 0-15 (ch3)
Entries 3244528
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

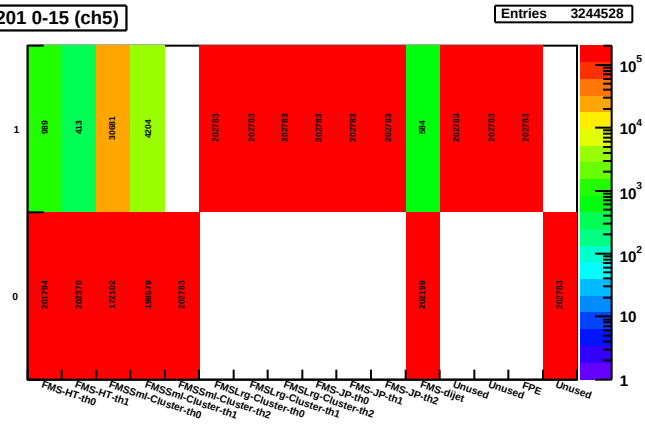

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

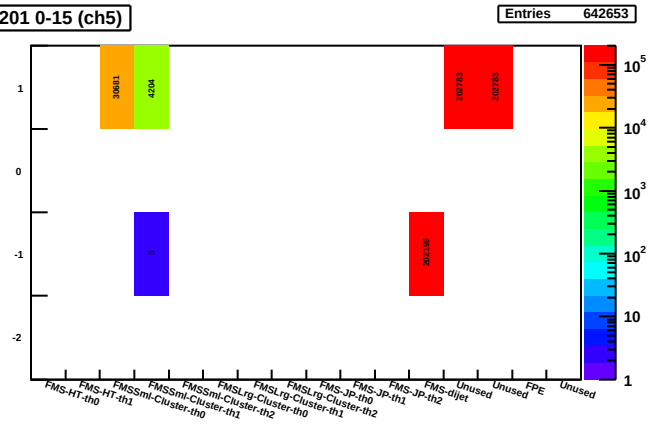




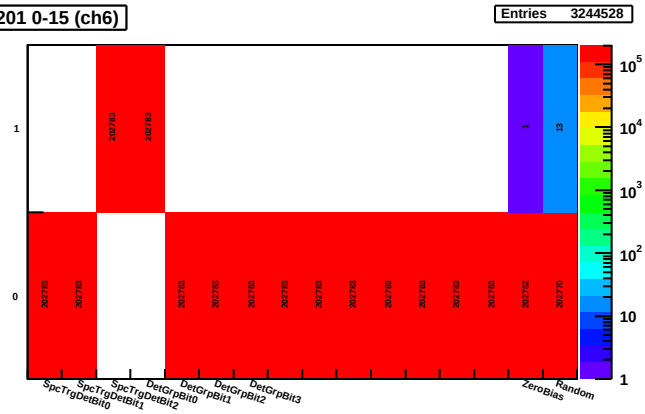
FP201 0-15 (ch5)

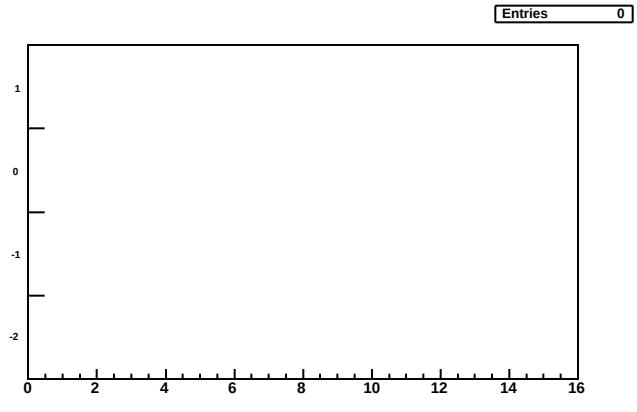
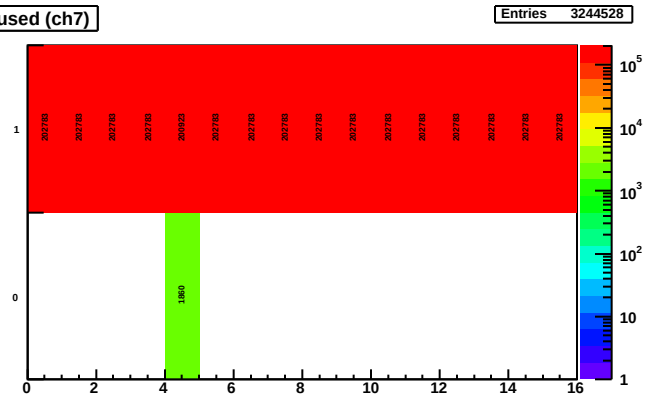
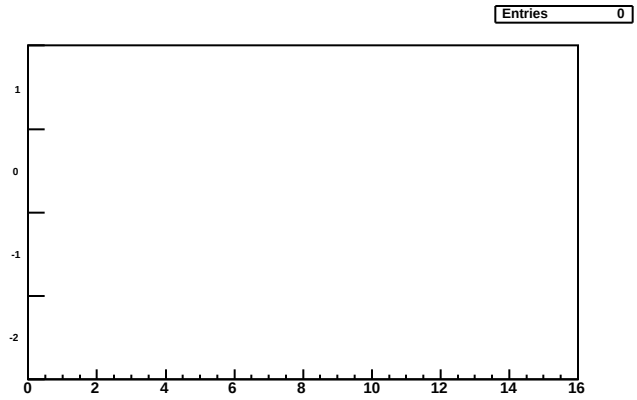


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)