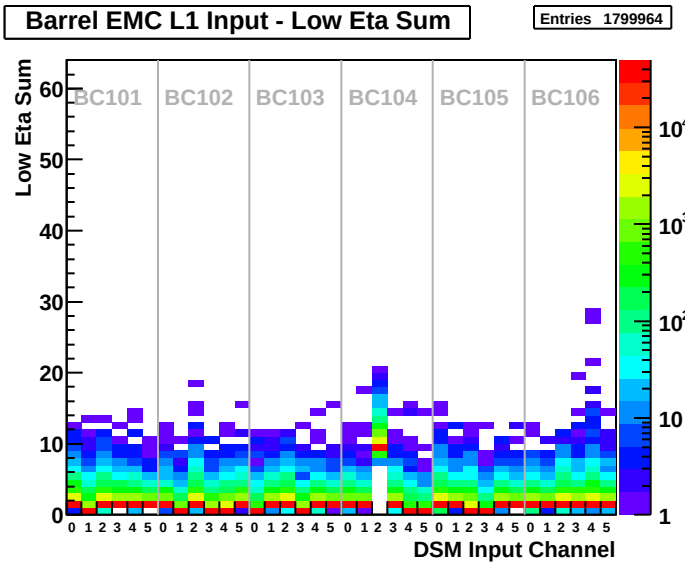
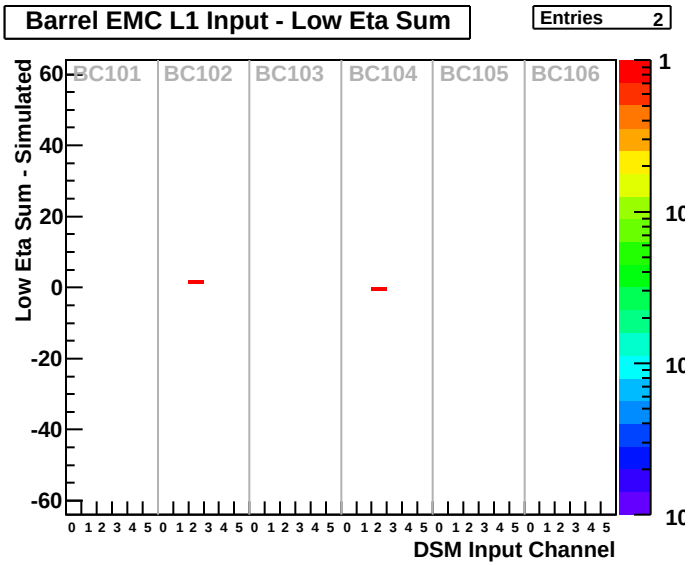
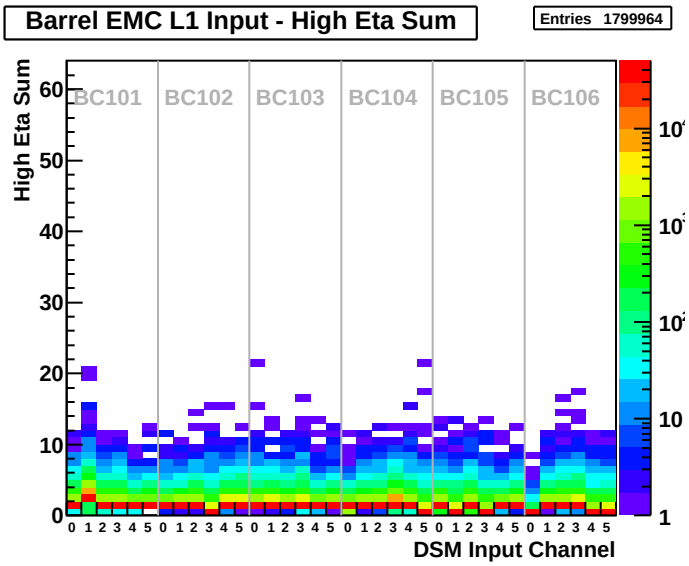
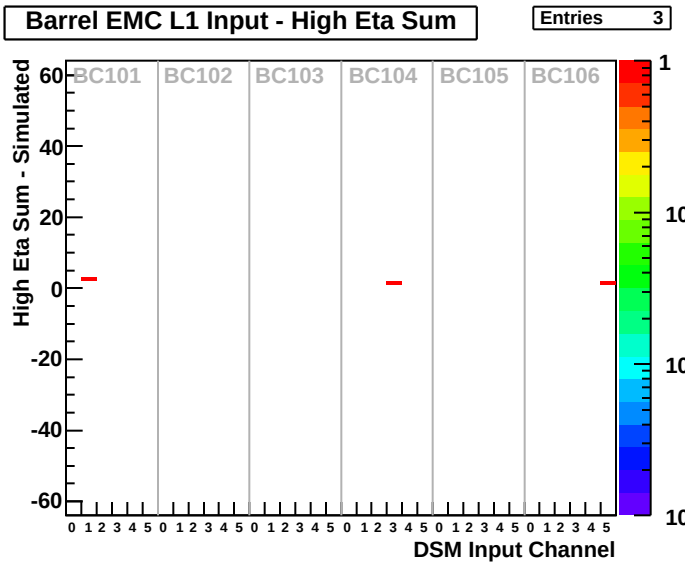
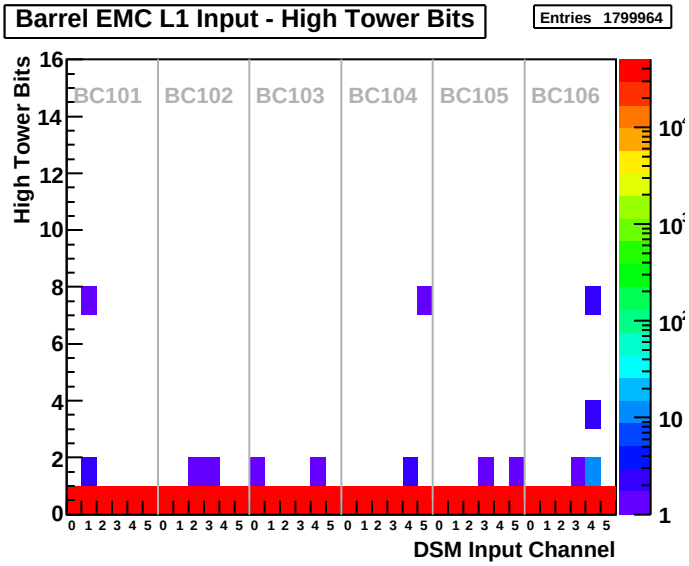
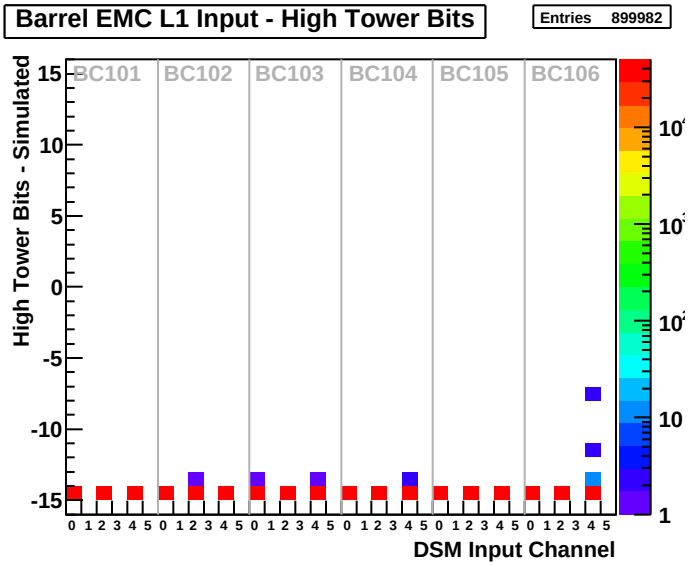

Barrel EMC L1 Input - Low Eta Sum
Entries 1799964
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

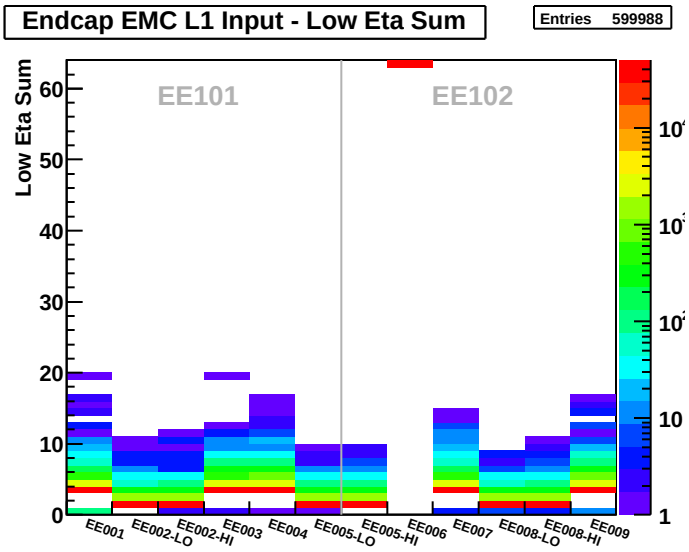

Barrel EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

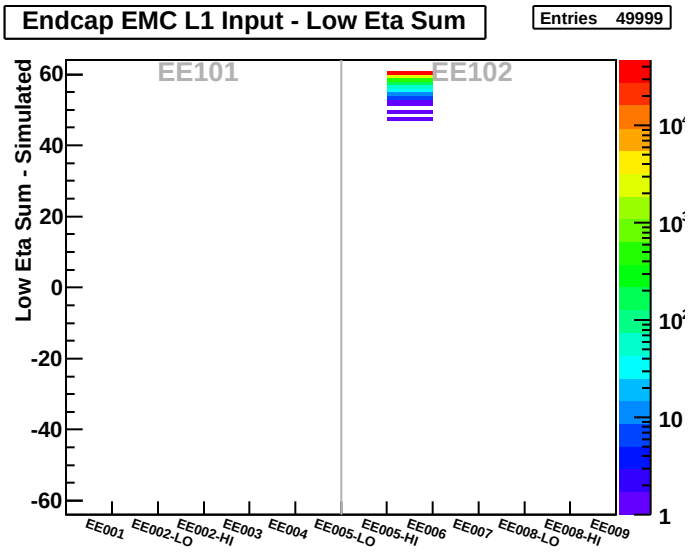

Barrel EMC L1 Input - High Eta Sum
Entries 1799964
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

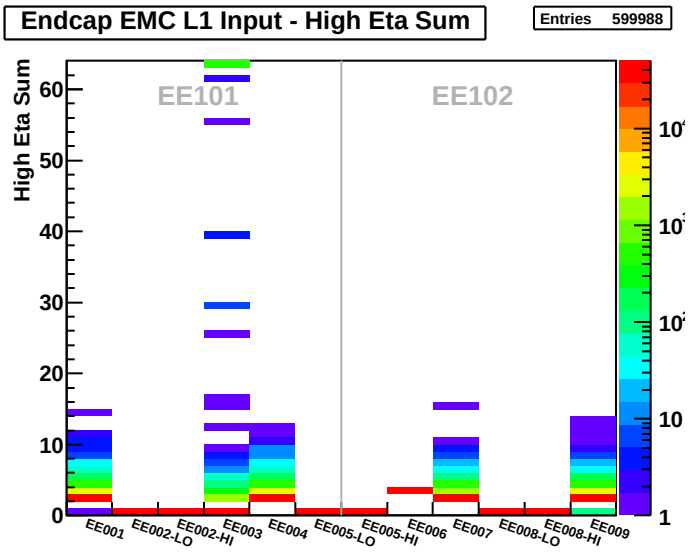

Barrel EMC L1 Input - High Eta Sum
Entries 3
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

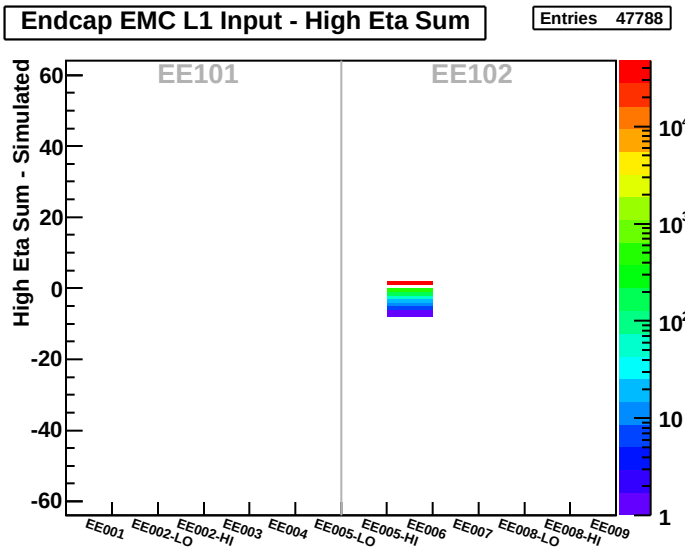

Barrel EMC L1 Input - High Tower Bits
Entries 1799964
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

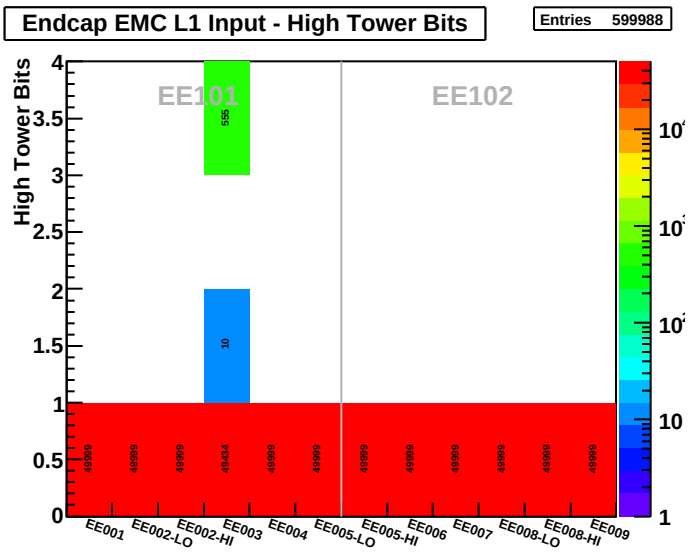

Barrel EMC L1 Input - High Tower Bits
Entries 899982
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

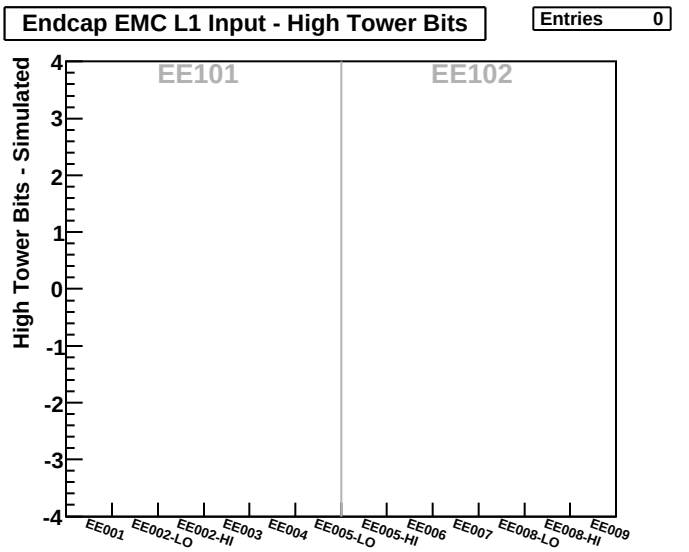

Endcap EMC L1 Input - Low Eta Sum
Entries 599988
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

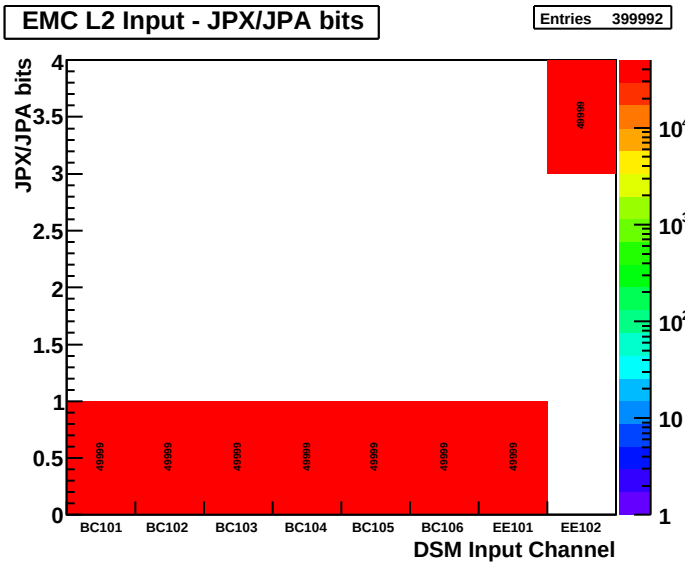

Endcap EMC L1 Input - Low Eta Sum
Entries 49999
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

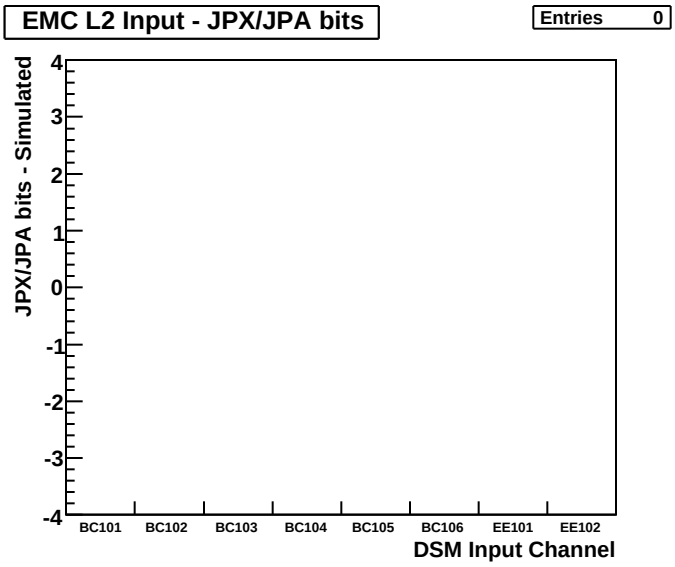

Endcap EMC L1 Input - High Eta Sum
Entries 599988
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

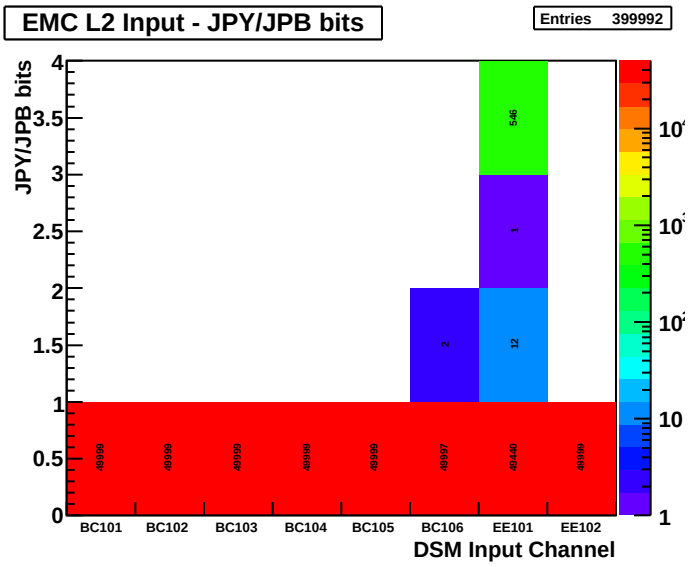

Endcap EMC L1 Input - High Eta Sum
Entries 47788
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

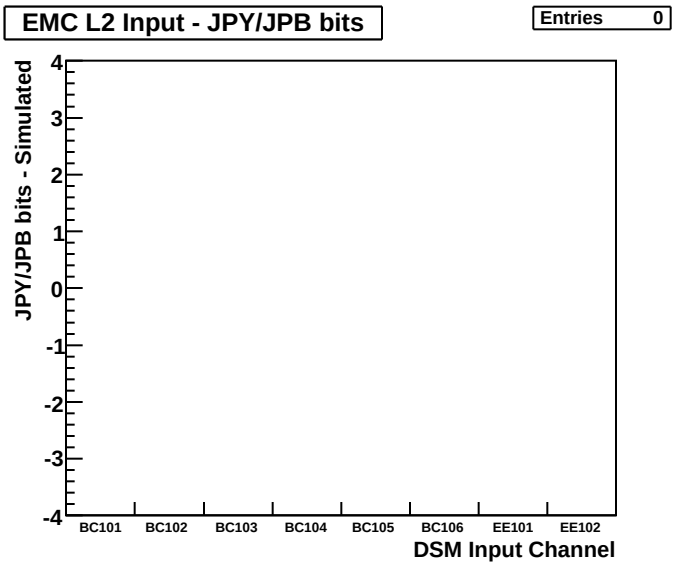

Endcap EMC L1 Input - High Tower Bits
Entries 599988
High Tower Bits
EE101
EE102
555
10
49999 49999 49999 49434 49999 49999 49999 49999 49999 49999 49999 49999
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

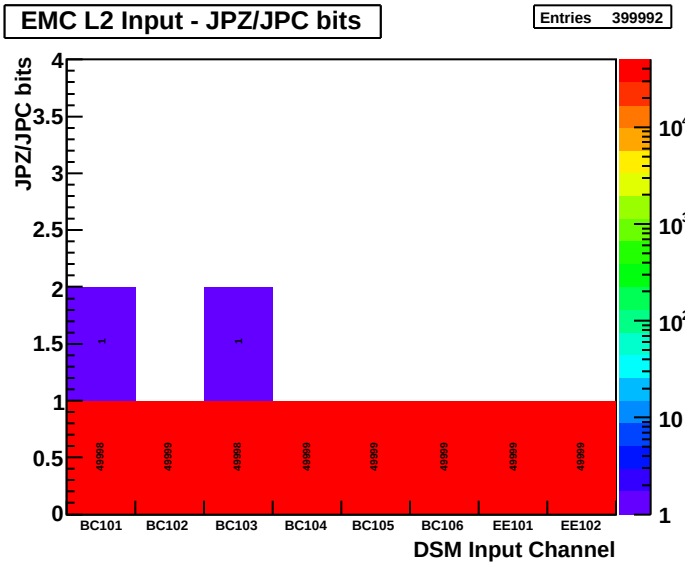

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

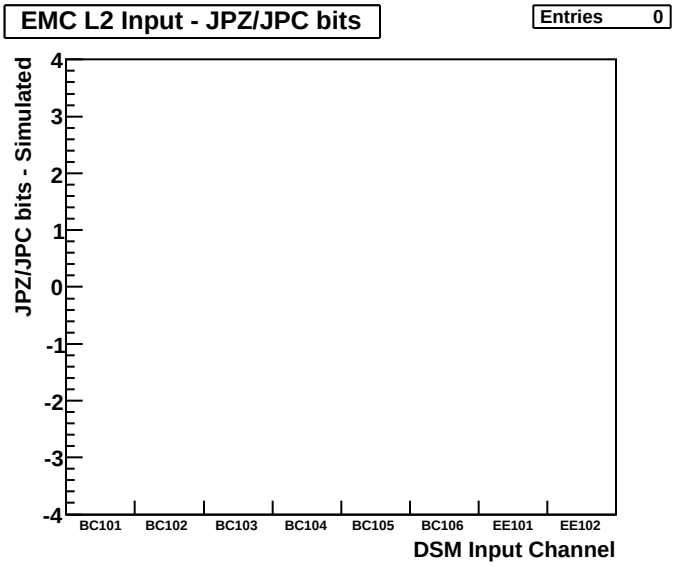

EMC L2 Input - JPX/JPA bits
Entries 399992
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

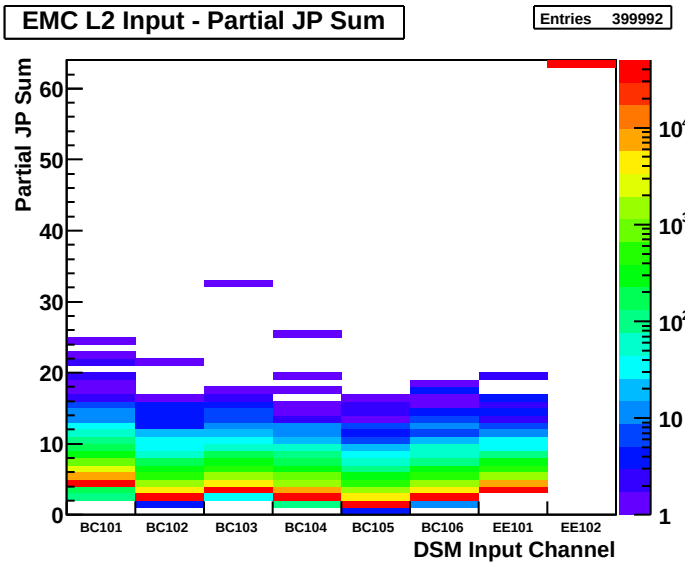

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

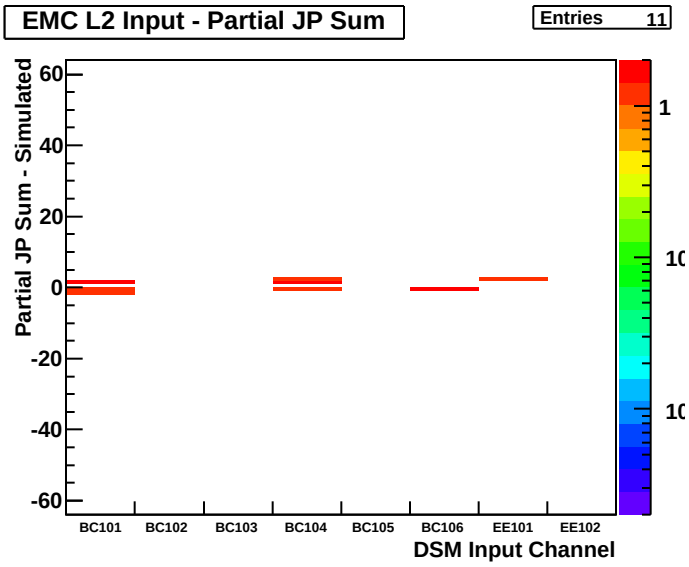

EMC L2 Input - JPY/JPB bits
Entries 399992
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

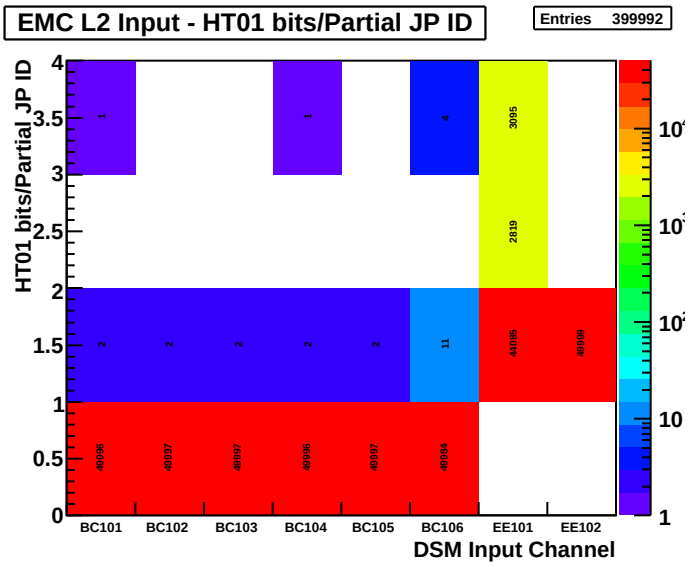

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

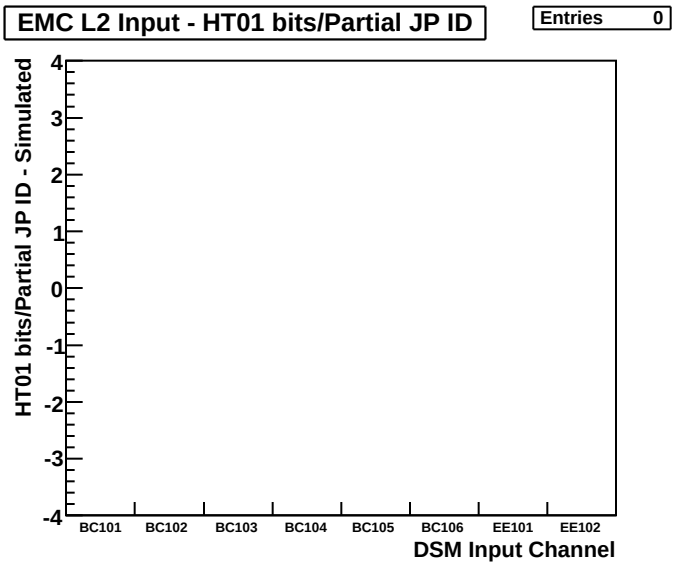

EMC L2 Input - JPZ/JPC bits
Entries 399992
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

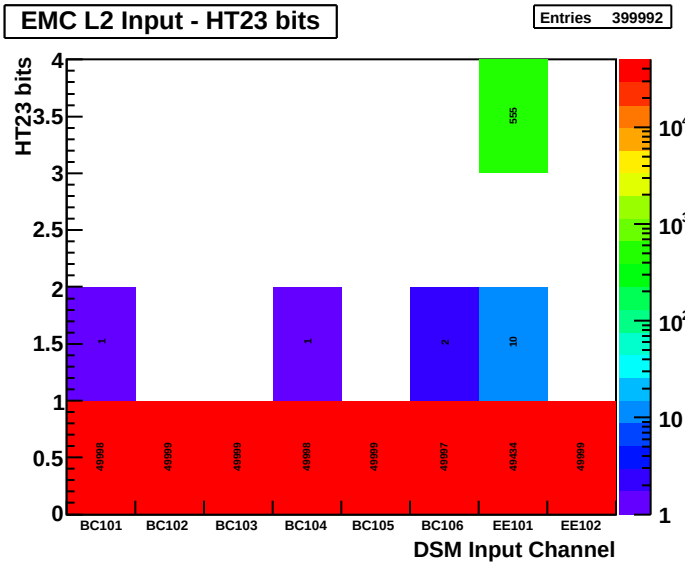

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

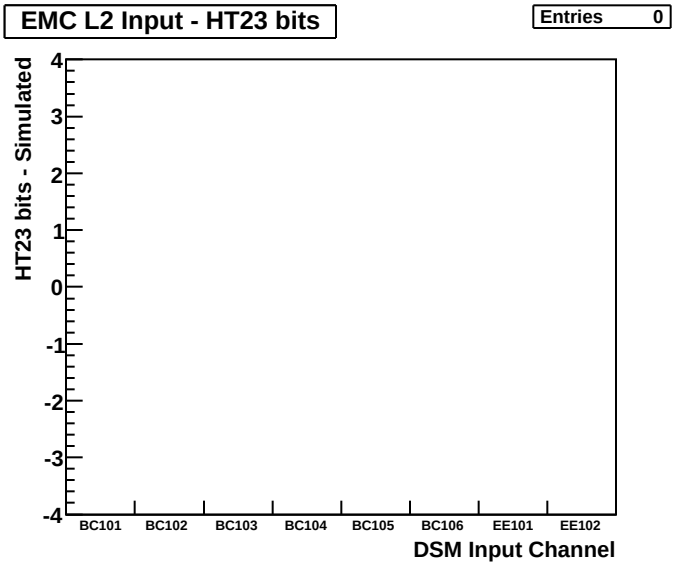

EMC L2 Input - Partial JP Sum
Entries 399992
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

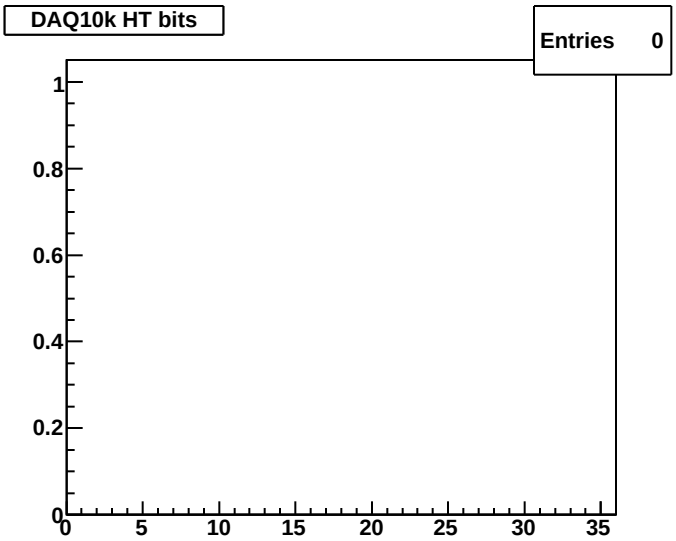

EMC L2 Input - Partial JP Sum
Entries 11
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

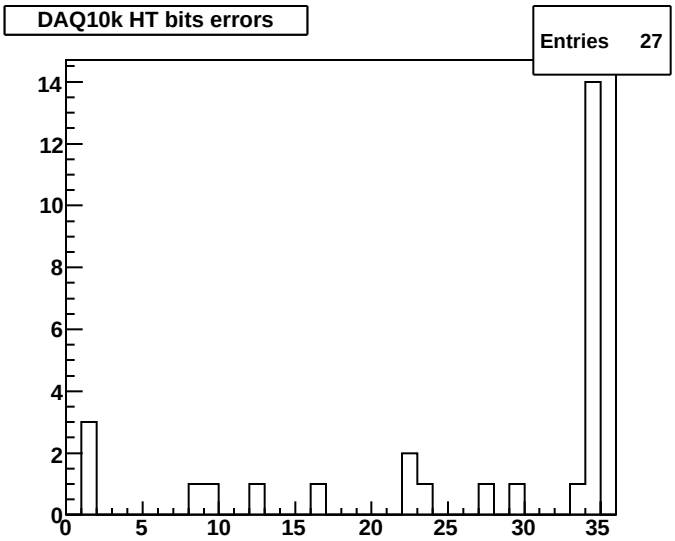

EMC L2 Input - HT01 bits/Partial JP ID
Entries 399992
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

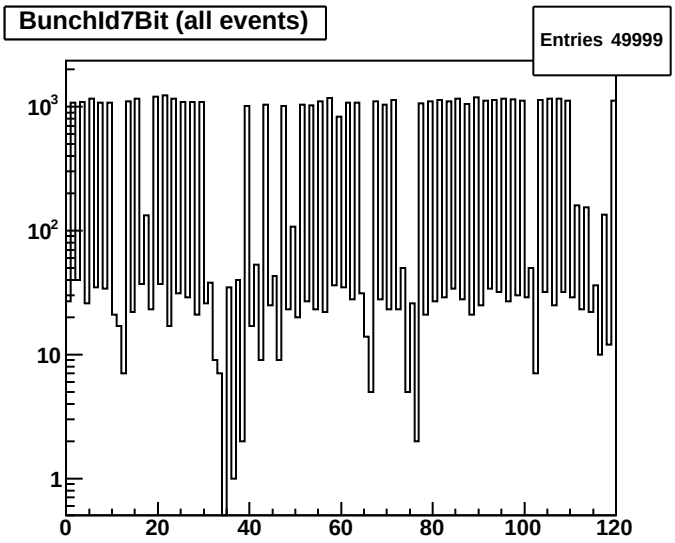

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

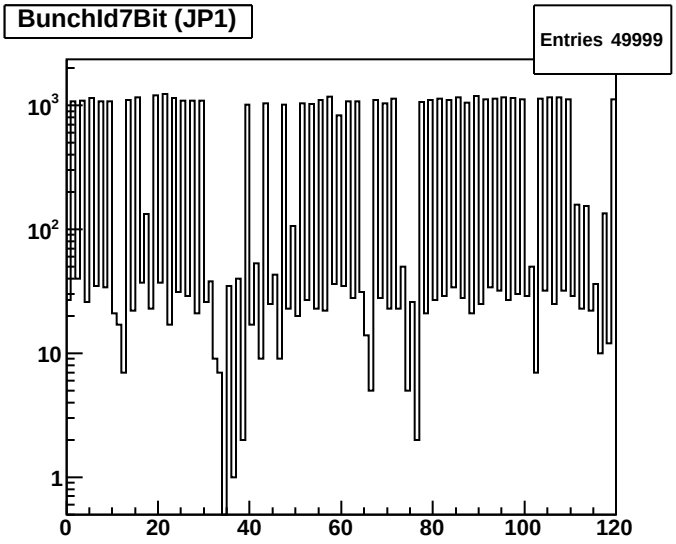

EMC L2 Input - HT23 bits
Entries 399992
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

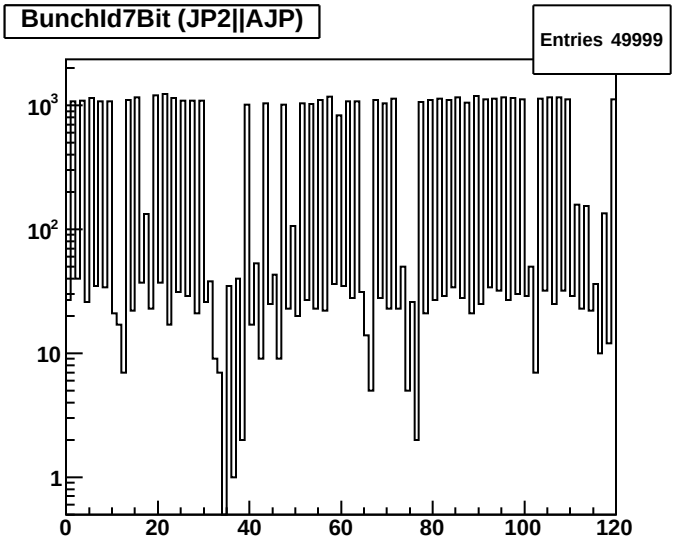

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

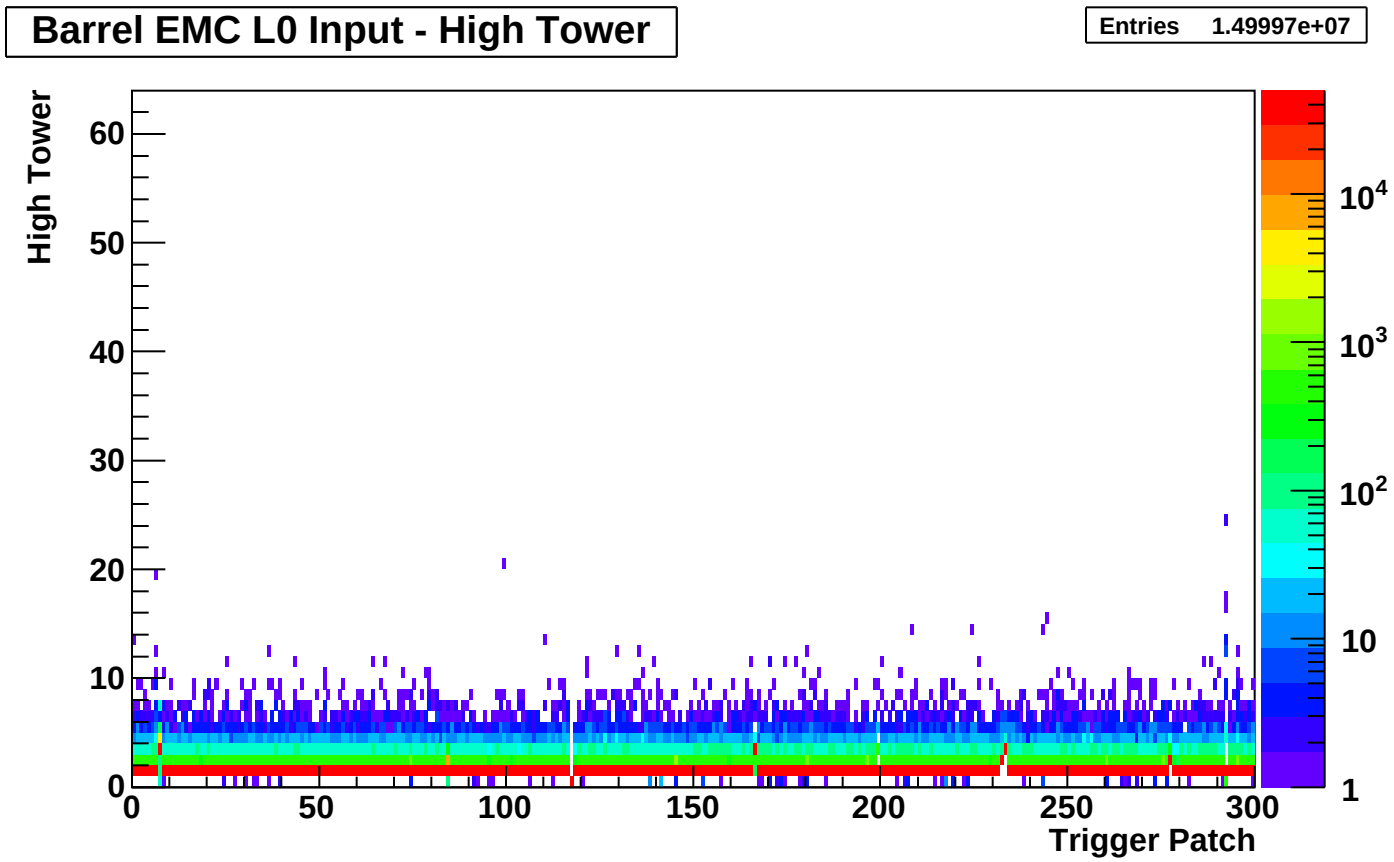

DAQ10k HT bits
Entries 0

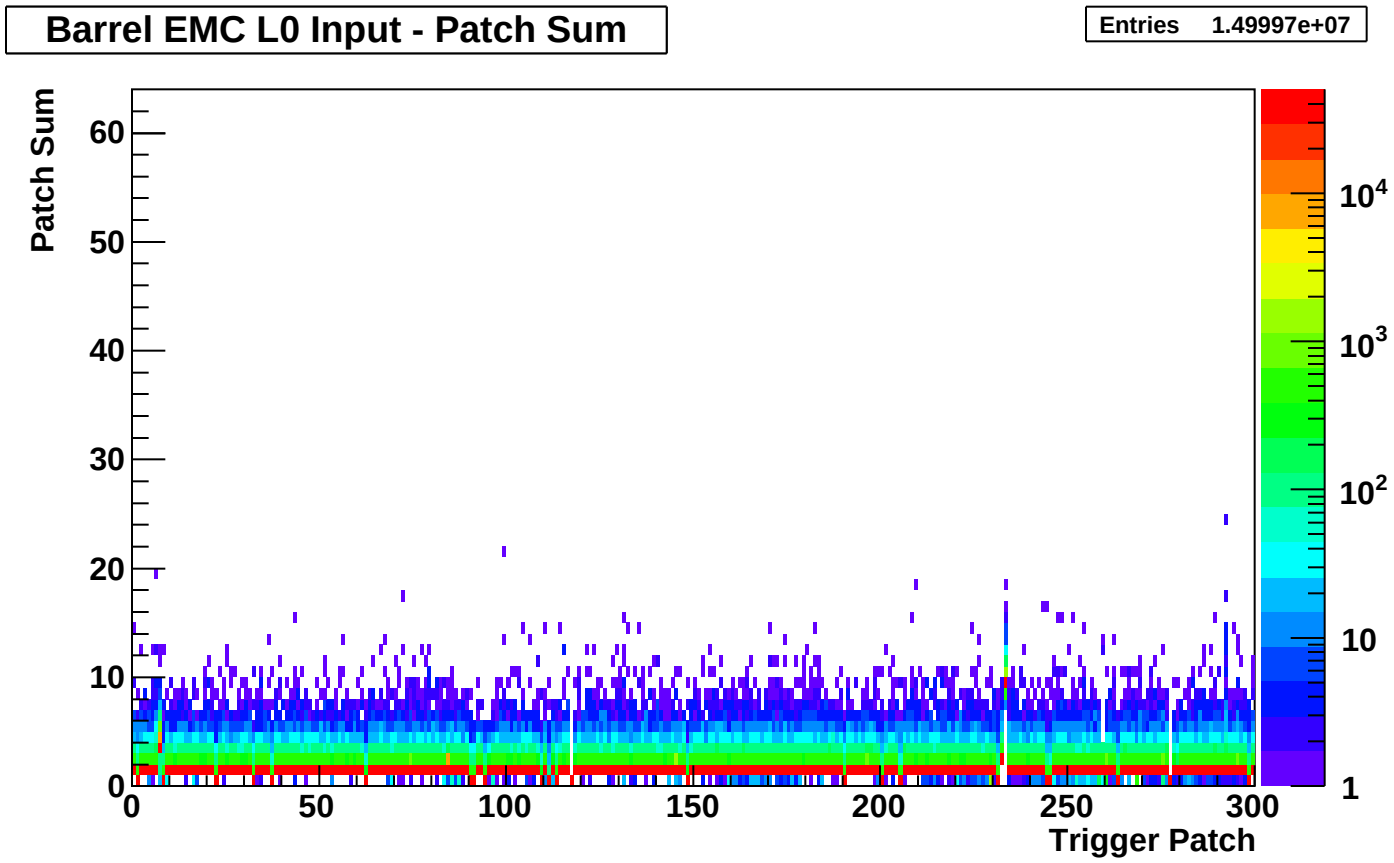

DAQ10k HT bits errors
Entries 27

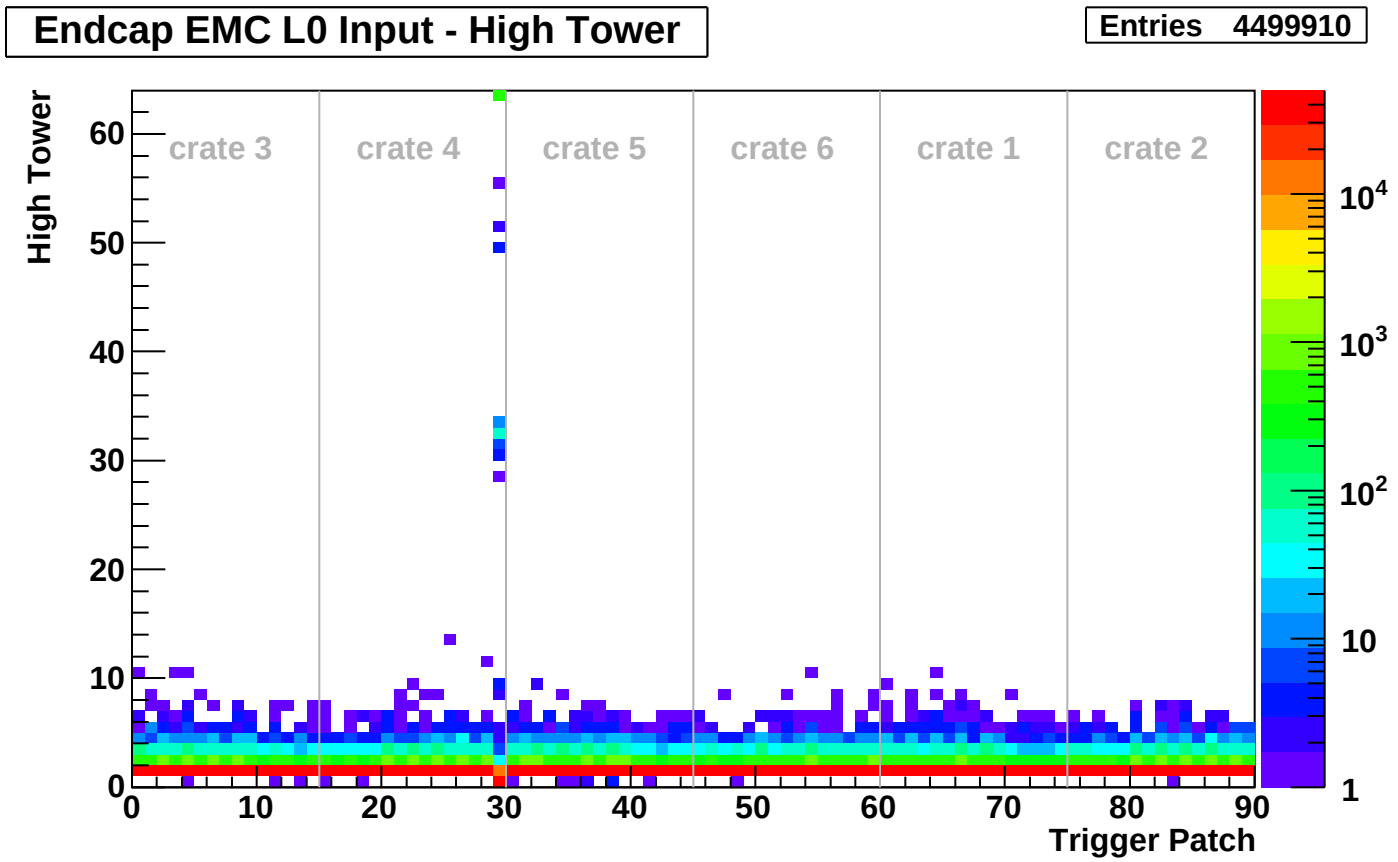

BunchId7Bit (all events)
Entries 49999

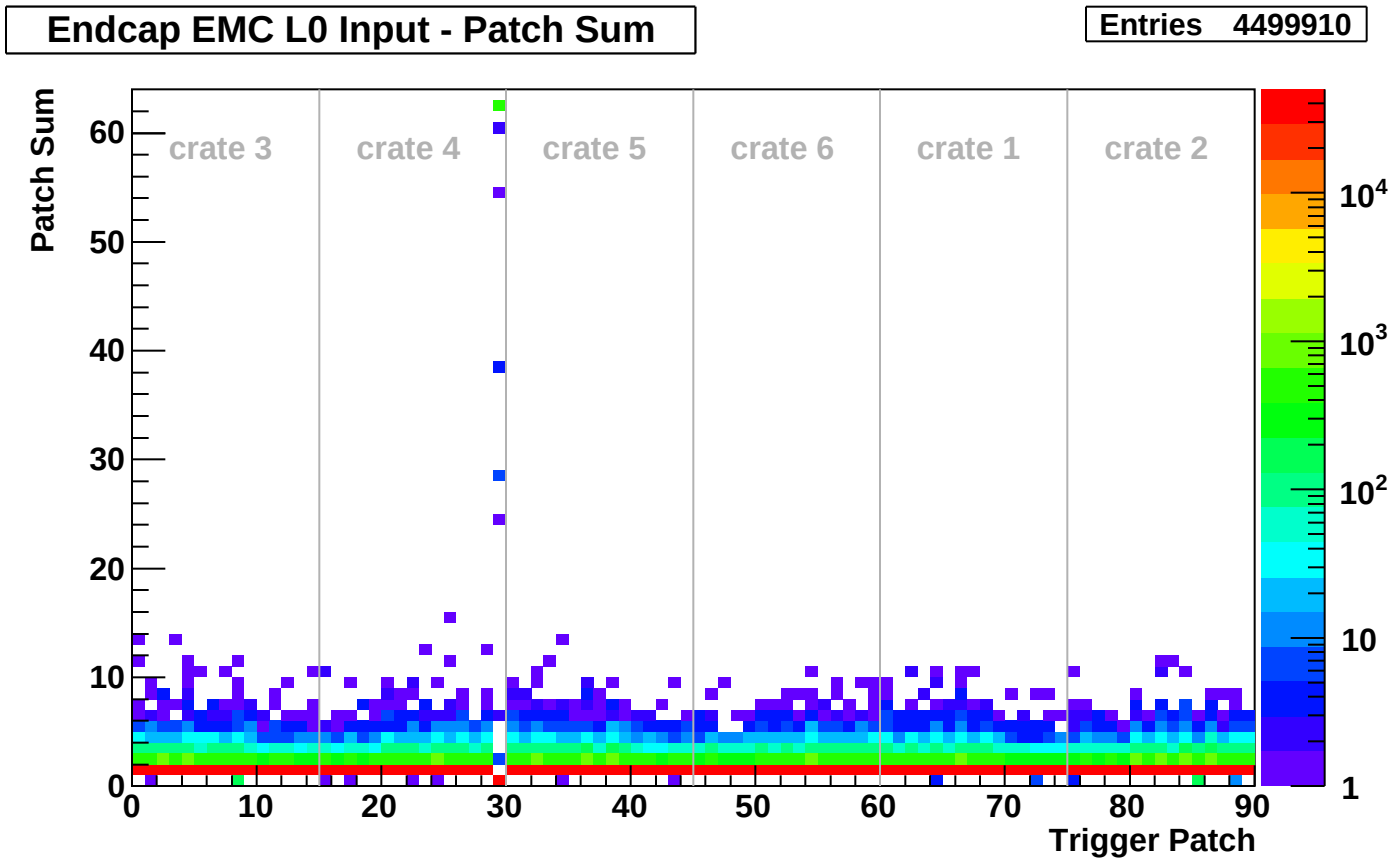

BunchId7Bit (JP1)
Entries 49999

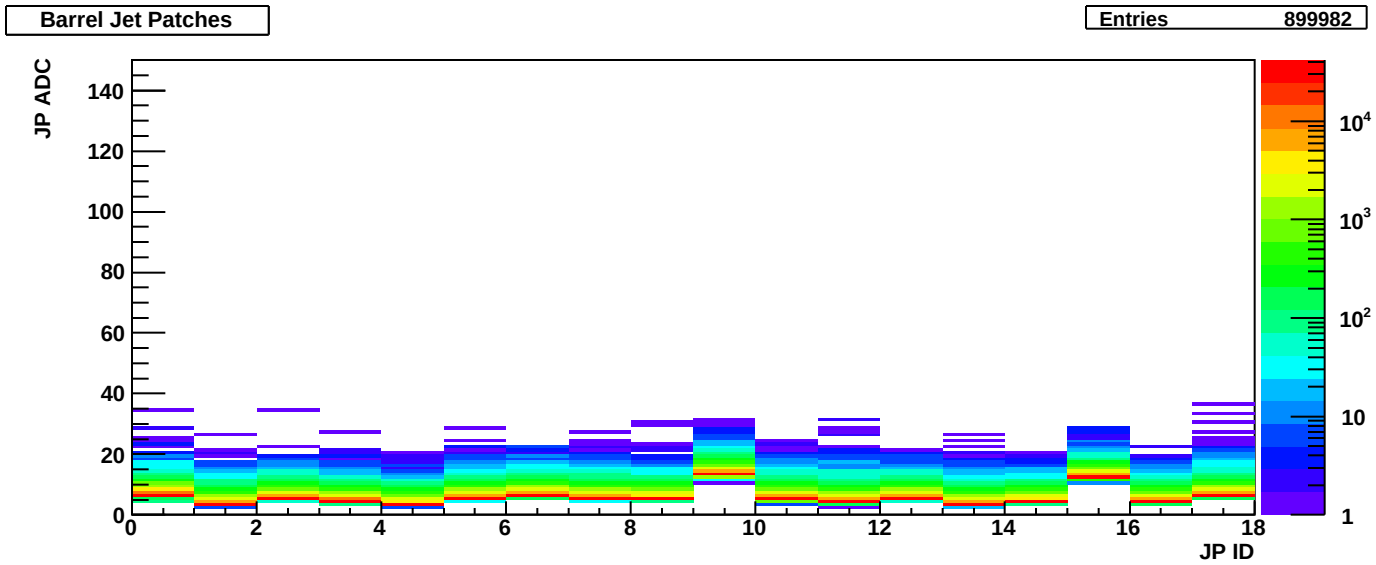

BunchId7Bit (JP2||AJP)
Entries 49999

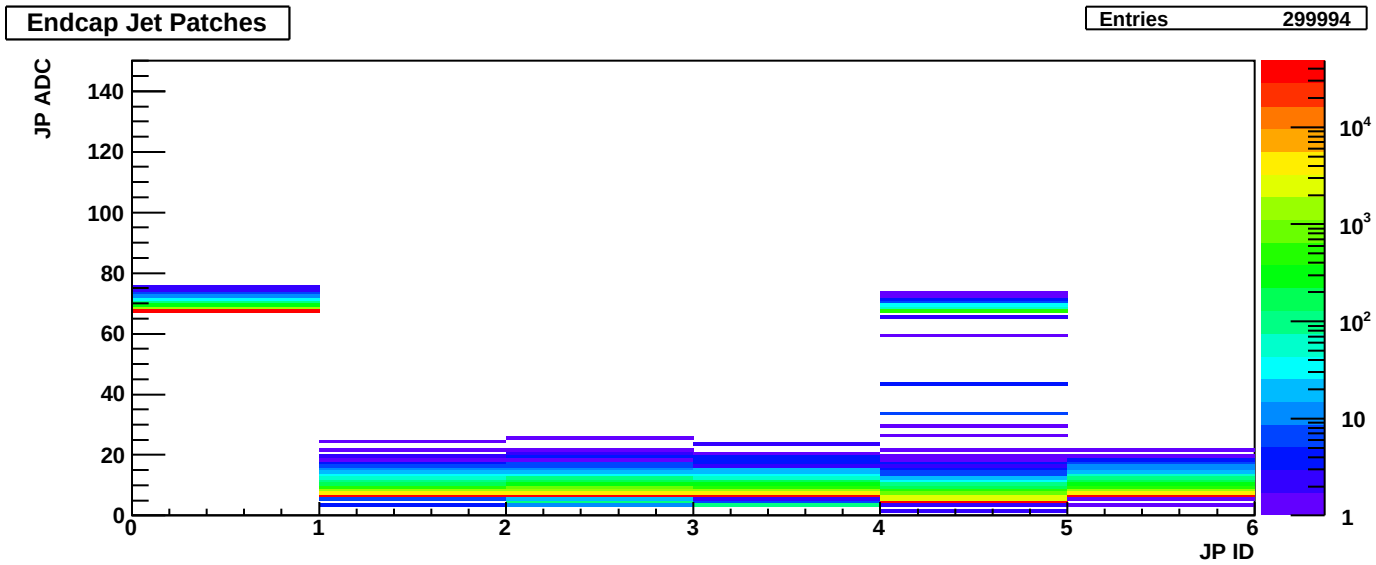

Barrel EMC L0 Input - High Tower
Entries 1.49997e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

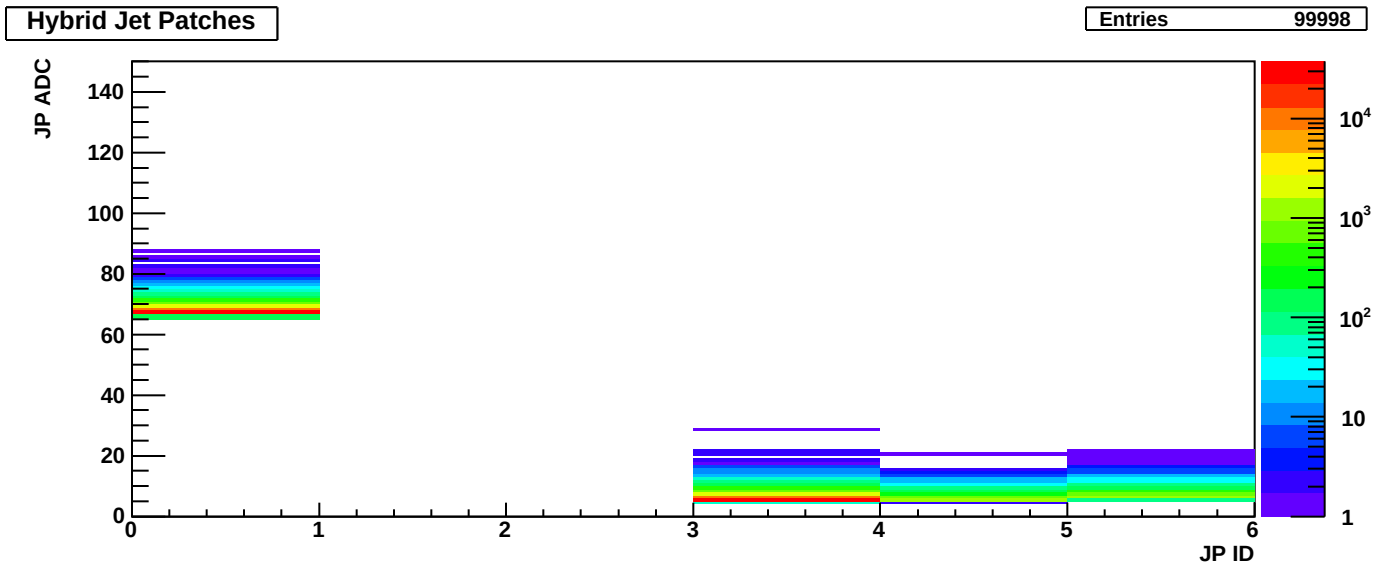

Barrel EMC L0 Input - Patch Sum
Entries 1.49997e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

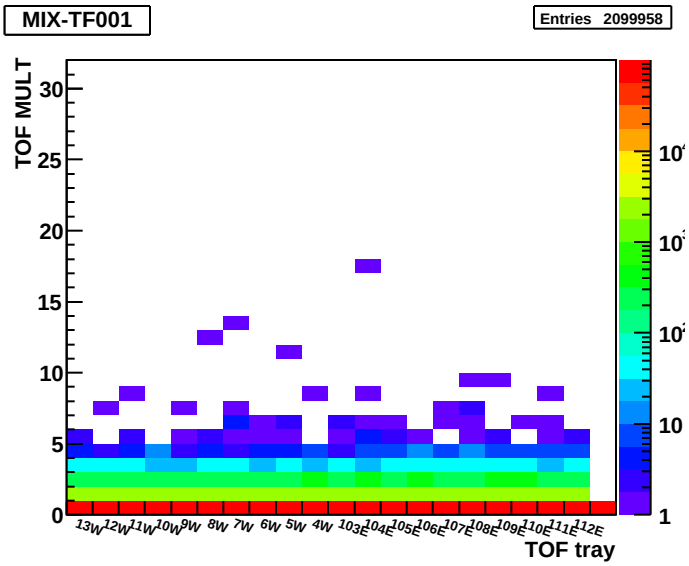

Endcap EMC L0 Input - High Tower
Entries 4499910
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

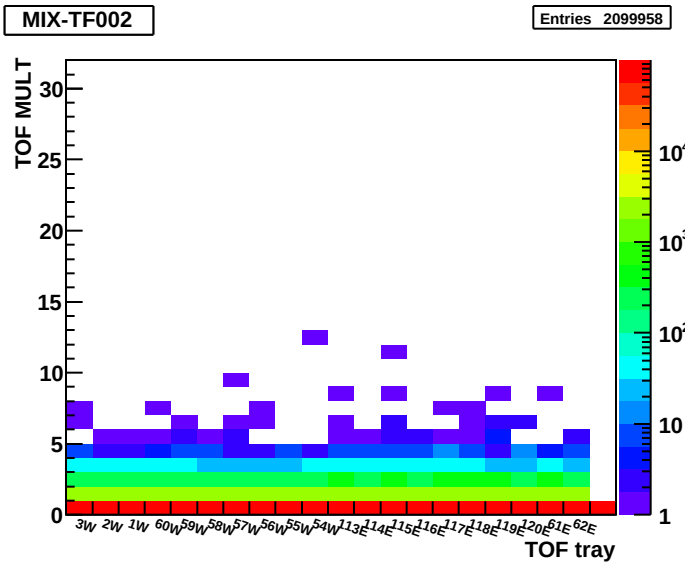

Endcap EMC L0 Input - Patch Sum
Entries 4499910
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

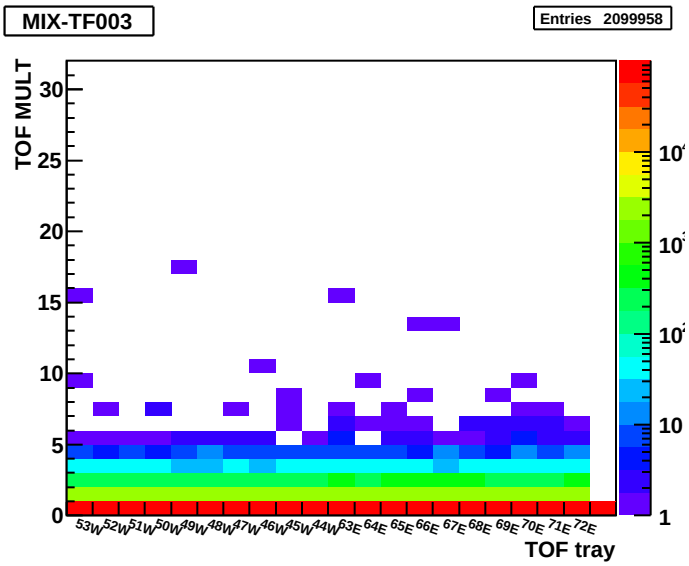

Barrel Jet Patches
Entries 899982
JP ADC
JP ID

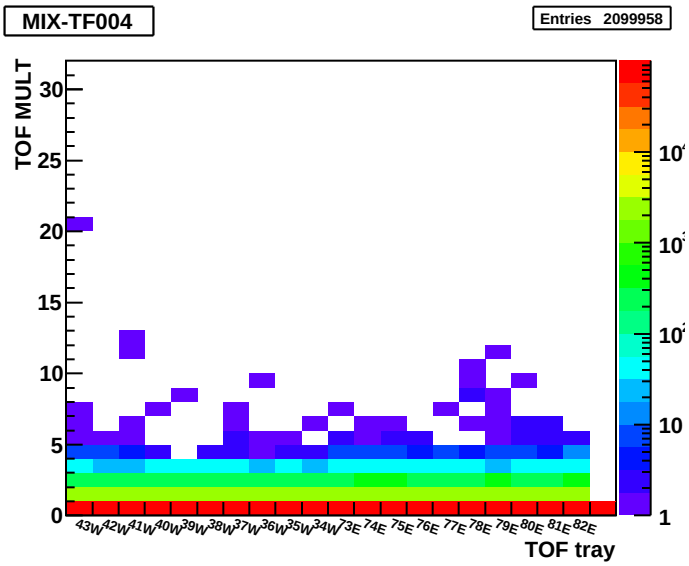

Endcap Jet Patches
Entries 299994
JP ADC
JP ID

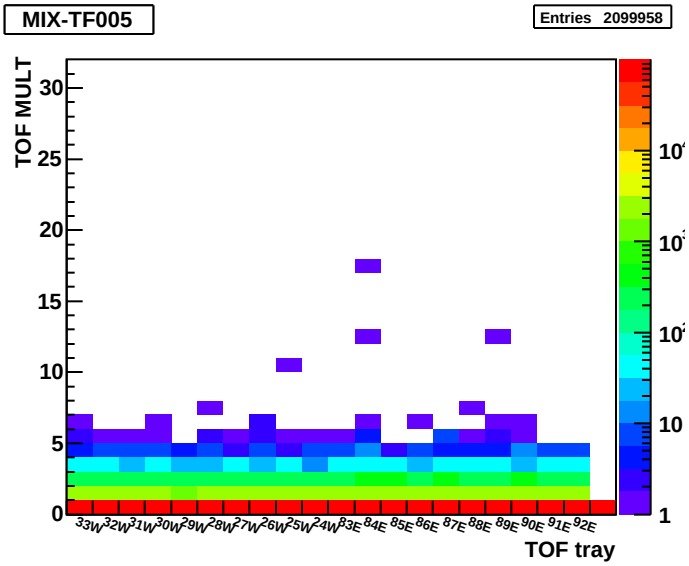

Hybrid Jet Patches
Entries 99998
JP ADC
JP ID

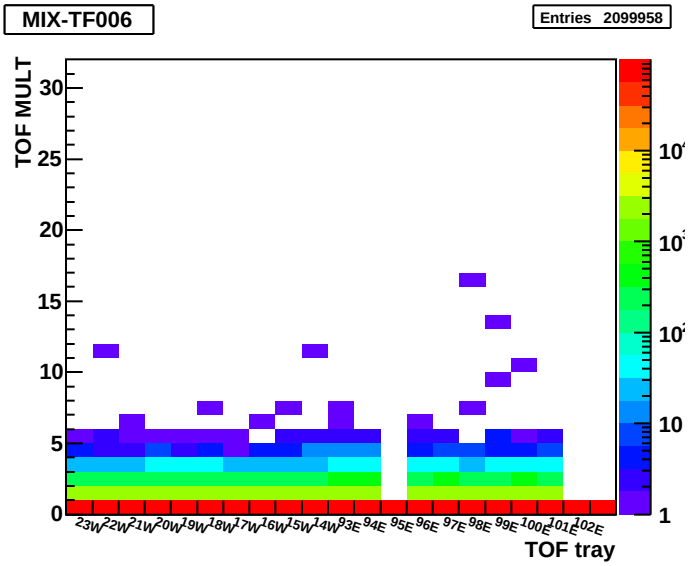

MIX-TF001
Entries 2099958
TOF MULT
TOF tray

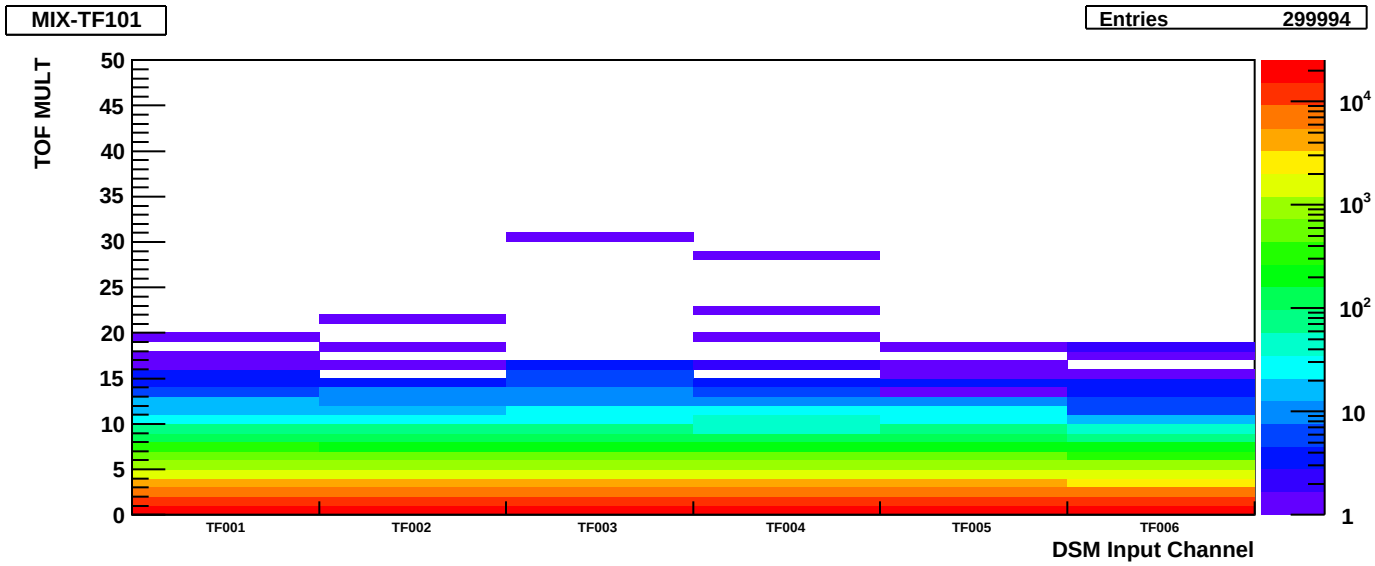

MIX-TF002
Entries 2099958
TOF MULT
TOF tray

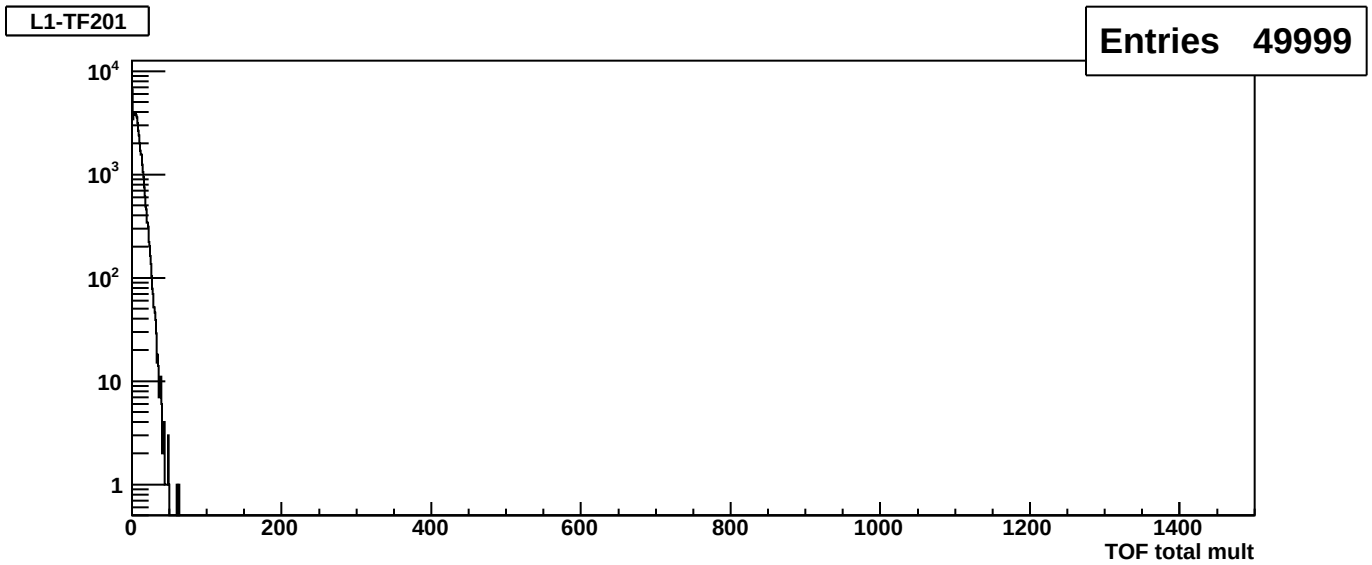

MIX-TF003
Entries 2099958
TOF MULT
TOF tray

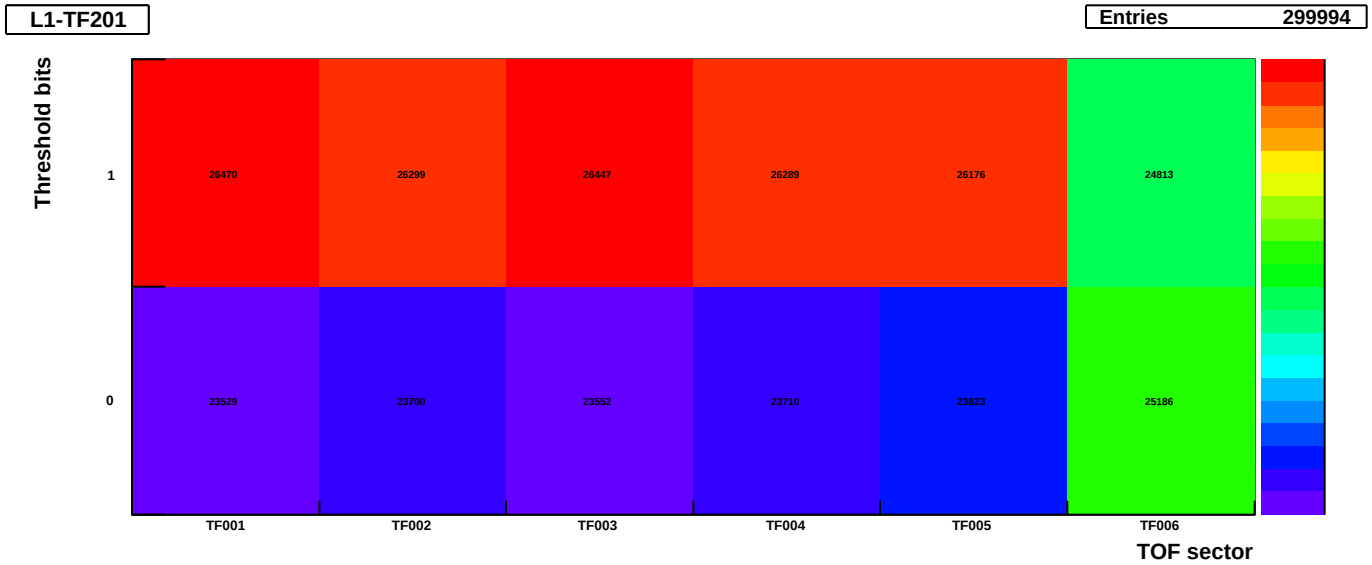

MIX-TF004
Entries 2099958
TOF MULT
TOF tray

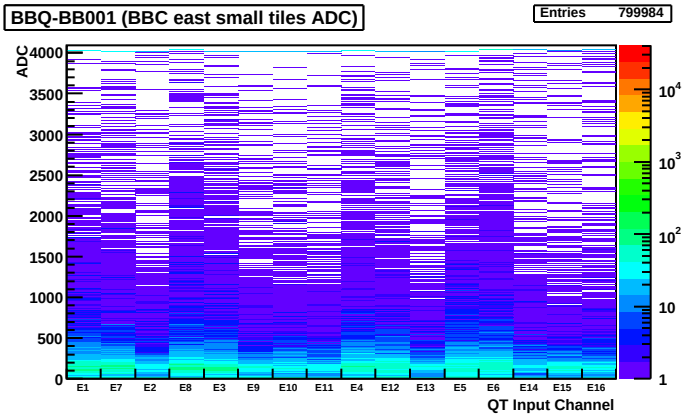

MIX-TF005
Entries 2099958
TOF MULT
TOF tray

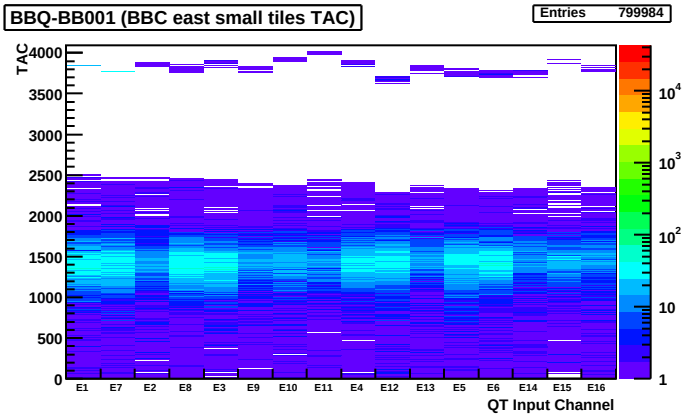

MIX-TF006
Entries 2099958
TOF MULT
TOF tray

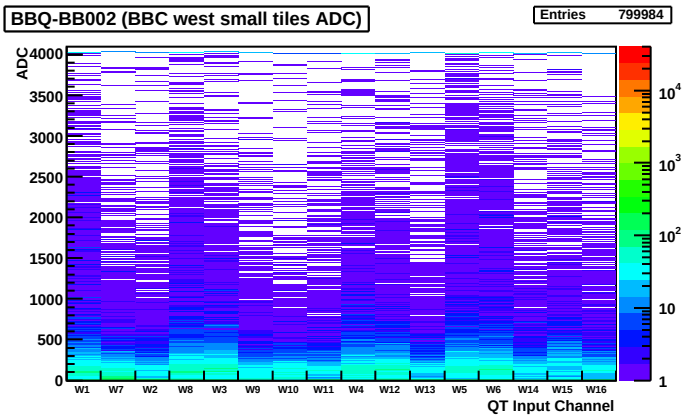

MIX-TF101
Entries 299994
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

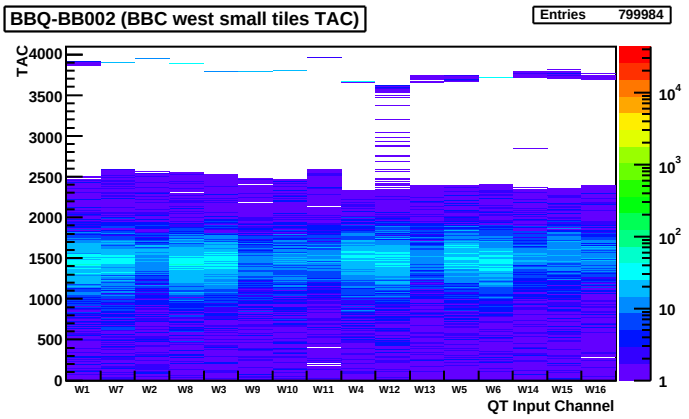

L1-TF201
Entries 49999
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

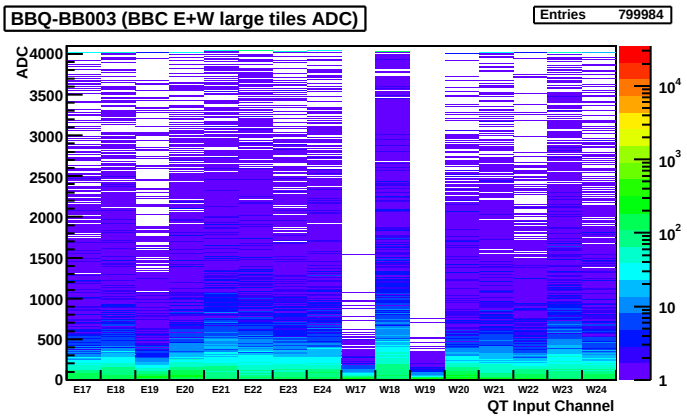

L1-TF201
Entries 299994
Threshold bits
1
0
26470
26299
26447
26289
26176
24813
23529
23700
23552
23710
23823
25186
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

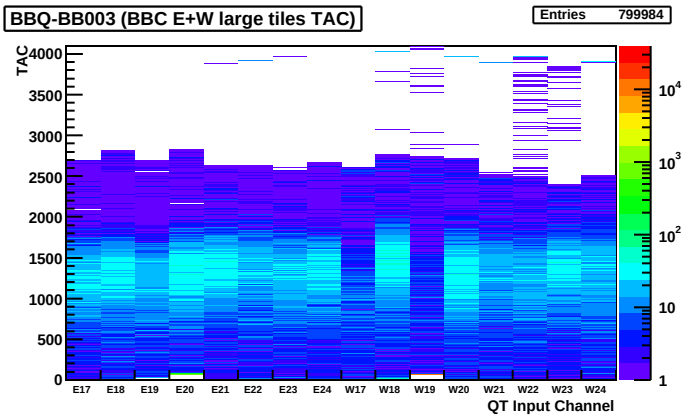

BBQ-BB001 (BBC east small tiles ADC)
Entries 799984
ADC
QT Input Channel

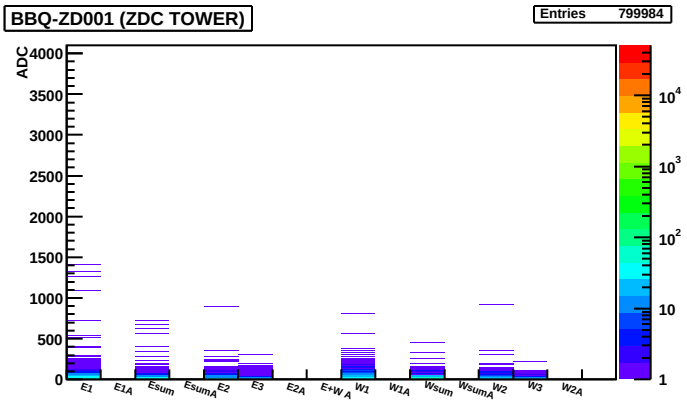

BBQ-BB001 (BBC east small tiles TAC)
Entries 799984
TAC
QT Input Channel

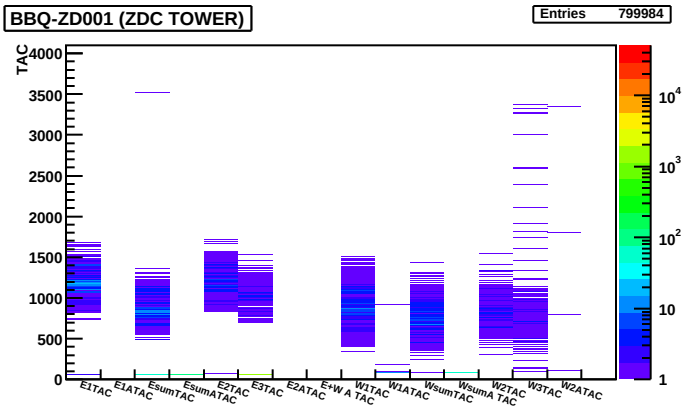

BBQ-BB002 (BBC west small tiles ADC)
Entries 799984
ADC
QT Input Channel

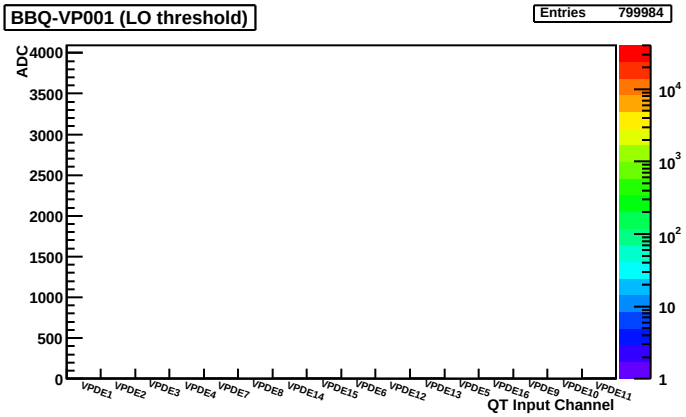

BBQ-BB002 (BBC west small tiles TAC)
Entries 799984
TAC
QT Input Channel

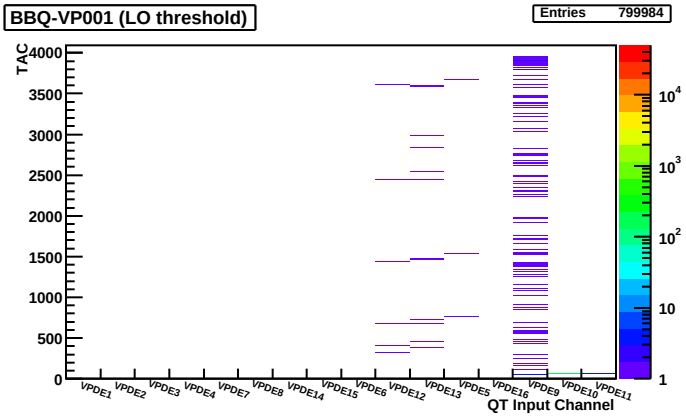

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 799984
ADC
QT Input Channel

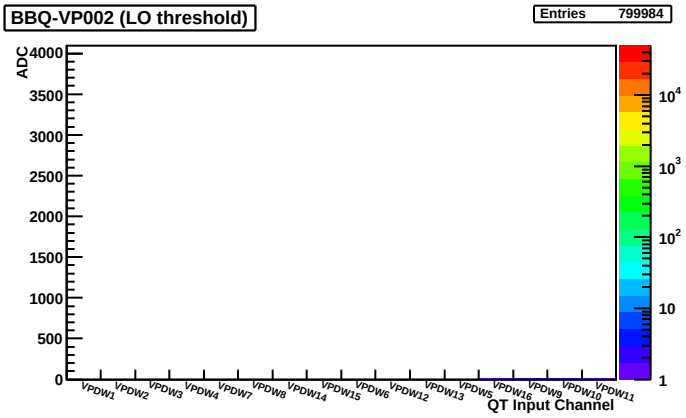

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 799984
TAC
QT Input Channel

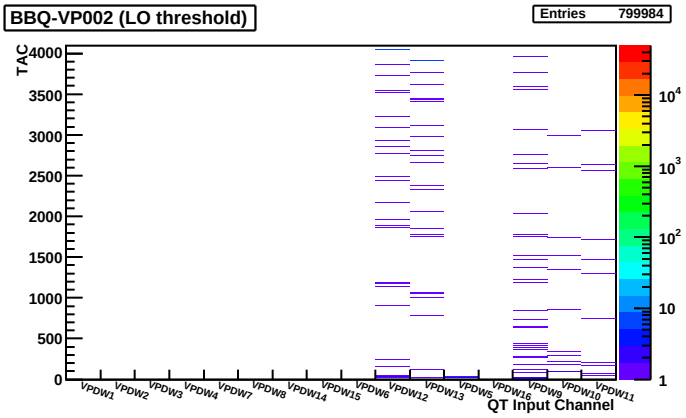

BBQ-ZD001 (ZDC TOWER)
Entries 799984
ADC

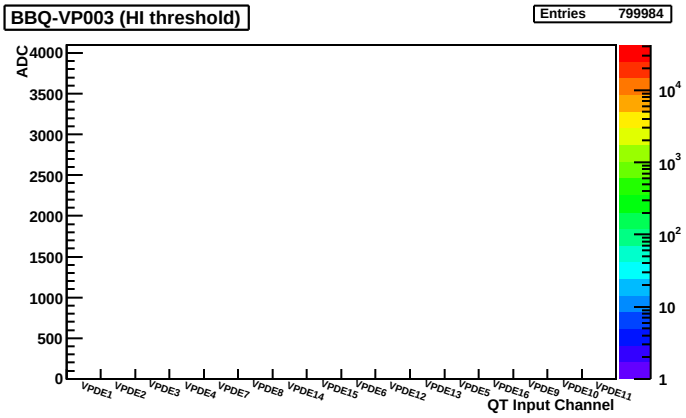

BBQ-ZD001 (ZDC TOWER)
Entries 799984
TAC

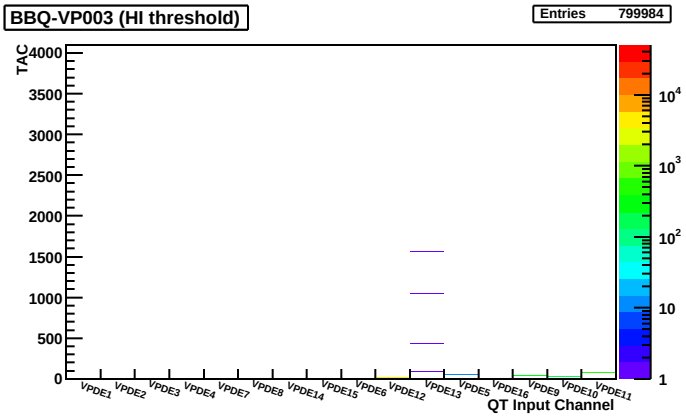

BBQ-VP001 (LO threshold)
Entries 799984
ADC
QT Input Channel

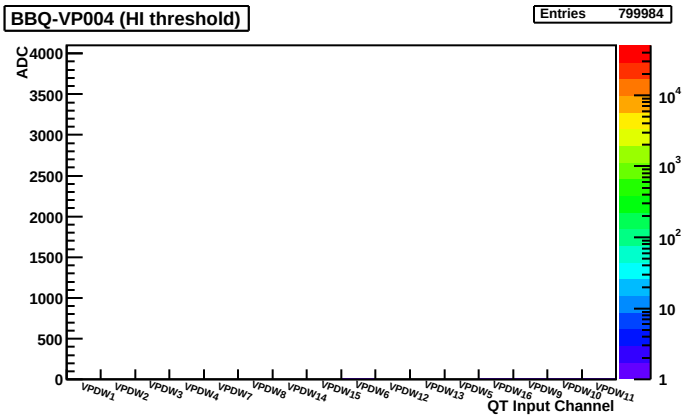

BBQ-VP001 (LO threshold)
Entries 799984
TAC
QT Input Channel

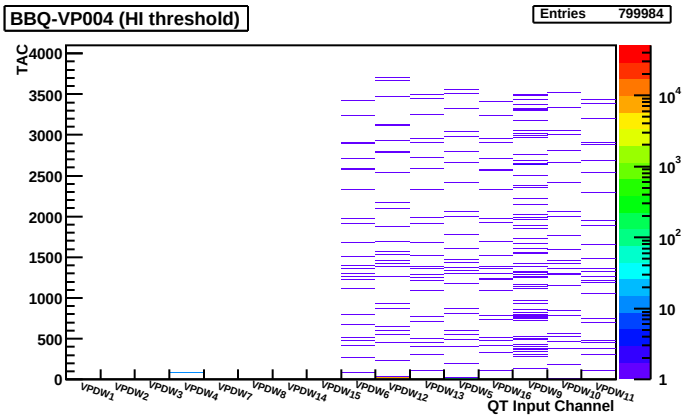

BBQ-VP002 (LO threshold)
Entries 799984
ADC
QT Input Channel

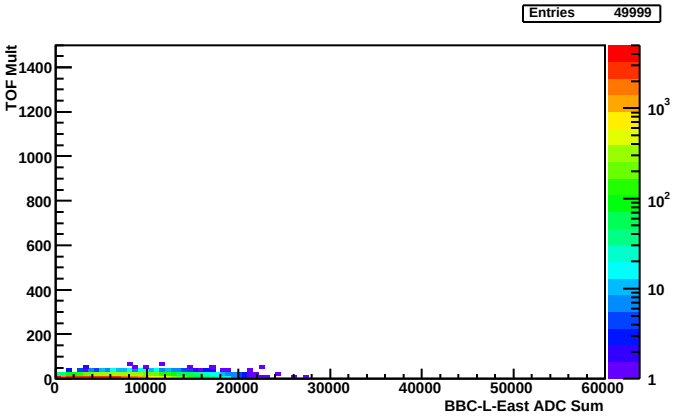

BBQ-VP002 (LO threshold)
Entries 799984
TAC
QT Input Channel

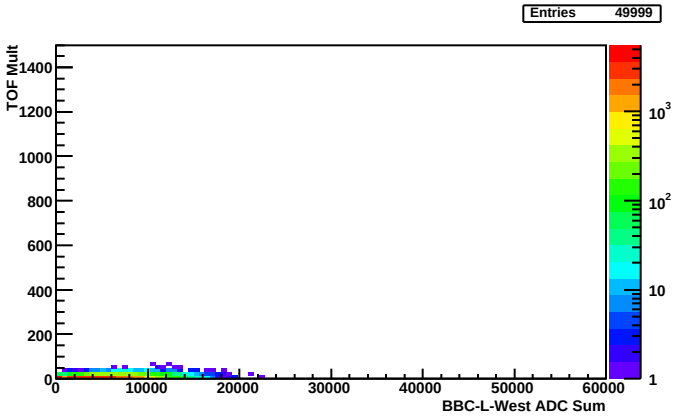

BBQ-VP003 (HI threshold)
Entries 799984
ADC
QT Input Channel

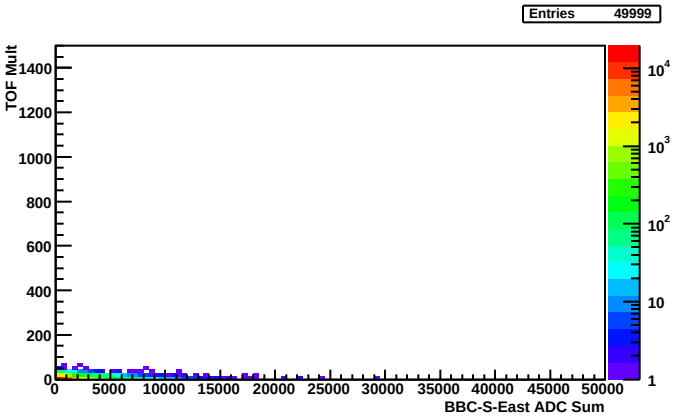

BBQ-VP003 (HI threshold)
Entries 799984
TAC
QT Input Channel

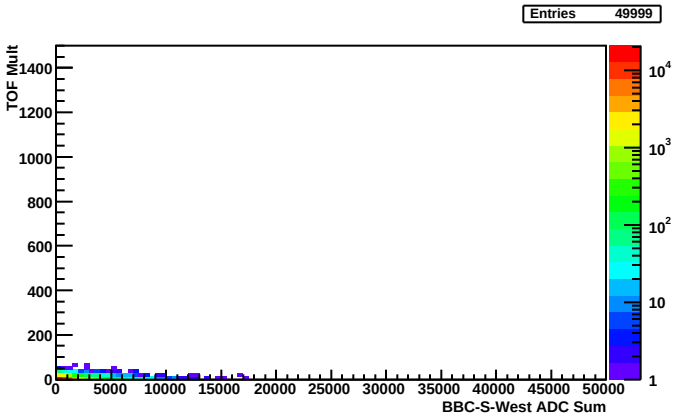

BBQ-VP004 (HI threshold)
Entries 799984
ADC
QT Input Channel

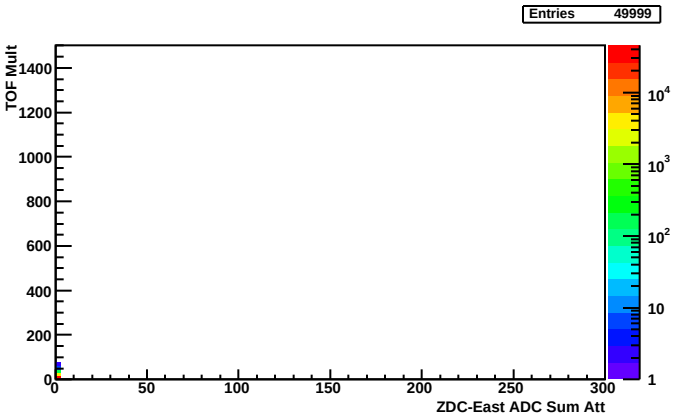

BBQ-VP004 (HI threshold)
Entries 799984
TAC
QT Input Channel

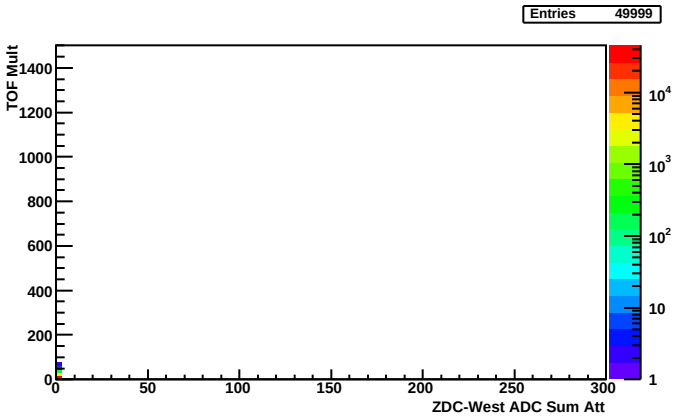

Entries 49999
TOF Mult
BBC-L-East ADC Sum

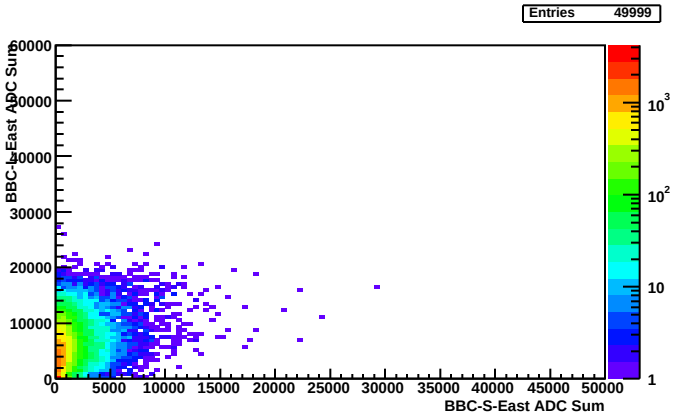

Entries 49999
TOF Mult
BBC-L-West ADC Sum

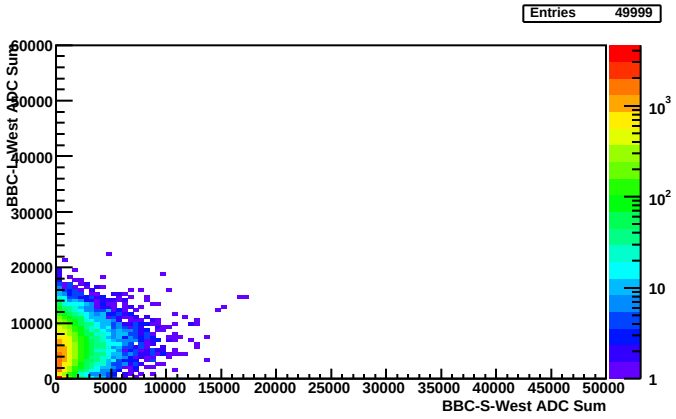

Entries 49999
TOF Mult
BBC-S-East ADC Sum

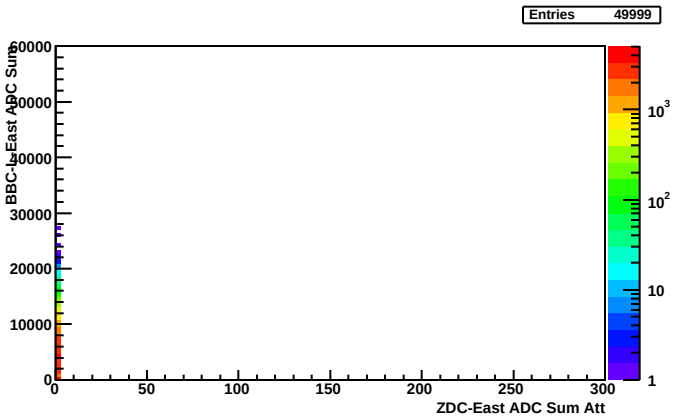

Entries 49999
TOF Mult
BBC-S-West ADC Sum

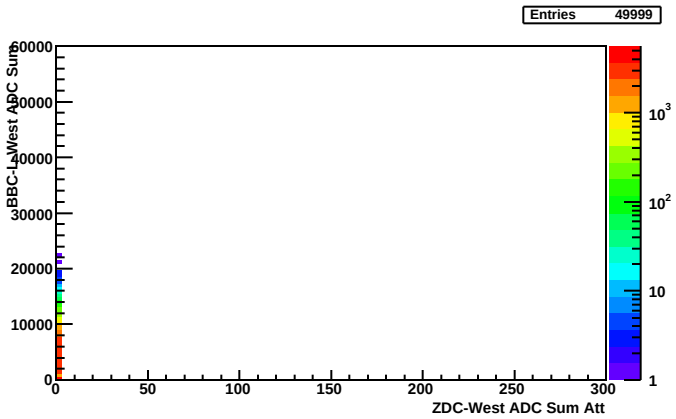

Entries 49999
TOF Mult
ZDC-East ADC Sum Att

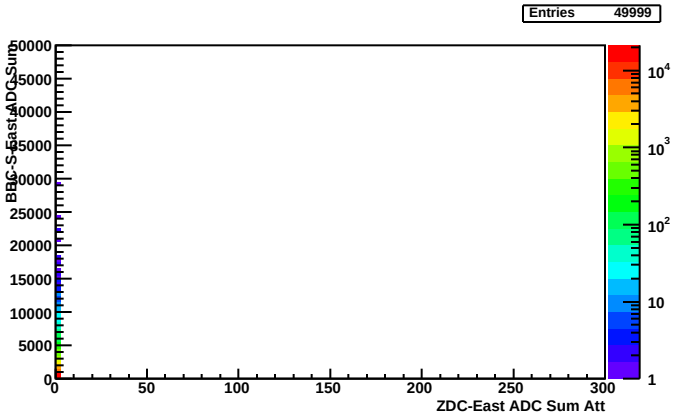

Entries 49999
TOF Mult
ZDC-West ADC Sum Att

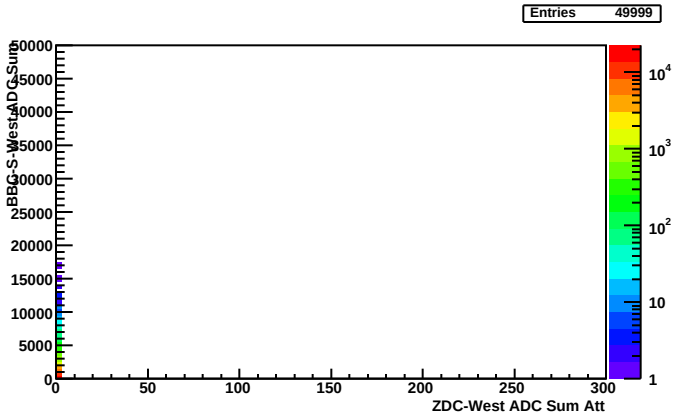

Entries 49999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

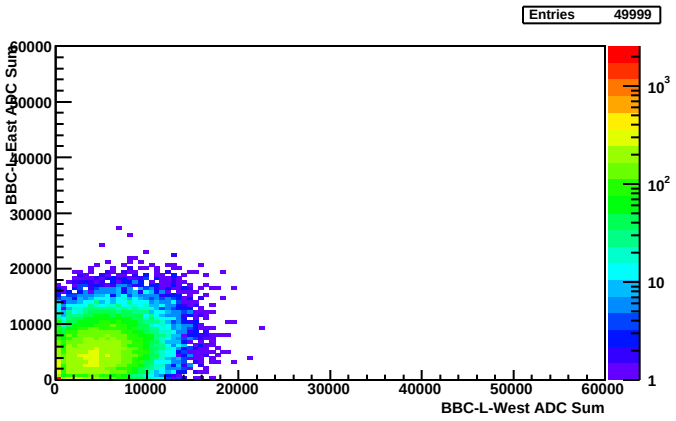

Entries 49999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

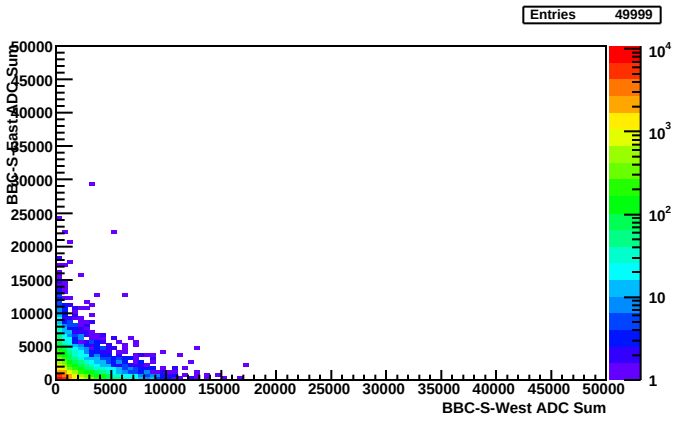

Entries 49999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

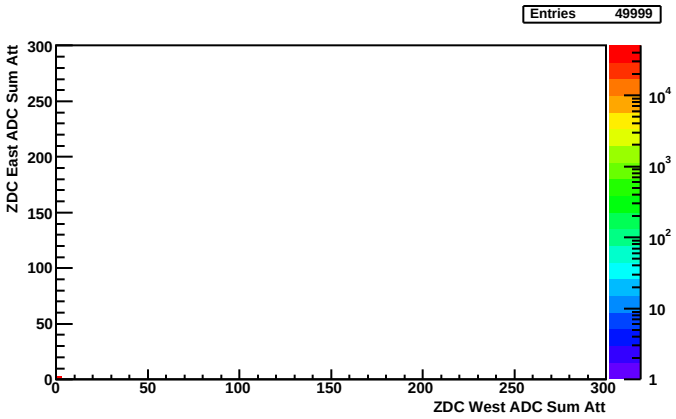

Entries 49999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

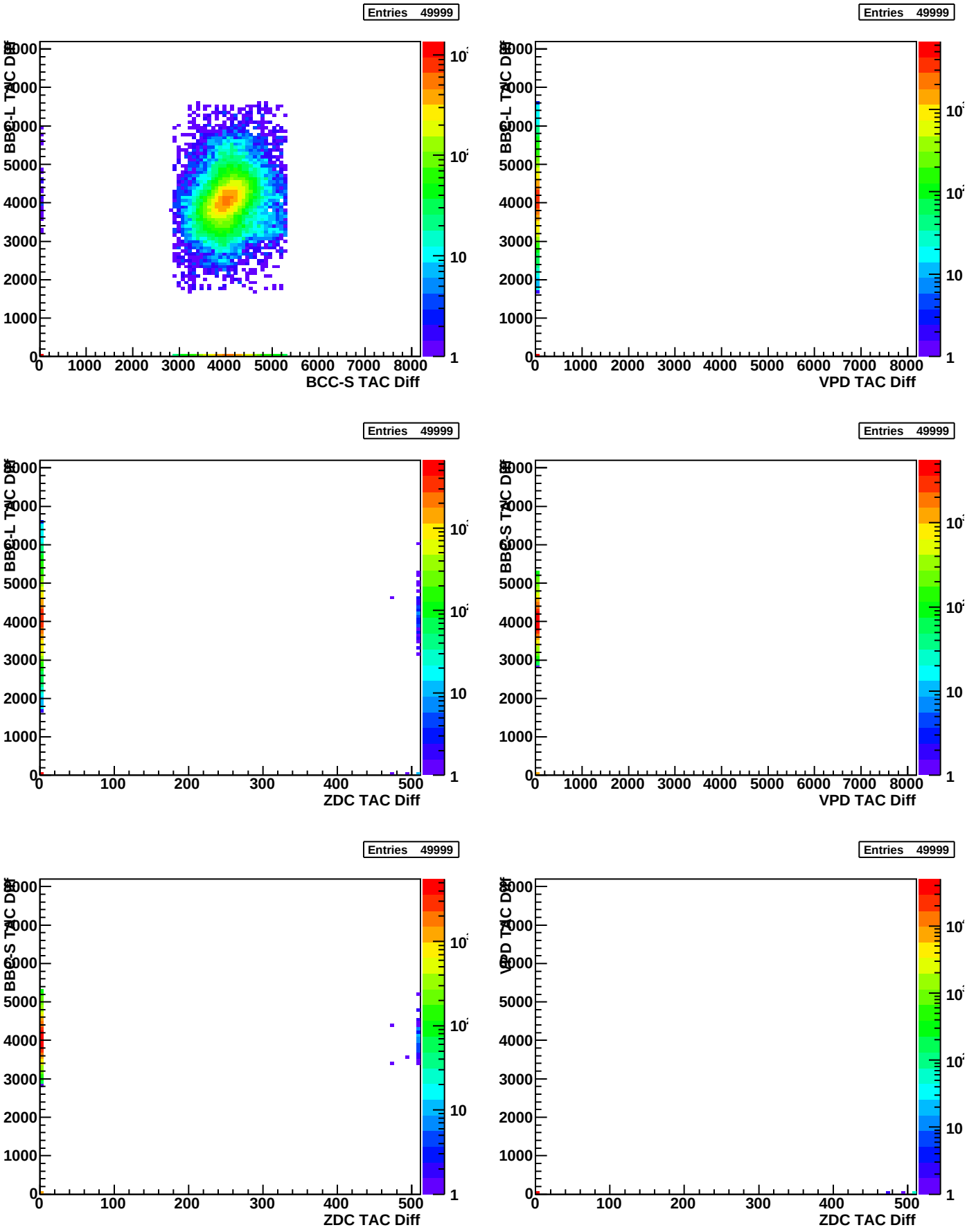

Entries 49999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

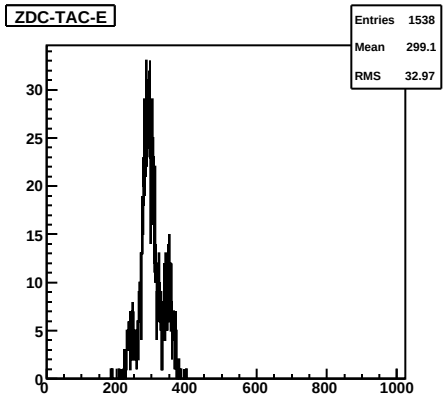

Entries 49999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

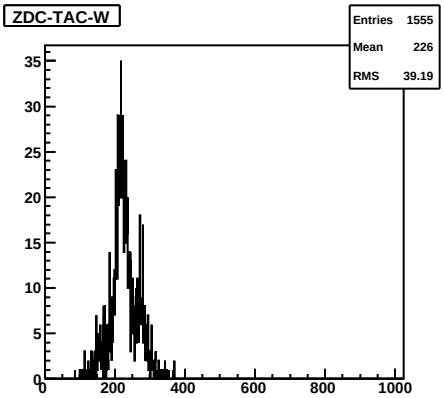

Entries 49999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

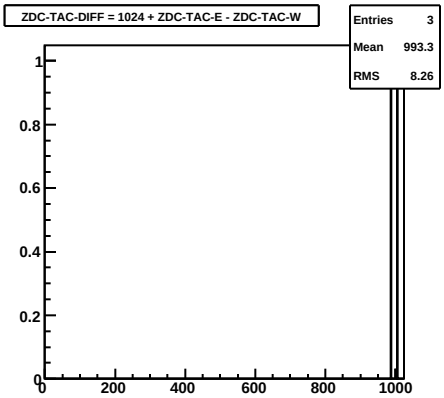

Entries 49999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

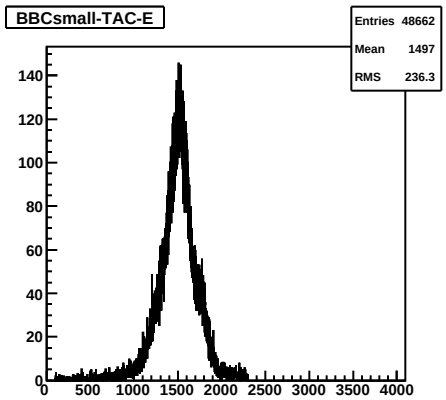

Entries 49999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

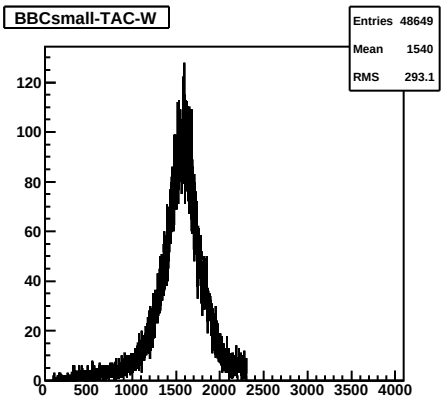

Entries 49999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 49999
BBC-L TAC Diff
VPD TAC Diff
Entries 49999
BBC-L TAC Diff
ZDC TAC Diff
Entries 49999
BBC-S TAC Diff
VPD TAC Diff
Entries 49999
BBC-S TAC Diff
ZDC TAC Diff
Entries 49999
VPD TAC Diff
ZDC TAC Diff

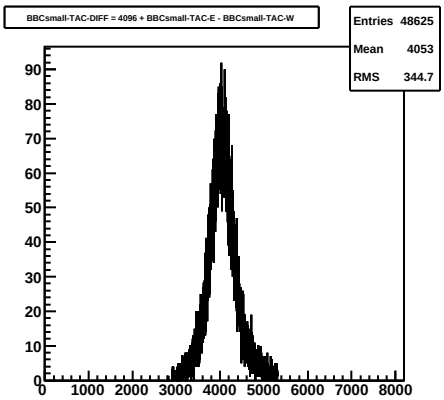

ZDC-TAC-E
Entries 1538
Mean 299.1
RMS 32.97

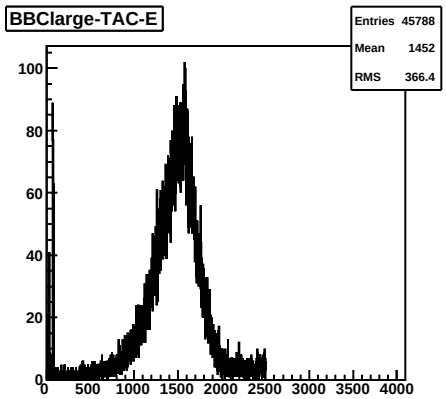

ZDC-TAC-W
Entries 1555
Mean 226
RMS 39.19

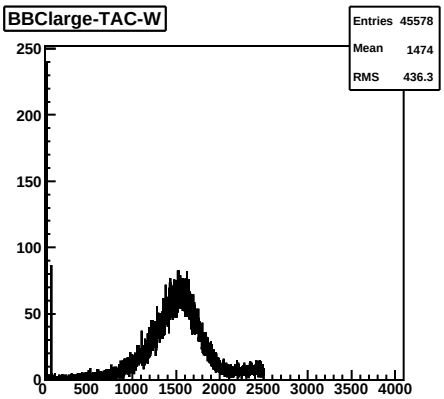

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3
Mean 993.3
RMS 8.26

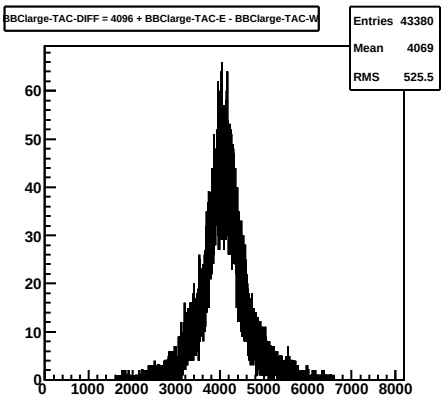

BBCsmall-TAC-E
Entries 48662
Mean 1497
RMS 236.3

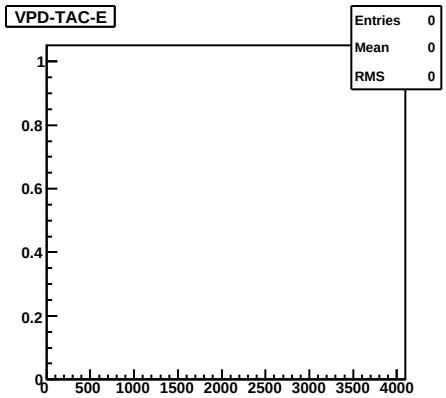

BBCsmall-TAC-W
Entries 48649
Mean 1540
RMS 293.1

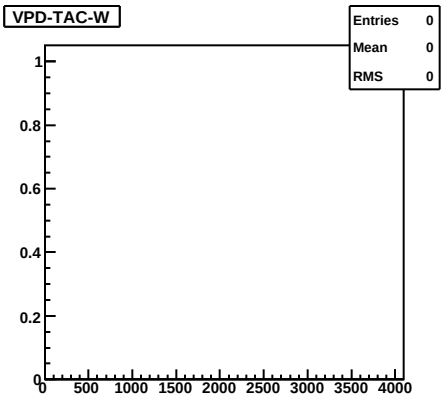

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 48625
Mean 4053
RMS 344.7

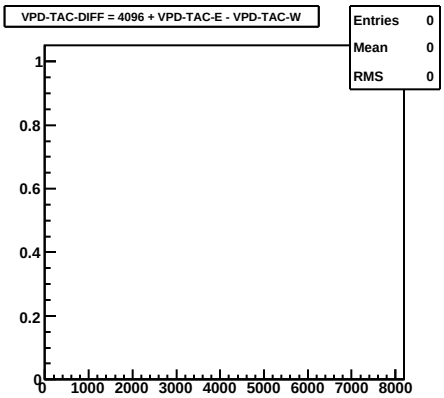

BBClarge-TAC-E
Entries 45788
Mean 1452
RMS 366.4

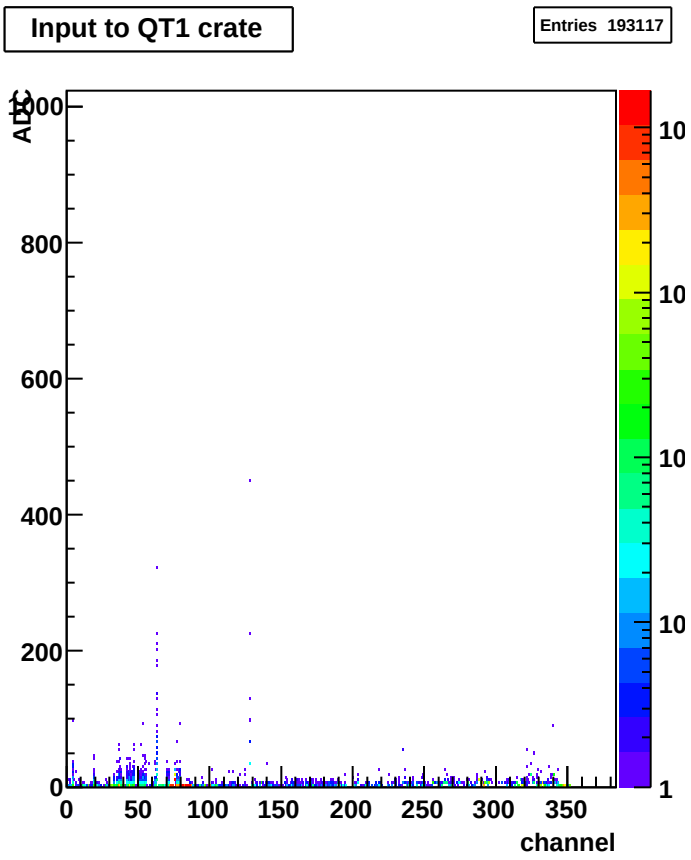

BBClarge-TAC-W
Entries 45578
Mean 1474
RMS 436.3

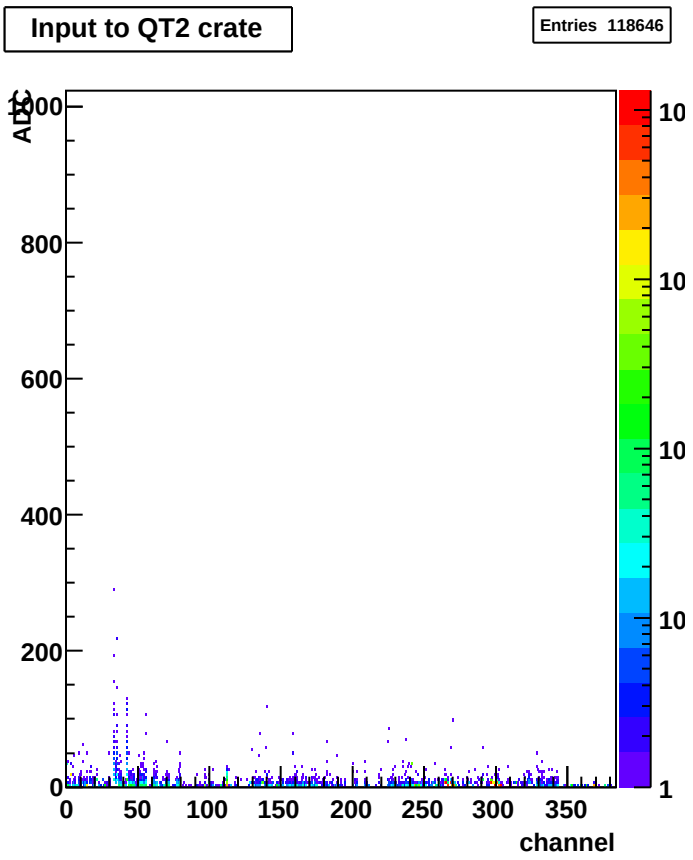

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 43380
Mean 4069
RMS 525.5

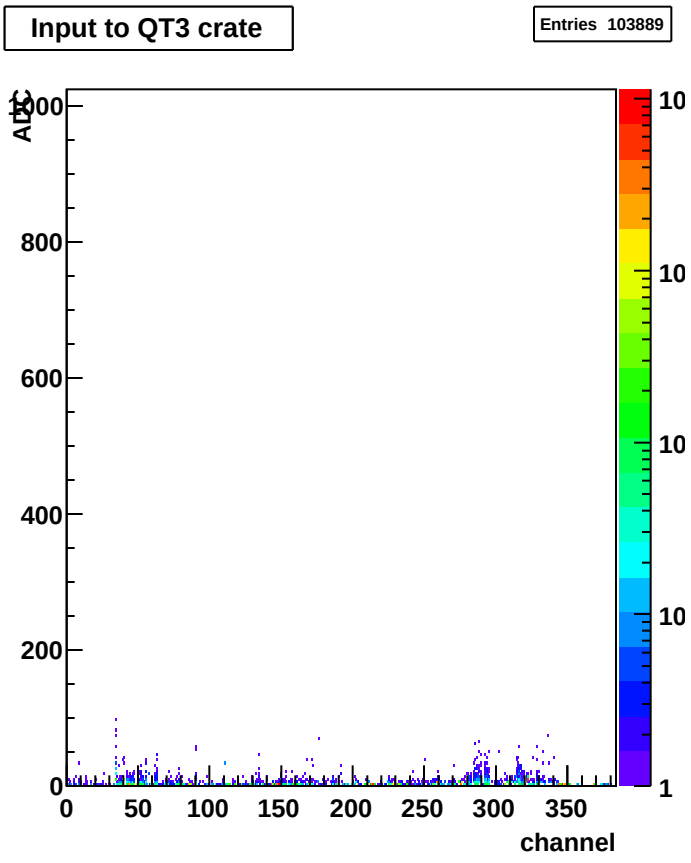

VPD-TAC-E
Entries 0
Mean 0
RMS 0

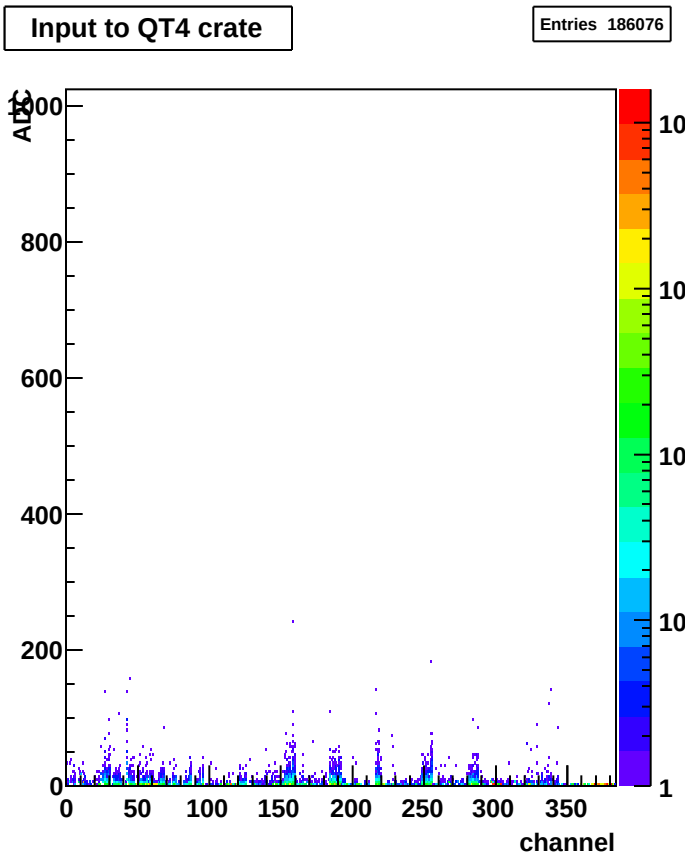

VPD-TAC-W
Entries 0
Mean 0
RMS 0

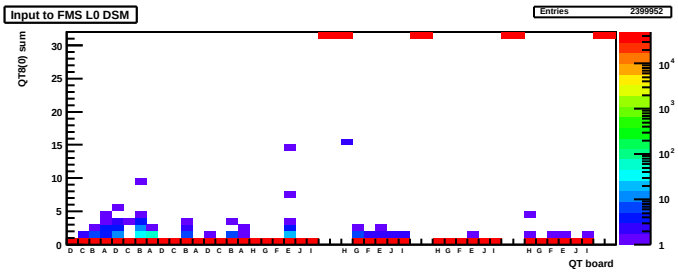

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 0
Mean 0
RMS 0

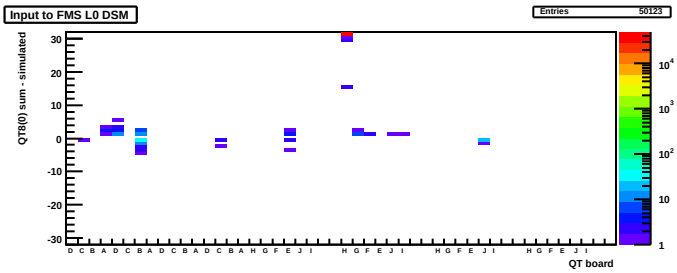

Input to QT1 crate
Entries 193117
ADC
channel

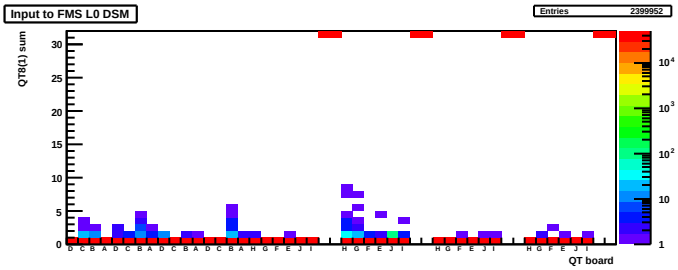

Input to QT2 crate
Entries 118646
ADC
channel

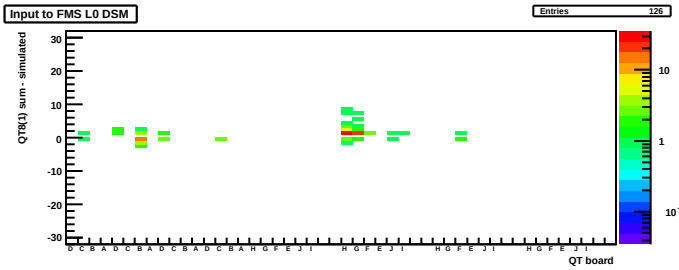

Input to QT3 crate
Entries 103889
ADC
channel

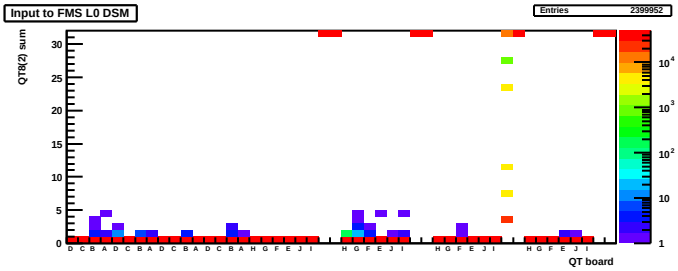

Input to QT4 crate
Entries 186076
ADC
channel

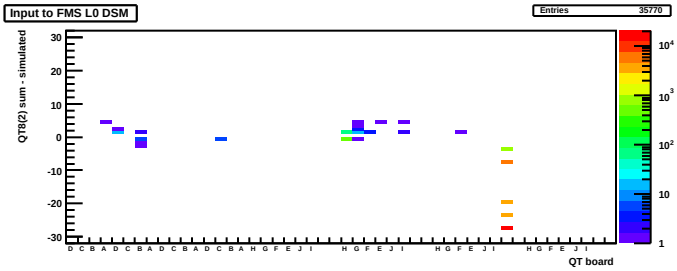

Input to FMS L0 DSM
Entries 2399952
QT8(0) sum
QT board

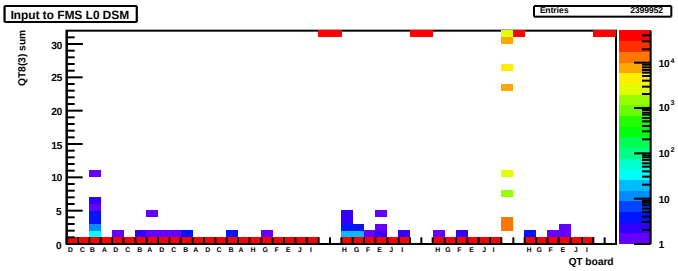

Input to FMS L0 DSM
Entries 50123
QT8(0) sum - simulated
QT board

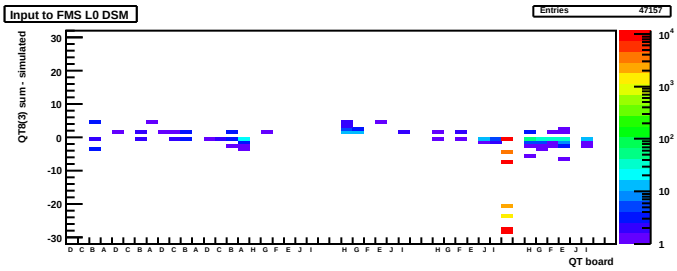

Input to FMS L0 DSM
Entries 2399952
QT8(1) sum
QT board

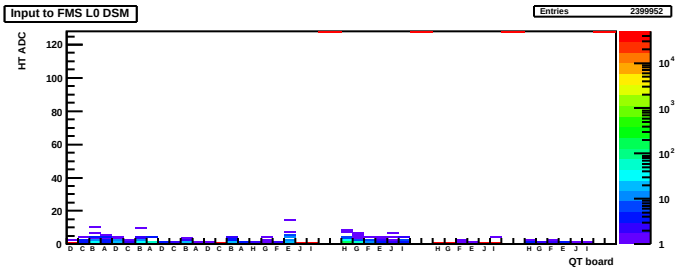

Input to FMS L0 DSM
Entries 126
QT8(1) sum - simulated
QT board

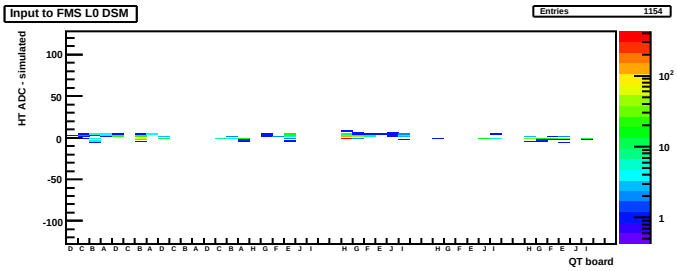

Input to FMS L0 DSM
Entries 2399952
QT8(2) sum
QT board

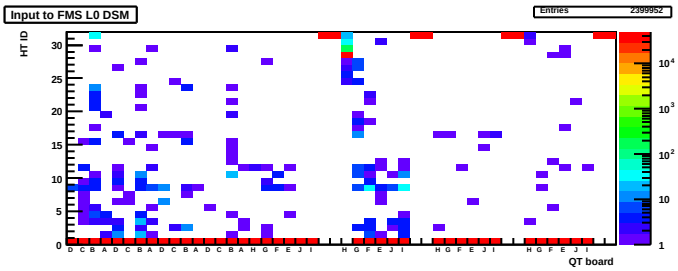

Input to FMS L0 DSM
Entries 35770
QT8(2) sum - simulated
QT board

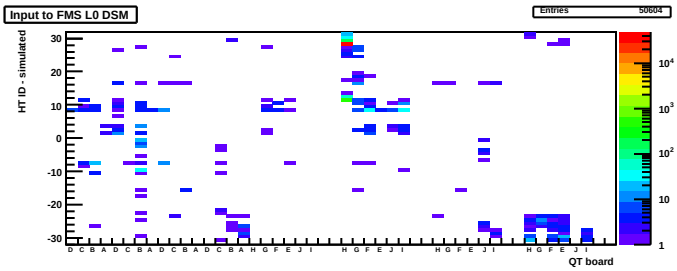

Input to FMS L0 DSM
Entries 2399952
QT8(3) sum
QT board

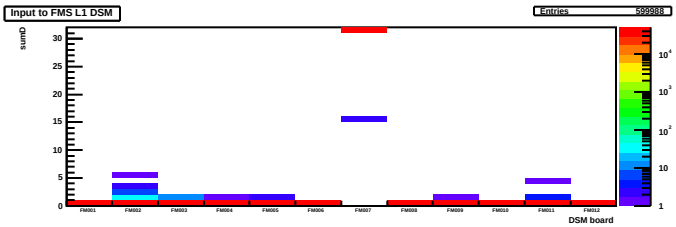

Input to FMS L0 DSM
Entries 47157
QT8(3) sum - simulated
QT board

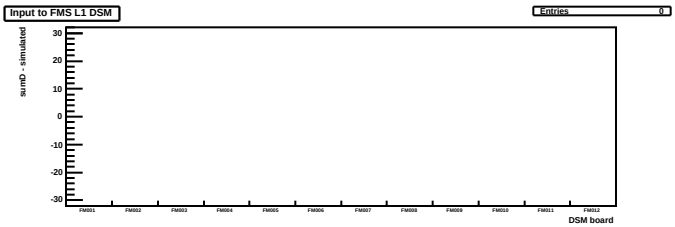

Input to FMS L0 DSM
Entries 2399952
HT ADC
QT board

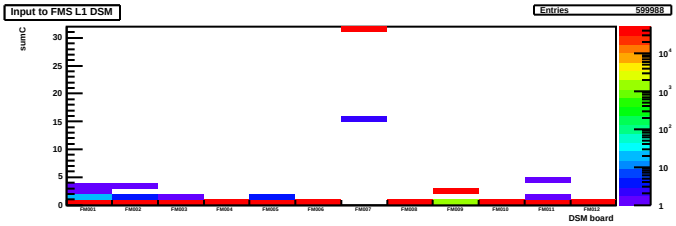

Input to FMS L0 DSM
Entries 1154
HT ADC - simulated
QT board

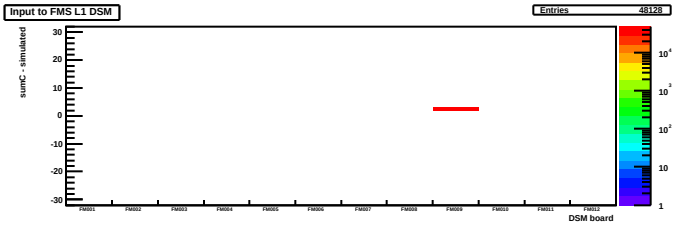

Input to FMS L0 DSM
Entries 2399952
HT ID
QT board

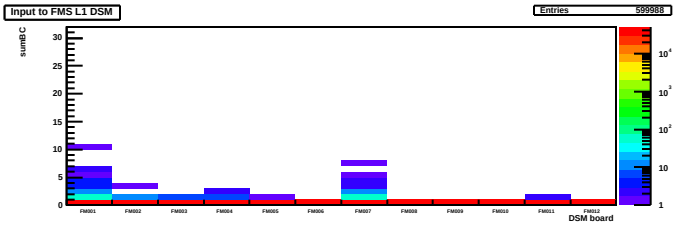

Input to FMS L0 DSM
Entries 50604
HT ID - simulated
QT board

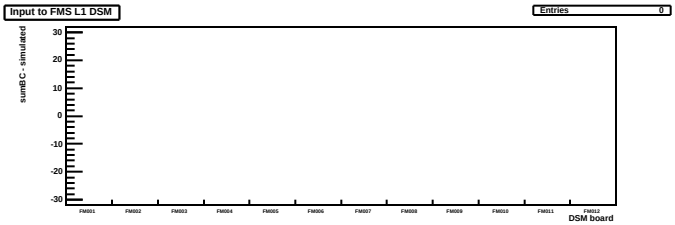

Input to FMS L1 DSM
Entries 599988
sumD
DSM board

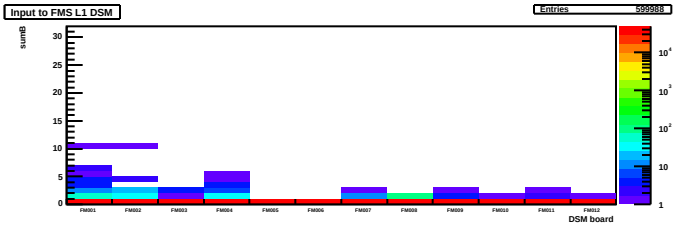

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

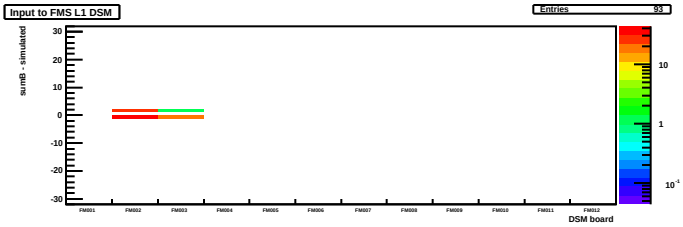

Input to FMS L1 DSM
Entries 599988
sumC
DSM board

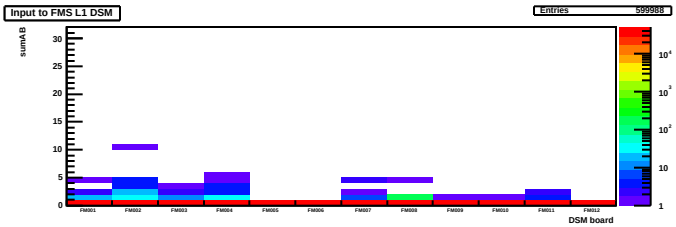

Input to FMS L1 DSM
Entries 48128
sumC - simulated
DSM board

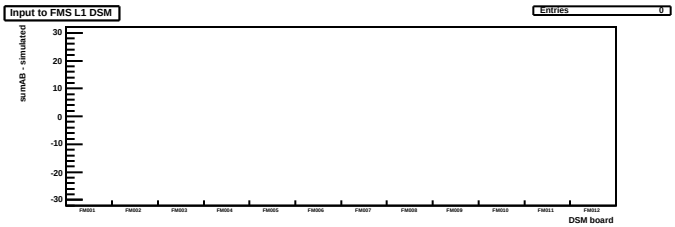

Input to FMS L1 DSM
Entries 599988
sumBC
DSM board

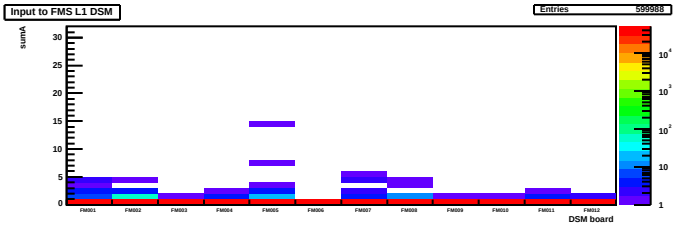

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

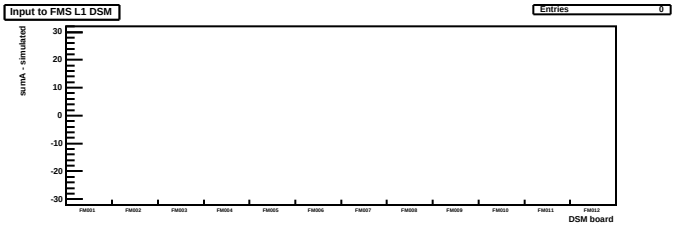

Input to FMS L1 DSM
Entries 599988
sumB
DSM board

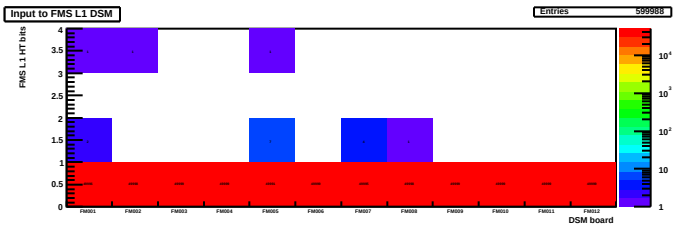

Input to FMS L1 DSM
Entries 93
sumB - simulated
DSM board

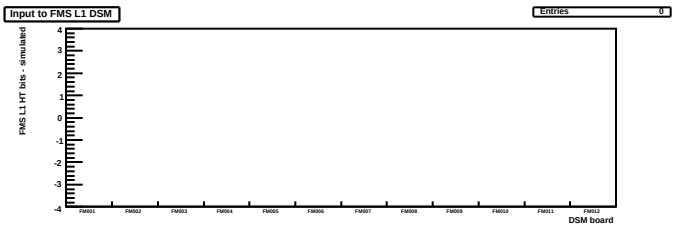

Input to FMS L1 DSM
Entries 599988
sumAB
DSM board

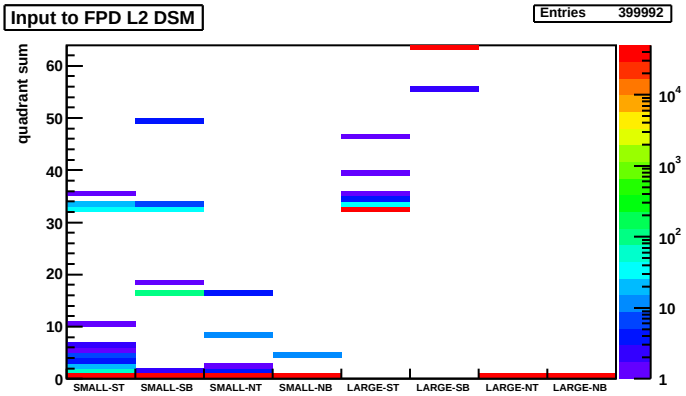

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

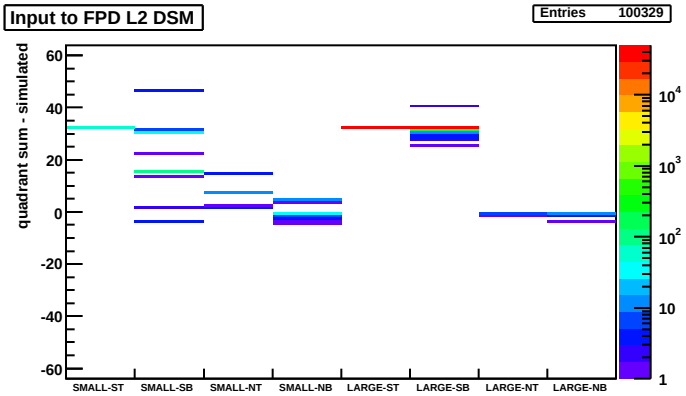

Input to FMS L1 DSM
Entries 599988
sumA
DSM board

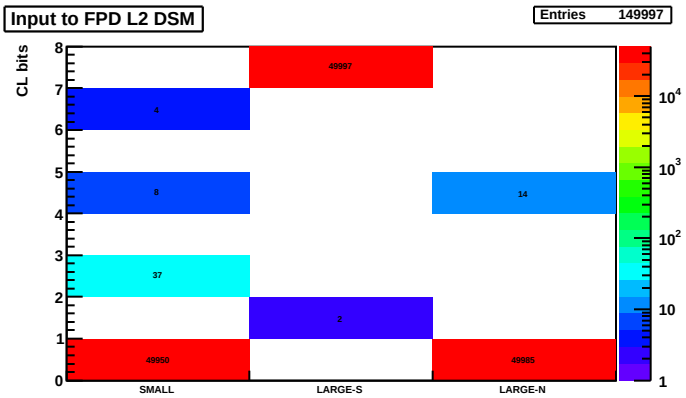

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

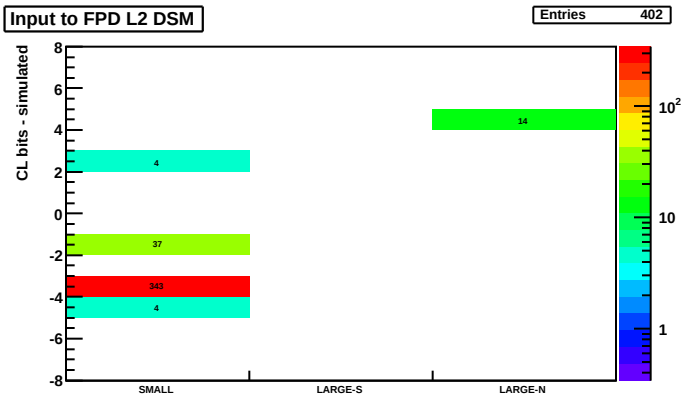

Input to FMS L1 DSM
Entries 599988
FMS L1 HT bits
DSM board

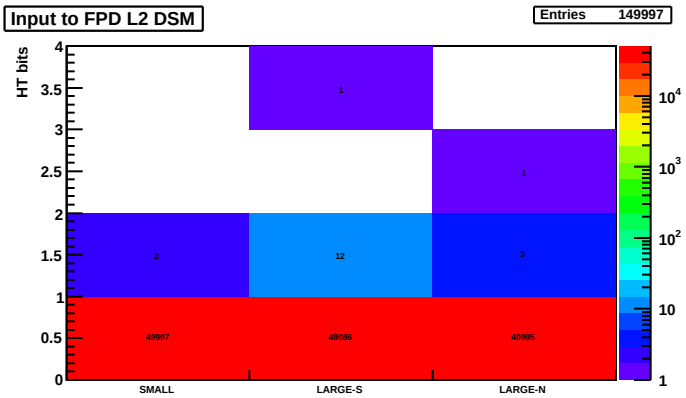

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

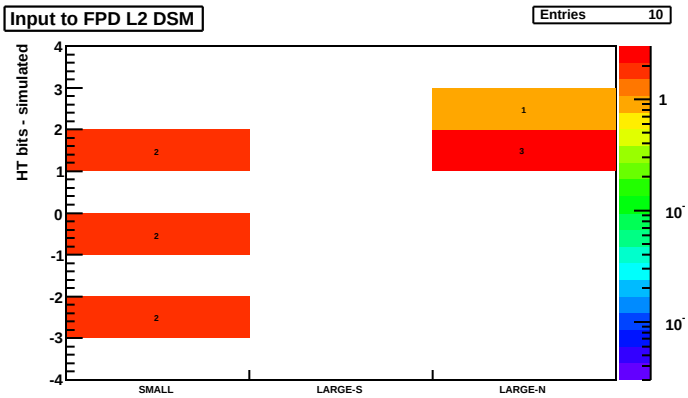

Input to FPD L2 DSM
Entries 399992
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

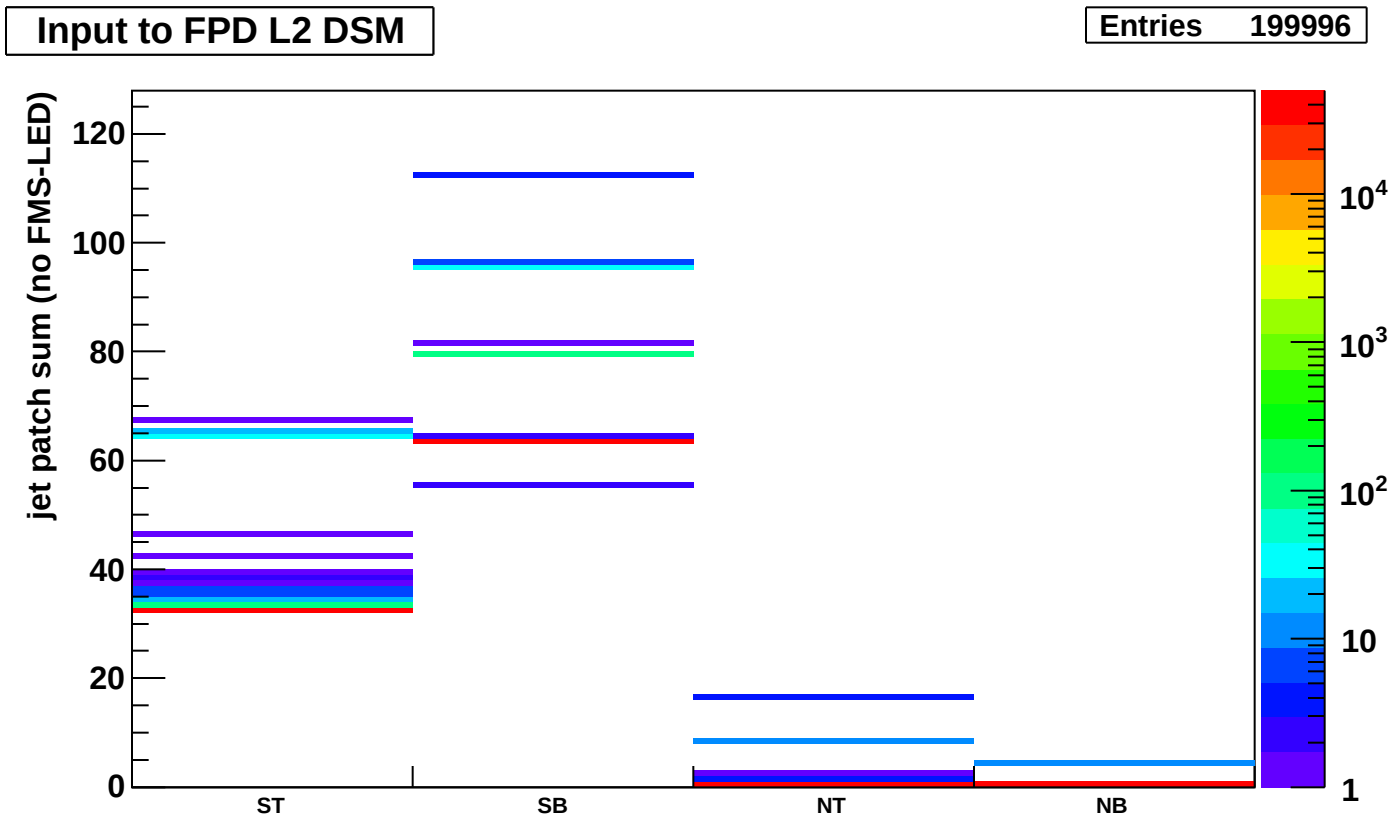

Input to FPD L2 DSM
Entries 100329
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

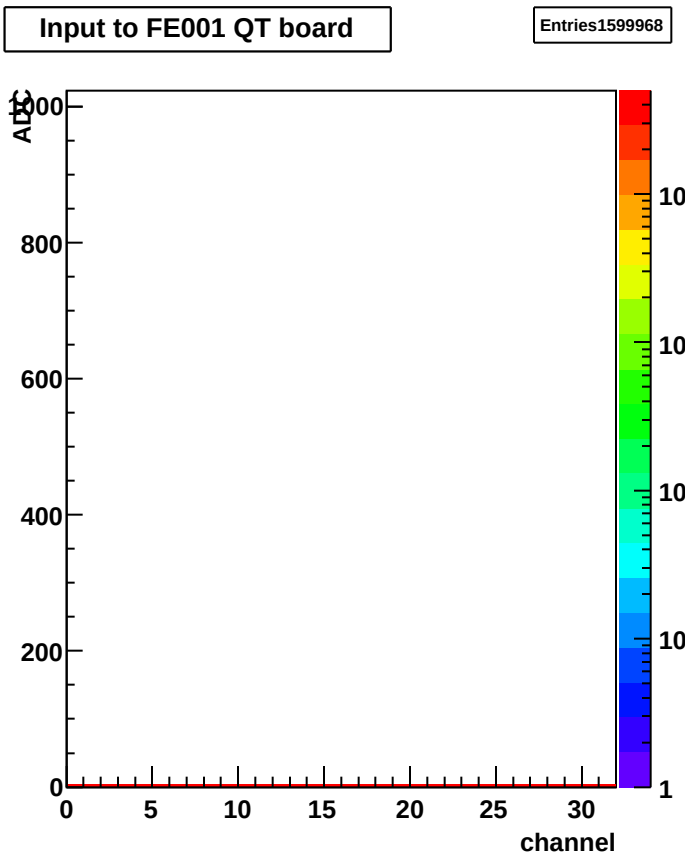

Input to FPD L2 DSM
Entries 149997
CL bits
SMALL
LARGE-S
LARGE-N

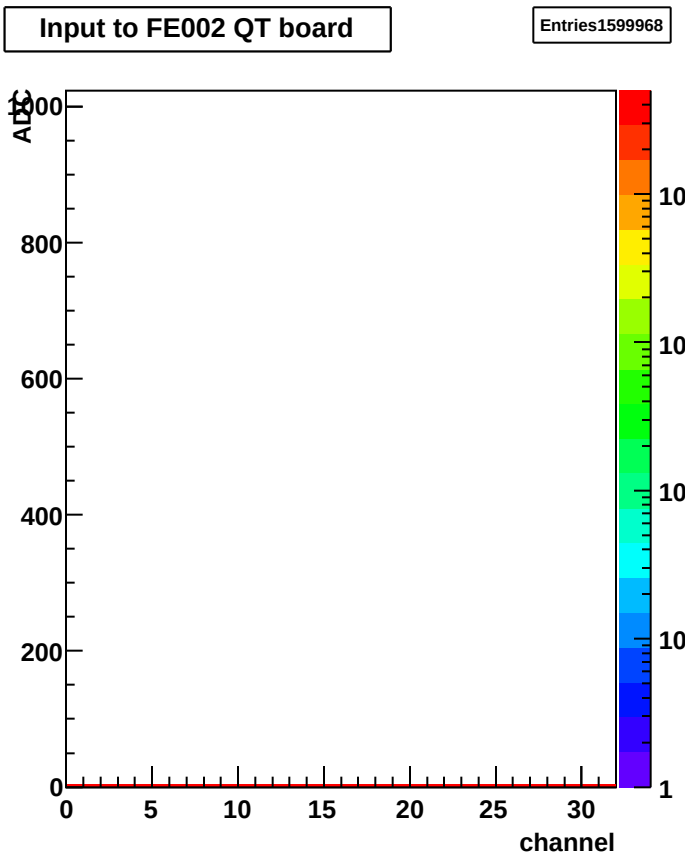

Input to FPD L2 DSM
Entries 402
CL bits - simulated
SMALL
LARGE-S
LARGE-N

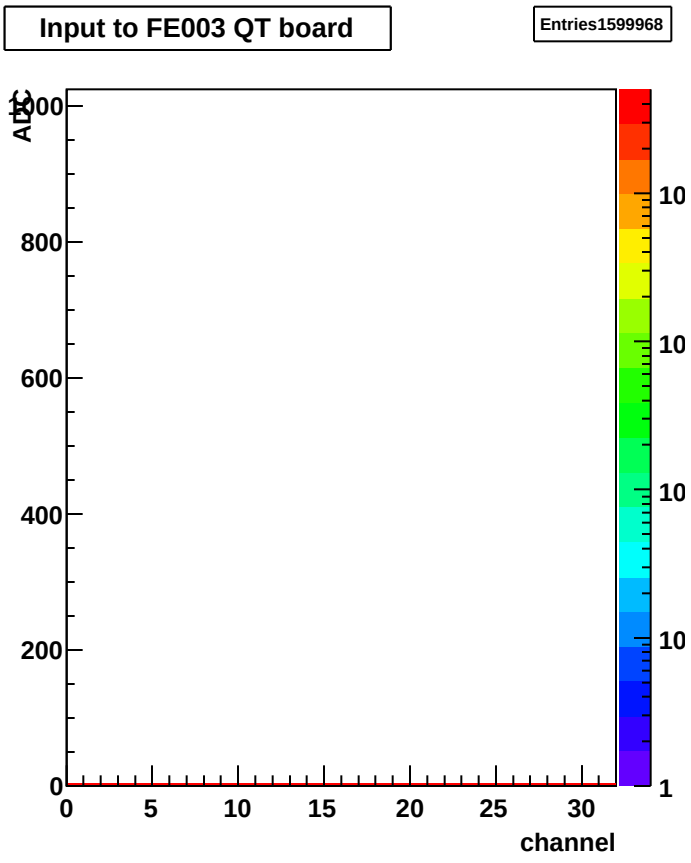

Input to FPD L2 DSM
Entries 149997
HT bits
SMALL
LARGE-S
LARGE-N

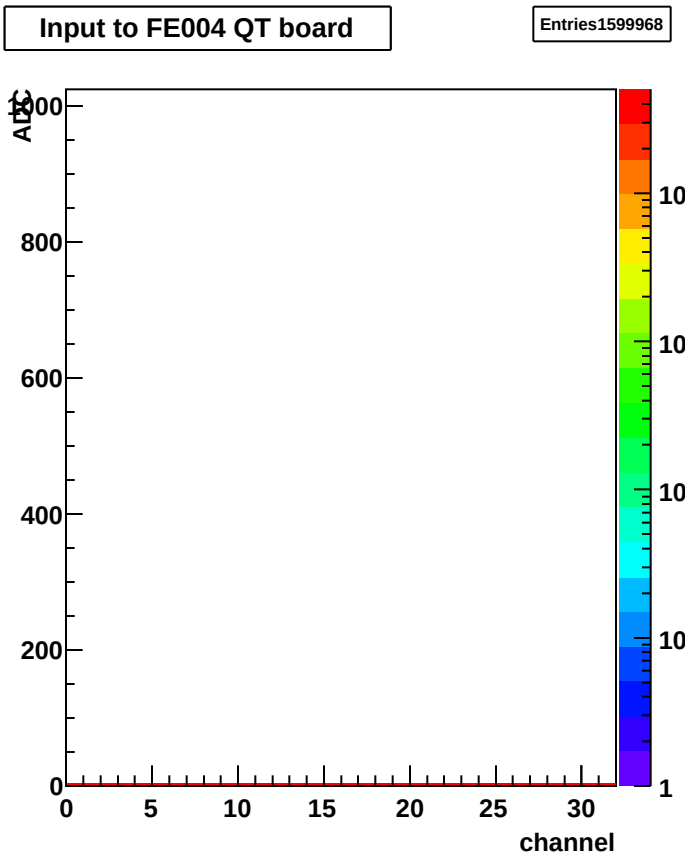

Input to FPD L2 DSM
Entries 10
HT bits - simulated
SMALL
LARGE-S
LARGE-N

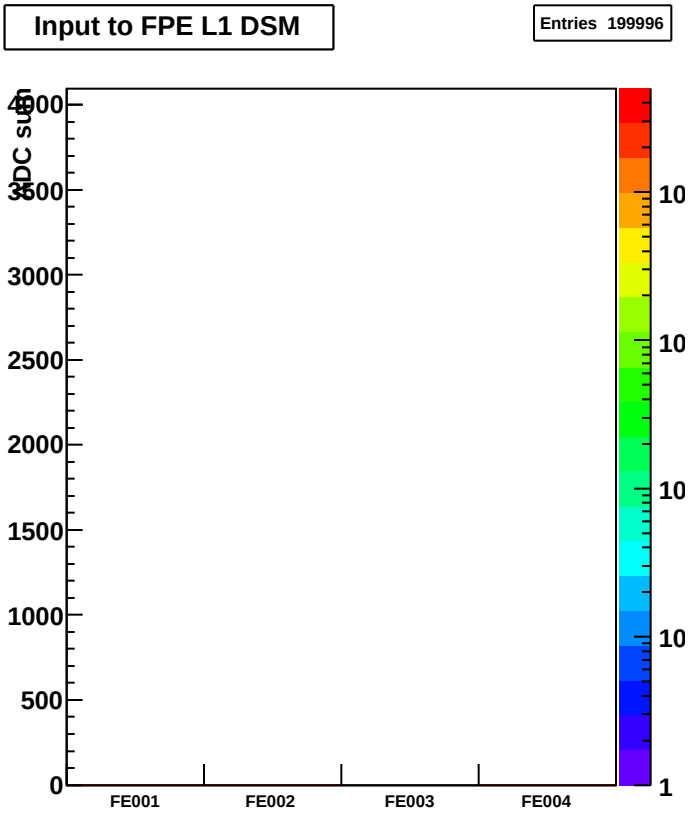

Input to FPD L2 DSM
Entries 199996
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

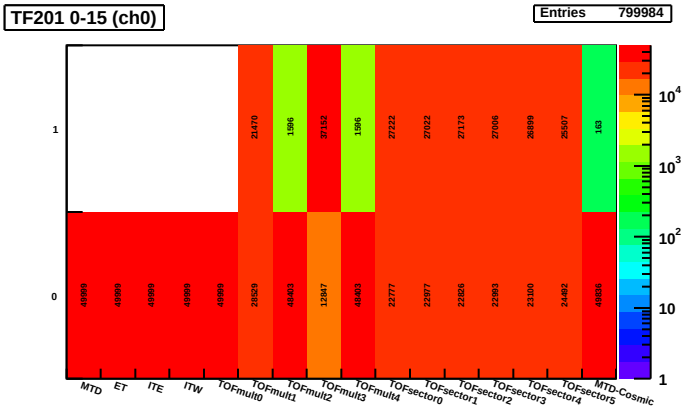

Input to FE001 QT board
Entries1599968
ADC
channel

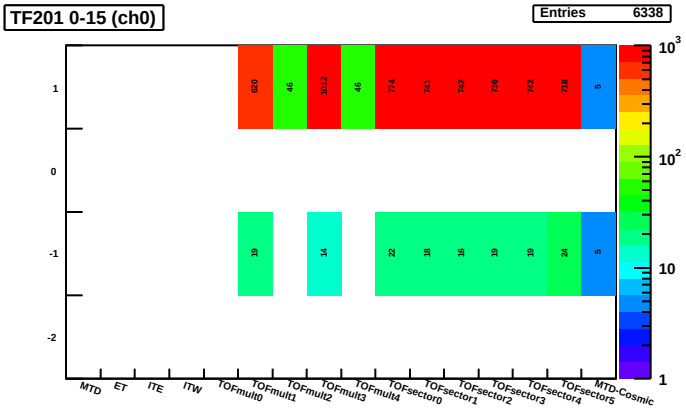

Input to FE002 QT board
Entries1599968
ADC
channel

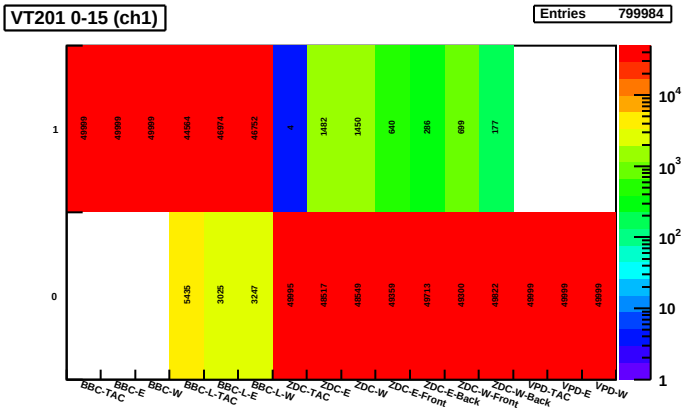

Input to FE003 QT board
Entries1599968
ADC
channel

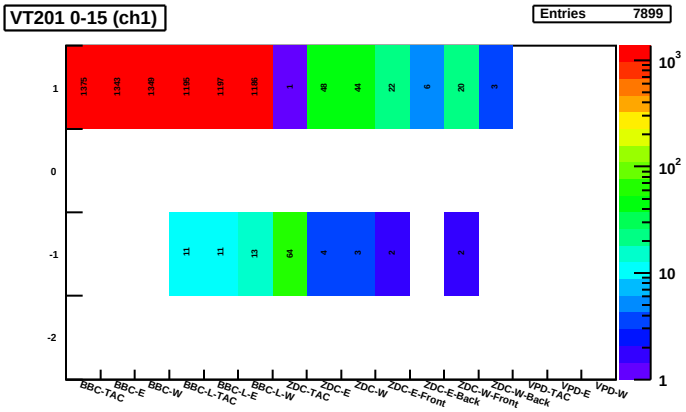

Input to FE004 QT board
Entries1599968
ADC
channel

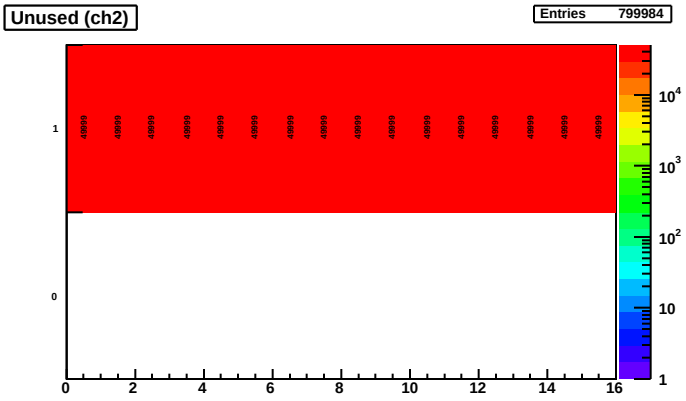

Input to FPE L1 DSM
Entries 199996
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

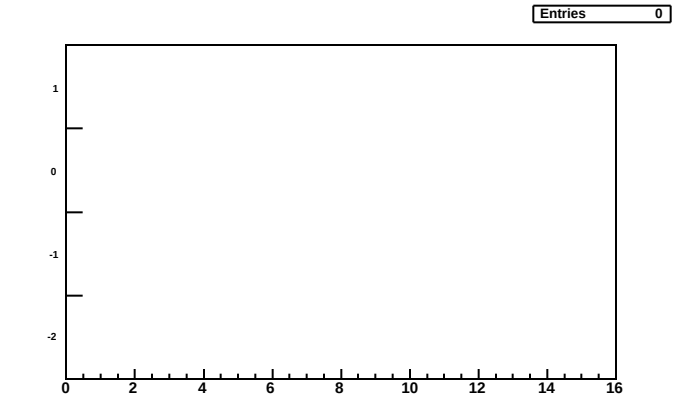

TF201 0-15 (ch0)
Entries 799984
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

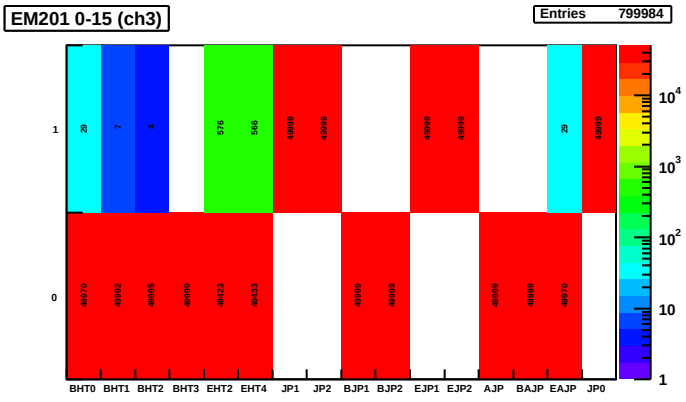

TF201 0-15 (ch0)
Entries 6338
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

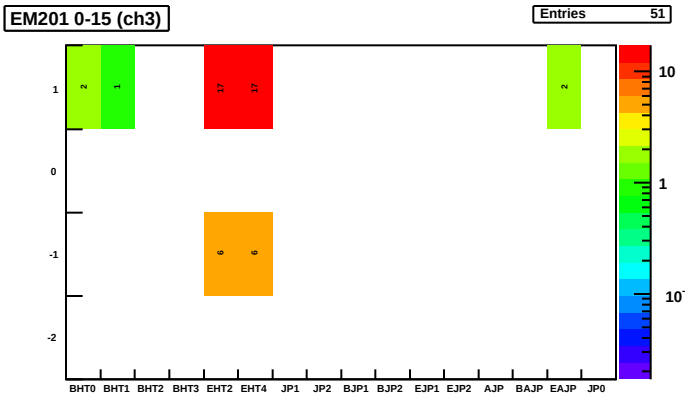

VT201 0-15 (ch1)
Entries 799984
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 7899
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

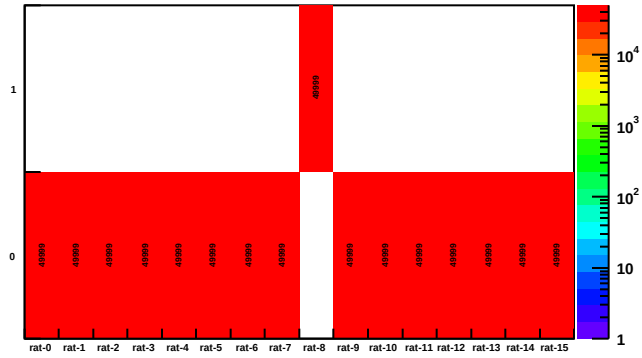

Unused (ch2)
Entries 799984

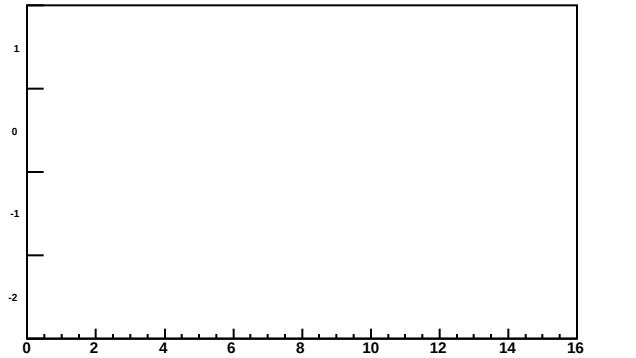

Entries 0


EM201 0-15 (ch3)
Entries 799984
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

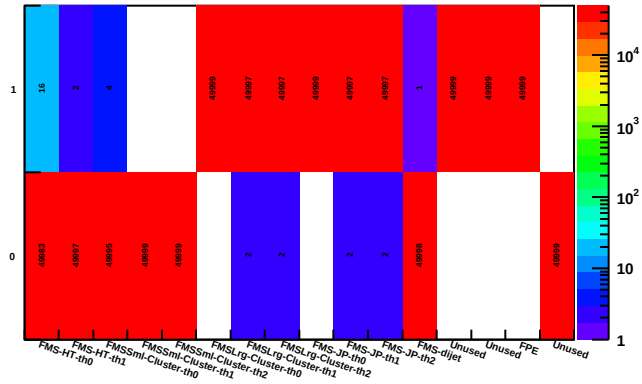

EM201 0-15 (ch3)
Entries 51
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

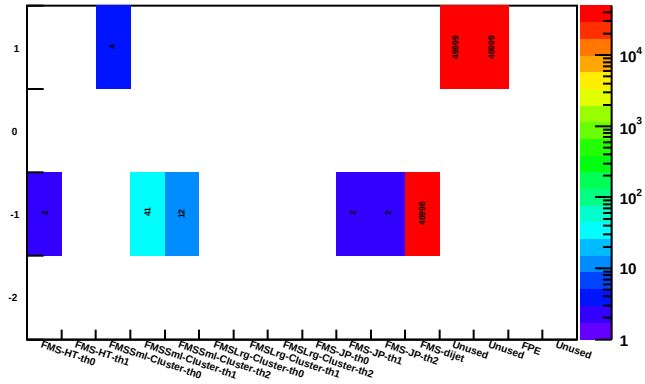




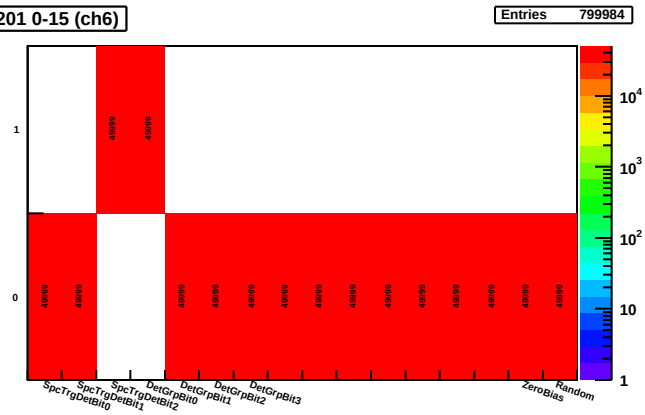
FP201 0-15 (ch5)

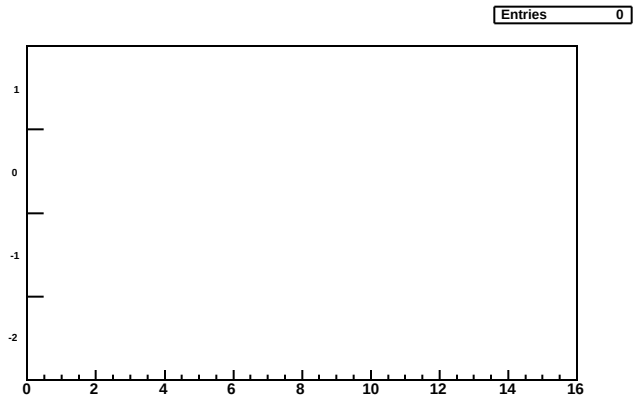
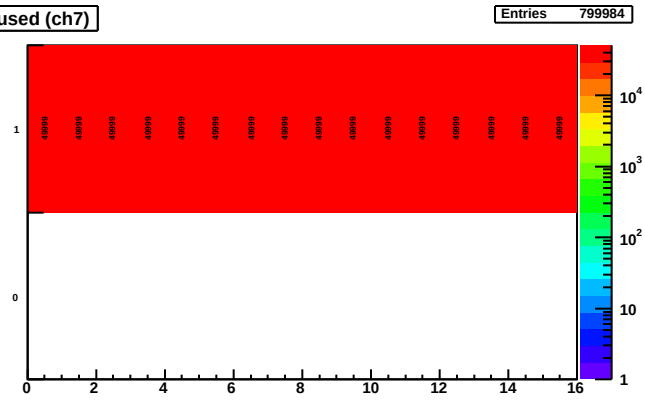
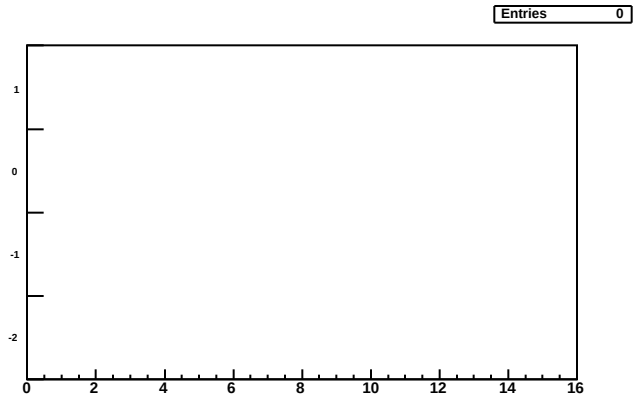


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)