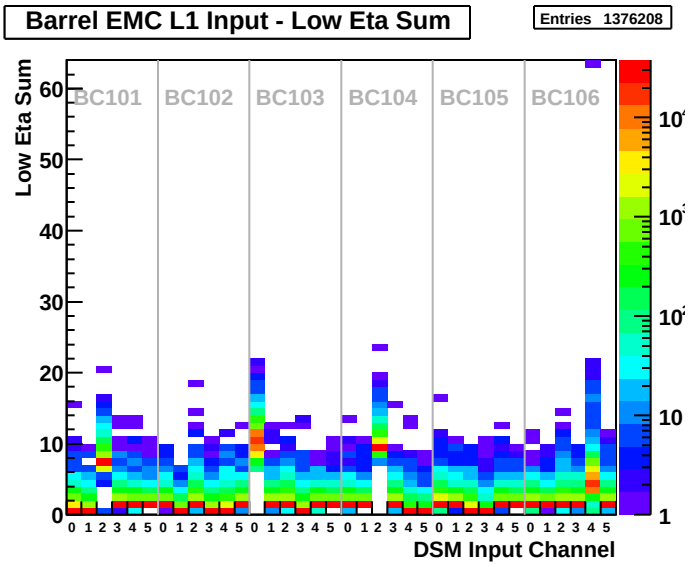
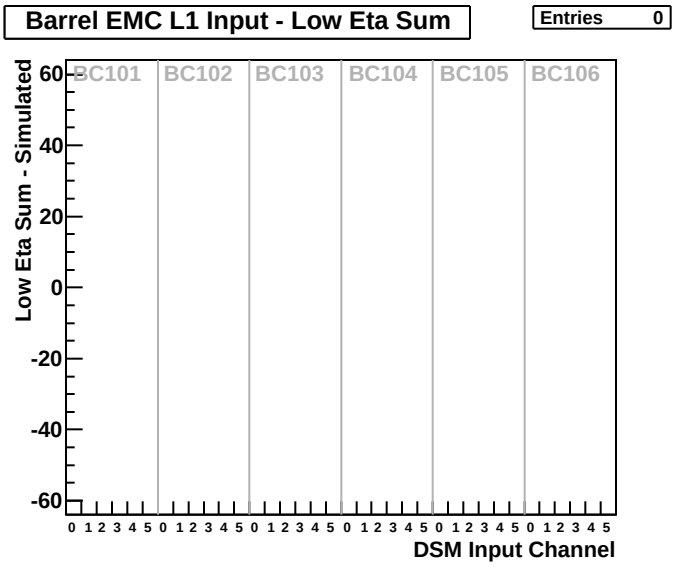
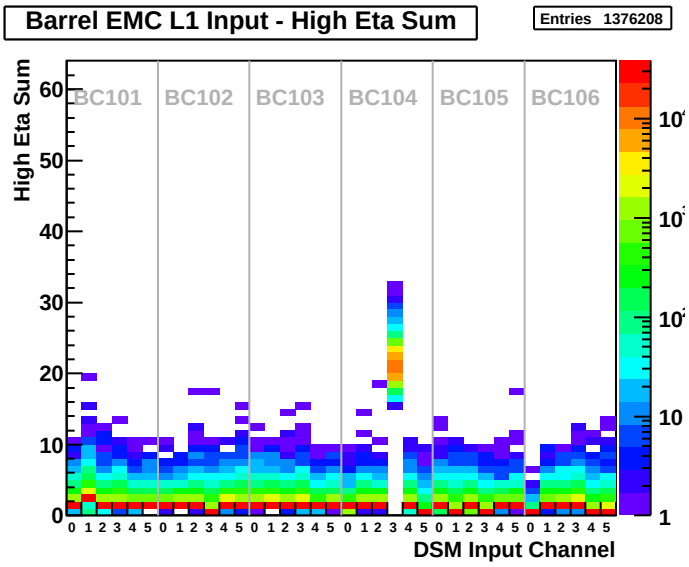
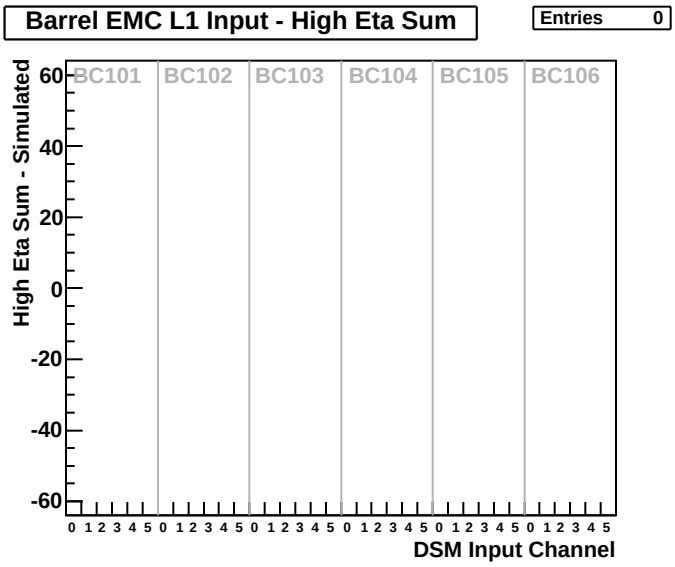
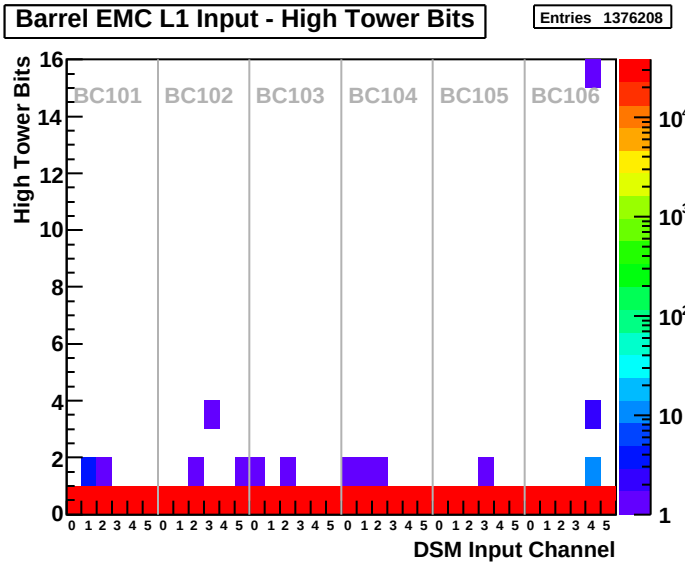
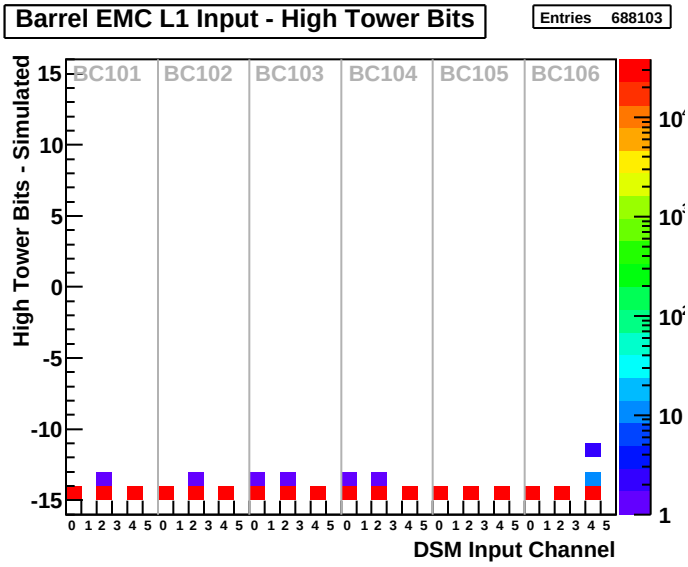

Barrel EMC L1 Input - Low Eta Sum
Entries 1376208
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

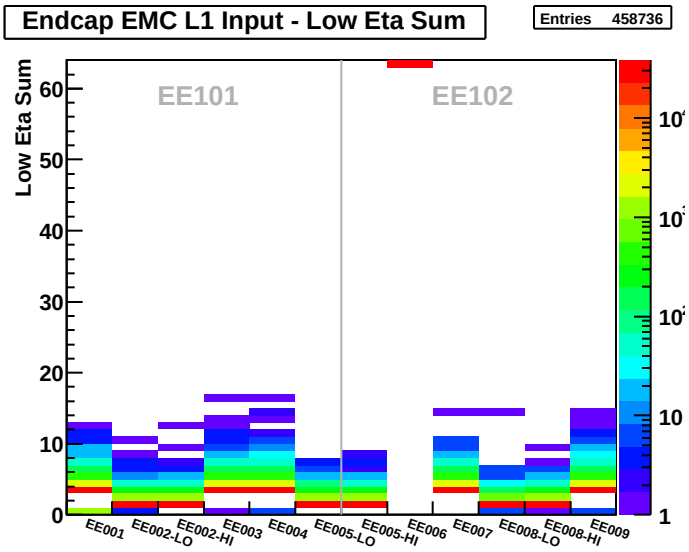

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

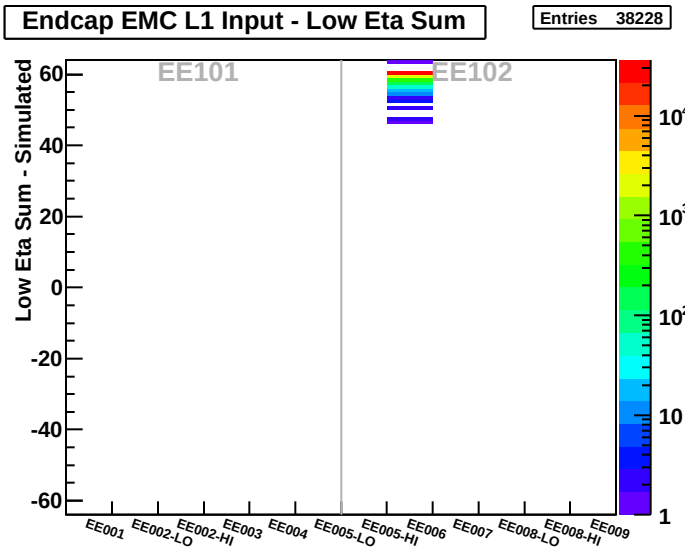

Barrel EMC L1 Input - High Eta Sum
Entries 1376208
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

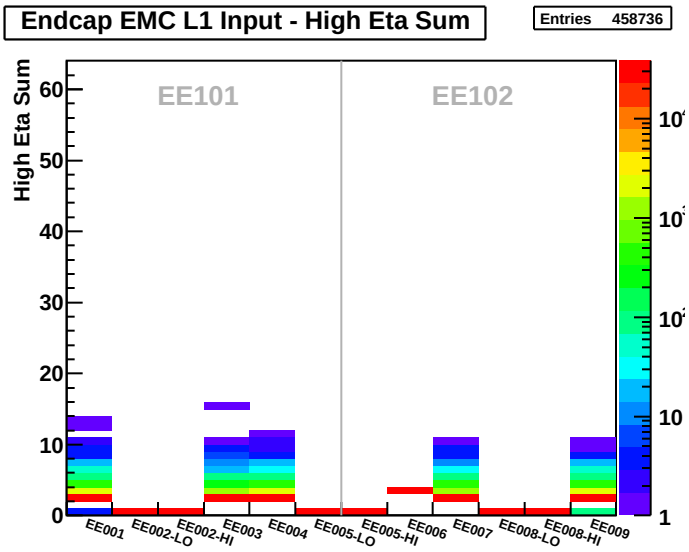

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

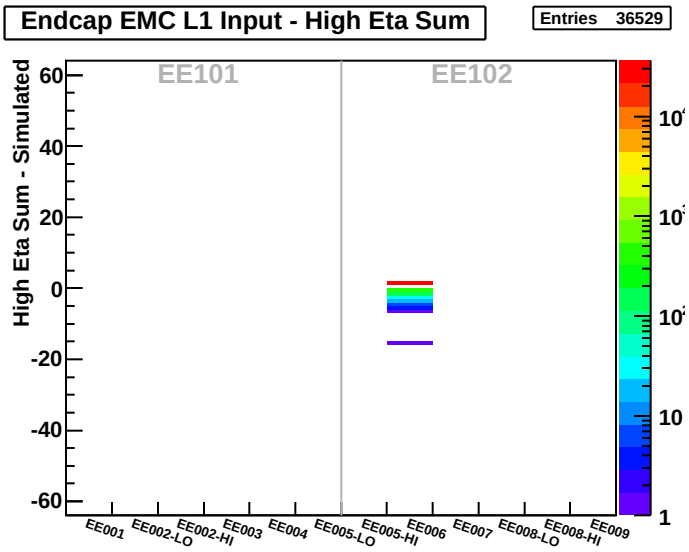

Barrel EMC L1 Input - High Tower Bits
Entries 1376208
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

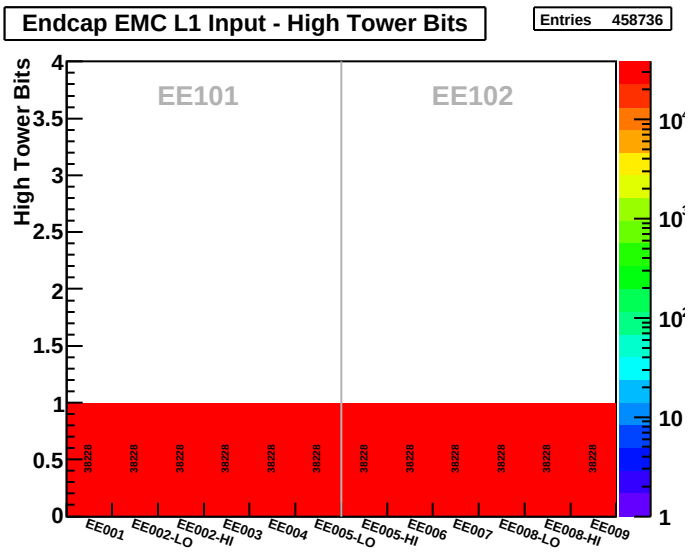

Barrel EMC L1 Input - High Tower Bits
Entries 688103
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

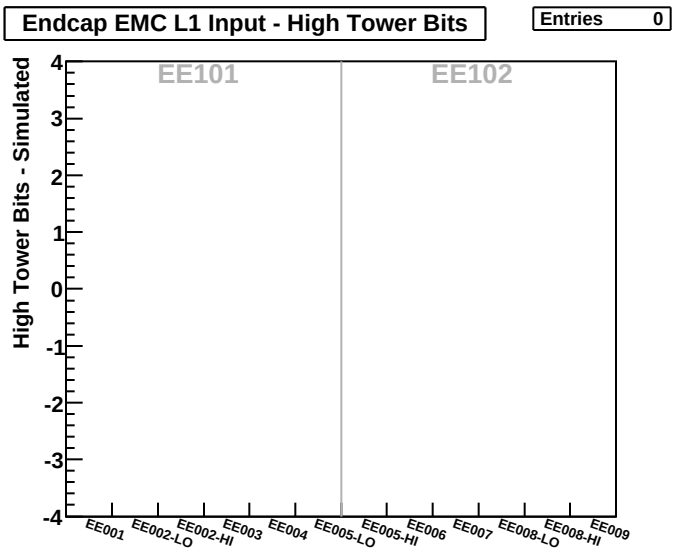

Endcap EMC L1 Input - Low Eta Sum
Entries 458736
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

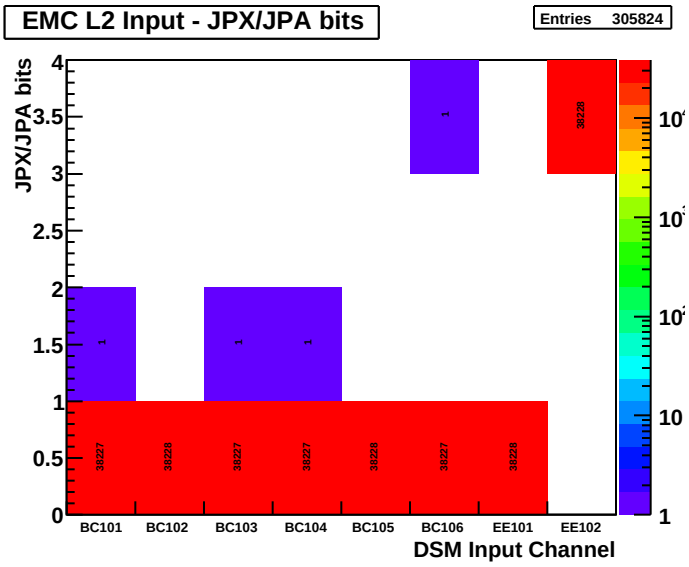

Endcap EMC L1 Input - Low Eta Sum
Entries 38228
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

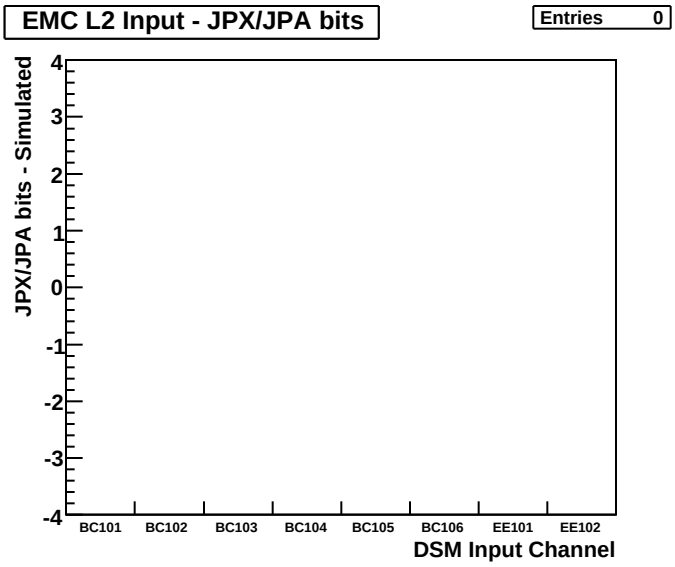

Endcap EMC L1 Input - High Eta Sum
Entries 458736
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

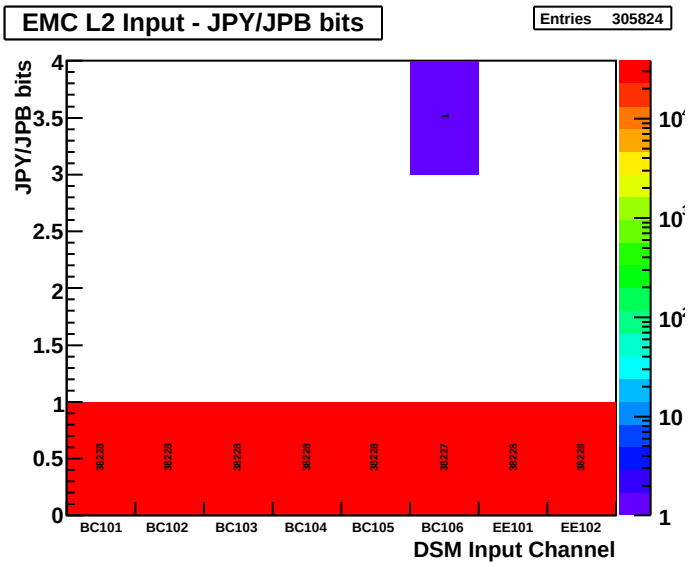

Endcap EMC L1 Input - High Eta Sum
Entries 36529
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

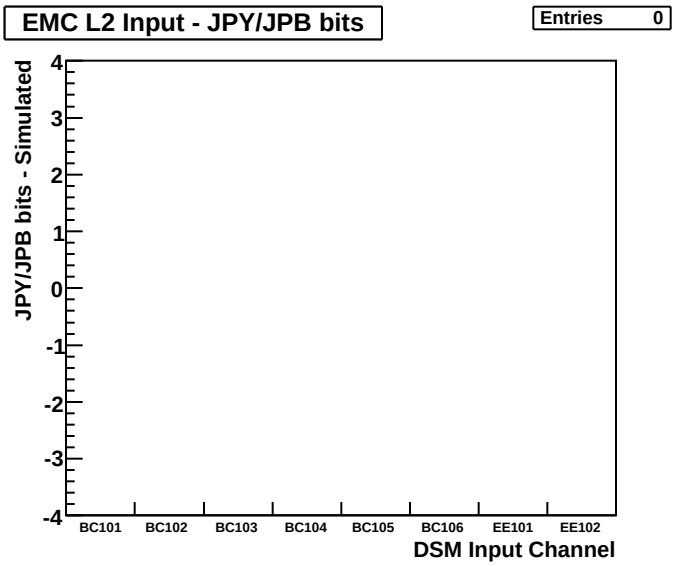

Endcap EMC L1 Input - High Tower Bits
Entries 458736
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

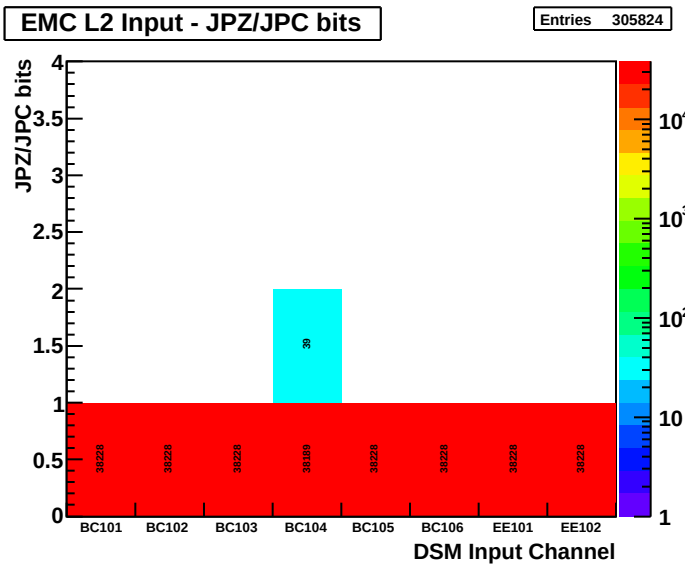

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

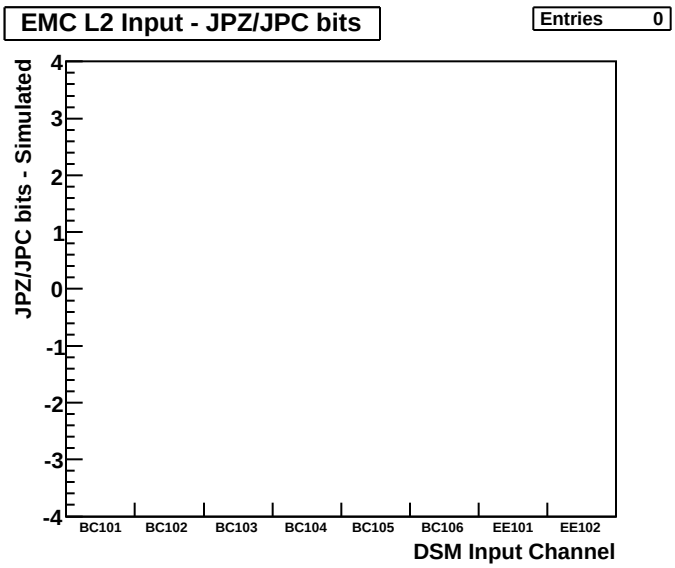

EMC L2 Input - JPX/JPA bits
Entries 305824
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

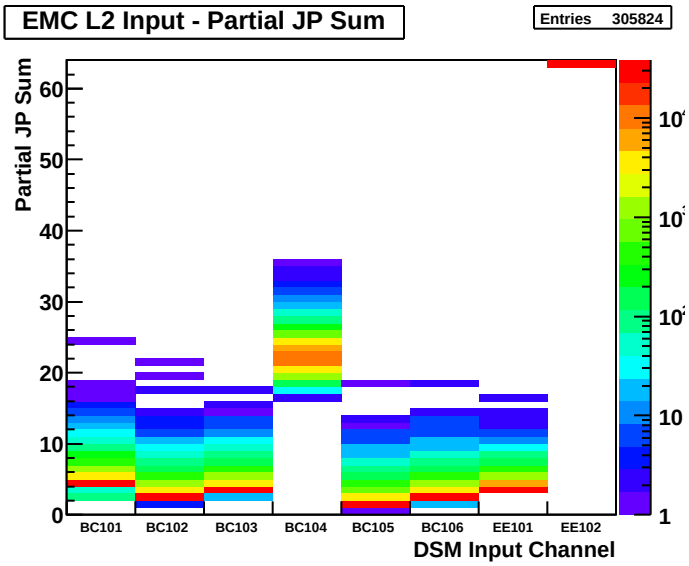

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

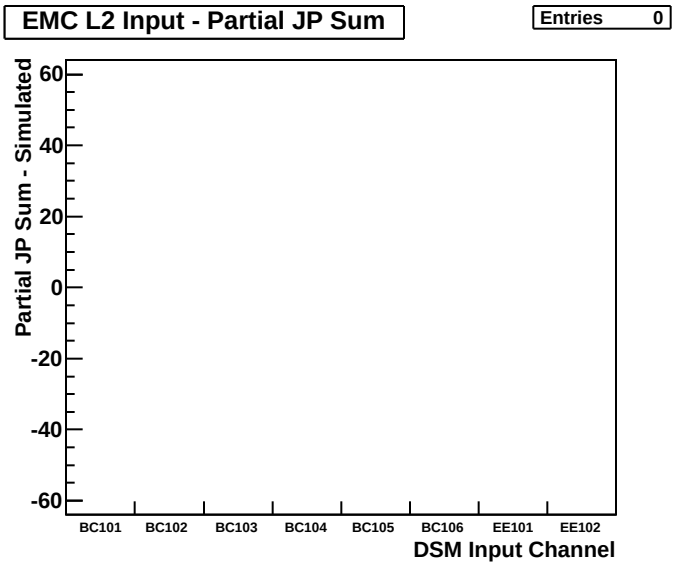

EMC L2 Input - JPY/JPB bits
Entries 305824
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

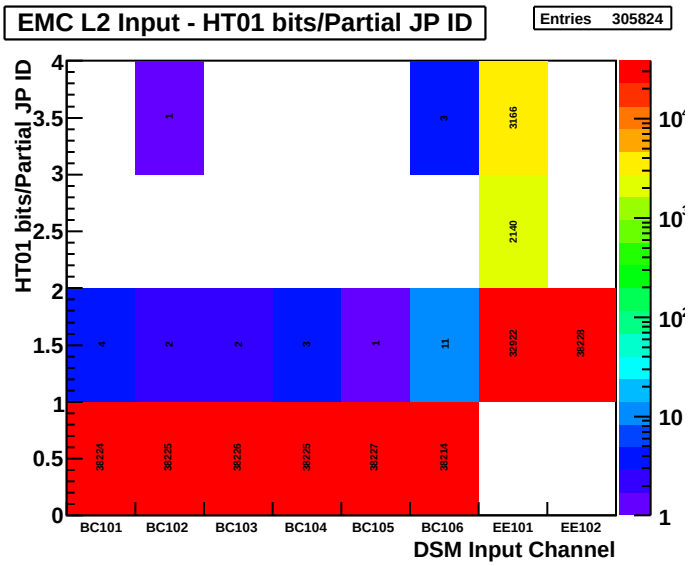

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

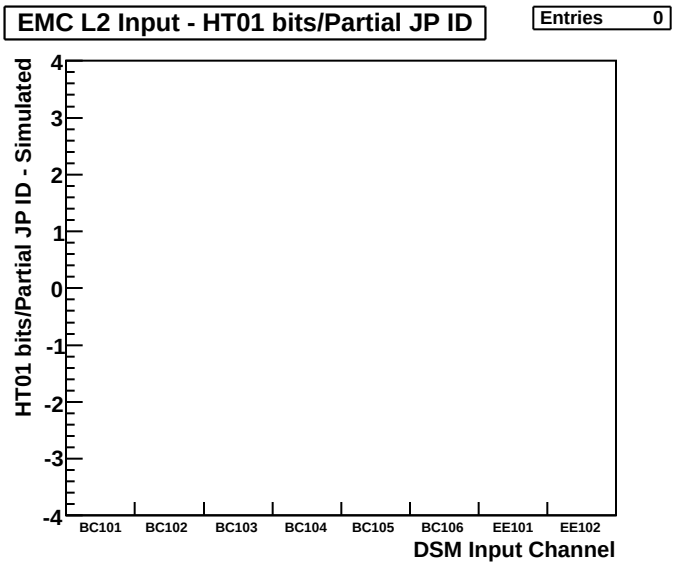

EMC L2 Input - JPZ/JPC bits
Entries 305824
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

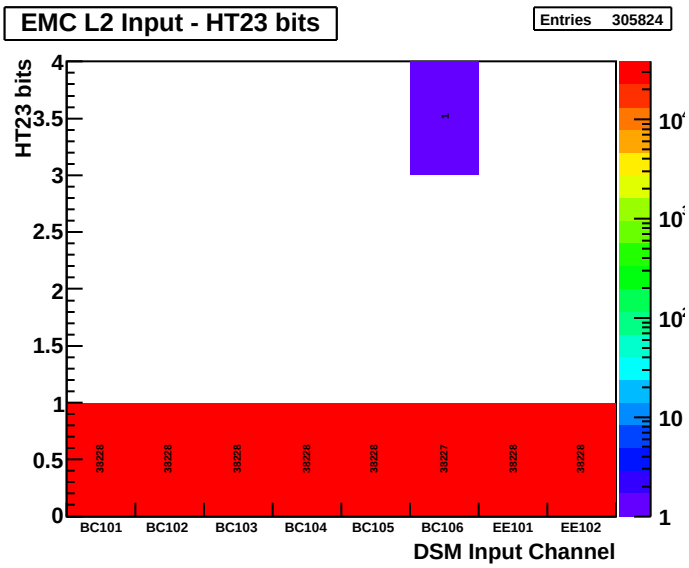

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

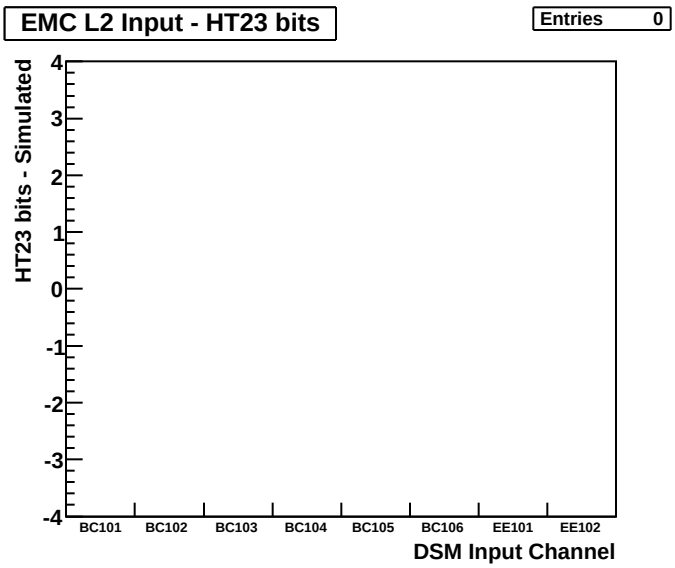

EMC L2 Input - Partial JP Sum
Entries 305824
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

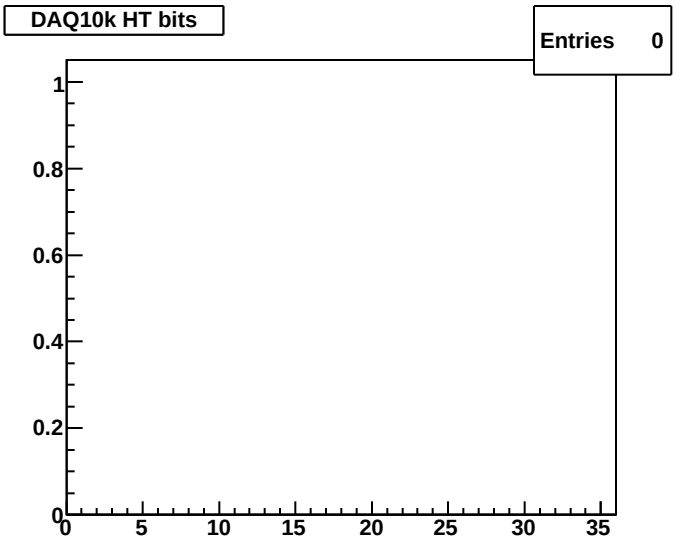

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

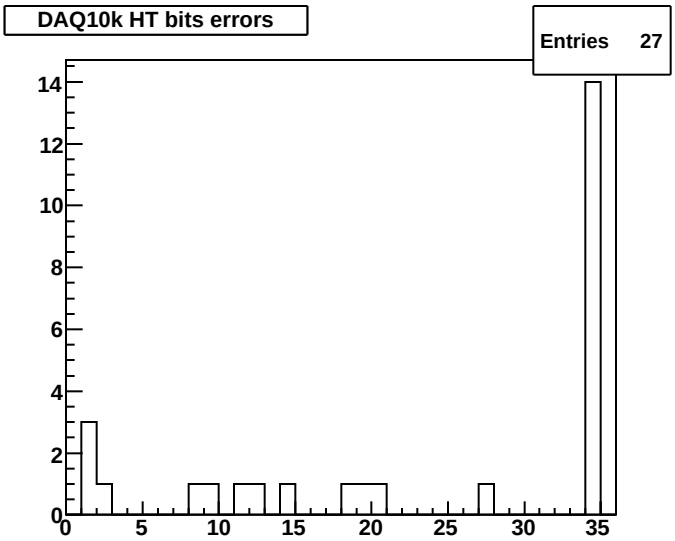

EMC L2 Input - HT01 bits/Partial JP ID
Entries 305824
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

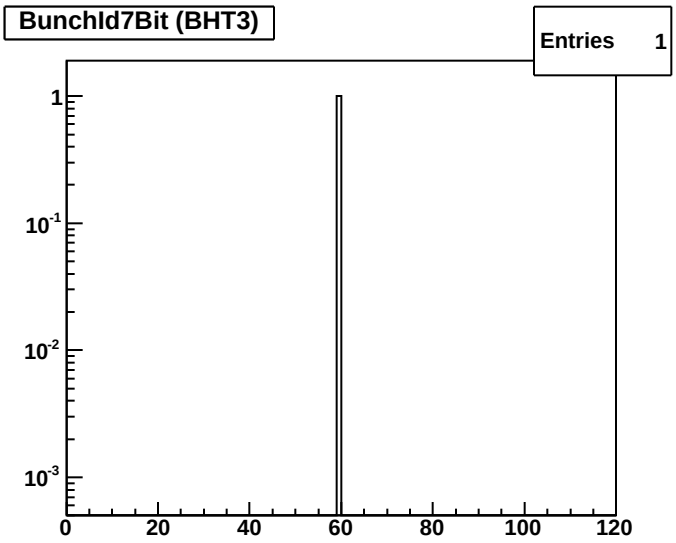

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

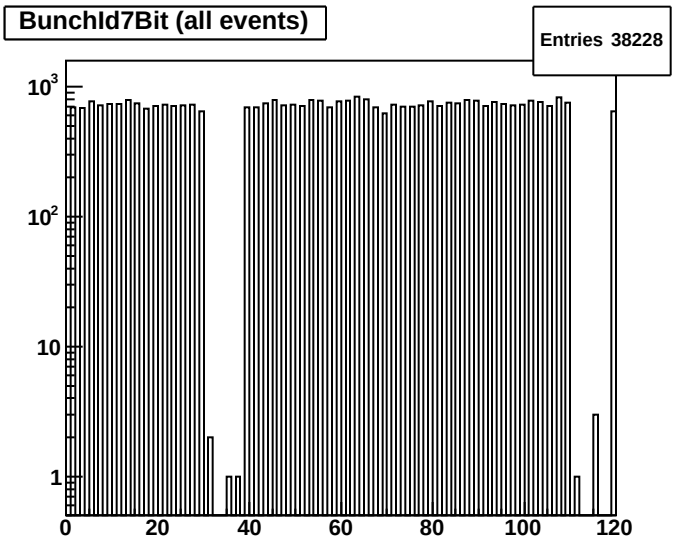

EMC L2 Input - HT23 bits
Entries 305824
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

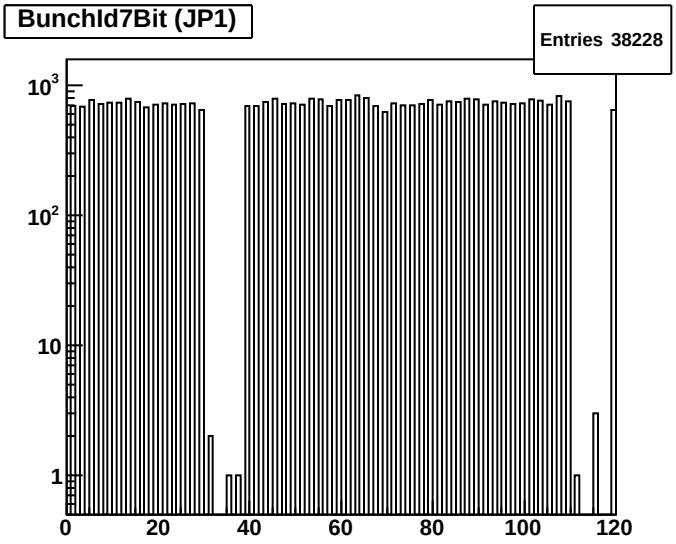

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

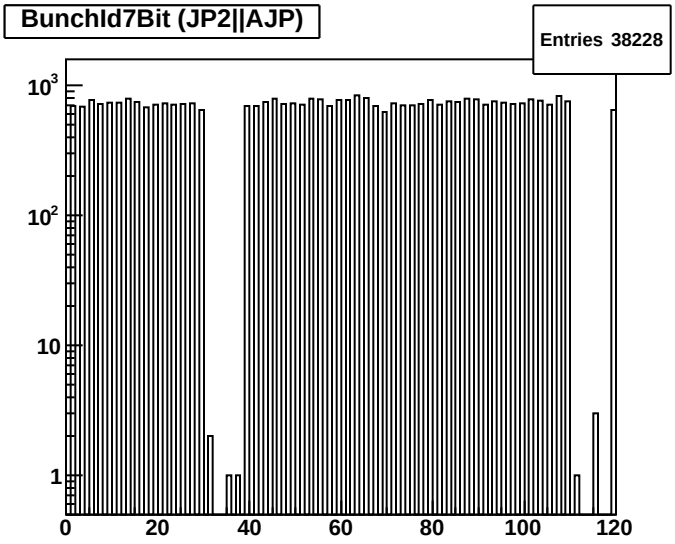

DAQ10k HT bits
Entries 0

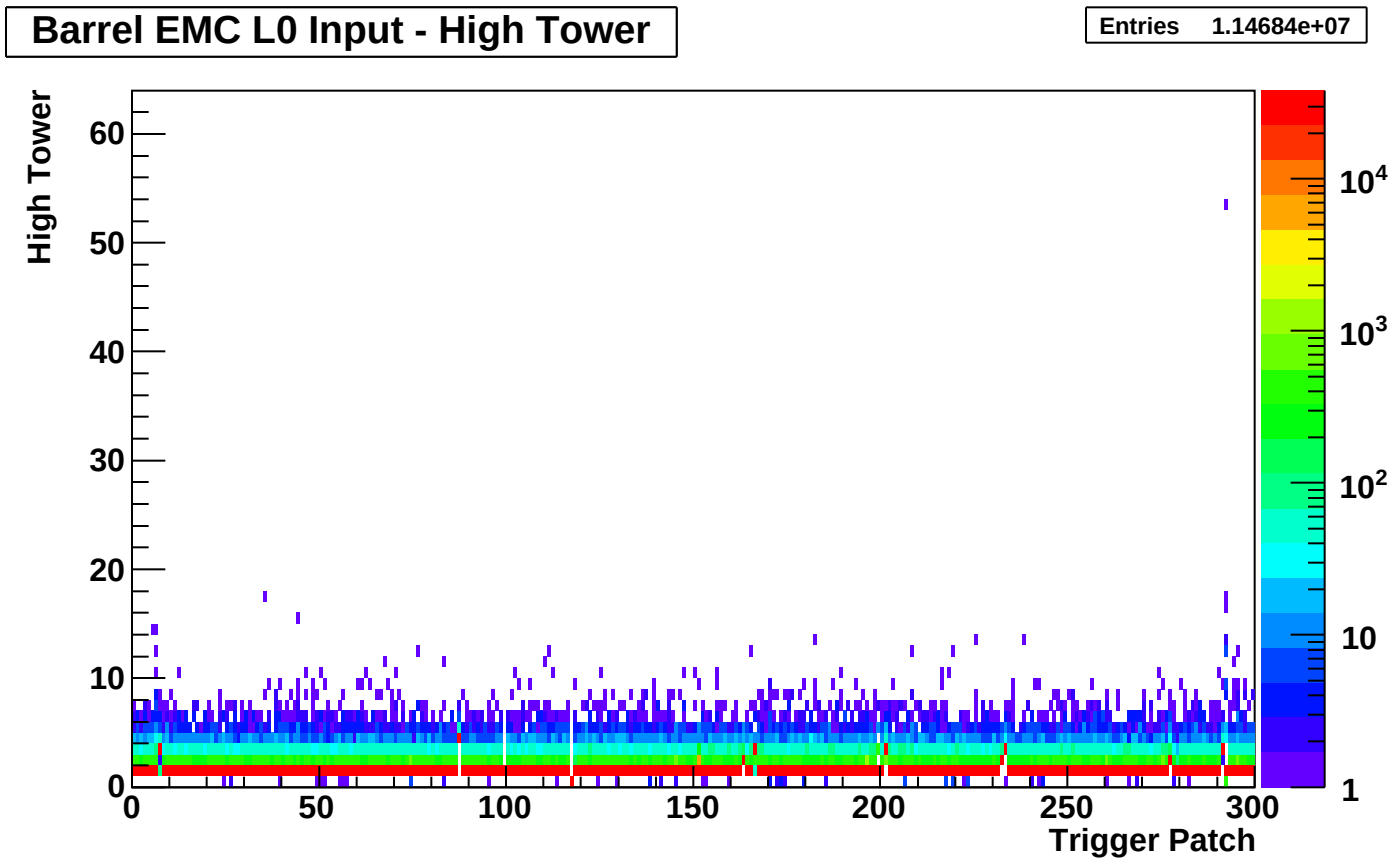

DAQ10k HT bits errors
Entries 27

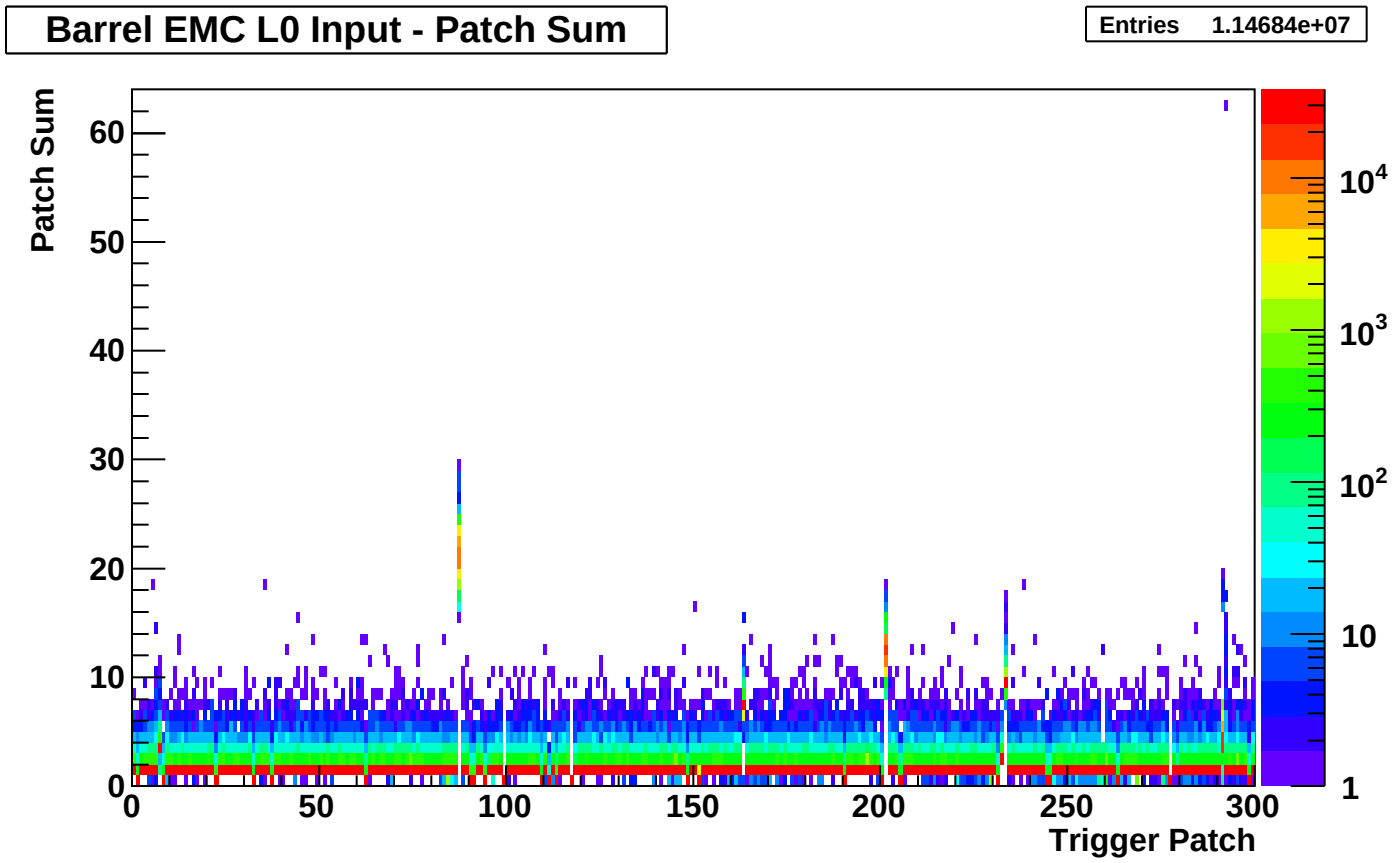

BunchId7Bit (BHT3)
Entries 1

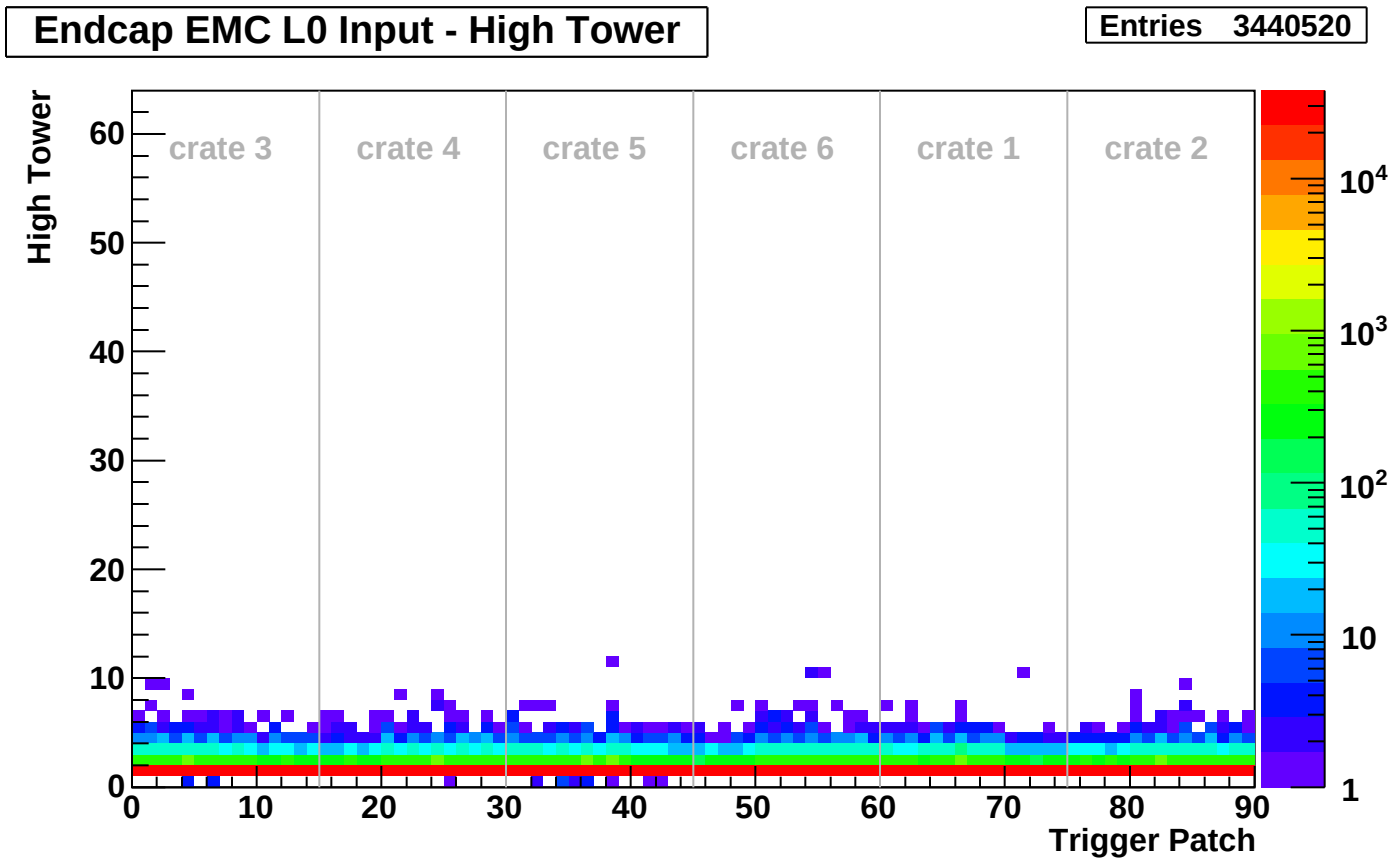

BunchId7Bit (all events)
Entries 38228

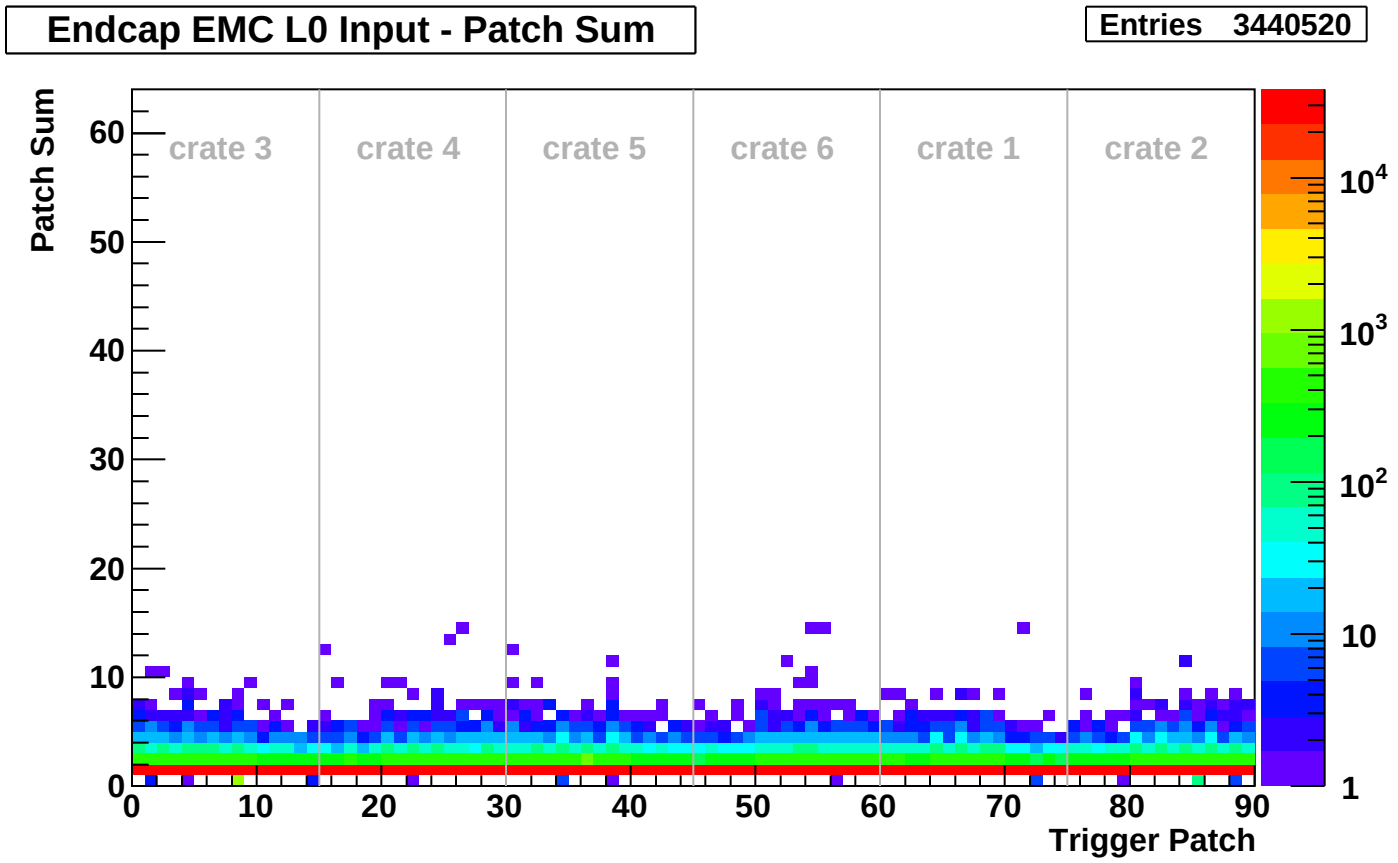

BunchId7Bit (JP1)
Entries 38228

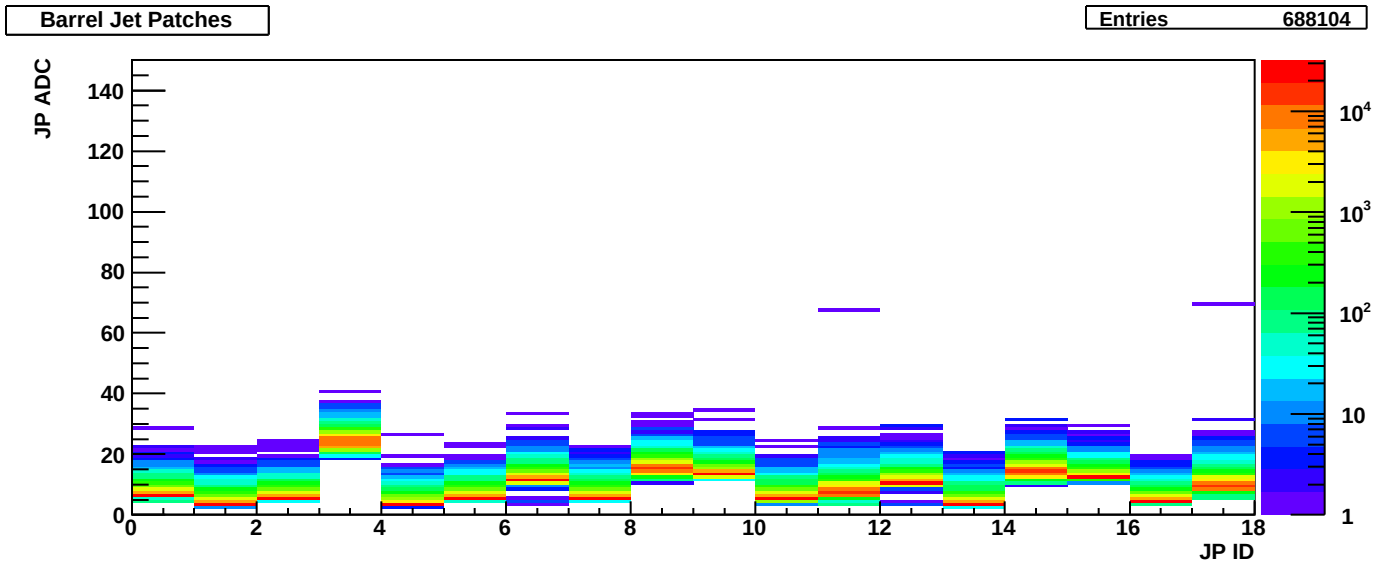

BunchId7Bit (JP2||AJP)
Entries 38228

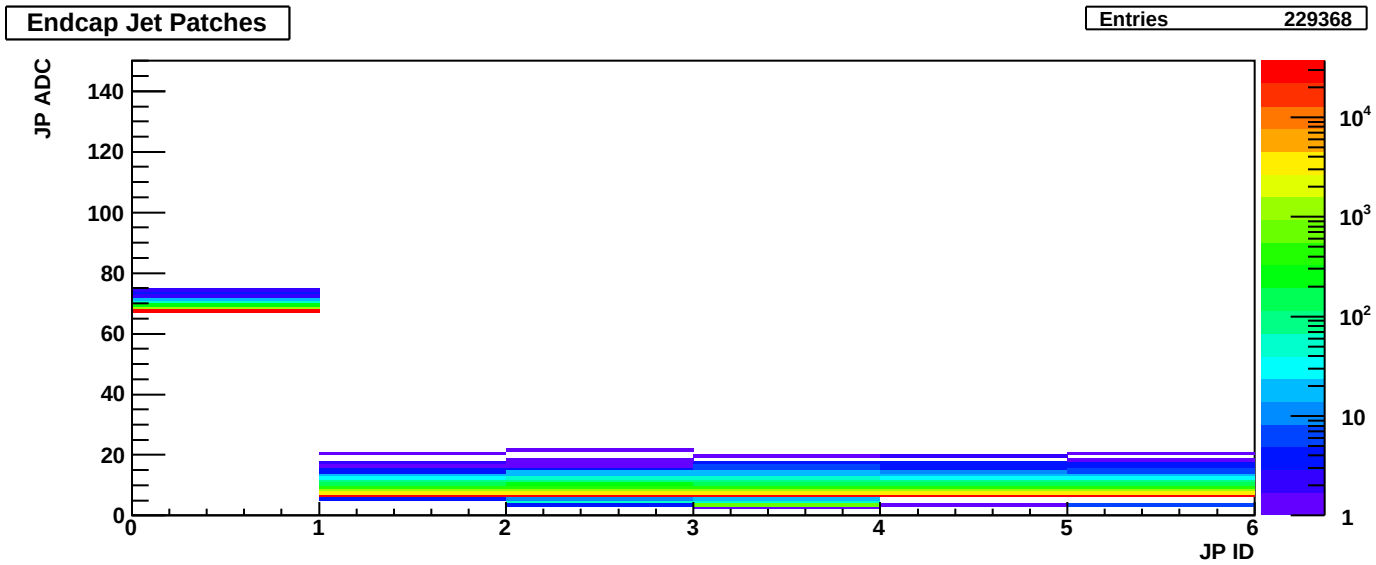

Barrel EMC L0 Input - High Tower
Entries 1.14684e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

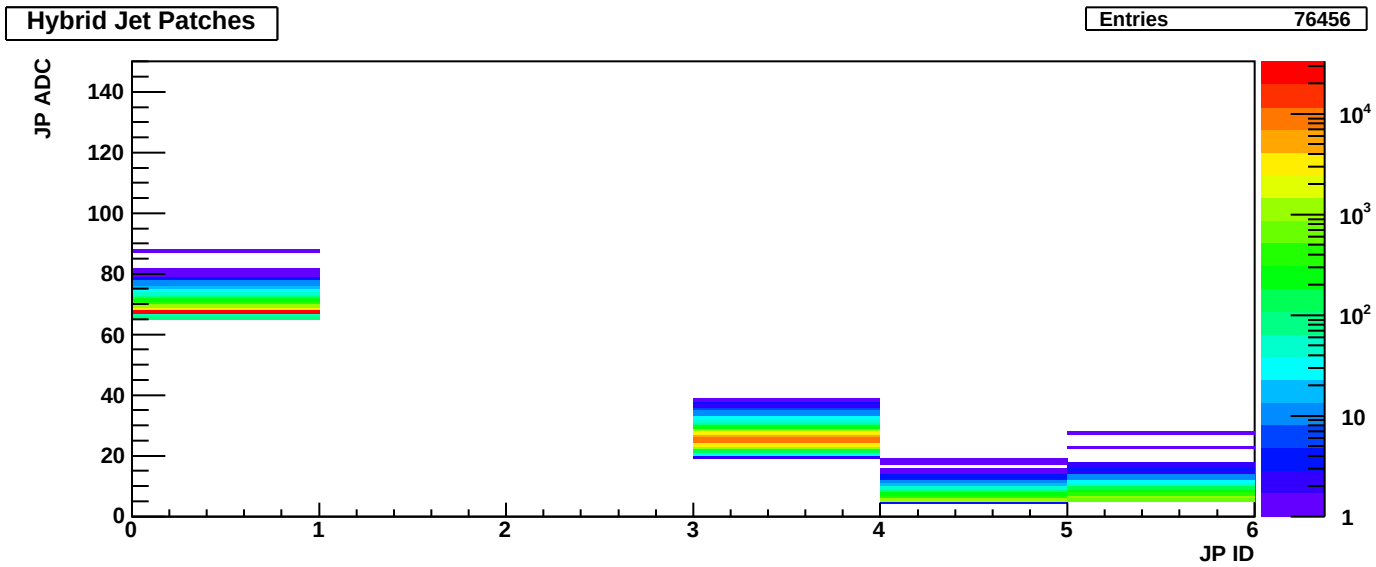

Barrel EMC L0 Input - Patch Sum
Entries 1.14684e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

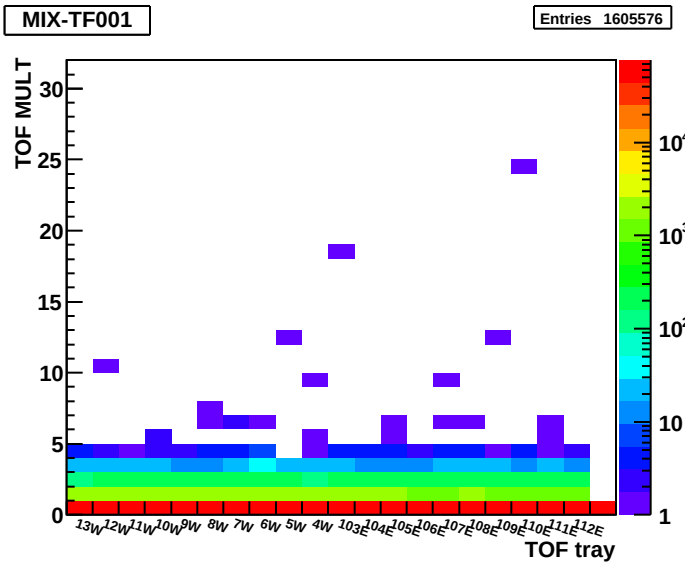

Endcap EMC L0 Input - High Tower
Entries 3440520
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

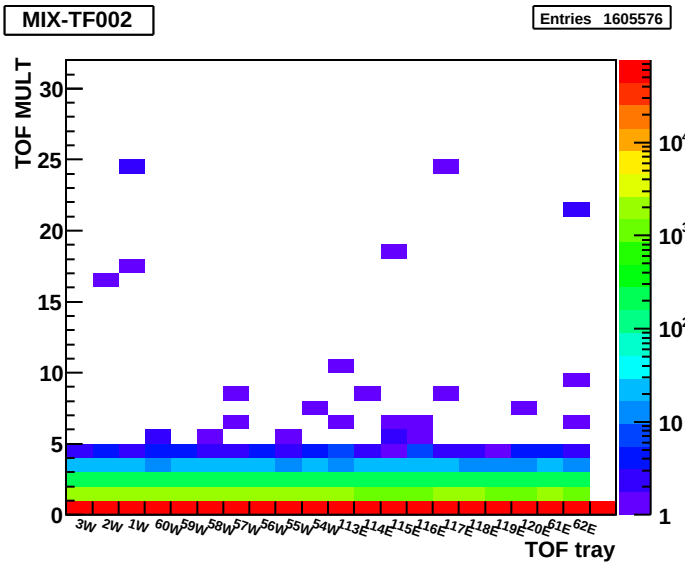

Endcap EMC L0 Input - Patch Sum
Entries 3440520
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

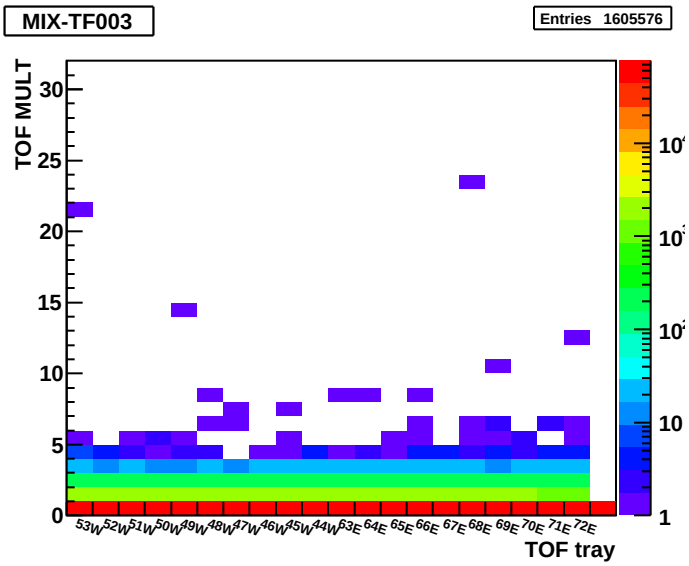

Barrel Jet Patches
Entries 688104
JP ADC
JP ID

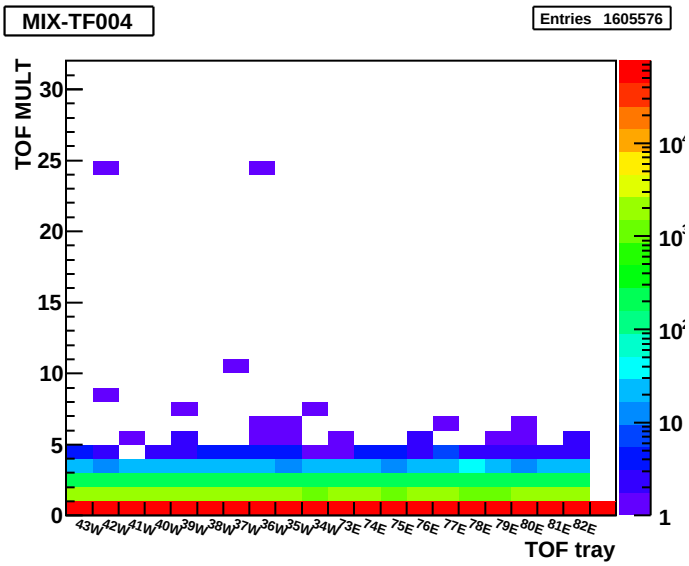

Endcap Jet Patches
Entries 229368
JP ADC
JP ID

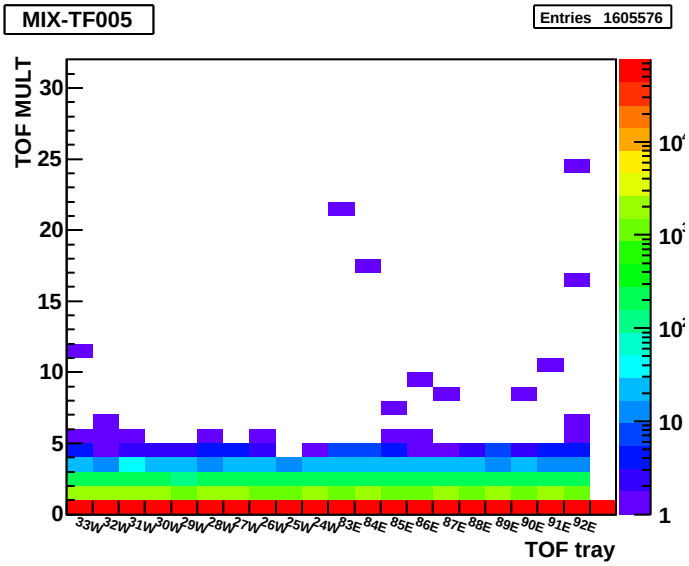

Hybrid Jet Patches
Entries 76456
JP ADC
JP ID

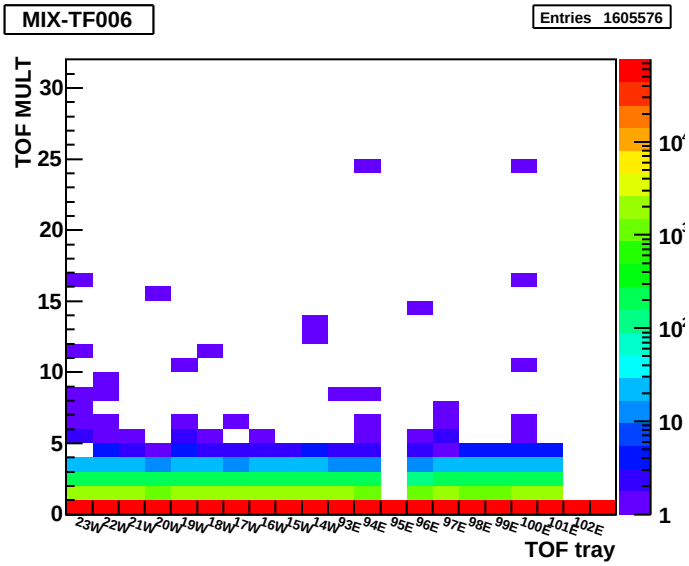

MIX-TF001
Entries 1605576
TOF MULT
TOF tray

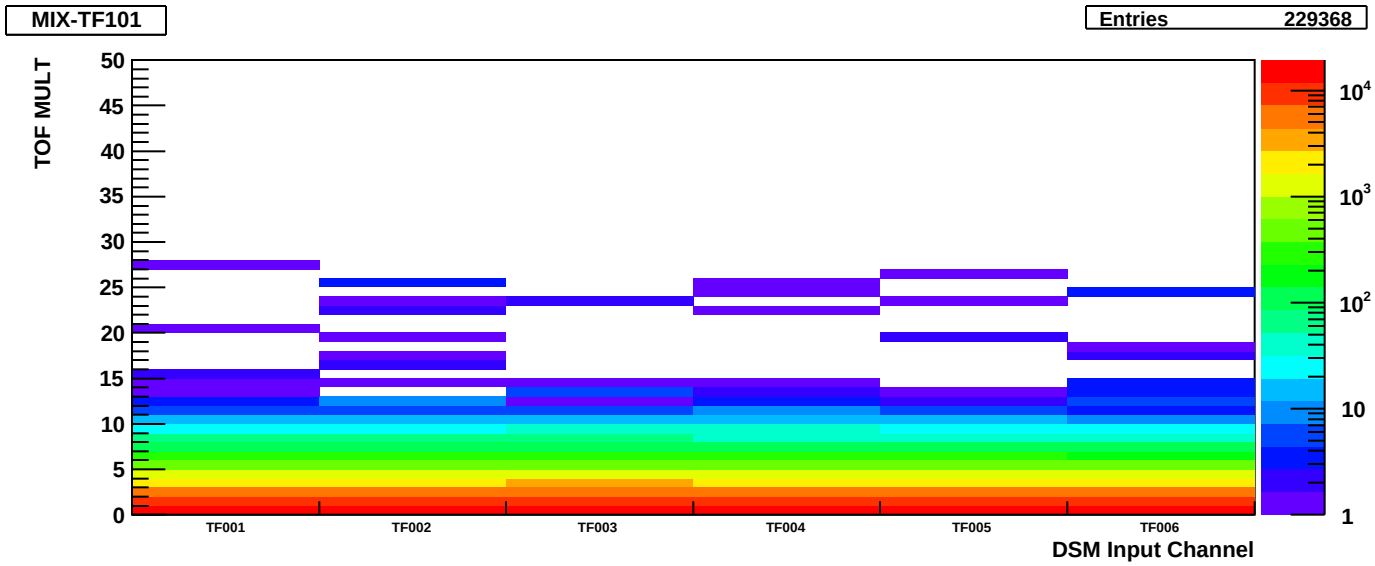

MIX-TF002
Entries 1605576
TOF MULT
TOF tray

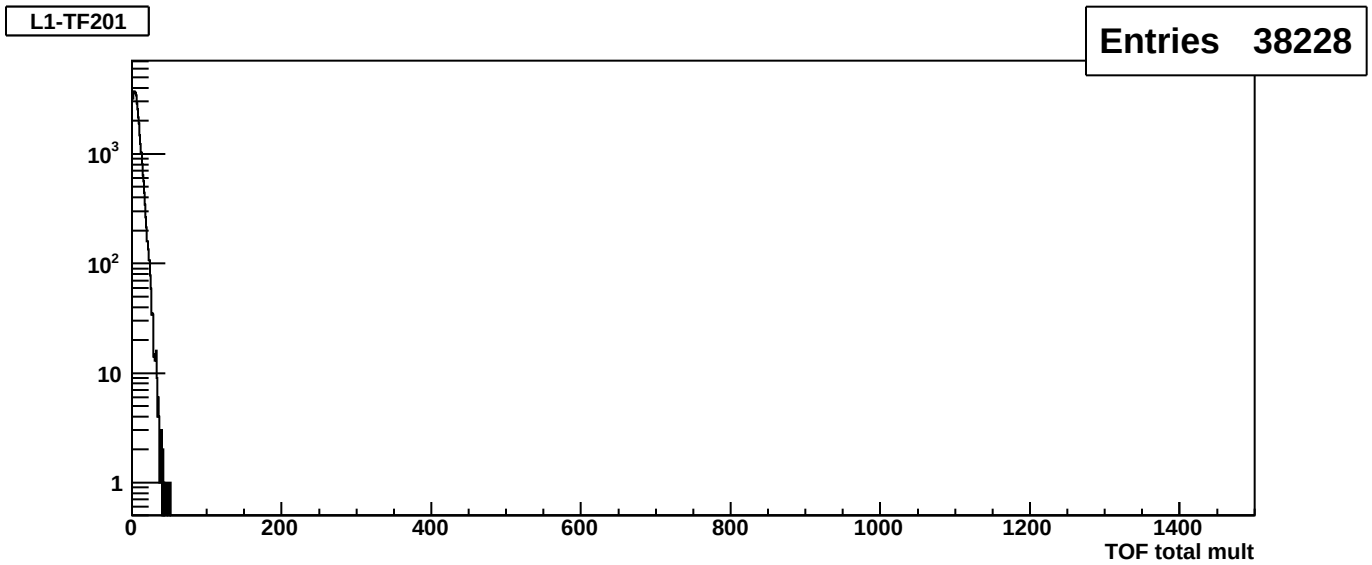

MIX-TF003
Entries 1605576
TOF MULT
TOF tray

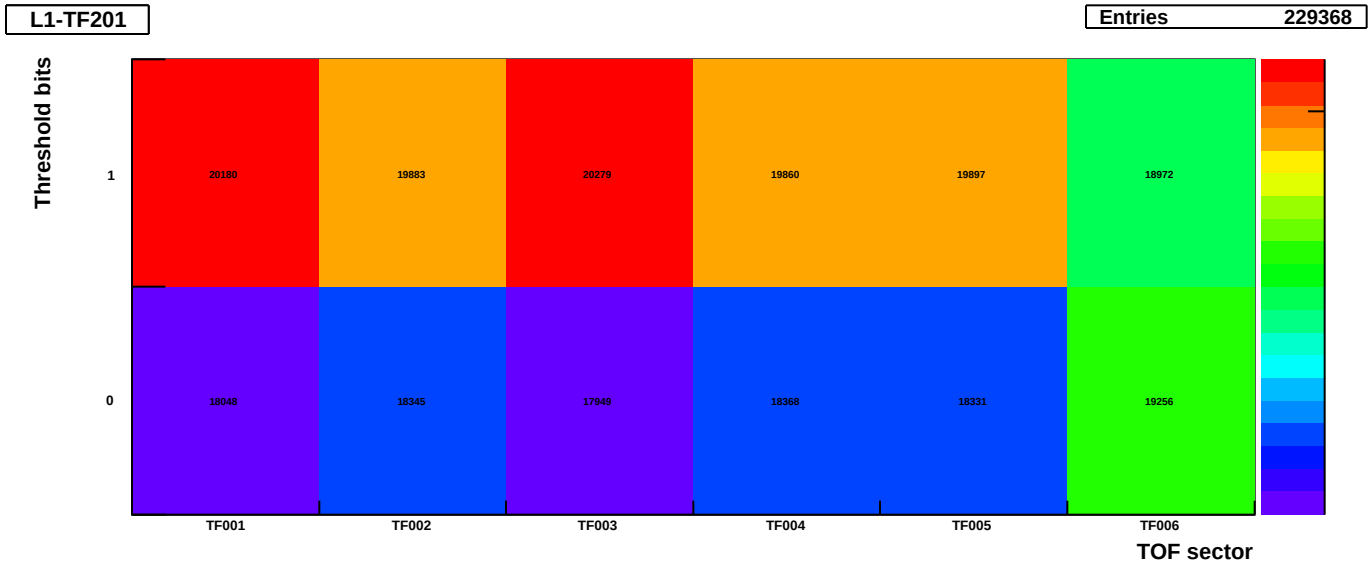

MIX-TF004
Entries 1605576
TOF MULT
TOF tray

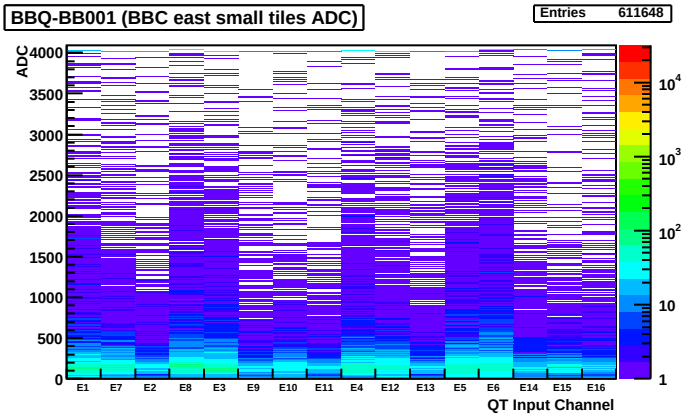

MIX-TF005
Entries 1605576
TOF MULT
TOF tray

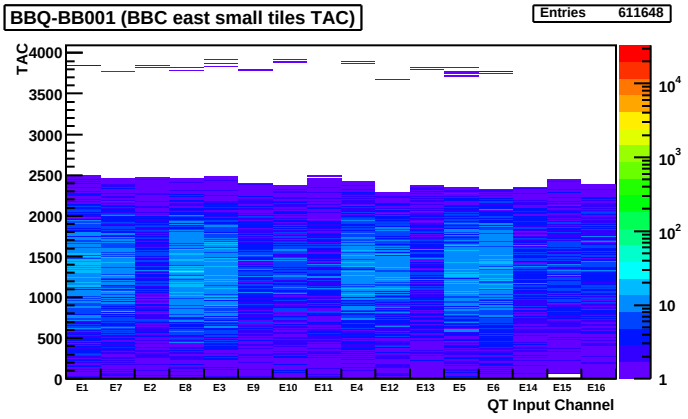

MIX-TF006
Entries 1605576
TOF MULT
TOF tray

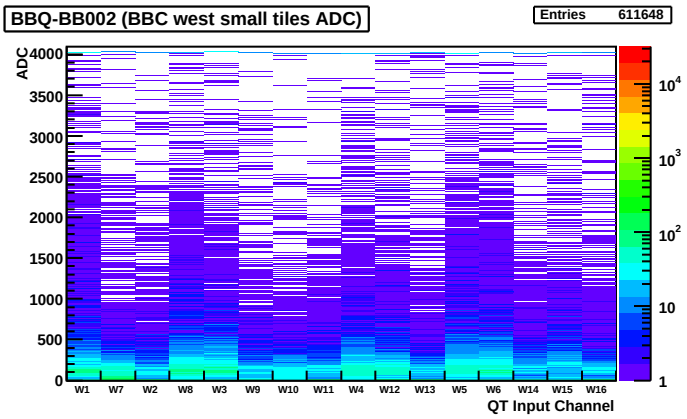

MIX-TF101
Entries 229368
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

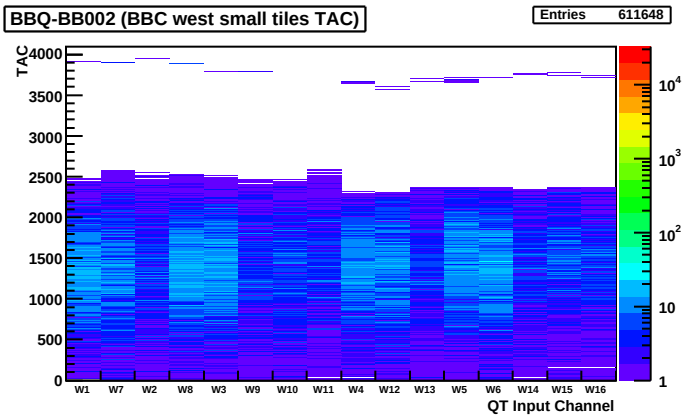

L1-TF201
Entries 38228
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

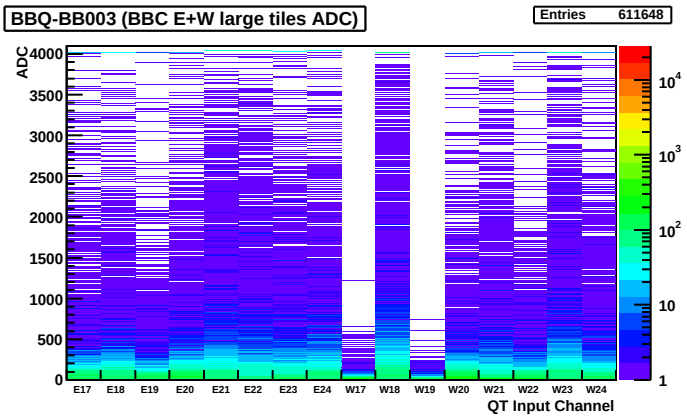

L1-TF201
Entries 229368
Threshold bits
1
0
20180
19883
20279
19860
19897
18972
18048
18345
17949
18368
18331
19256
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

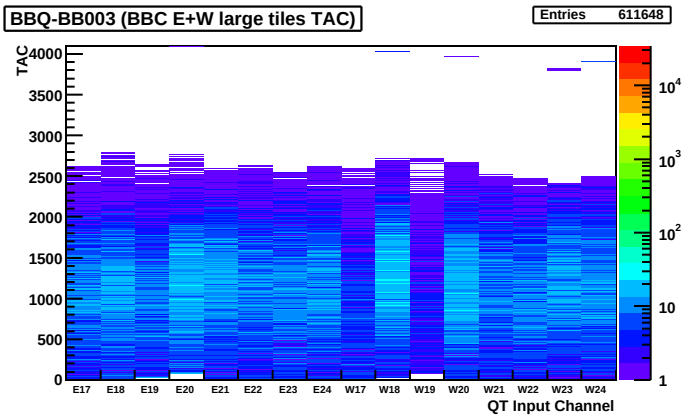

BBQ-BB001 (BBC east small tiles ADC)
Entries 611648
ADC
QT Input Channel

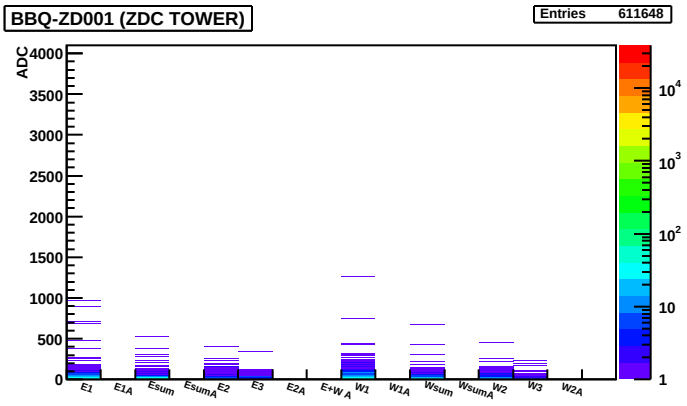

BBQ-BB001 (BBC east small tiles TAC)
Entries 611648
TAC
QT Input Channel

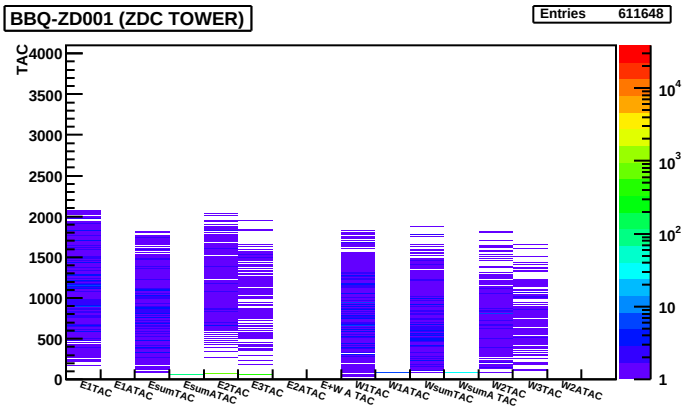

BBQ-BB002 (BBC west small tiles ADC)
Entries 611648
ADC
QT Input Channel

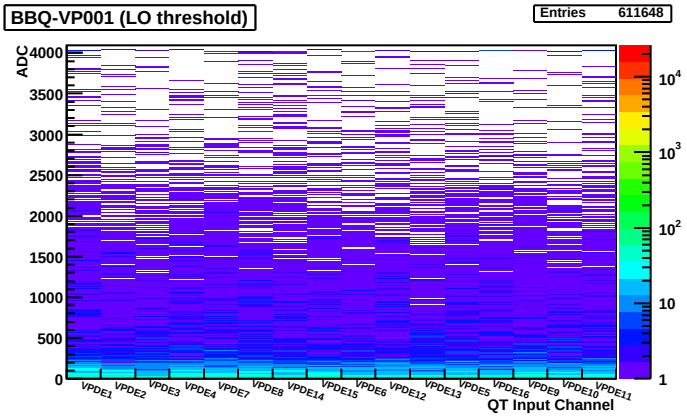

BBQ-BB002 (BBC west small tiles TAC)
Entries 611648
TAC
QT Input Channel

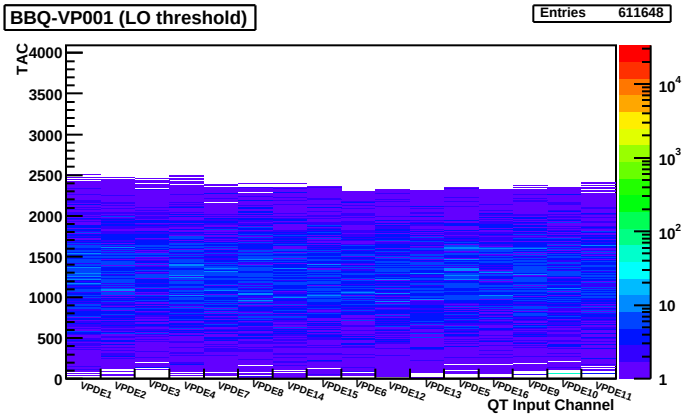

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 611648
ADC
QT Input Channel

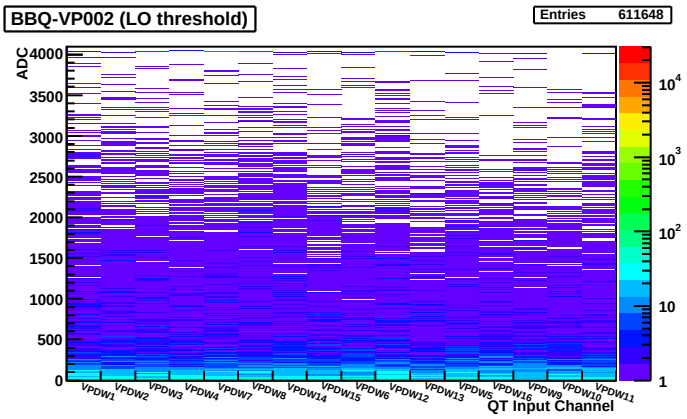

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 611648
TAC
QT Input Channel

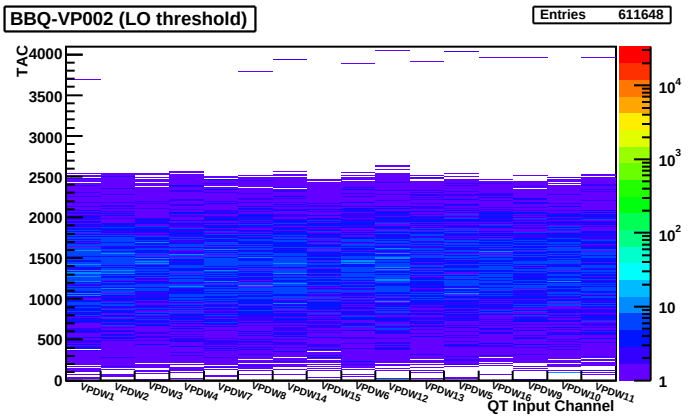

BBQ-ZD001 (ZDC TOWER)
Entries 611648
ADC

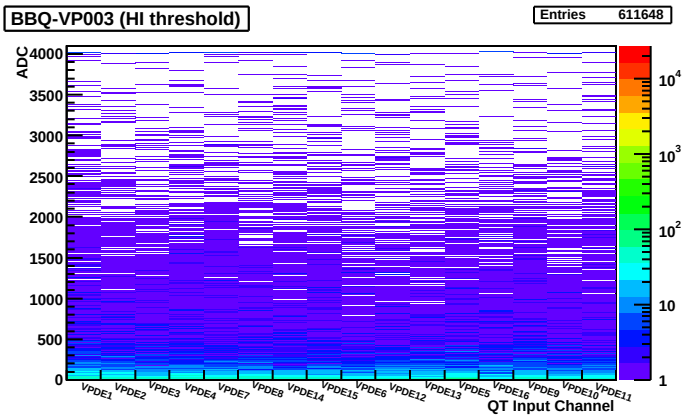

BBQ-ZD001 (ZDC TOWER)
Entries 611648
TAC

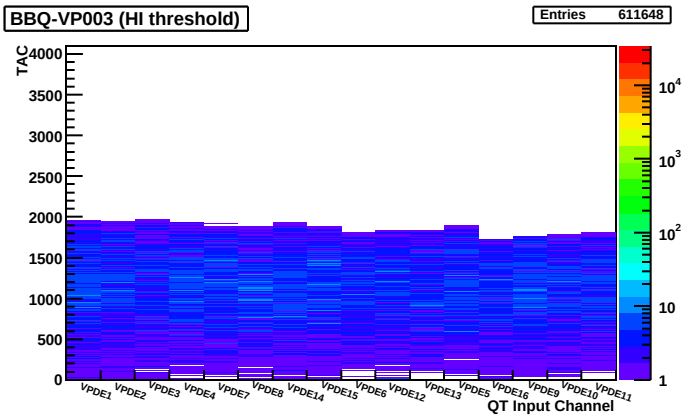

BBQ-VP001 (LO threshold)
Entries 611648
ADC
QT Input Channel

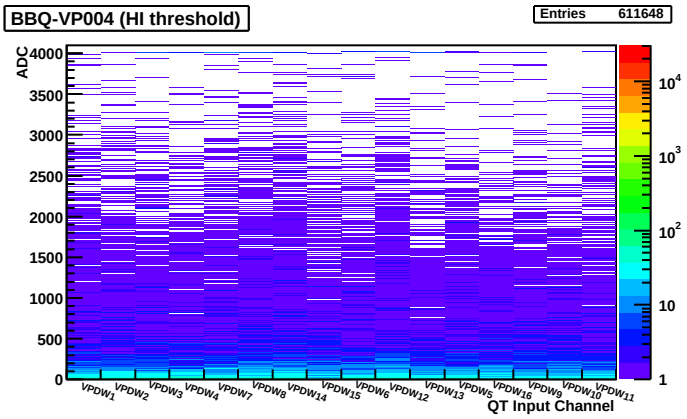

BBQ-VP001 (LO threshold)
Entries 611648
TAC
QT Input Channel

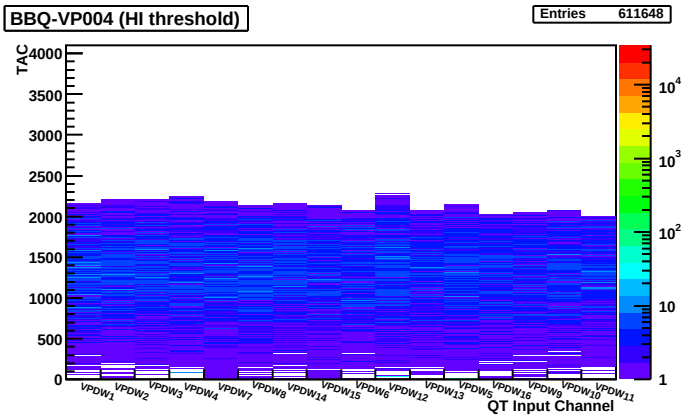

BBQ-VP002 (LO threshold)
Entries 611648
ADC
QT Input Channel

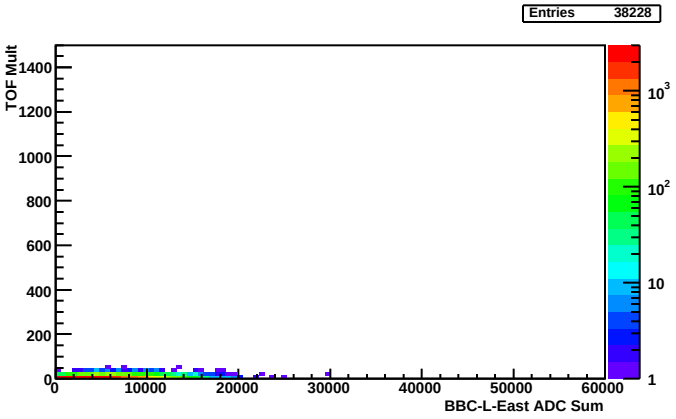

BBQ-VP002 (LO threshold)
Entries 611648
TAC
QT Input Channel

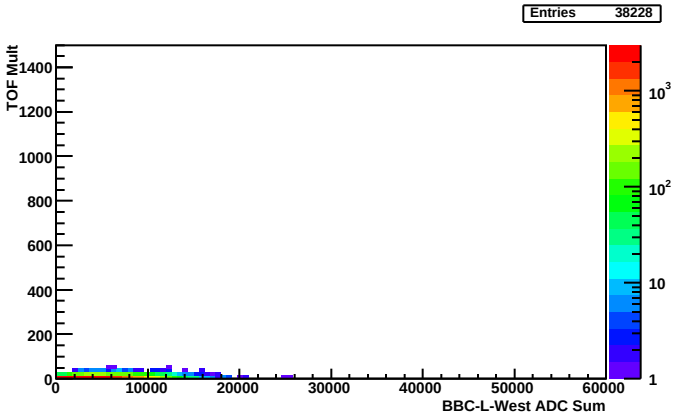

BBQ-VP003 (HI threshold)
Entries 611648
ADC
QT Input Channel

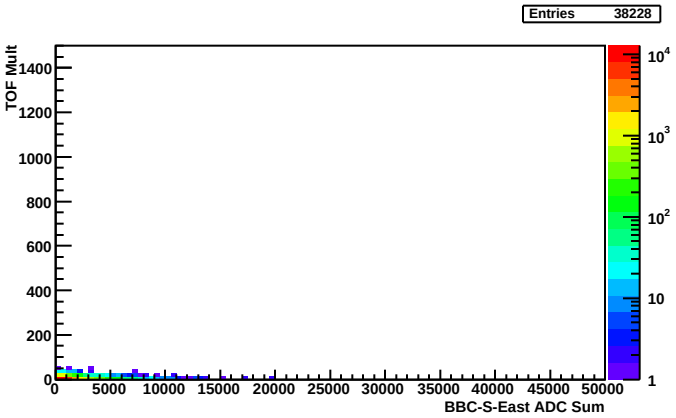

BBQ-VP003 (HI threshold)
Entries 611648
TAC
QT Input Channel

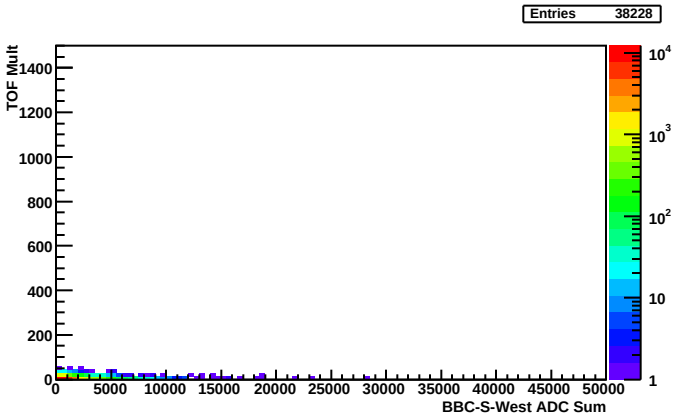

BBQ-VP004 (HI threshold)
Entries 611648
ADC
QT Input Channel

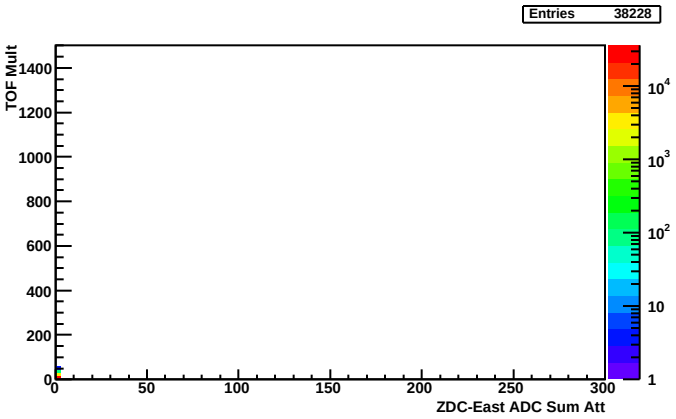

BBQ-VP004 (HI threshold)
Entries 611648
TAC
QT Input Channel

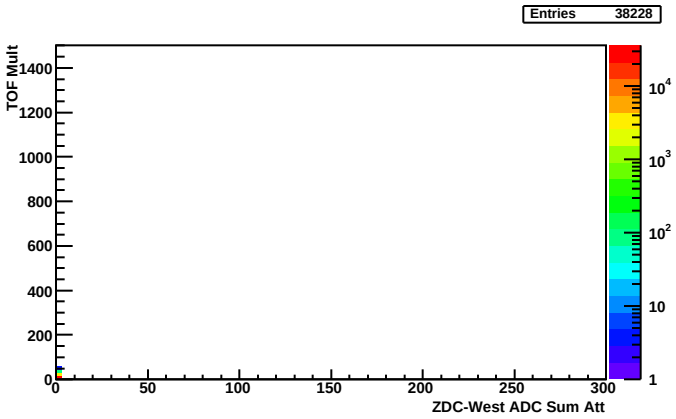

Entries 38228
TOF Mult
BBC-L-East ADC Sum

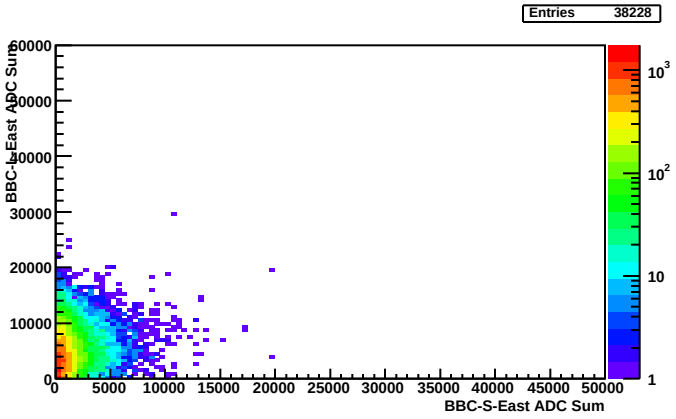

Entries 38228
TOF Mult
BBC-L-West ADC Sum

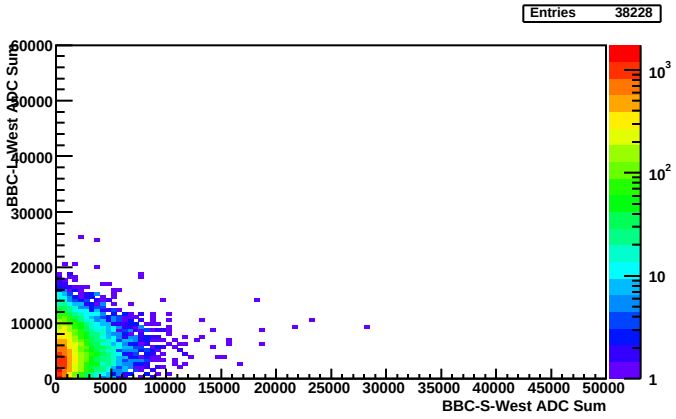

Entries 38228
TOF Mult
BBC-S-East ADC Sum

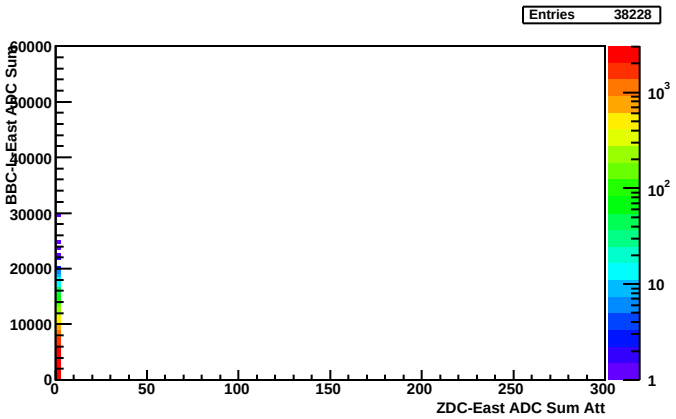

Entries 38228
TOF Mult
BBC-S-West ADC Sum

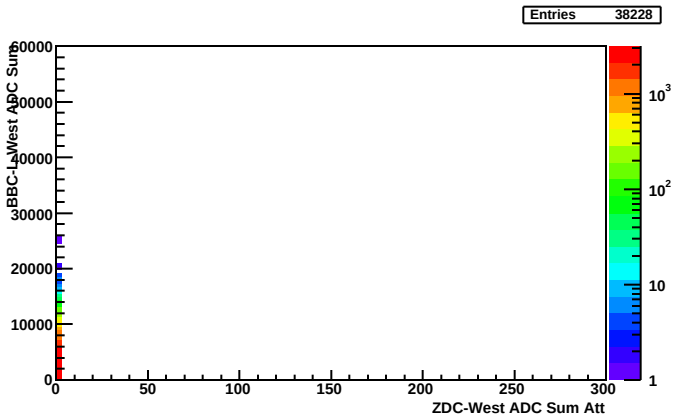

Entries 38228
TOF Mult
ZDC-East ADC Sum Att

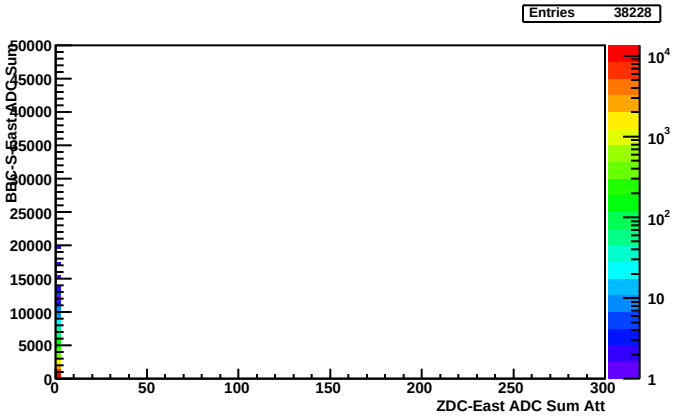

Entries 38228
TOF Mult
ZDC-West ADC Sum Att

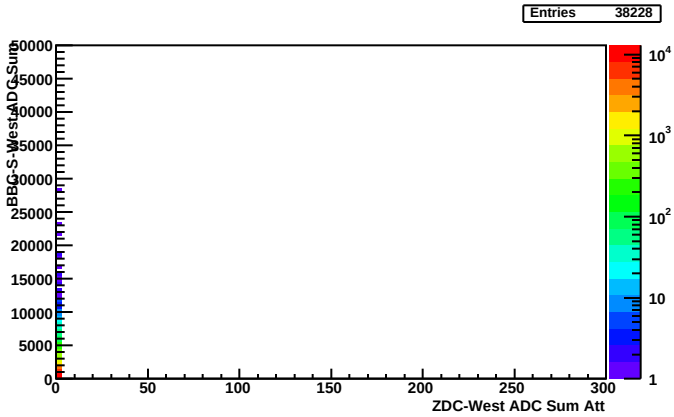

Entries 38228
BBC-L-East ADC Sum
BBC-S-East ADC Sum

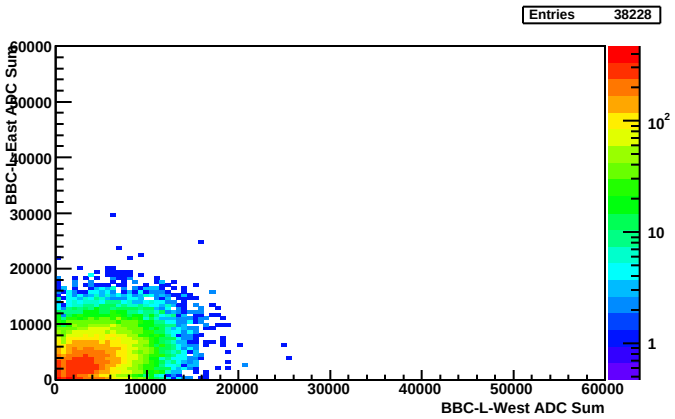

Entries 38228
BBC-L-West ADC Sum
BBC-S-West ADC Sum

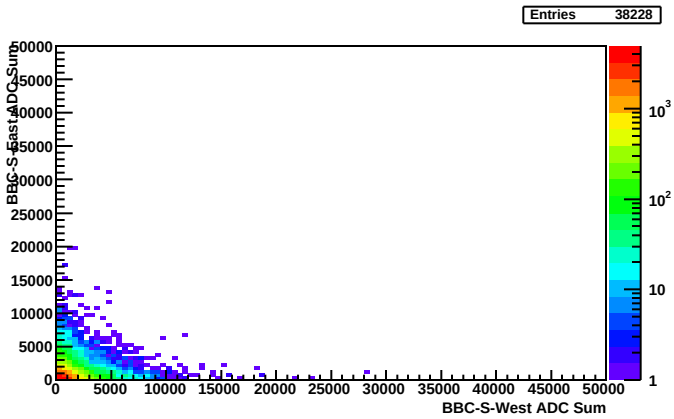

Entries 38228
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

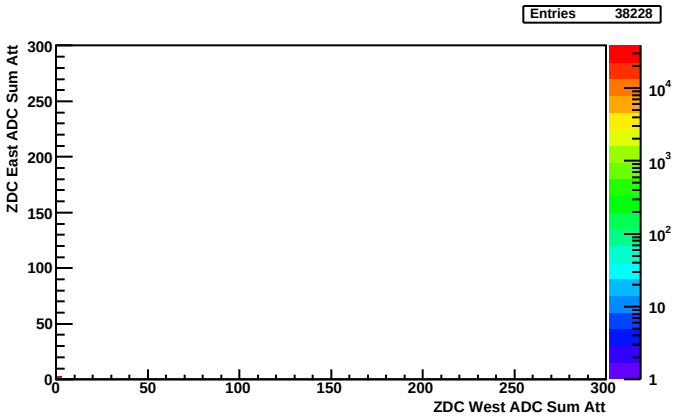

Entries 38228
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

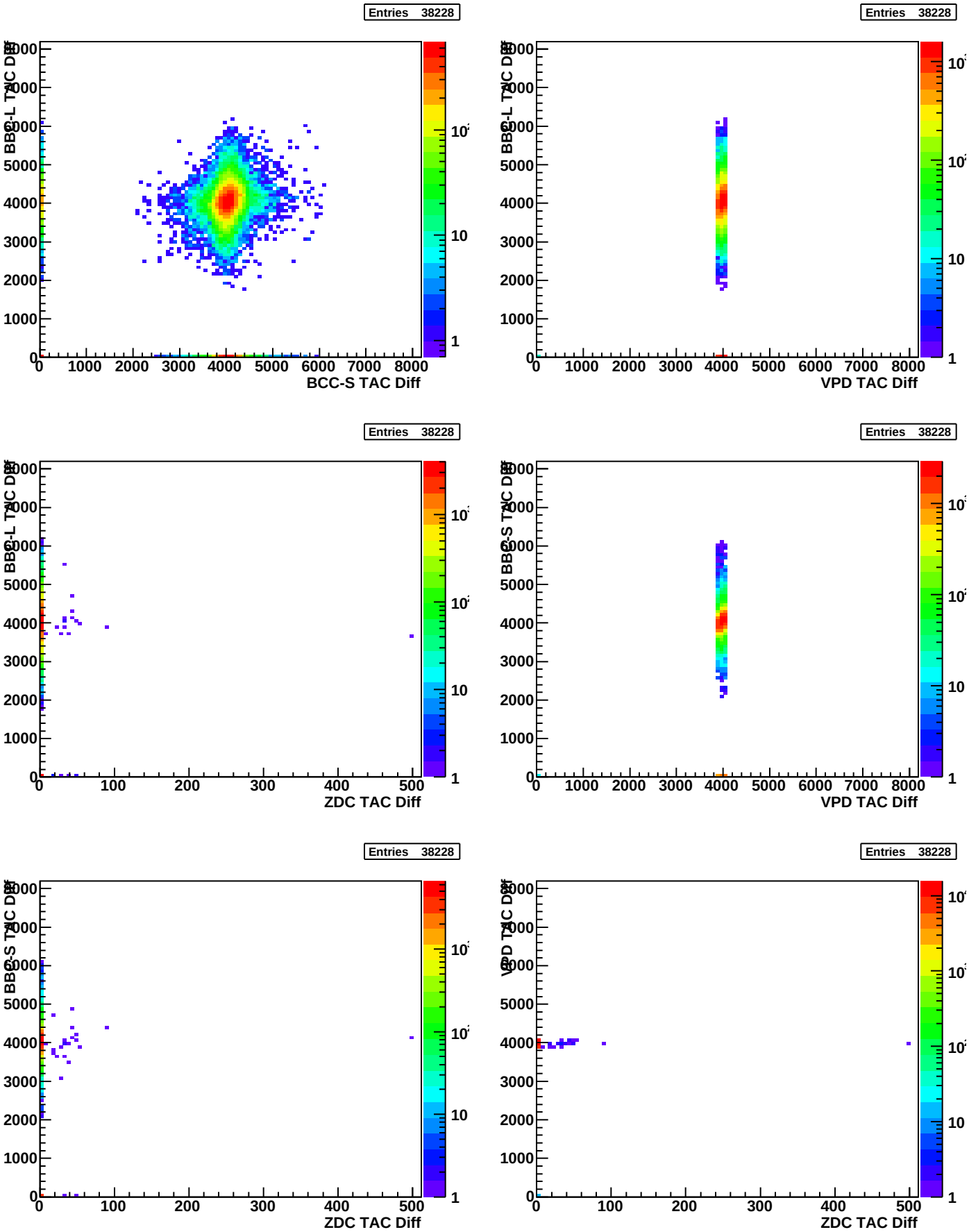

Entries 38228
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

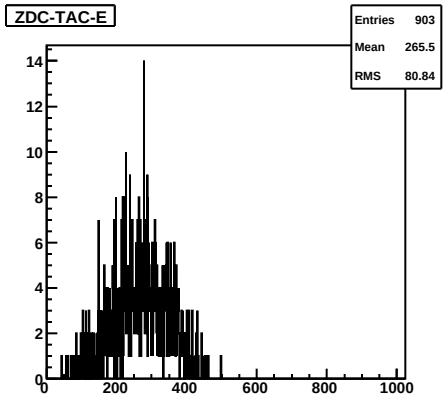

Entries 38228
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

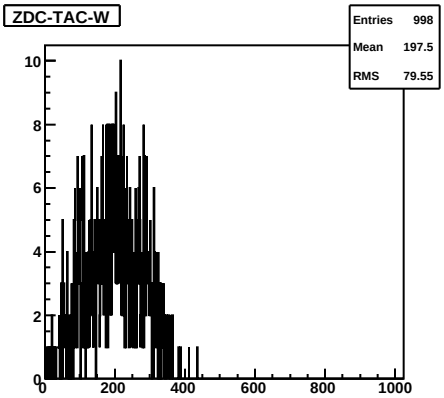

Entries 38228
BBC-L-East ADC Sum
BBC-L-West ADC Sum

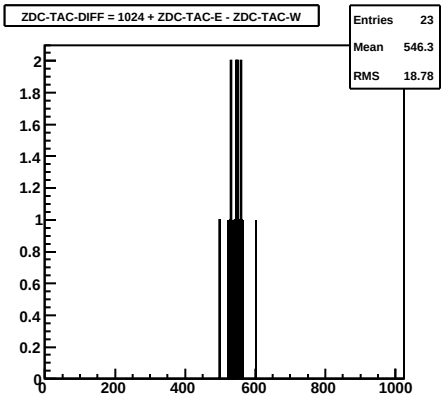

Entries 38228
BBC-S-East ADC Sum
BBC-S-West ADC Sum

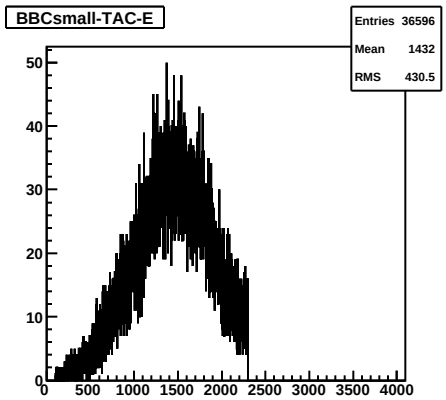

Entries 38228
ZDC East ADC Sum Att
ZDC West ADC Sum Att

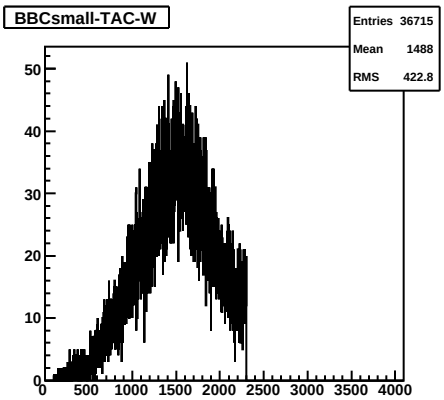

Entries 38228
BBC-L TAC Diff
BCC-S TAC Diff
Entries 38228
BBC-L TAC Diff
VPD TAC Diff
Entries 38228
BBC-L TAC Diff
ZDC TAC Diff
Entries 38228
BBC-S TAC Diff
VPD TAC Diff
Entries 38228
BBC-S TAC Diff
ZDC TAC Diff
Entries 38228
VPD TAC Diff
ZDC TAC Diff

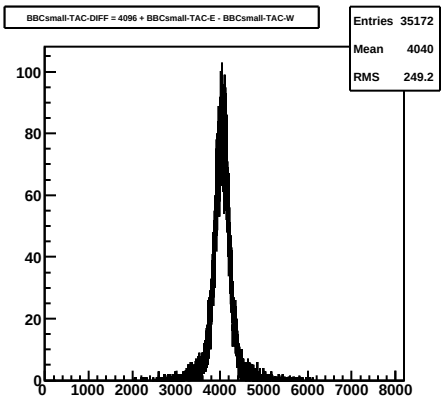

ZDC-TAC-E
Entries 903
Mean 265.5
RMS 80.84

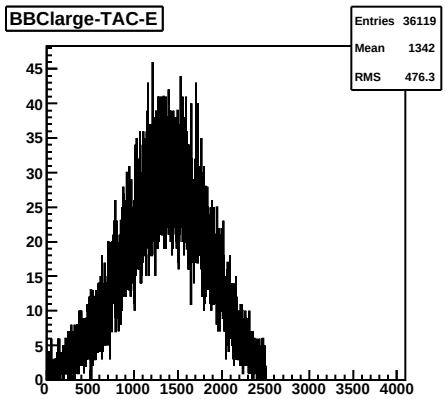

ZDC-TAC-W
Entries 998
Mean 197.5
RMS 79.55

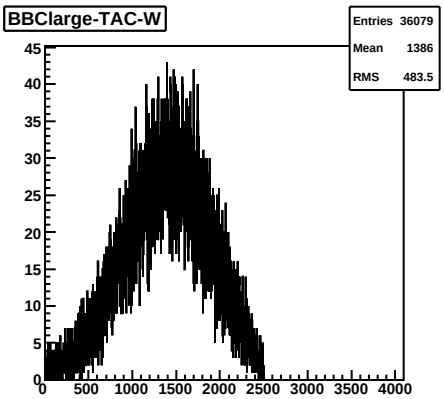

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 23
Mean 546.3
RMS 18.78

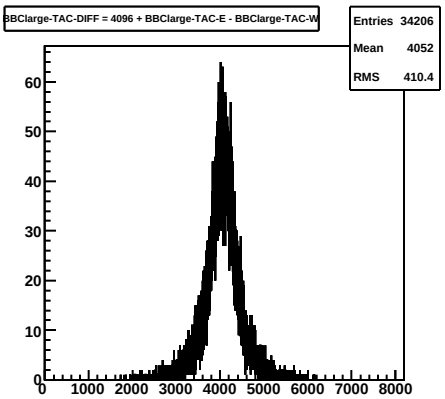

BBCsmall-TAC-E
Entries 36596
Mean 1432
RMS 430.5

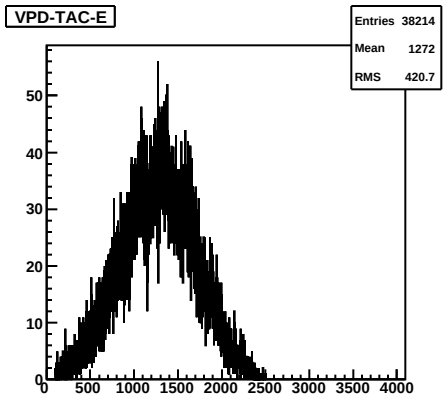

BBCsmall-TAC-W
Entries 36715
Mean 1488
RMS 422.8

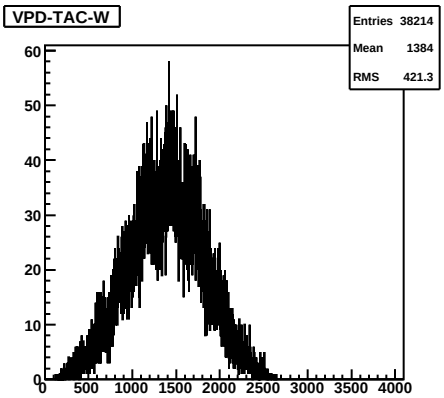

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 35172
Mean 4040
RMS 249.2

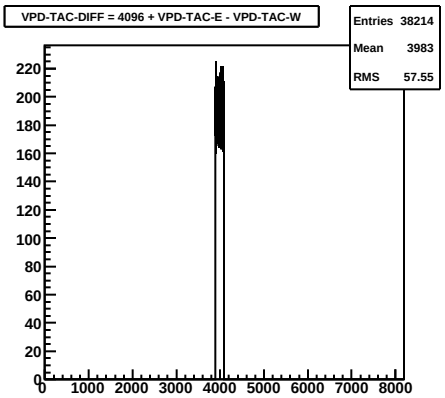

BBClarge-TAC-E
Entries 36119
Mean 1342
RMS 476.3

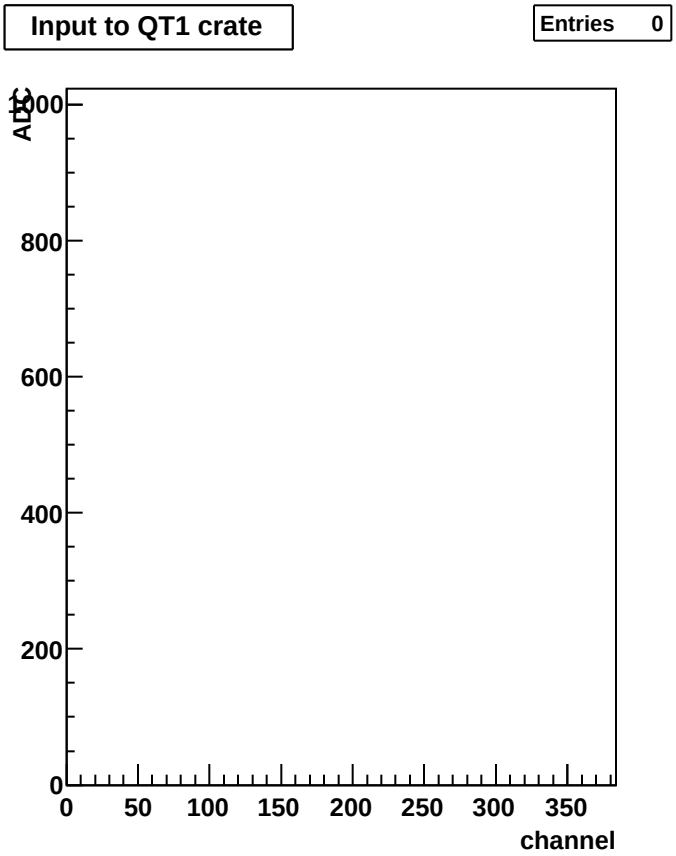

BBClarge-TAC-W
Entries 36079
Mean 1386
RMS 483.5

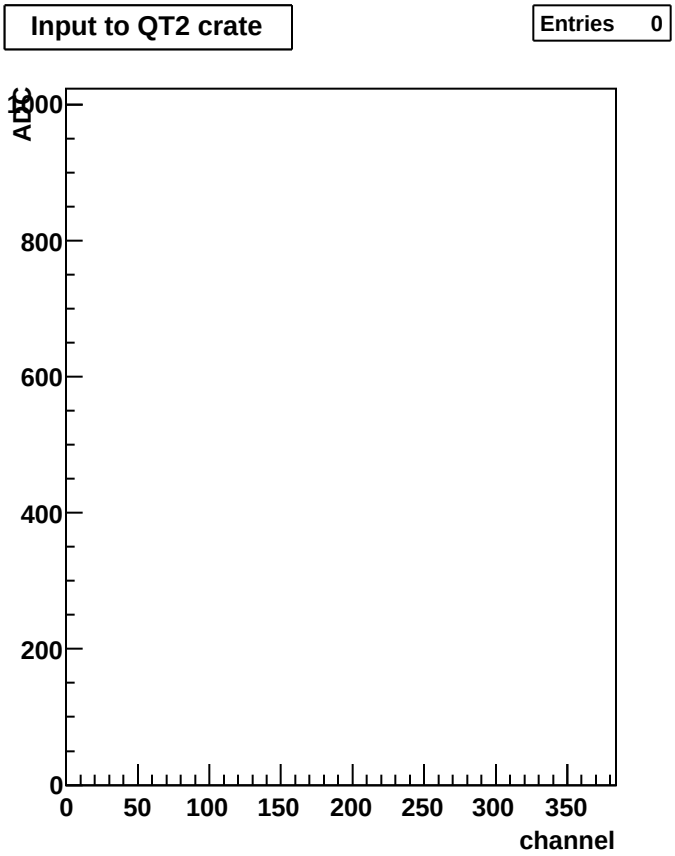

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 34206
Mean 4052
RMS 410.4

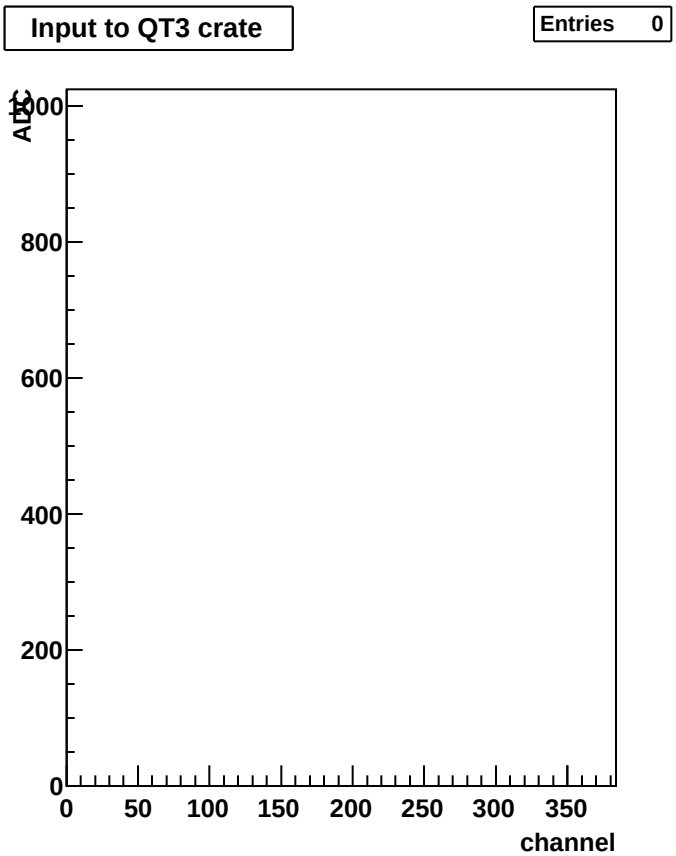

VPD-TAC-E
Entries 38214
Mean 1272
RMS 420.7

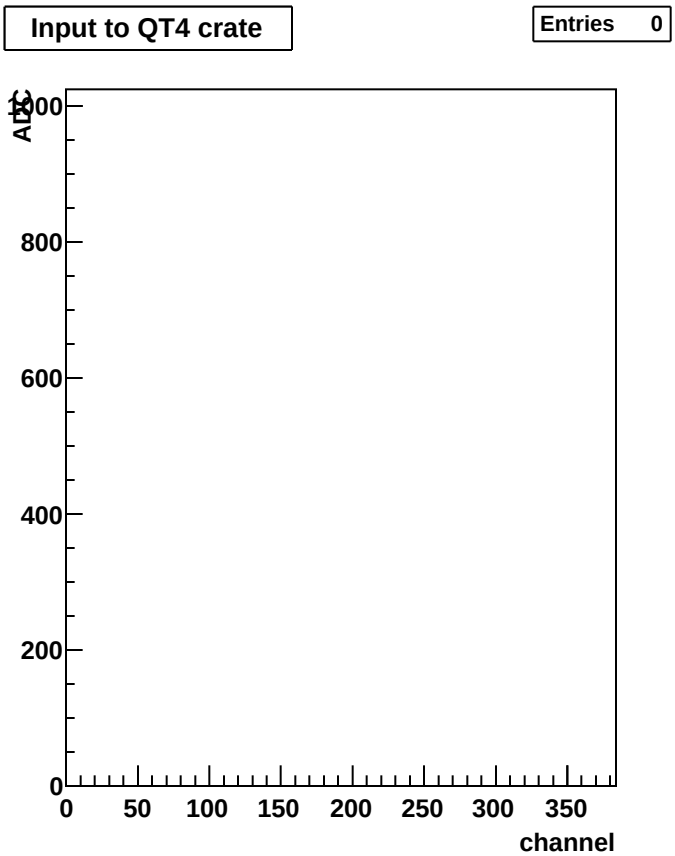

VPD-TAC-W
Entries 38214
Mean 1384
RMS 421.3

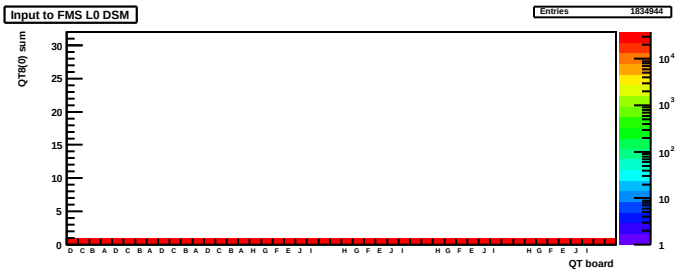

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 38214
Mean 3983
RMS 57.55

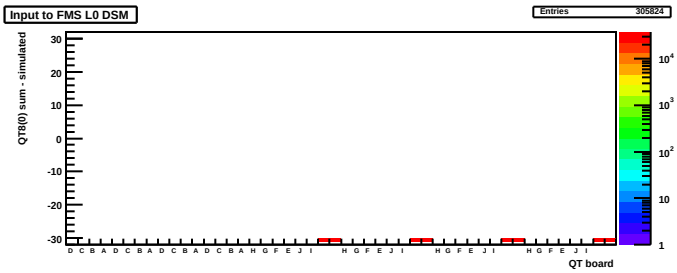

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

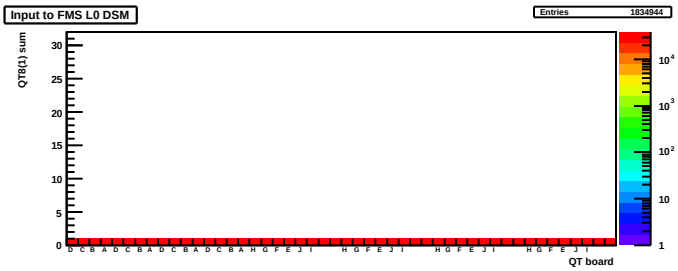

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

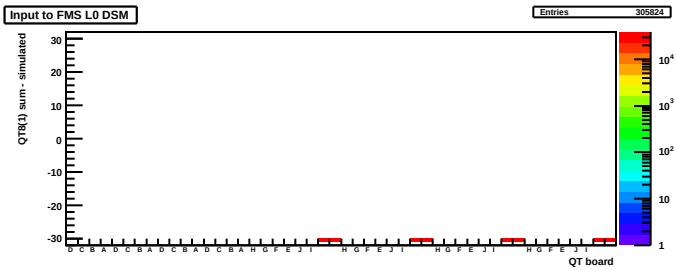

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

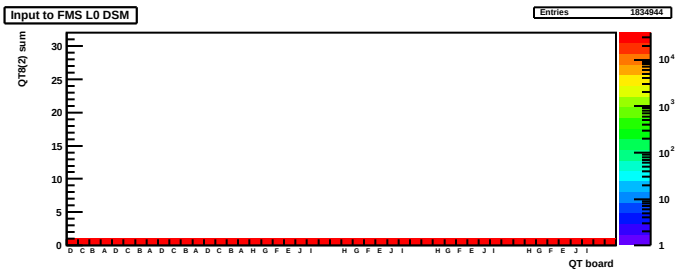

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

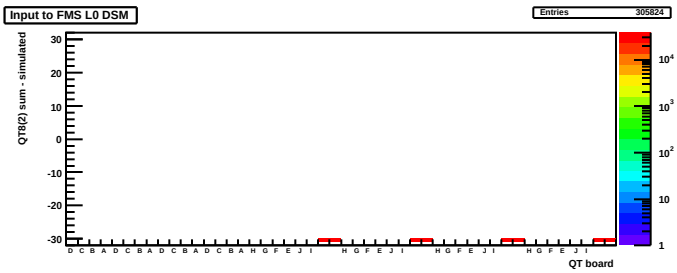

Input to FMS L0 DSM
Entries 1834944
QT8(0) sum
QT board

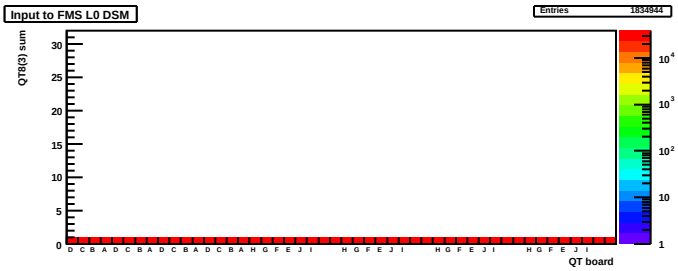

Input to FMS L0 DSM
Entries 305824
QT8(0) sum - simulated
QT board

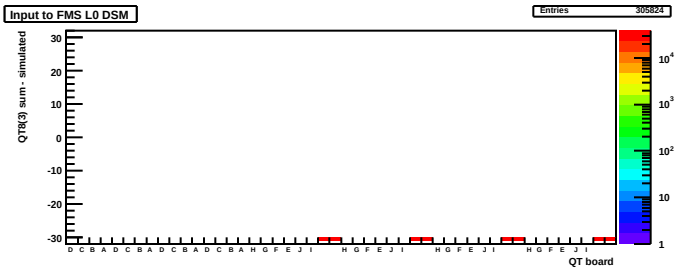

Input to FMS L0 DSM
Entries 1834944
QT8(1) sum
QT board

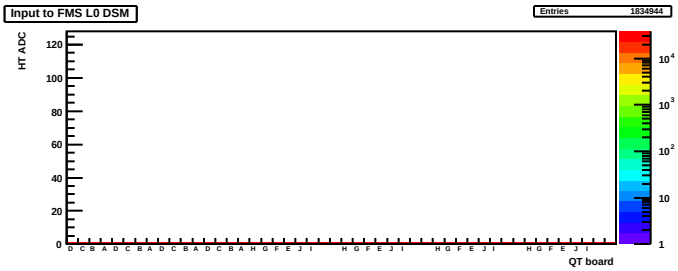

Input to FMS L0 DSM
Entries 305824
QT8(1) sum - simulated
QT board

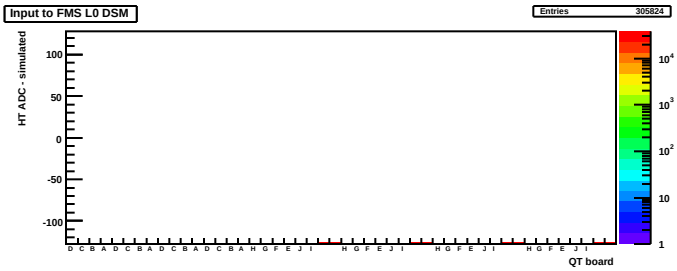

Input to FMS L0 DSM
Entries 1834944
QT8(2) sum
QT board

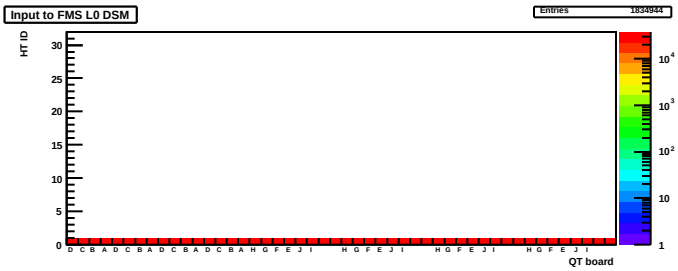

Input to FMS L0 DSM
Entries 305824
QT8(2) sum - simulated
QT board

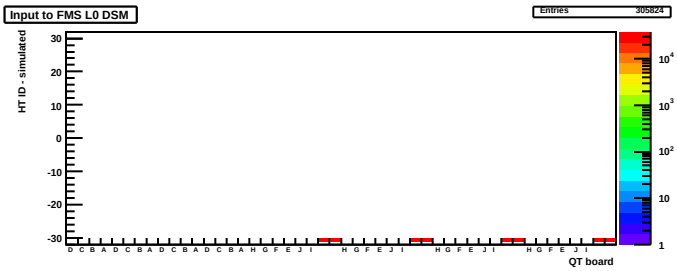

Input to FMS L0 DSM
Entries 1834944
QT8(3) sum
QT board

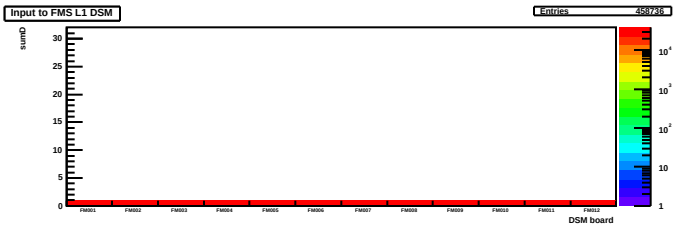

Input to FMS L0 DSM
Entries 305824
QT8(3) sum - simulated
QT board

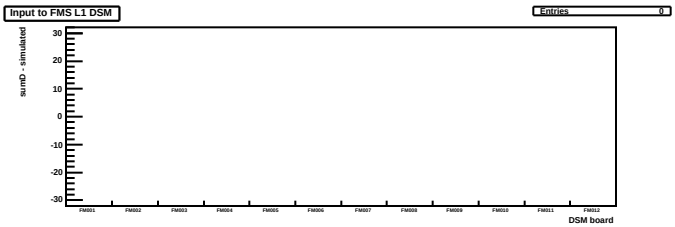

Input to FMS L0 DSM
Entries 1834944
HT ADC
QT board

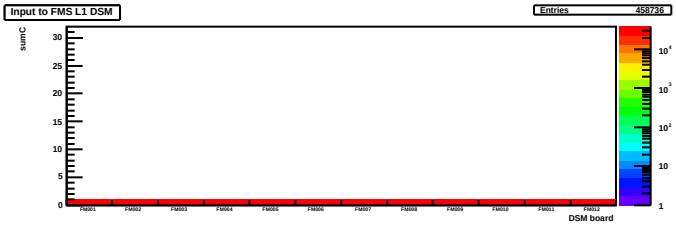

Input to FMS L0 DSM
Entries 305824
HT ADC - simulated
QT board

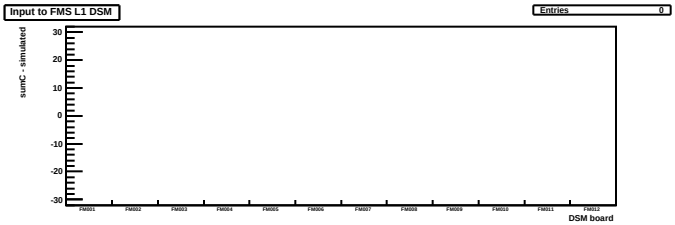

Input to FMS L0 DSM
Entries 1834944
HT ID
QT board

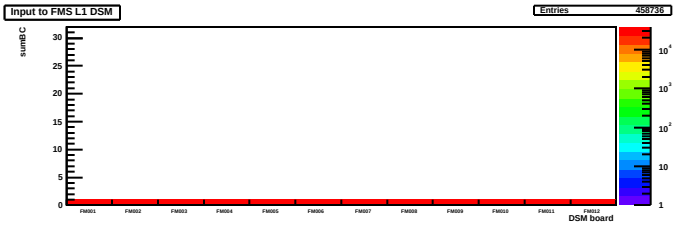

Input to FMS L0 DSM
Entries 305824
HT ID - simulated
QT board

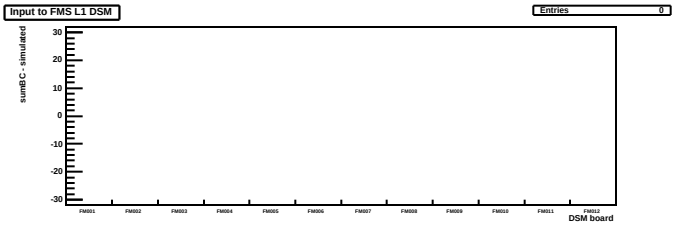

Input to FMS L1 DSM
Entries 458736
sumD
DSM board

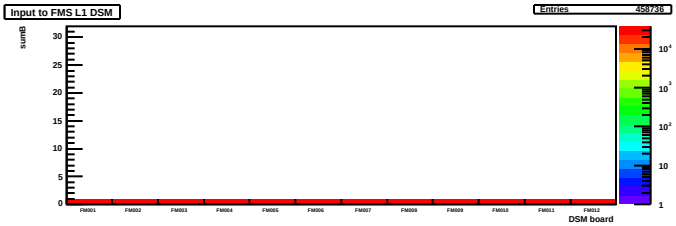

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

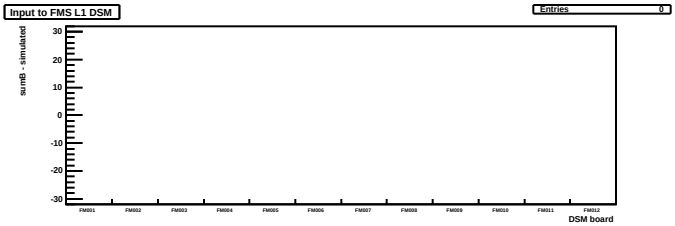

Input to FMS L1 DSM
Entries 458736
sumC
DSM board

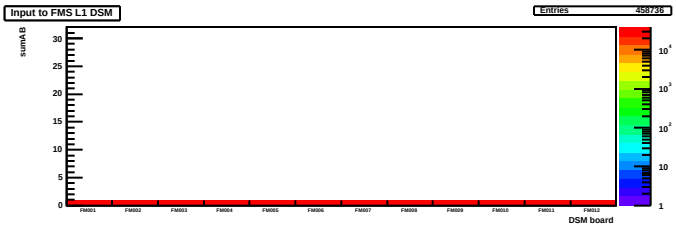

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

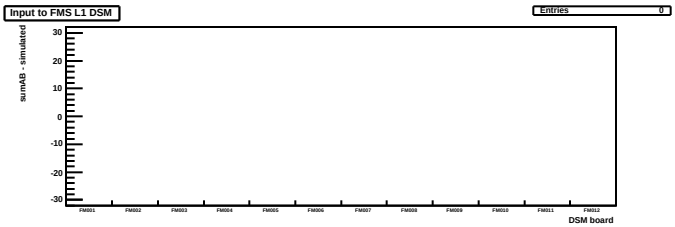

Input to FMS L1 DSM
Entries 458736
sumBC
DSM board

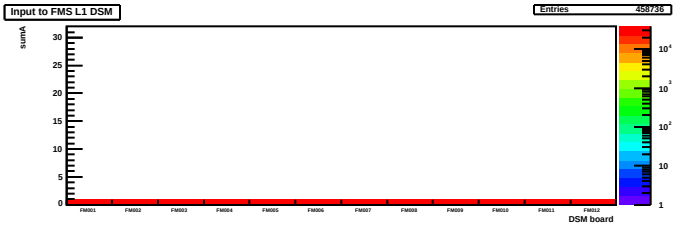

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

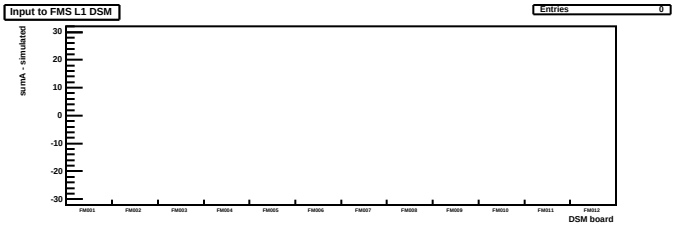

Input to FMS L1 DSM
Entries 458736
sumB
DSM board

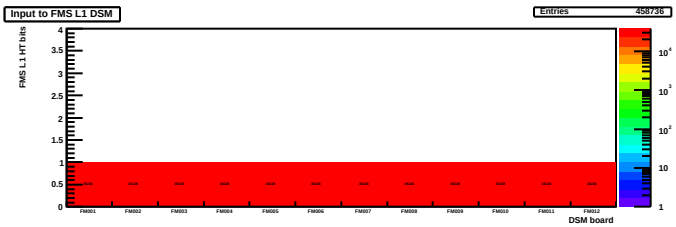

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

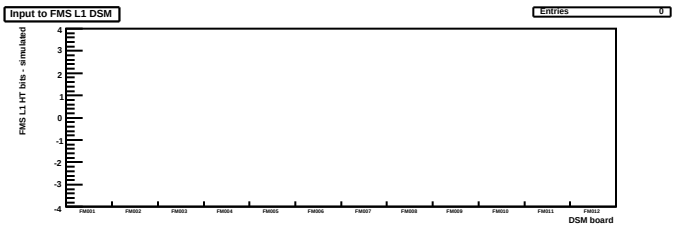

Input to FMS L1 DSM
Entries 458736
sumAB
DSM board

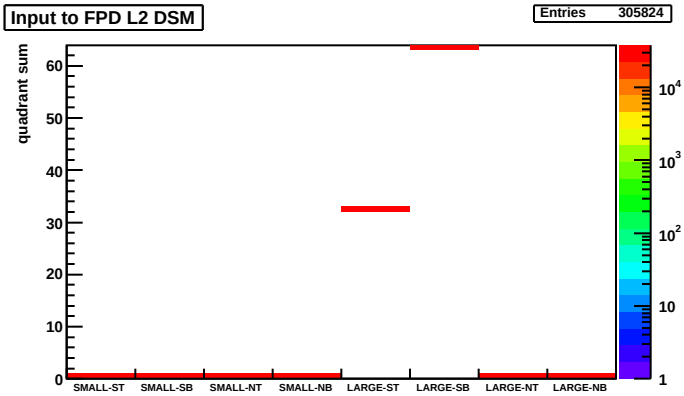

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

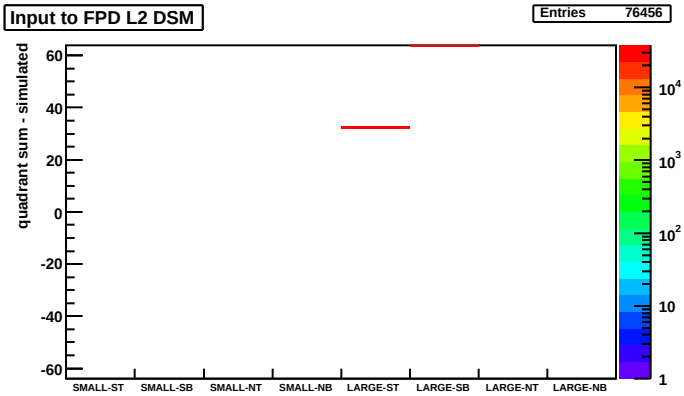

Input to FMS L1 DSM
Entries 458736
sumA
DSM board

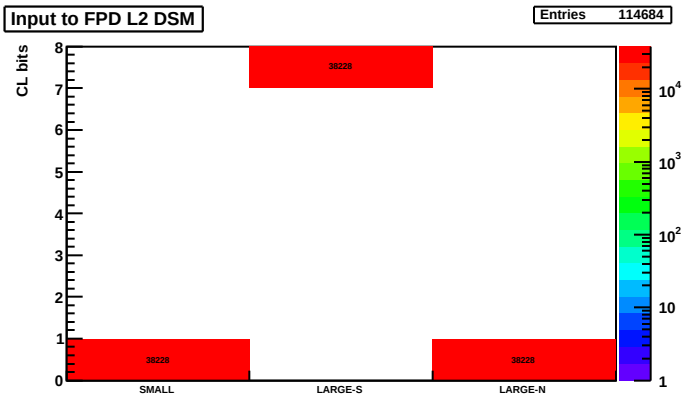

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

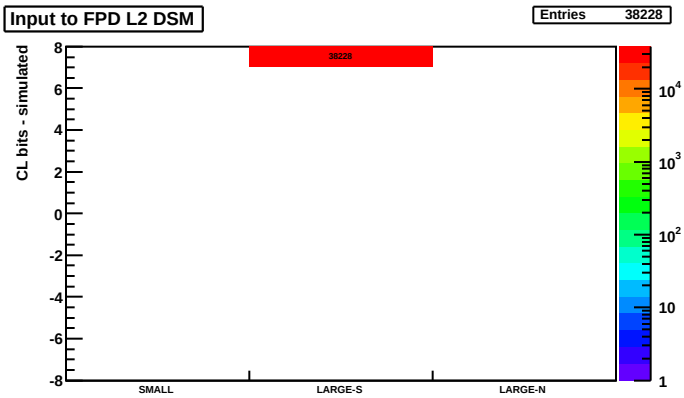

Input to FMS L1 DSM
Entries 458736
FMS L1 HT bits
DSM board

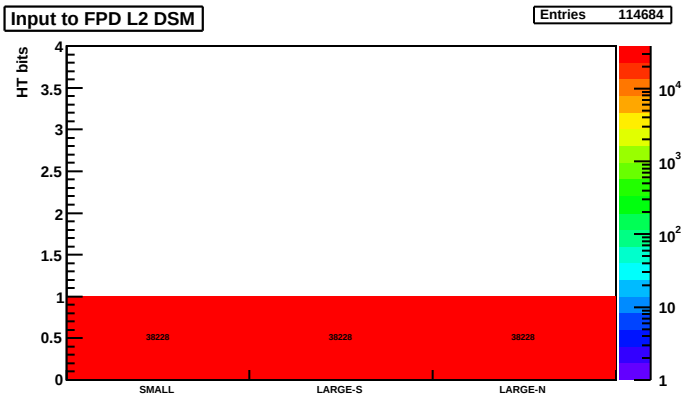

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

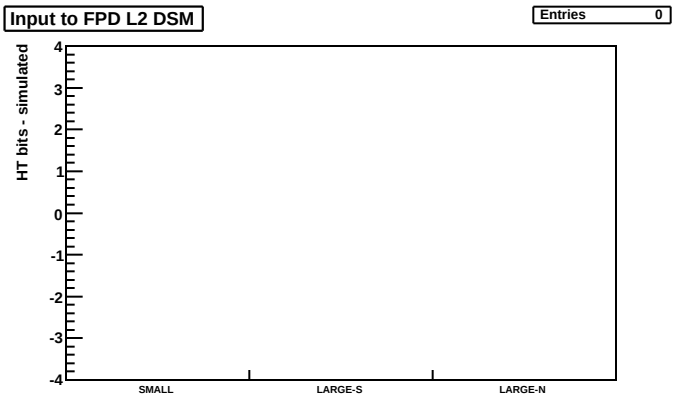

Input to FPD L2 DSM
Entries 305824
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

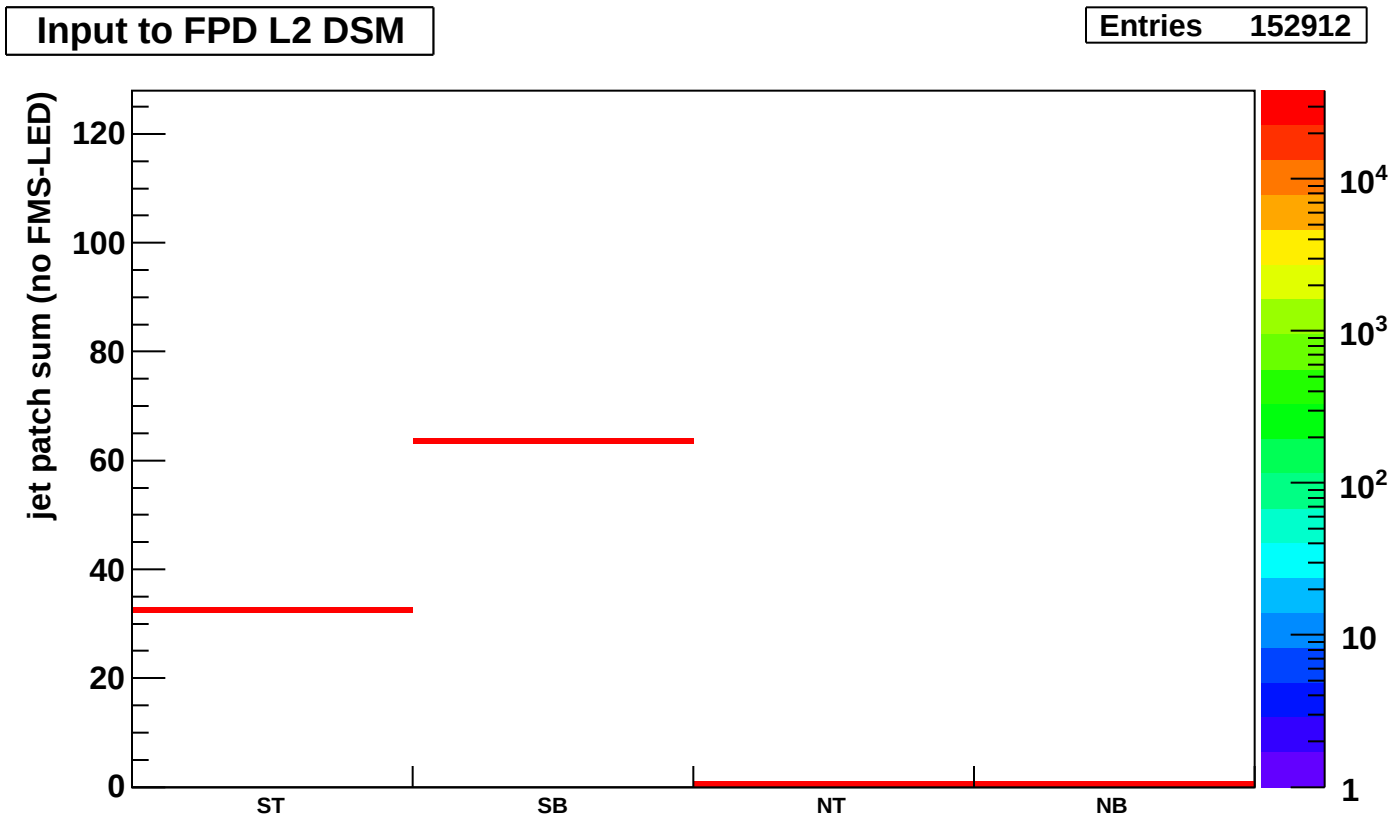

Input to FPD L2 DSM
Entries 76456
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

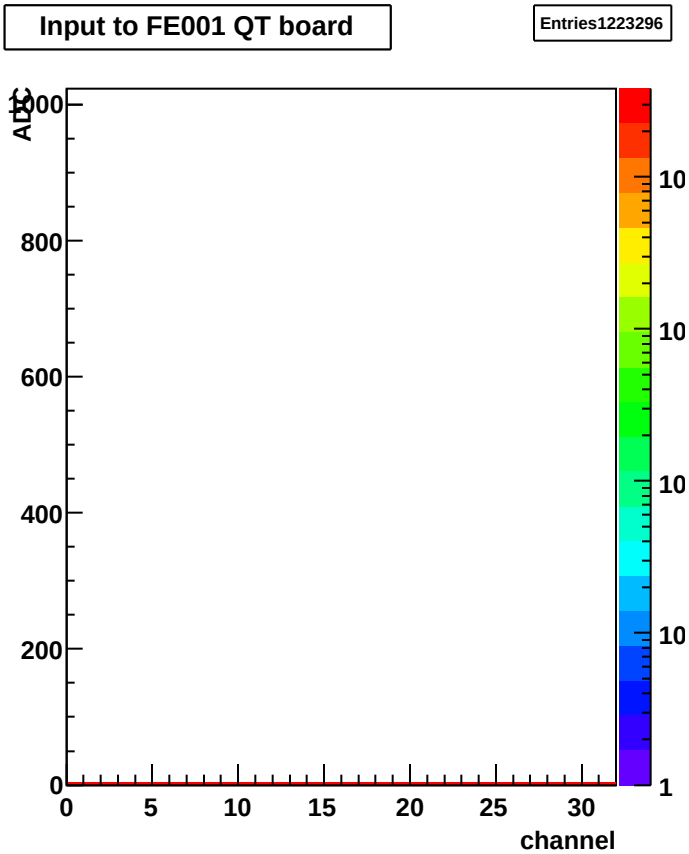

Input to FPD L2 DSM
Entries 114684
CL bits
38228
38228
38228
SMALL
LARGE-S
LARGE-N

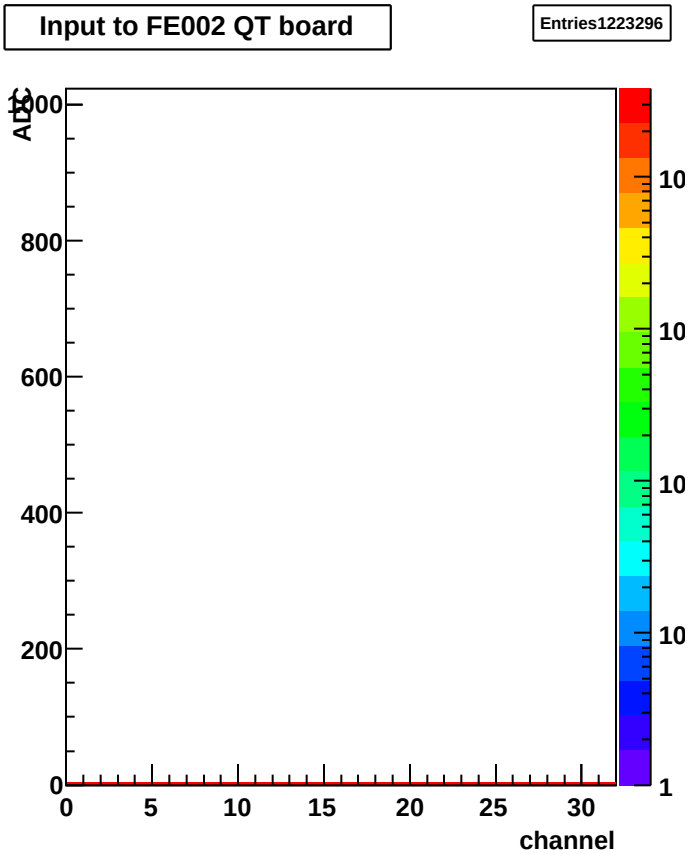

Input to FPD L2 DSM
Entries 38228
CL bits - simulated
38228
SMALL
LARGE-S
LARGE-N

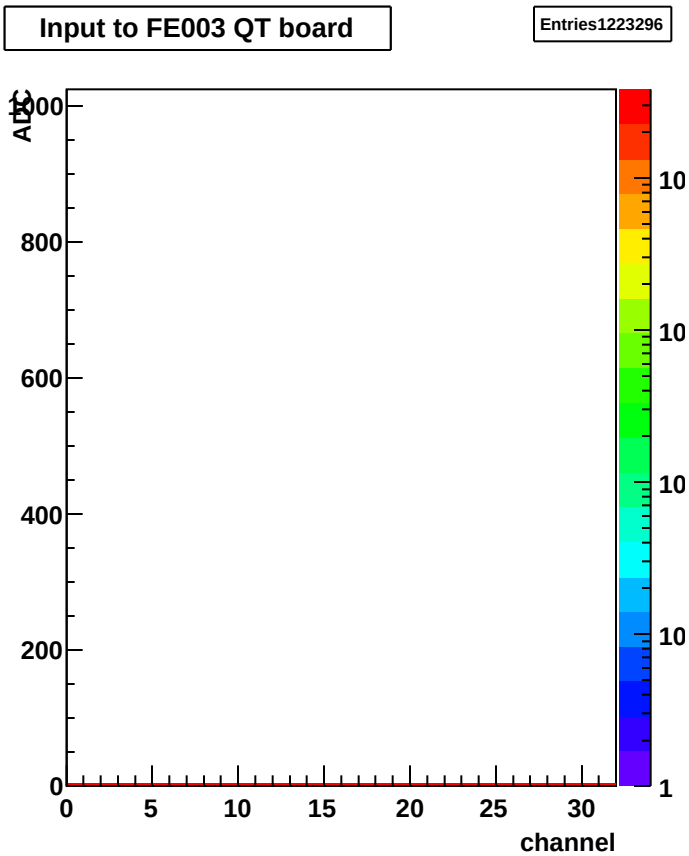

Input to FPD L2 DSM
Entries 114684
HT bits
38228
38228
38228
SMALL
LARGE-S
LARGE-N

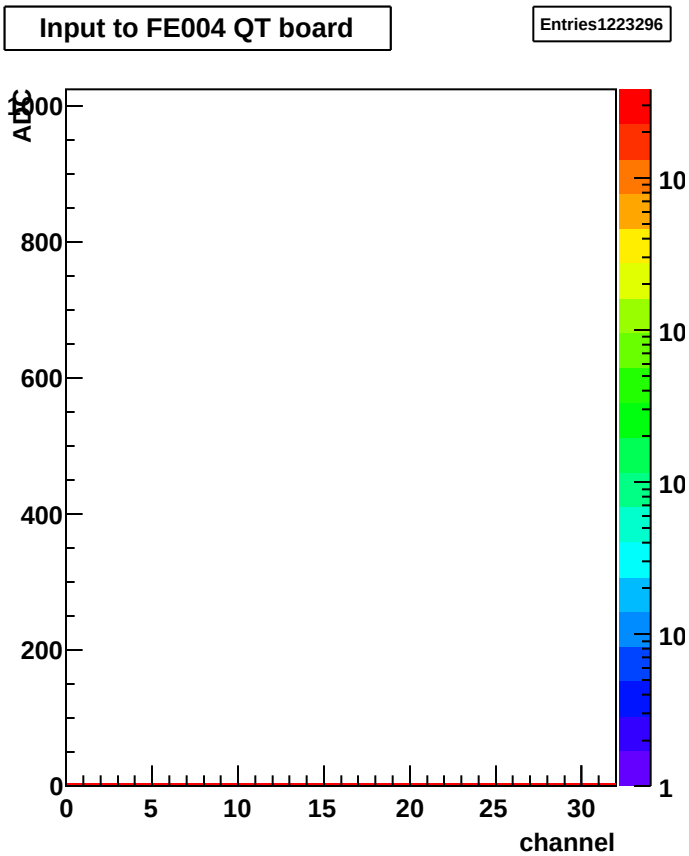

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

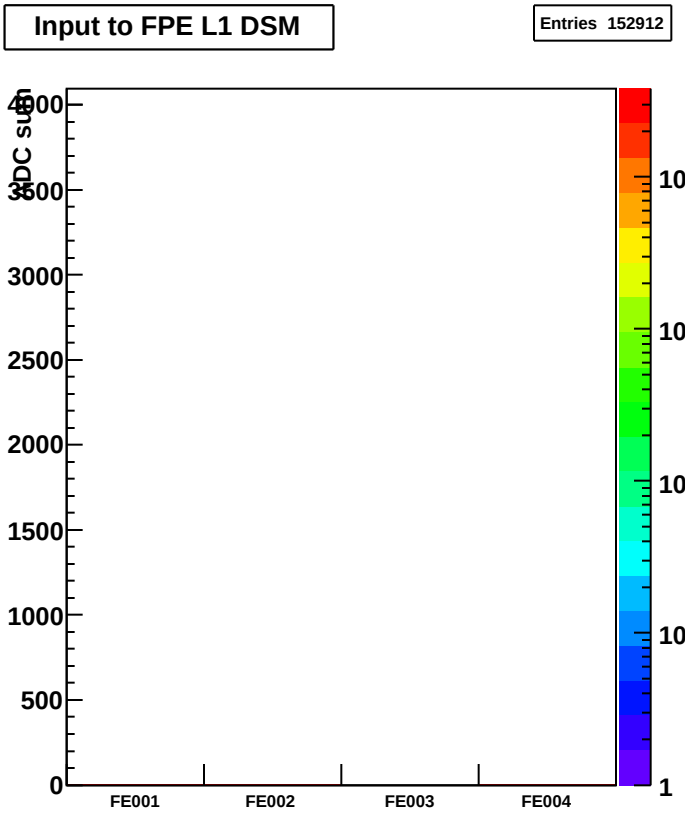

Input to FPD L2 DSM
Entries 152912
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4


Input to FE001 QT board
Entries1223296
ADC
channel

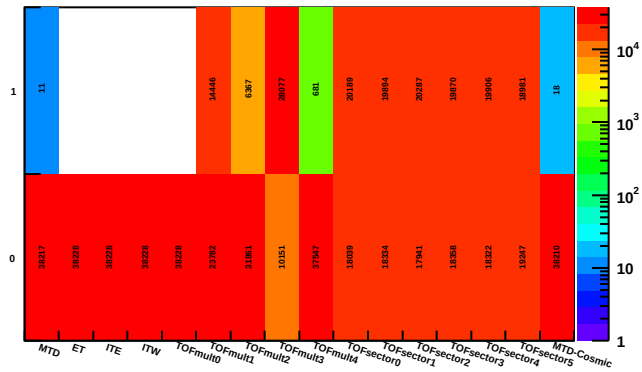

Input to FE002 QT board
Entries1223296
ADC
channel


Input to FE003 QT board
Entries1223296
ADC
channel

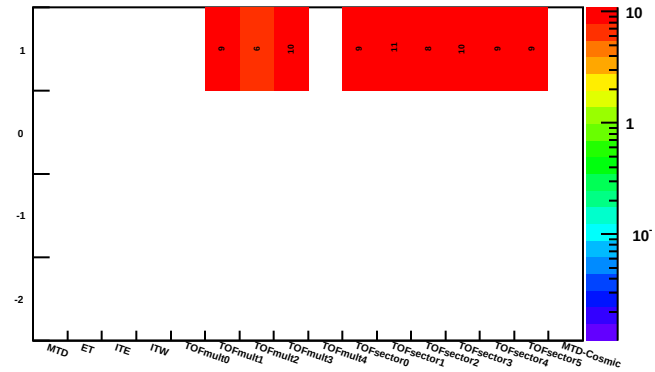

Input to FE004 QT board
Entries1223296
ADC
channel


Input to FPE L1 DSM
Entries 152912
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

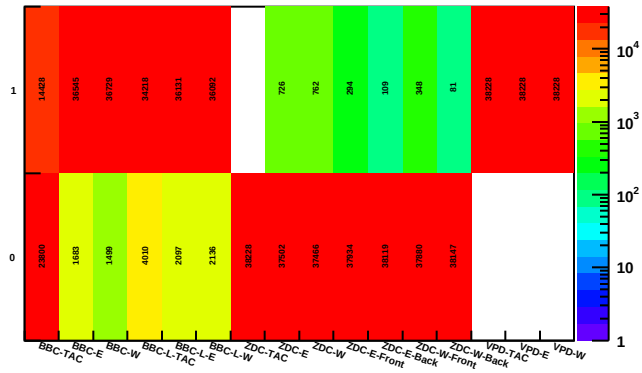
TF201 0-15 (ch0)



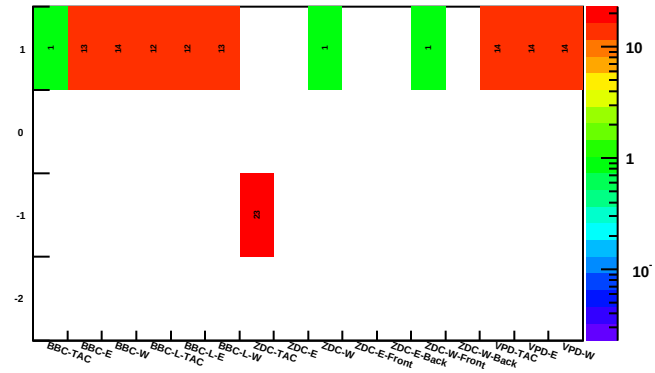
TF201 0-15 (ch0)



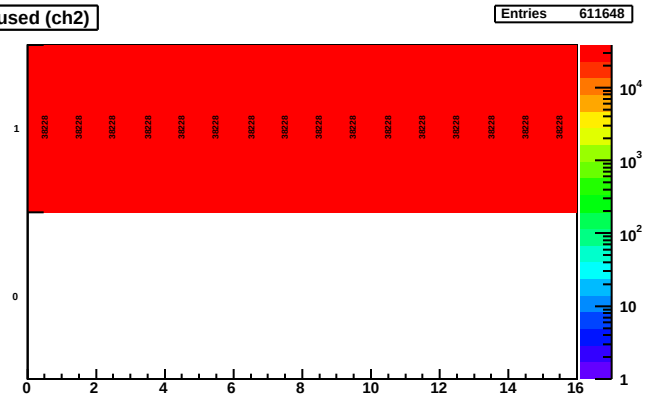
VT201 0-15 (ch1)

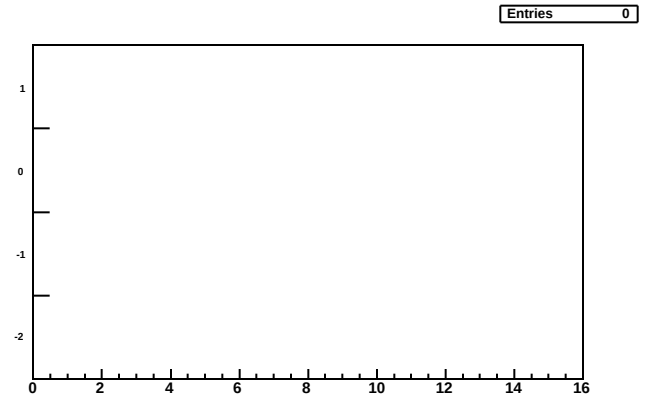


VT201 0-15 (ch1)

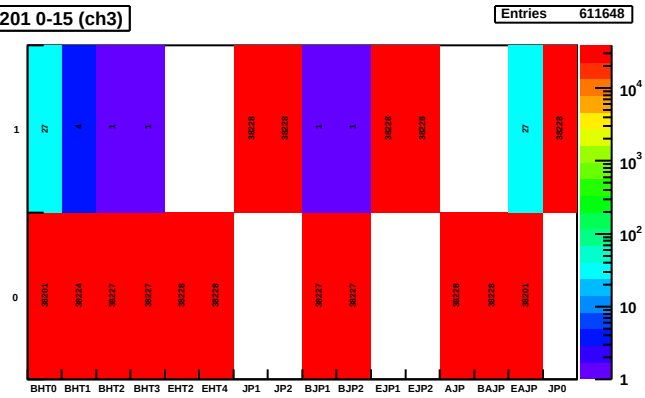


Unused (ch2)

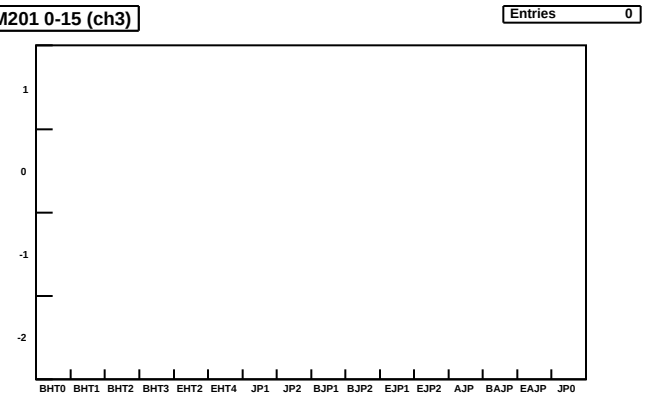




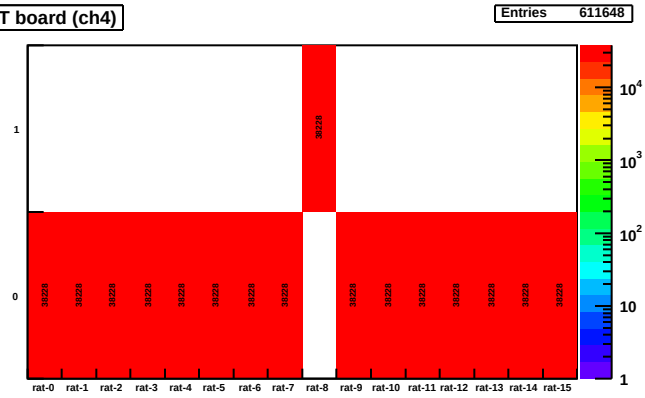
EM201 0-15 (ch3)

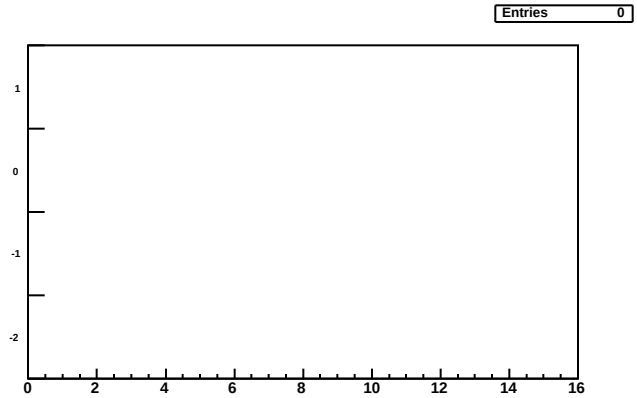


EM201 0-15 (ch3)

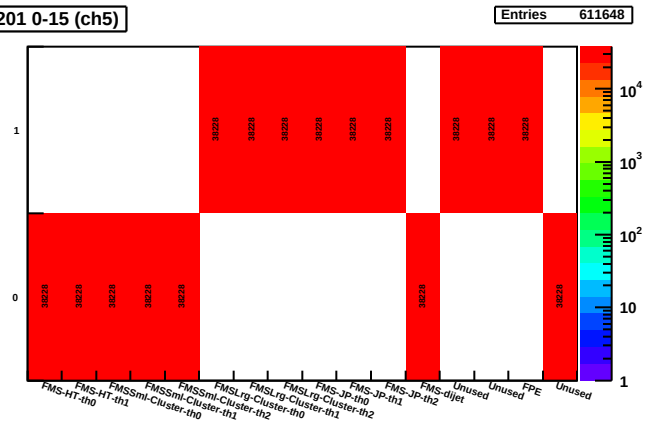


RAT board (ch4)

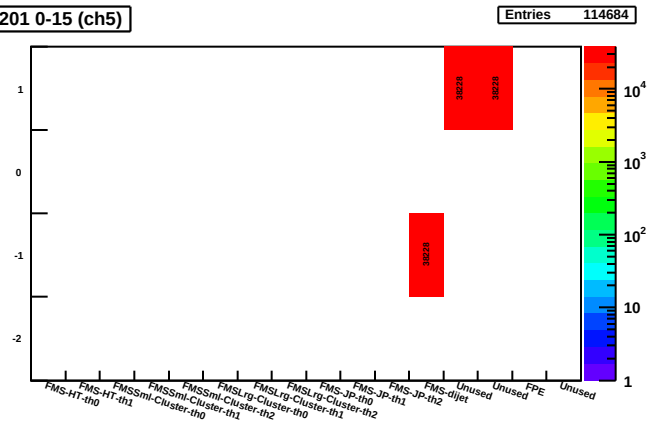




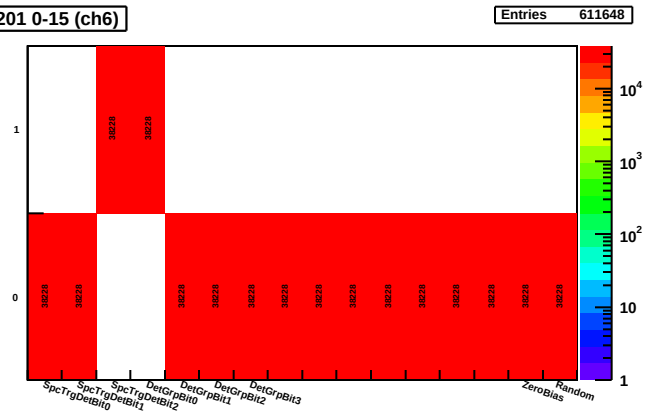
FP201 0-15 (ch5)

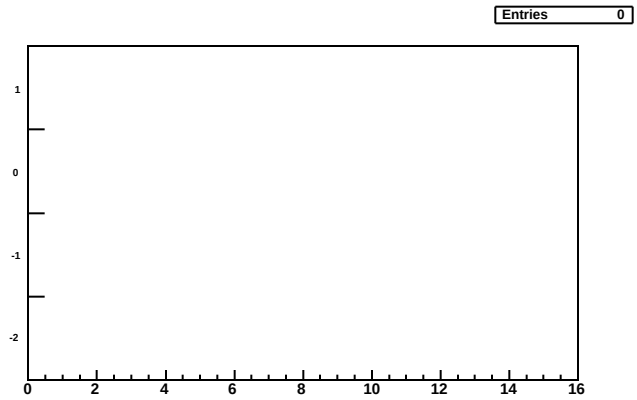
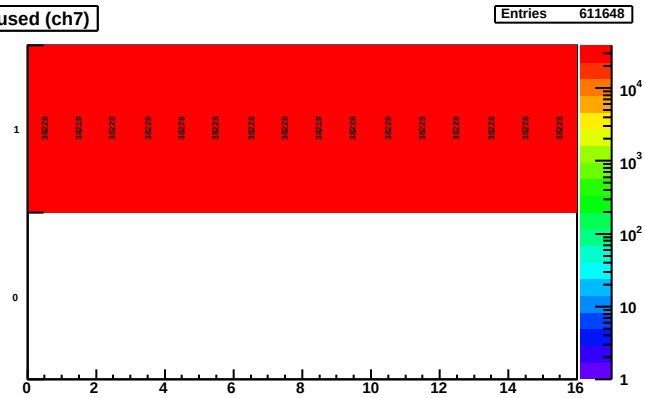
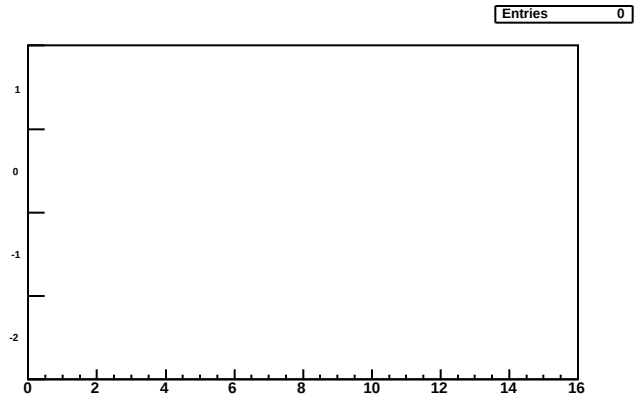


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)