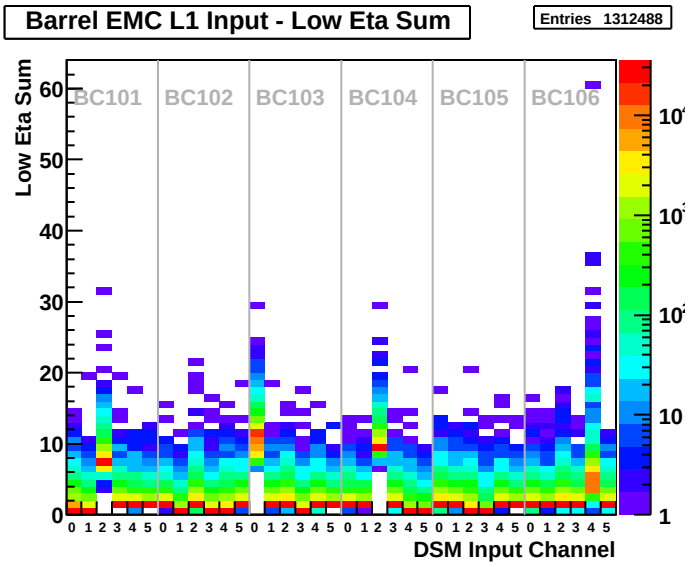
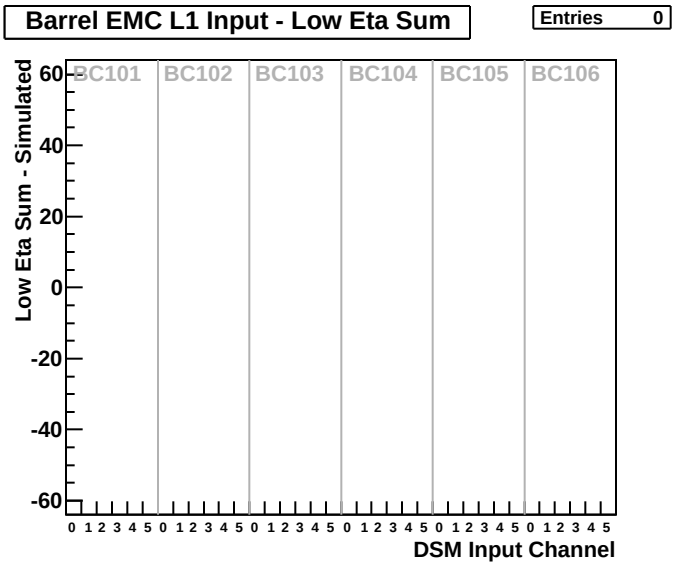
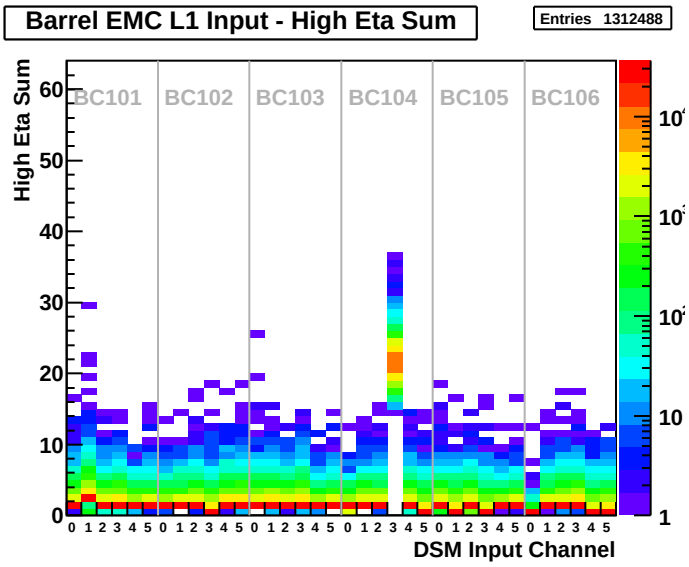
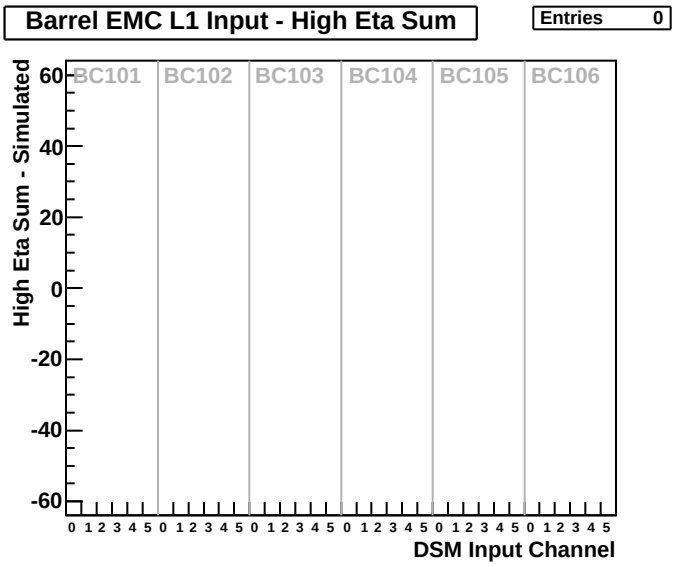
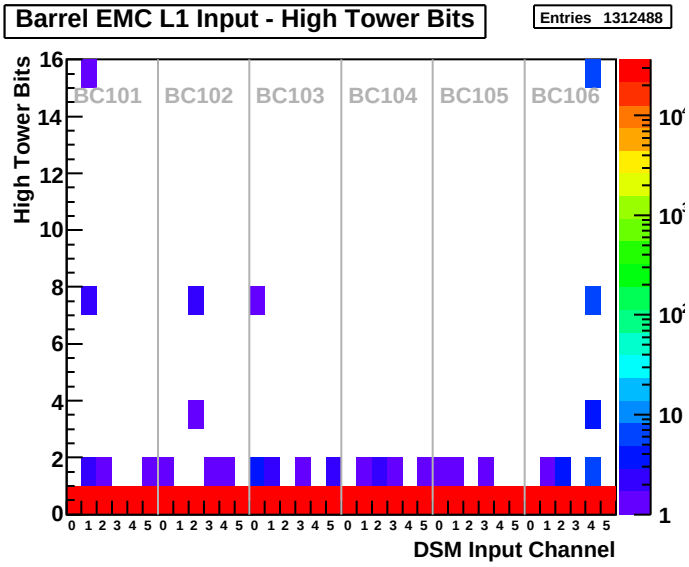
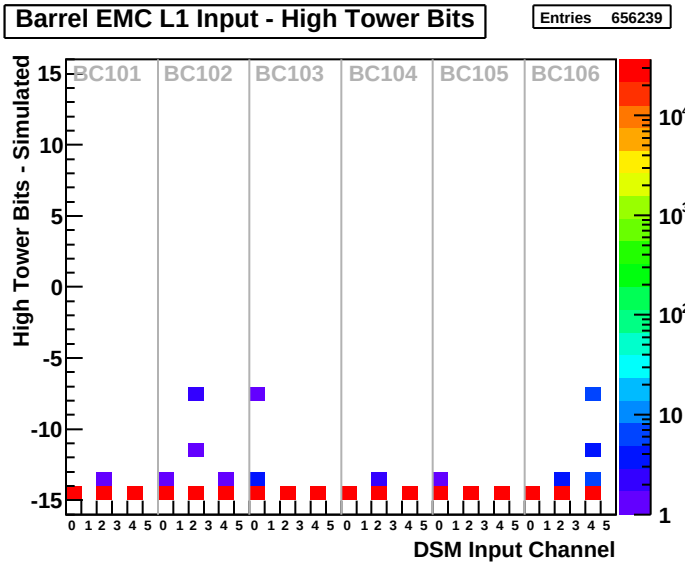

Barrel EMC L1 Input - Low Eta Sum
Entries 1312488
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

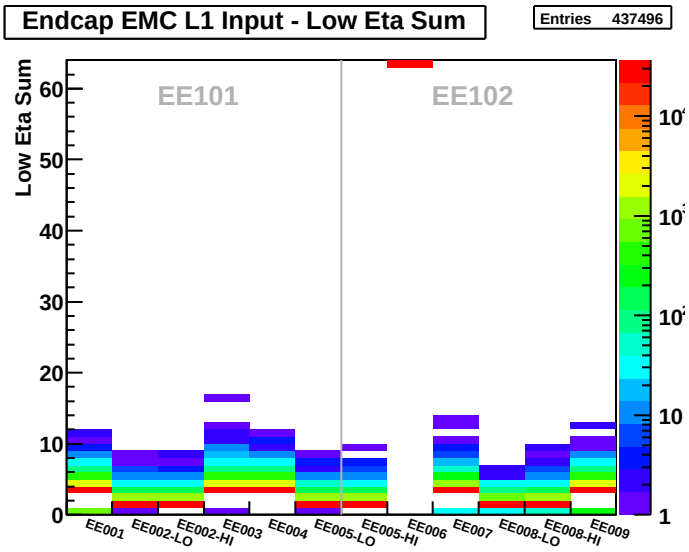

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

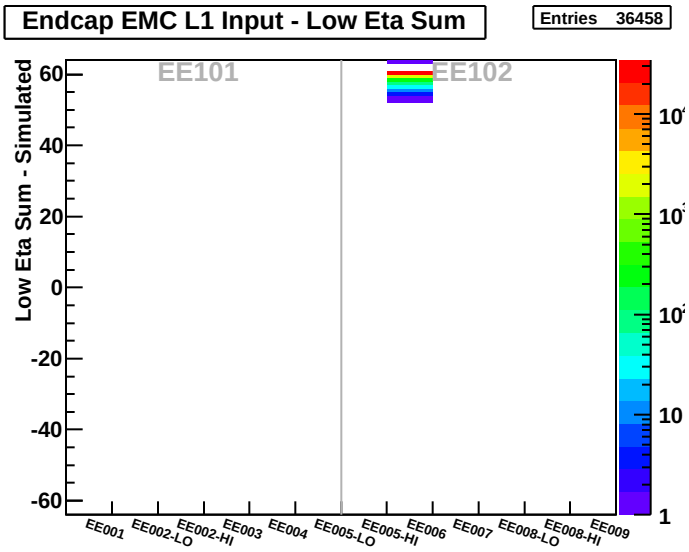

Barrel EMC L1 Input - High Eta Sum
Entries 1312488
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

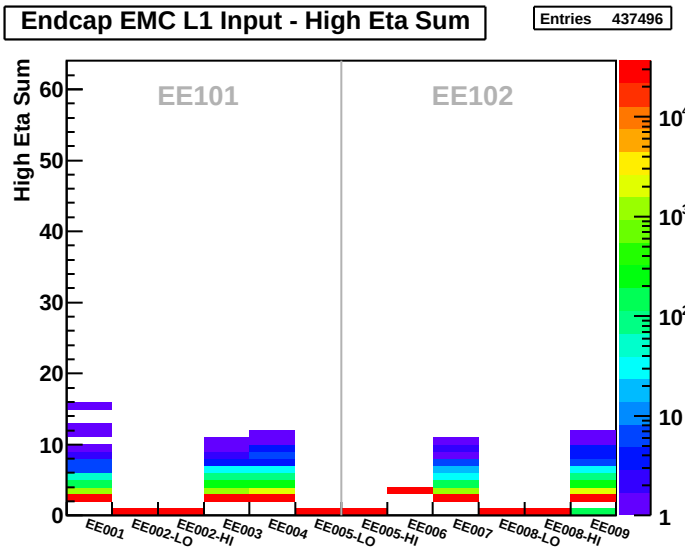

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

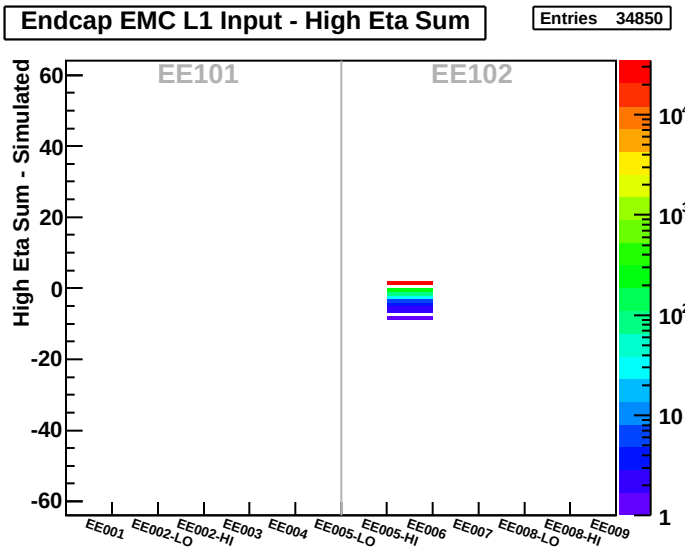

Barrel EMC L1 Input - High Tower Bits
Entries 1312488
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

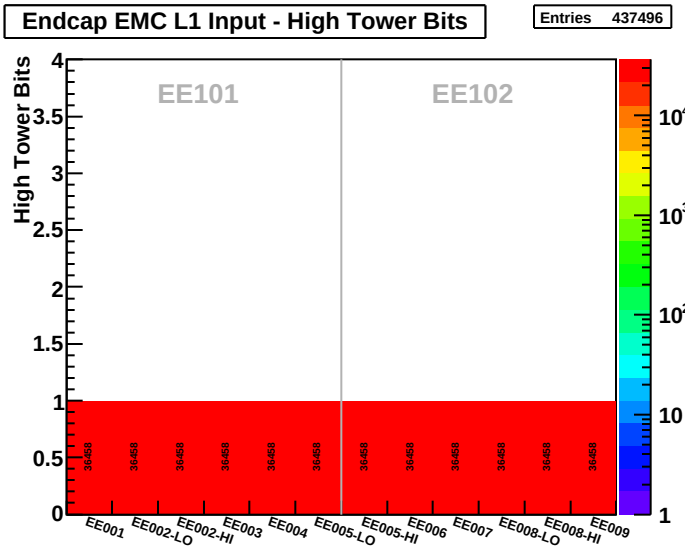

Barrel EMC L1 Input - High Tower Bits
Entries 656239
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

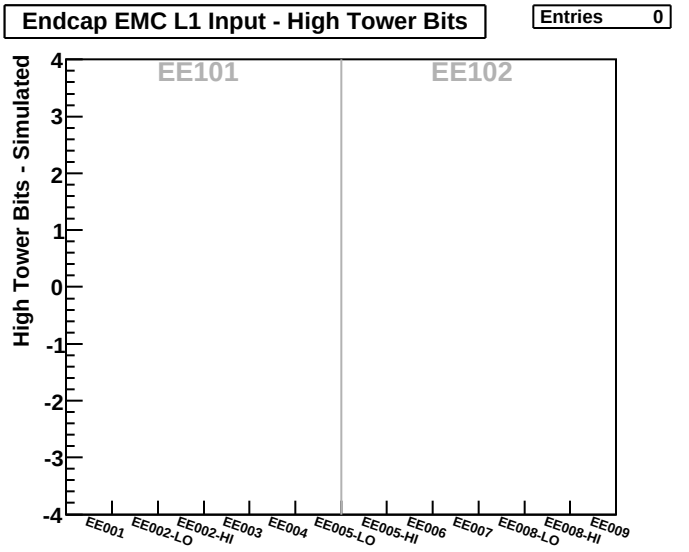

Endcap EMC L1 Input - Low Eta Sum
Entries 437496
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

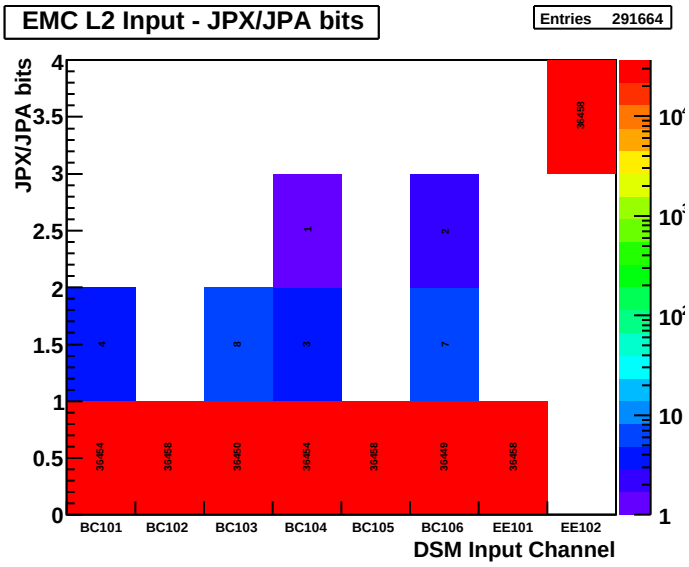

Endcap EMC L1 Input - Low Eta Sum
Entries 36458
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

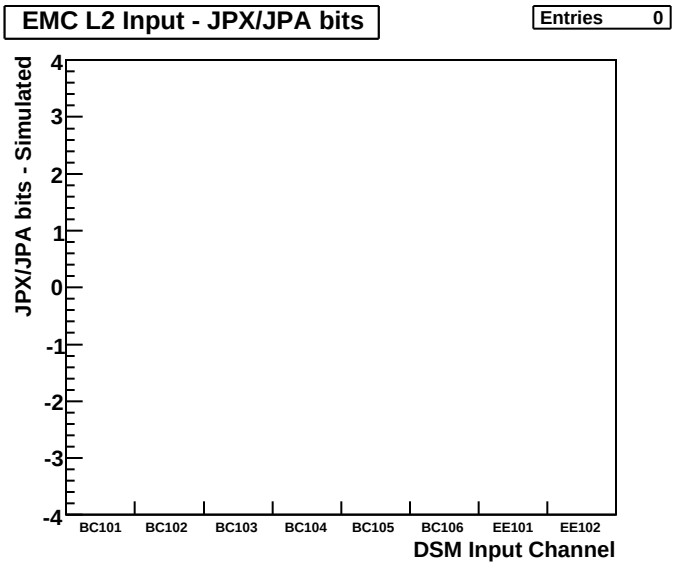

Endcap EMC L1 Input - High Eta Sum
Entries 437496
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

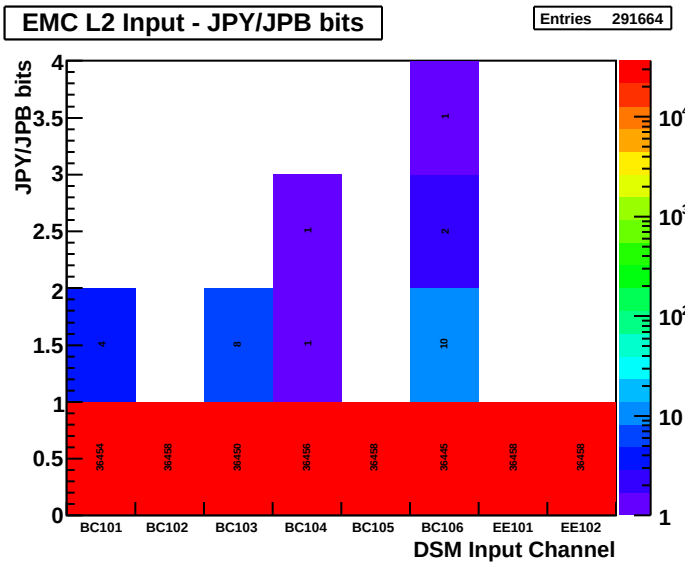

Endcap EMC L1 Input - High Eta Sum
Entries 34850
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

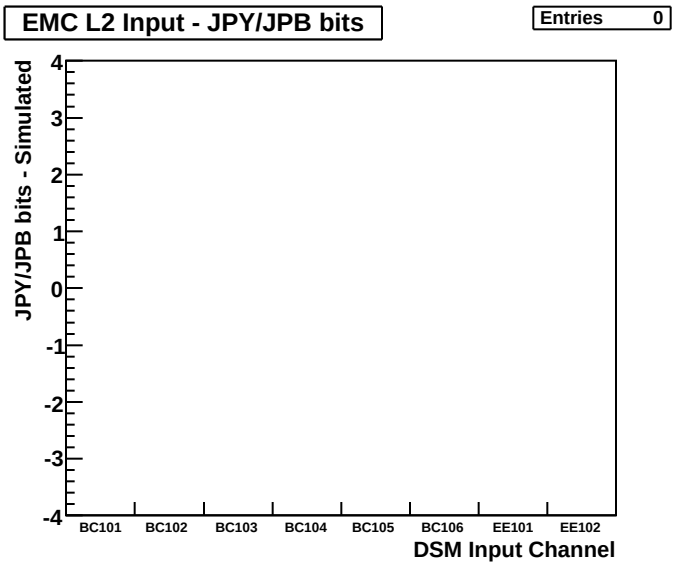

Endcap EMC L1 Input - High Tower Bits
Entries 437496
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

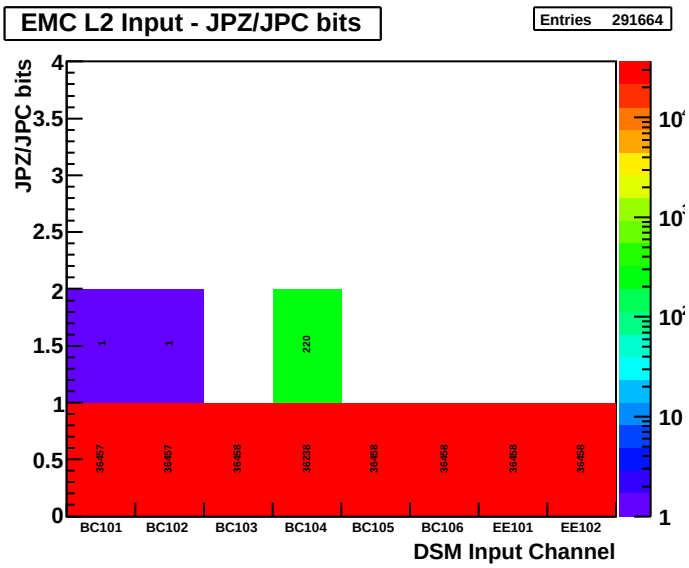

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

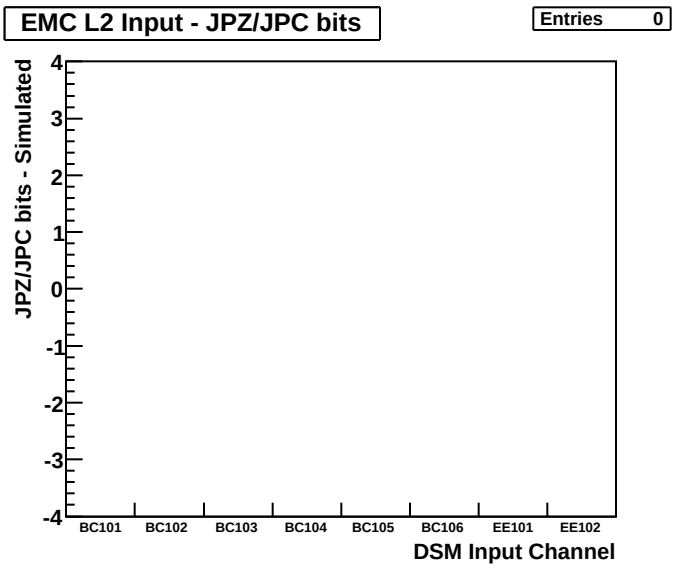

EMC L2 Input - JPX/JPA bits
Entries 291664
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

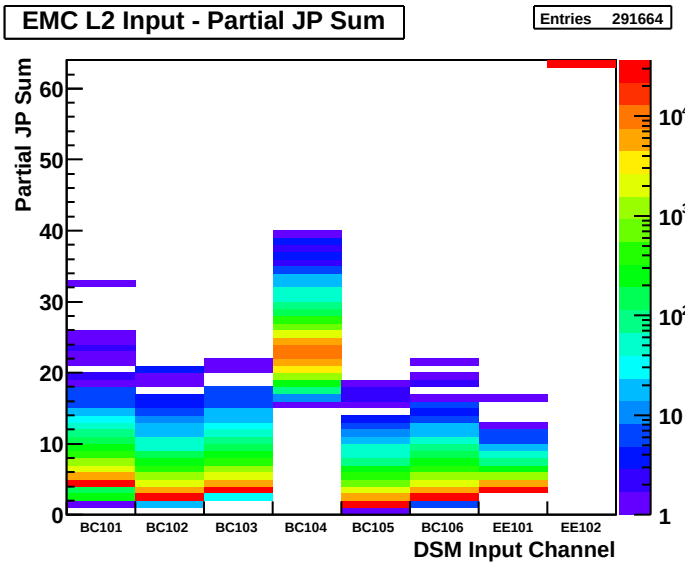

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

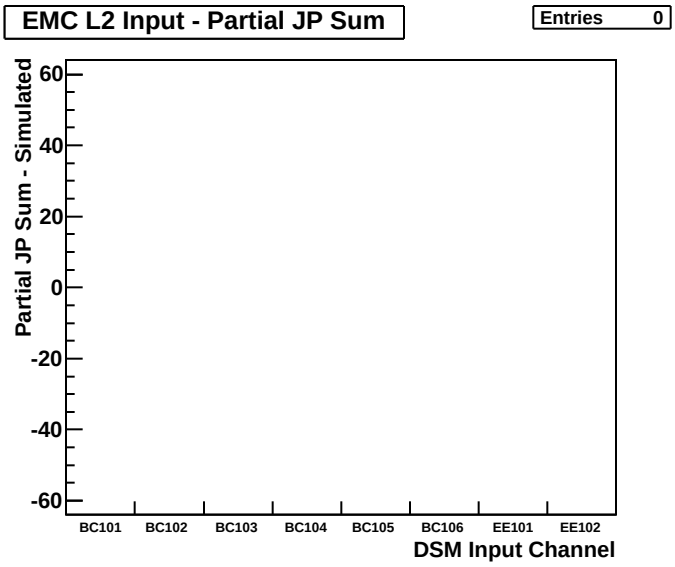

EMC L2 Input - JPY/JPB bits
Entries 291664
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

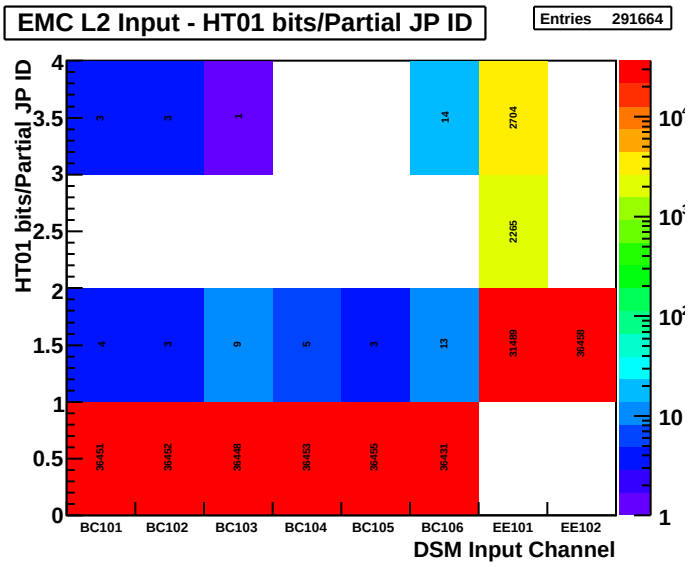

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

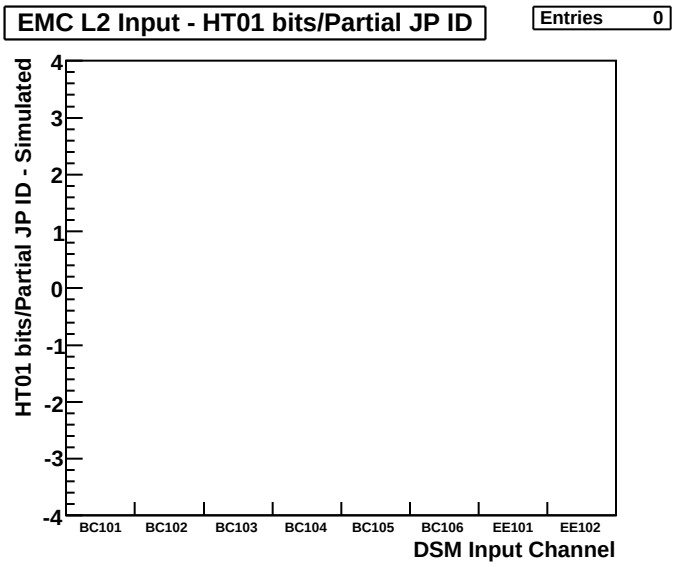

EMC L2 Input - JPZ/JPC bits
Entries 291664
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

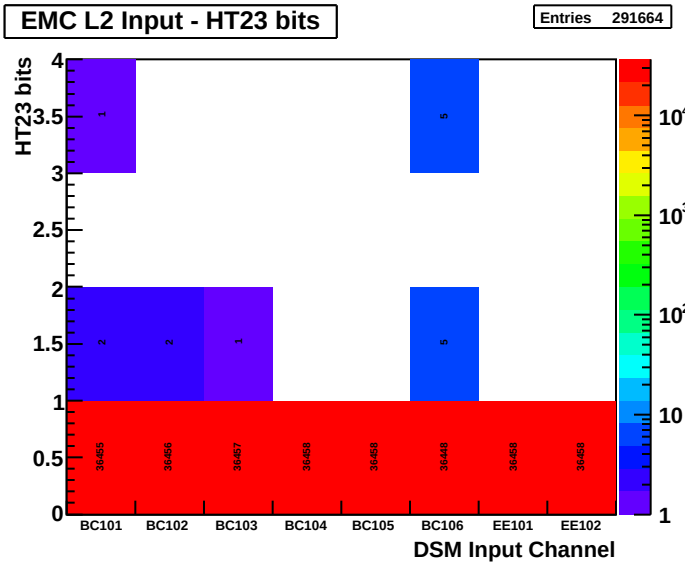

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

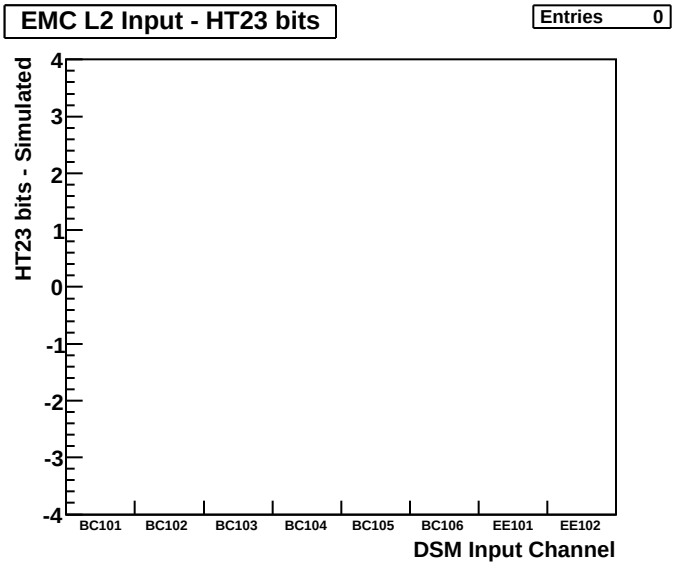

EMC L2 Input - Partial JP Sum
Entries 291664
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

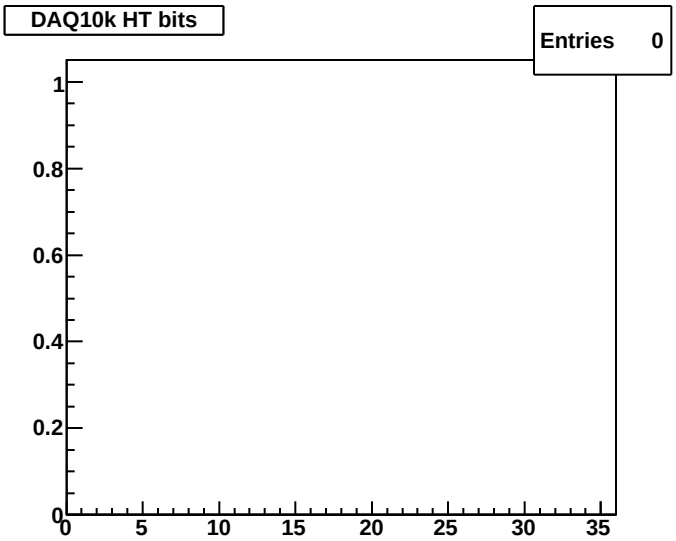

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

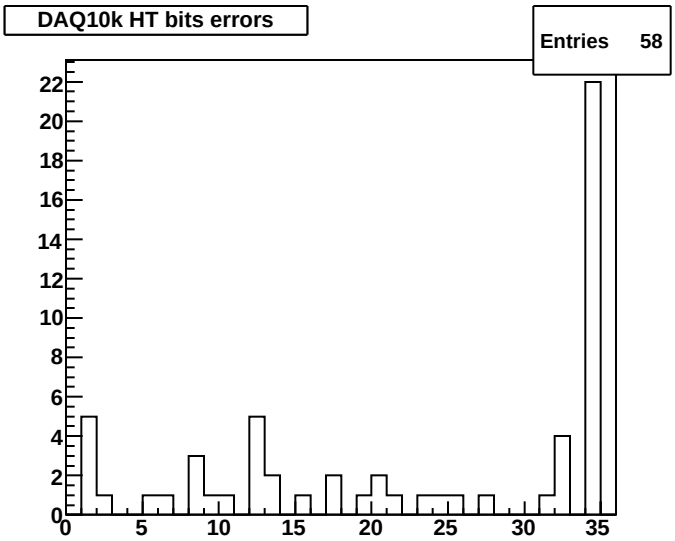

EMC L2 Input - HT01 bits/Partial JP ID
Entries 291664
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

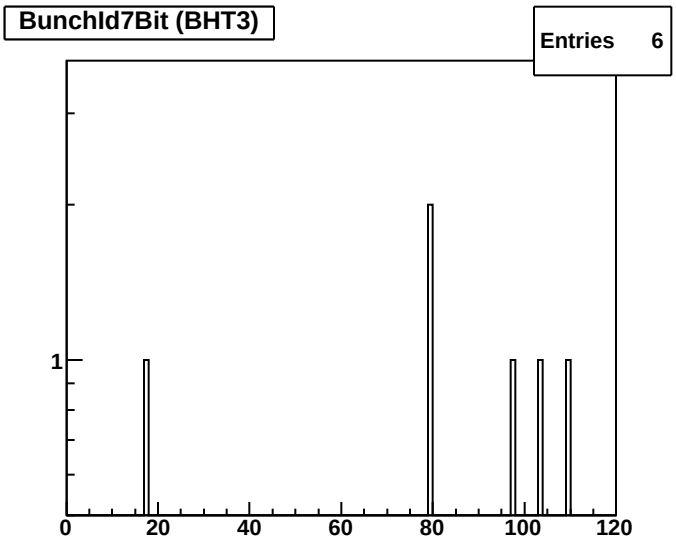

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

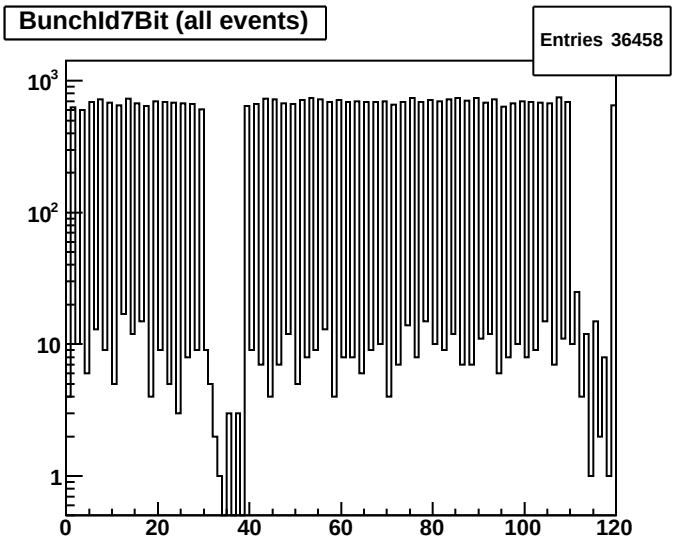

EMC L2 Input - HT23 bits
Entries 291664
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

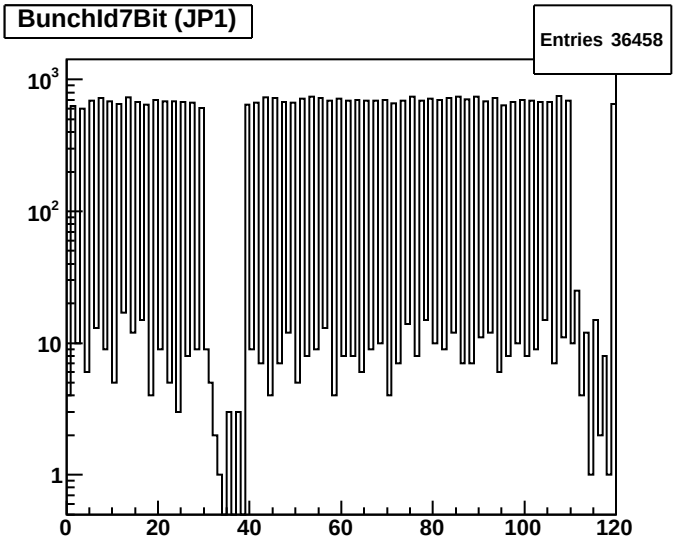

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

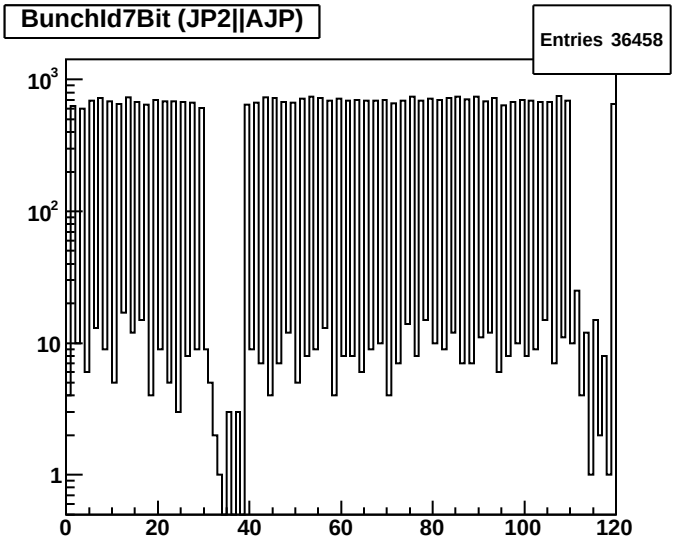

DAQ10k HT bits
Entries 0

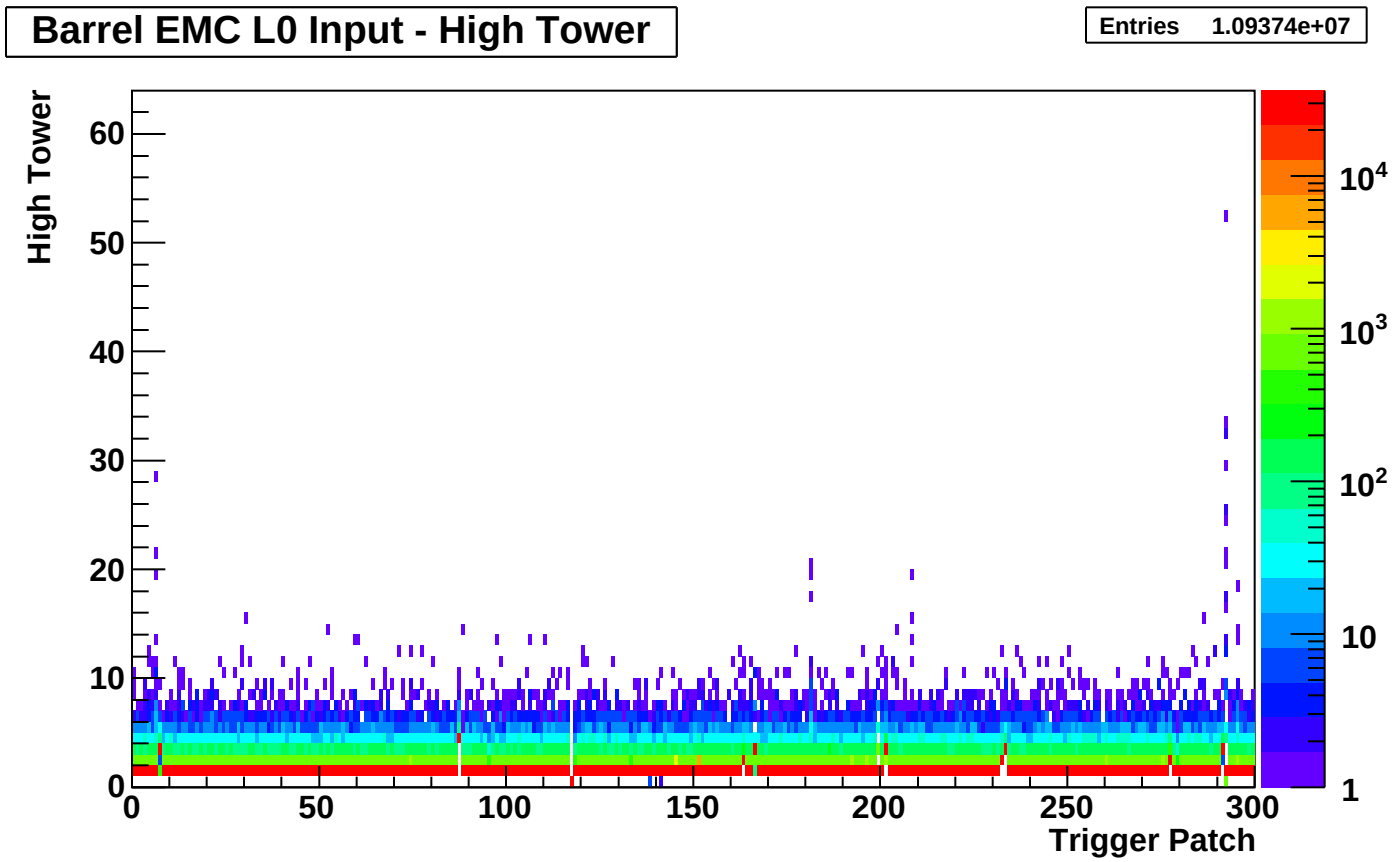

DAQ10k HT bits errors
Entries 58

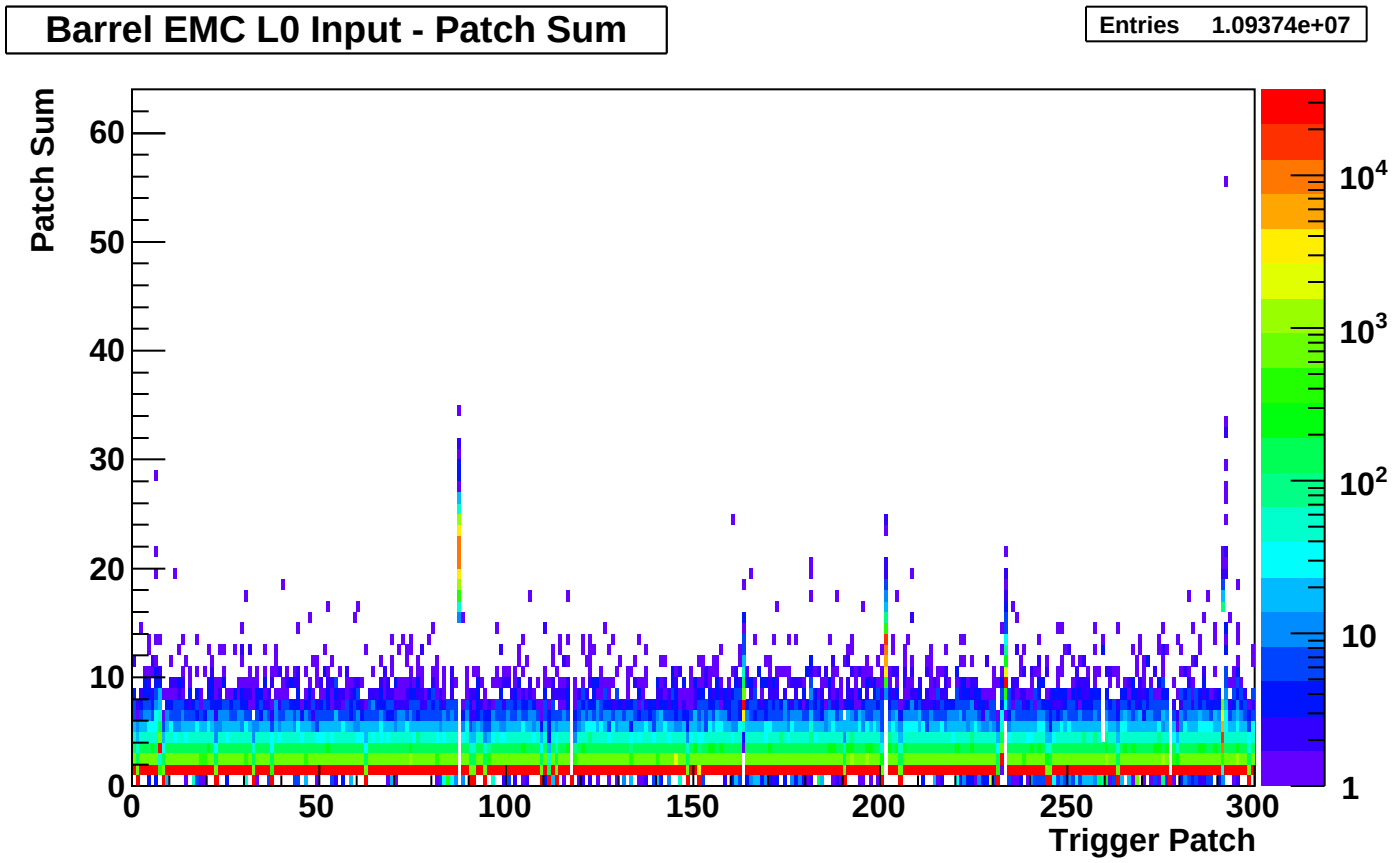

BunchId7Bit (BHT3)
Entries 6

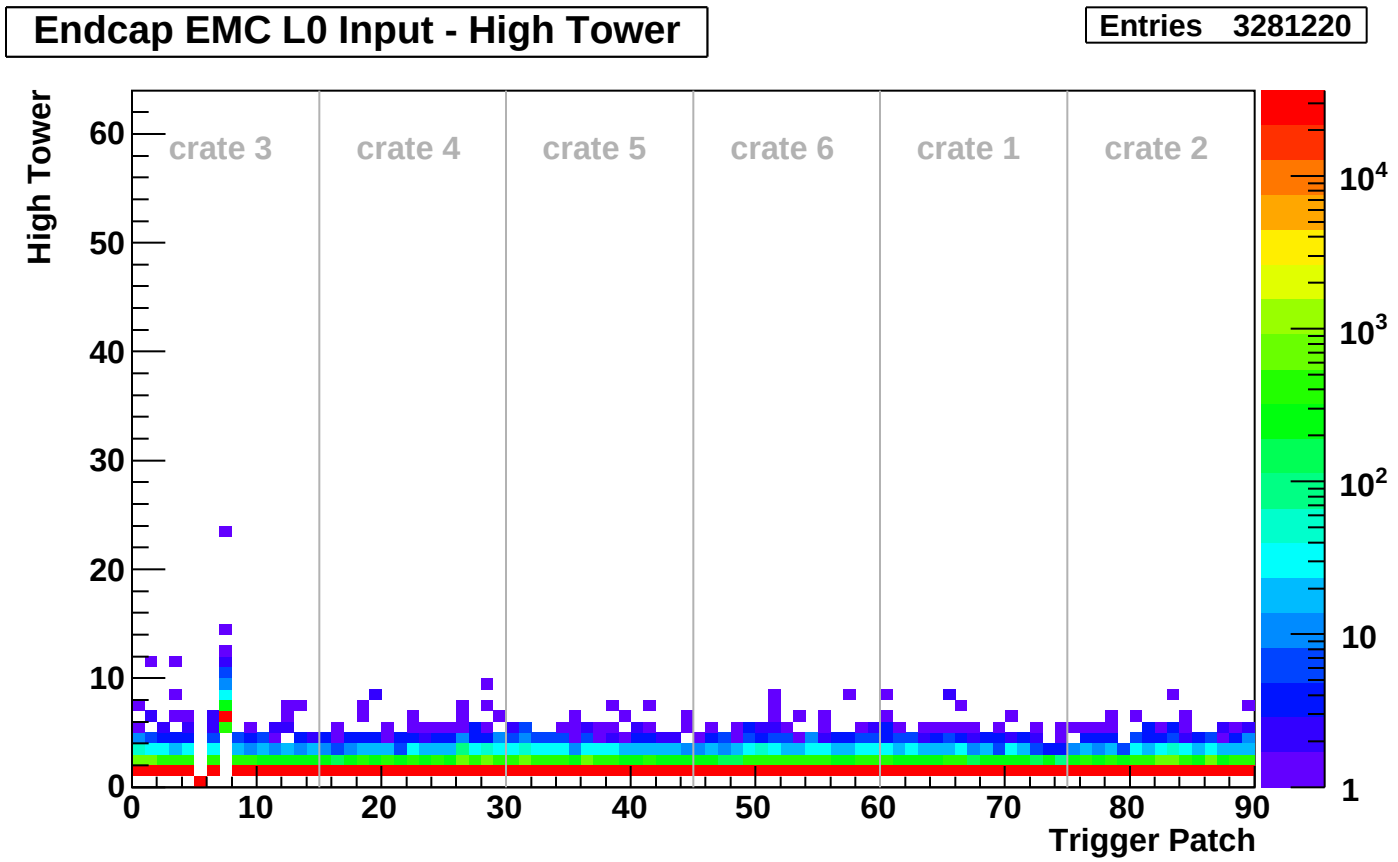

BunchId7Bit (all events)
Entries 36458

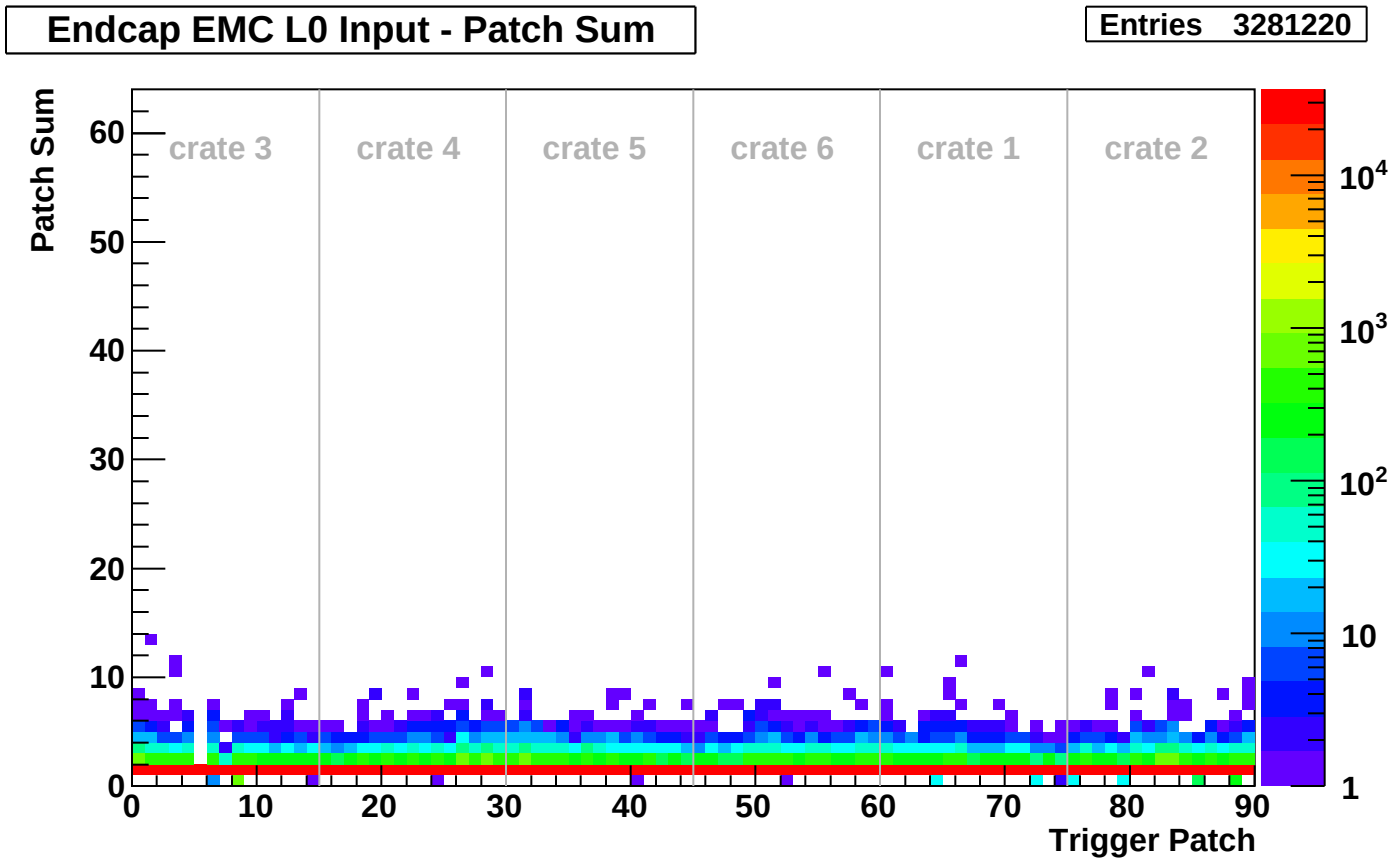

BunchId7Bit (JP1)
Entries 36458

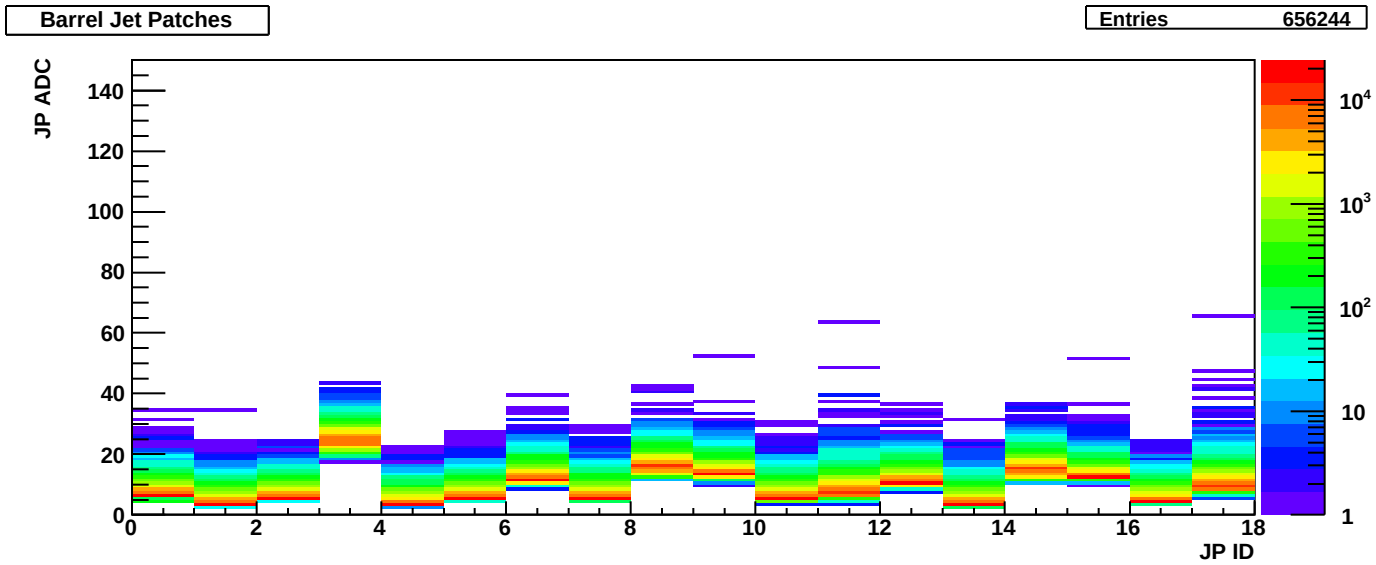

BunchId7Bit (JP2||AJP)
Entries 36458

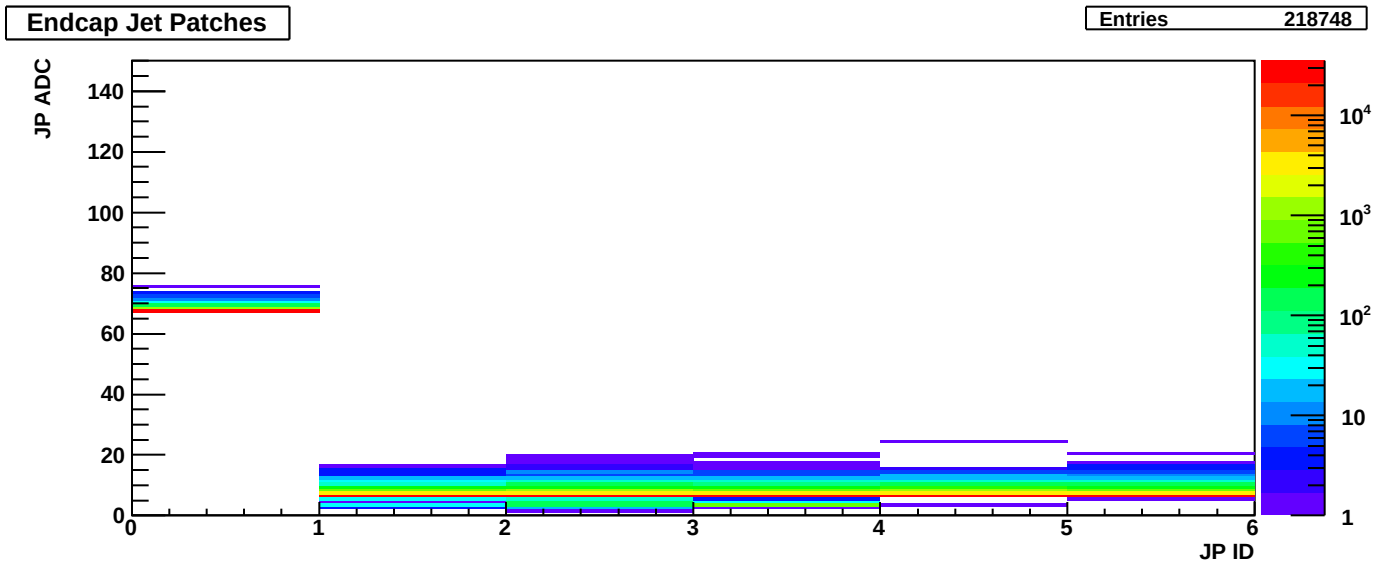

Barrel EMC L0 Input - High Tower
Entries 1.09374e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

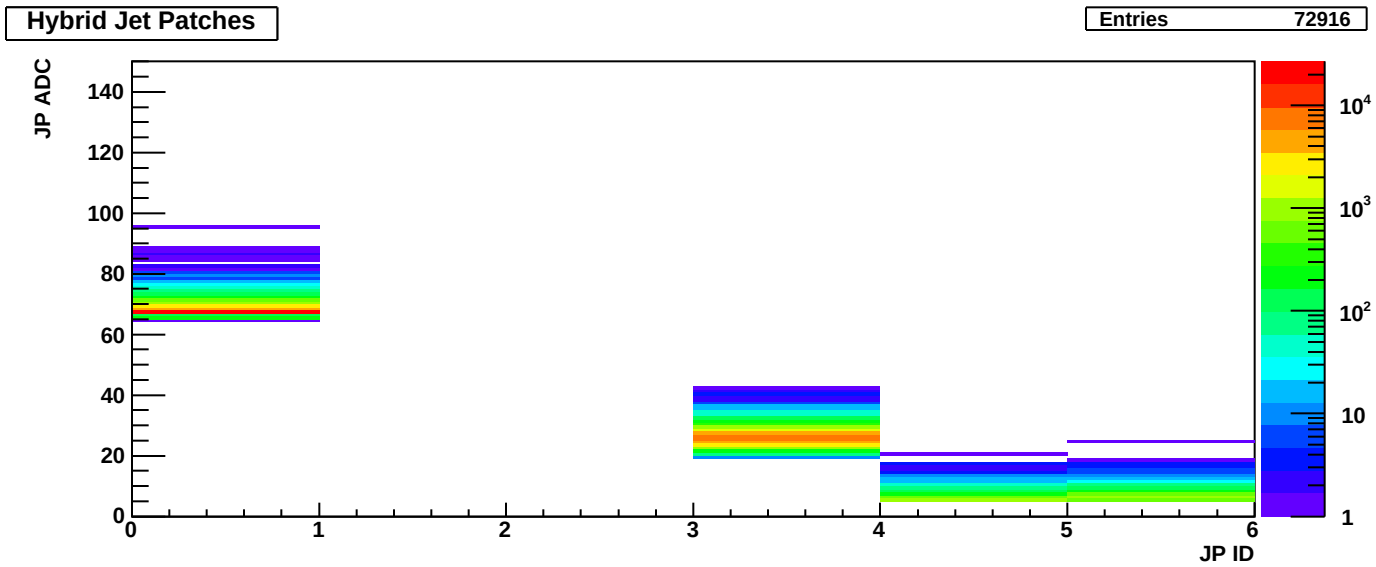

Barrel EMC L0 Input - Patch Sum
Entries 1.09374e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

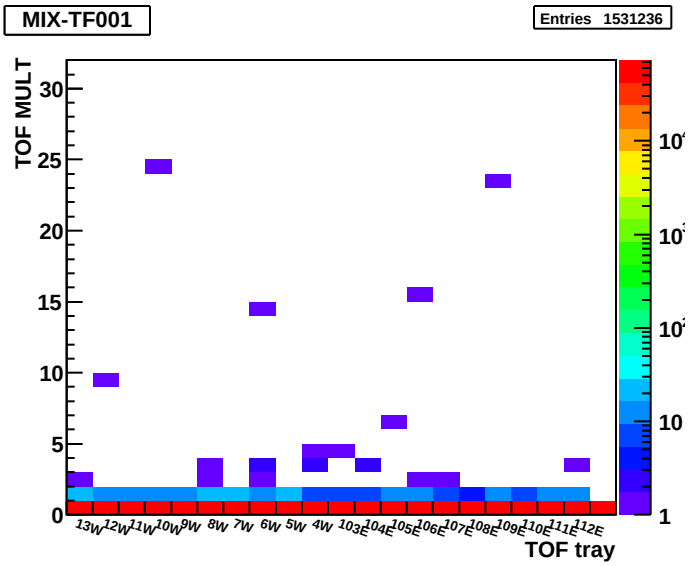

Endcap EMC L0 Input - High Tower
Entries 3281220
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

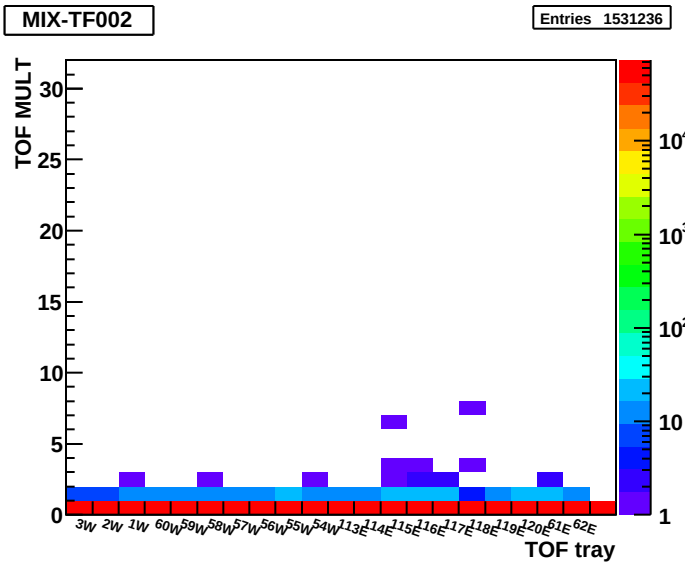

Endcap EMC L0 Input - Patch Sum
Entries 3281220
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

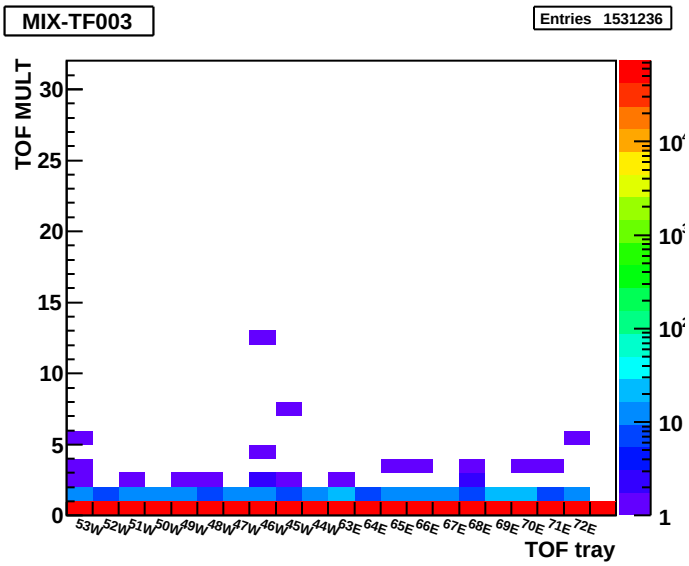

Barrel Jet Patches
Entries 656244
JP ADC
JP ID

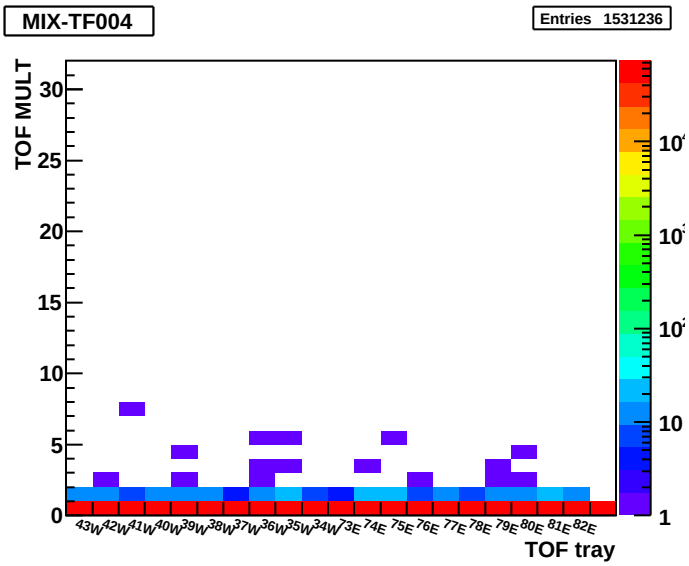

Endcap Jet Patches
Entries 218748
JP ADC
JP ID

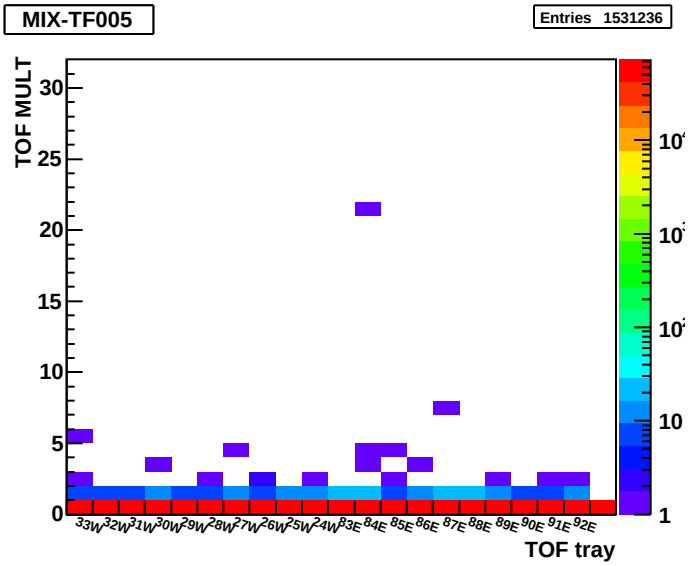

Hybrid Jet Patches
Entries 72916
JP ADC
JP ID

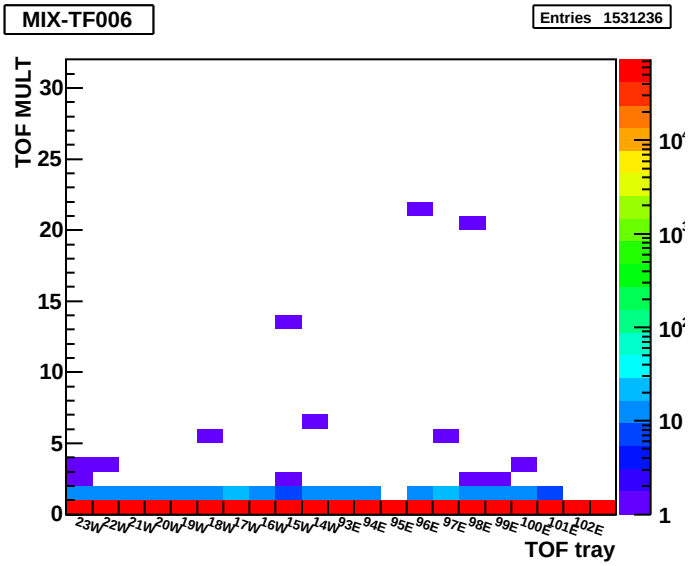

MIX-TF001
Entries 1531236
TOF MULT
TOF tray

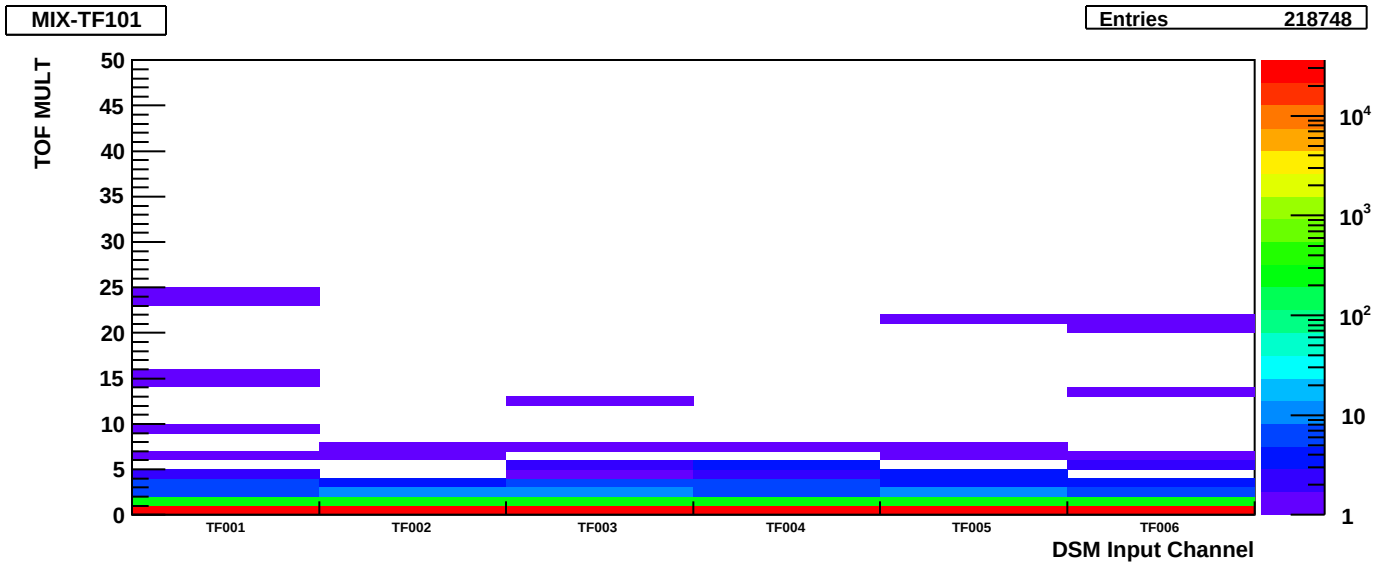

MIX-TF002
Entries 1531236
TOF MULT
TOF tray

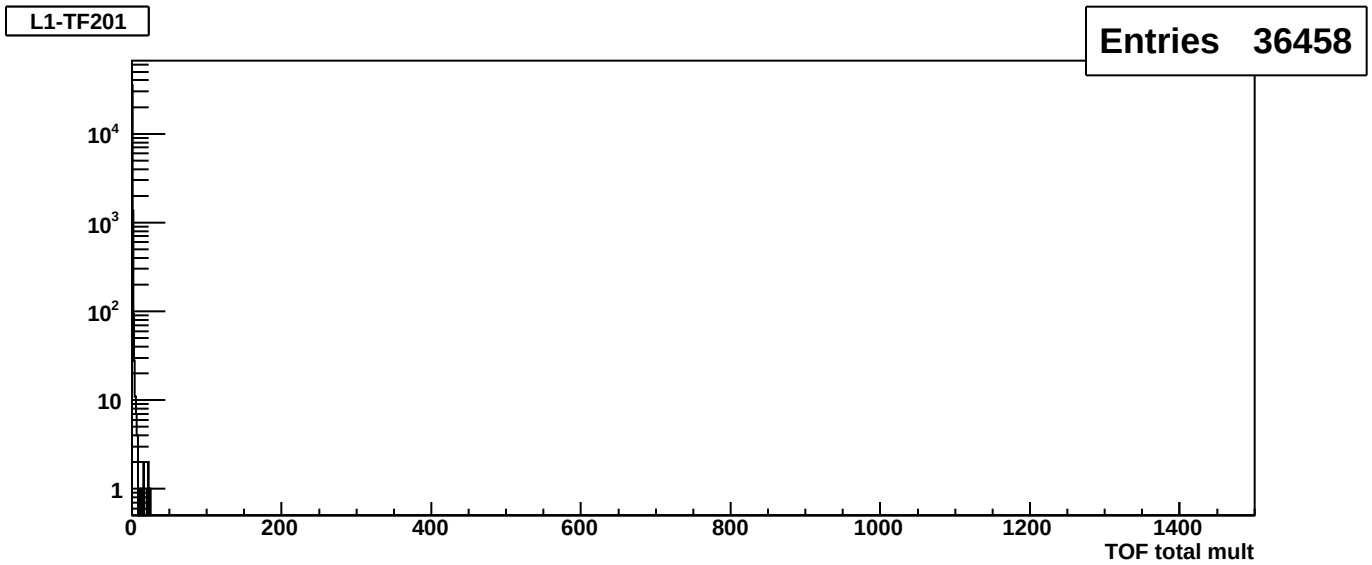

MIX-TF003
Entries 1531236
TOF MULT
TOF tray

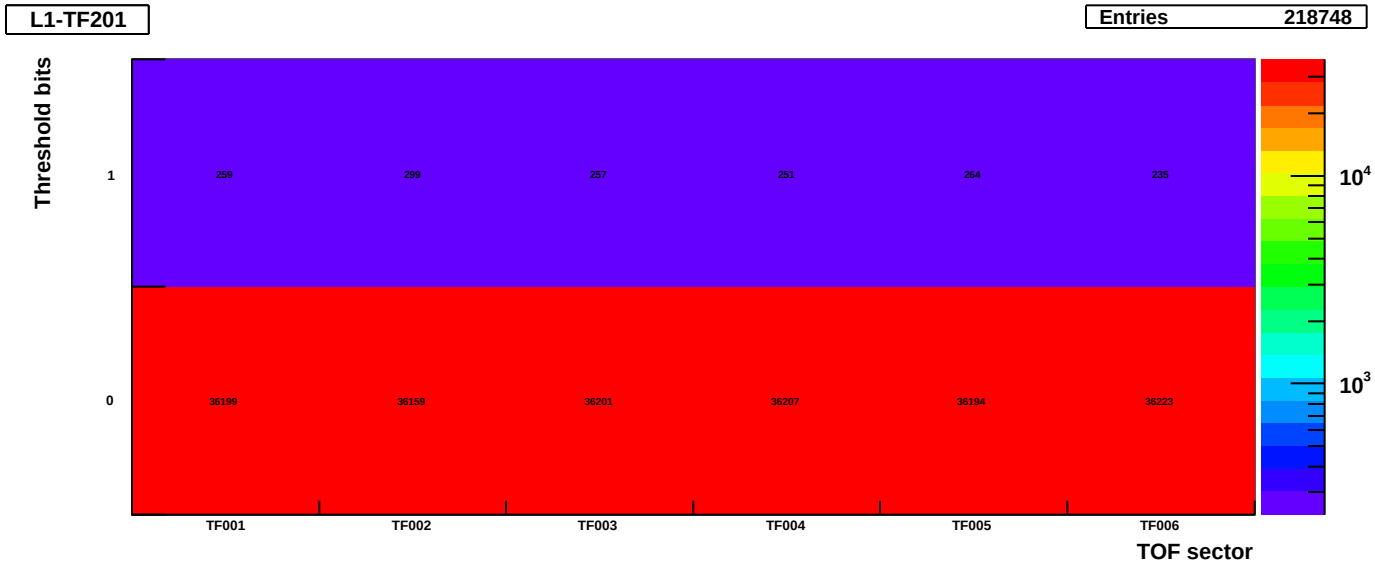

MIX-TF004
Entries 1531236
TOF MULT
TOF tray

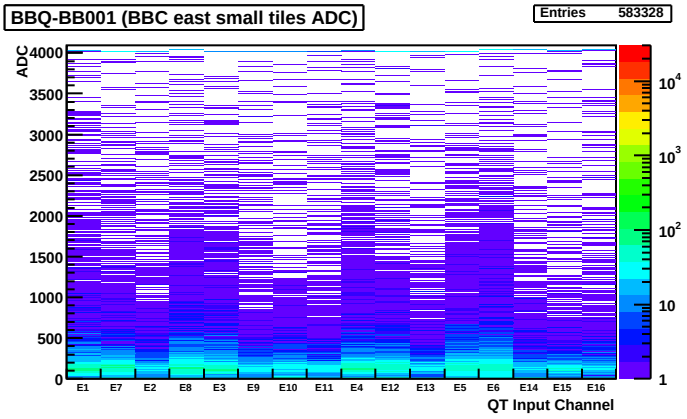

MIX-TF005
Entries 1531236
TOF MULT
TOF tray

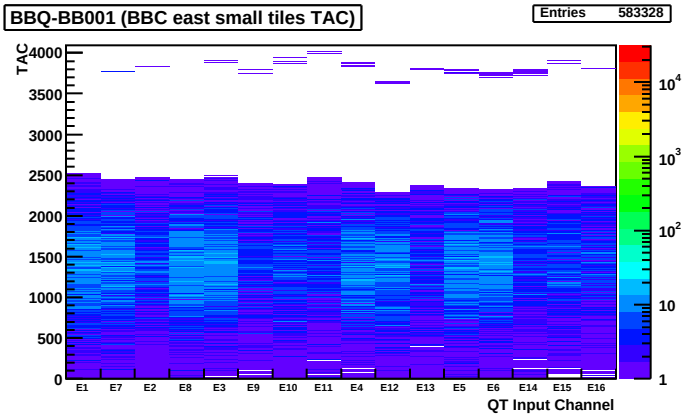

MIX-TF006
Entries 1531236
TOF MULT
TOF tray

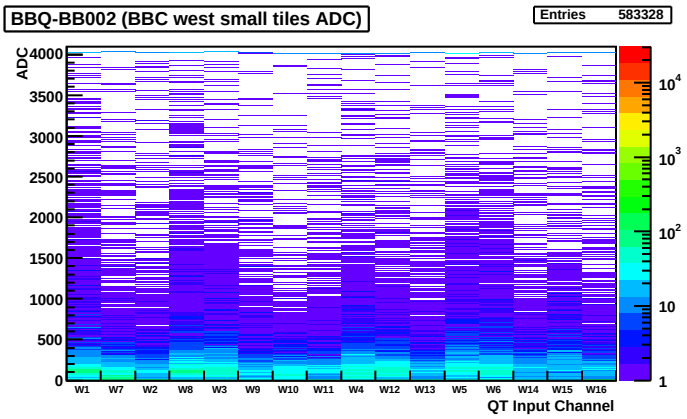

MIX-TF101
Entries 218748
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

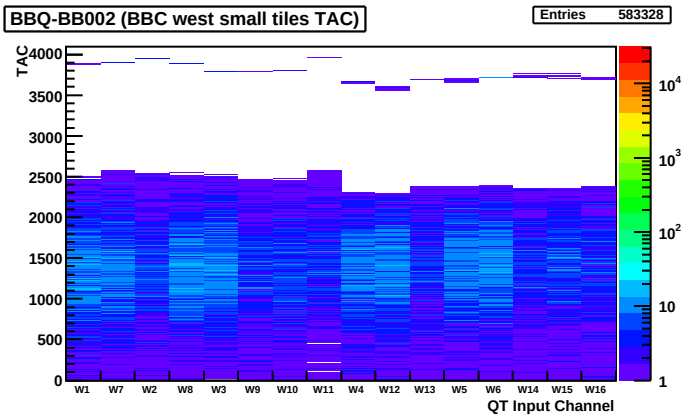

L1-TF201
Entries 36458
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

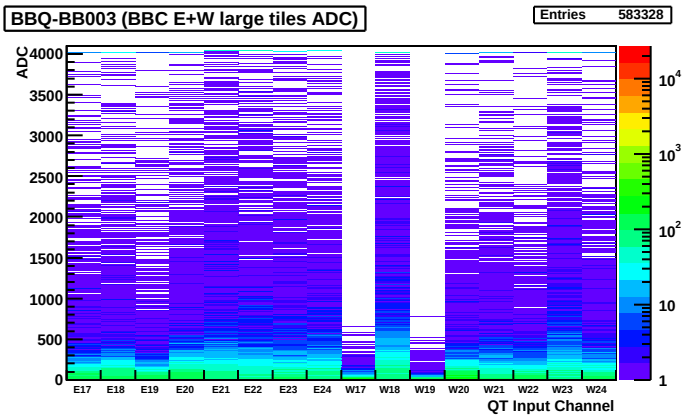

L1-TF201
Entries 218748
Threshold bits
1
0
259
299
257
251
264
235
36199
36159
36201
36207
36194
36223
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3

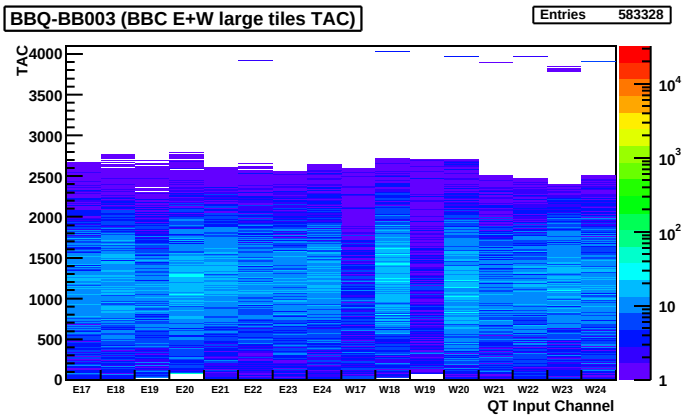

BBQ-BB001 (BBC east small tiles ADC)
Entries 583328
ADC
QT Input Channel

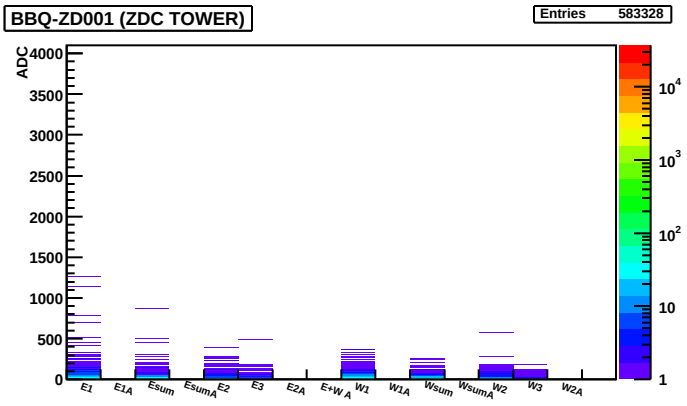

BBQ-BB001 (BBC east small tiles TAC)
Entries 583328
TAC
QT Input Channel

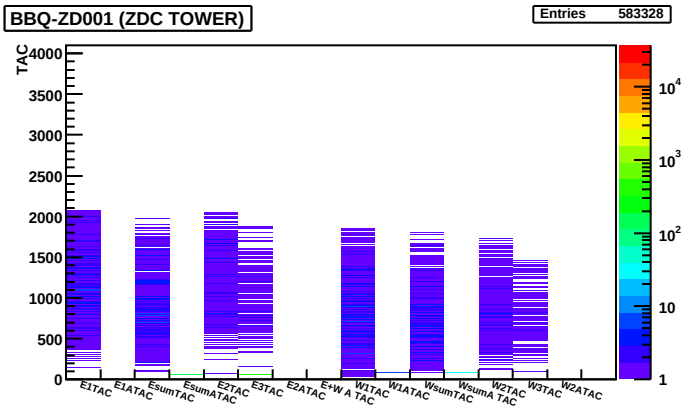

BBQ-BB002 (BBC west small tiles ADC)
Entries 583328
ADC
QT Input Channel

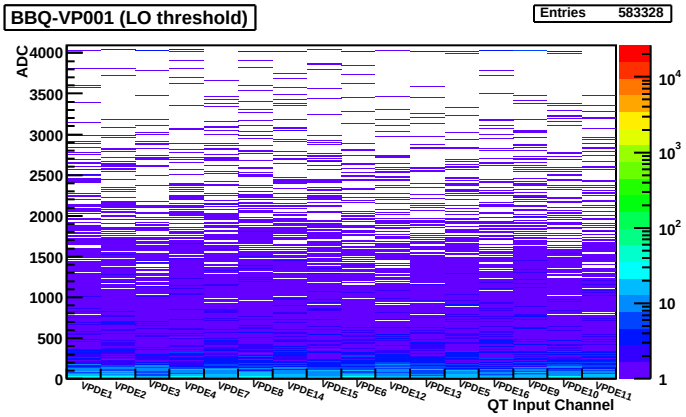

BBQ-BB002 (BBC west small tiles TAC)
Entries 583328
TAC
QT Input Channel

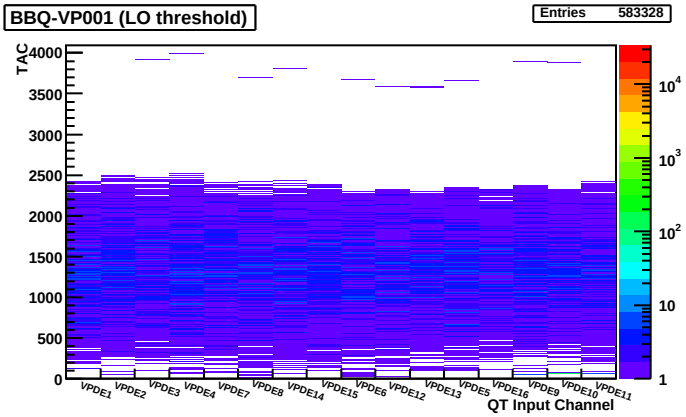

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 583328
ADC
QT Input Channel

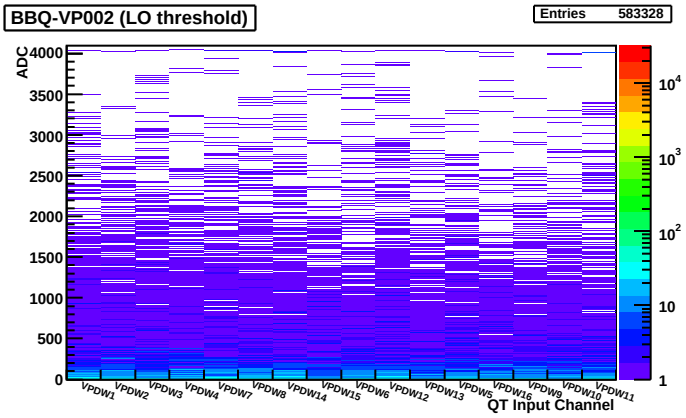

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 583328
TAC
QT Input Channel

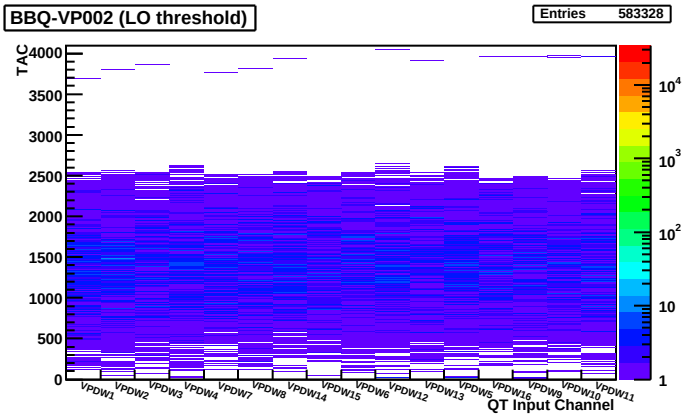

BBQ-ZD001 (ZDC TOWER)
Entries 583328
ADC

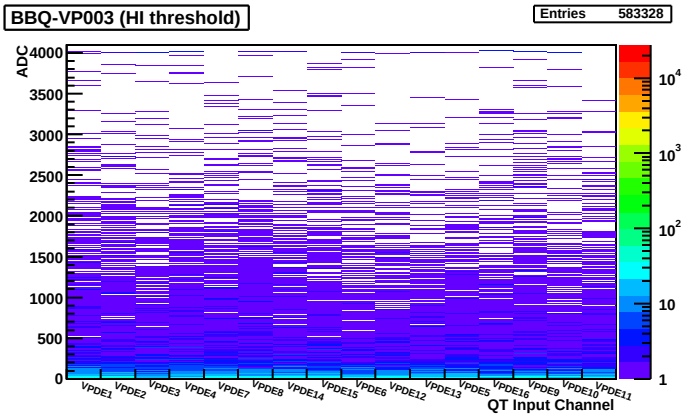

BBQ-ZD001 (ZDC TOWER)
Entries 583328
TAC

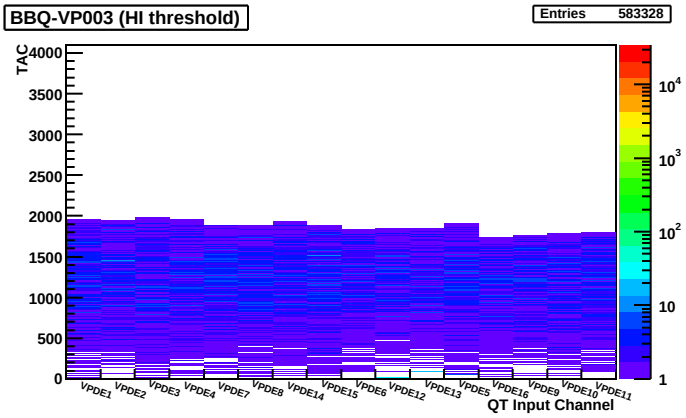

BBQ-VP001 (LO threshold)
Entries 583328
ADC
QT Input Channel

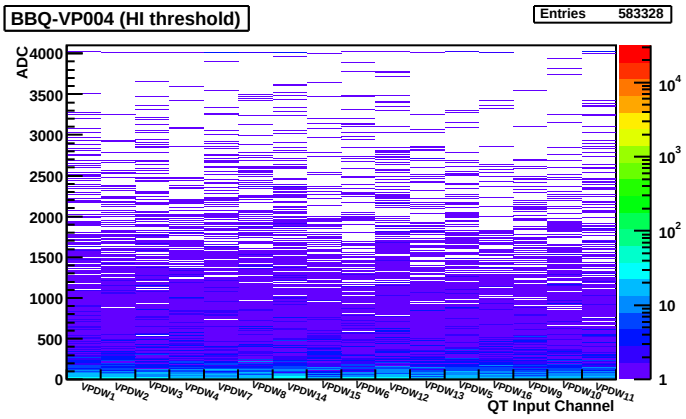

BBQ-VP001 (LO threshold)
Entries 583328
TAC
QT Input Channel

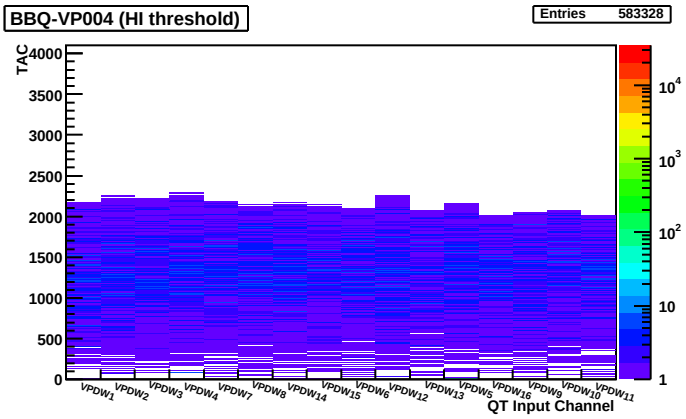

BBQ-VP002 (LO threshold)
Entries 583328
ADC
QT Input Channel

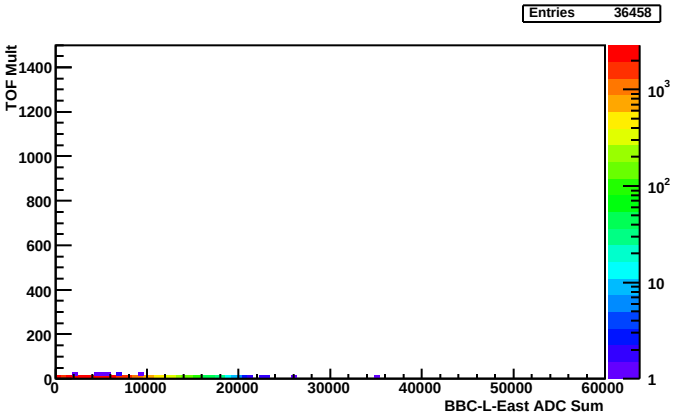

BBQ-VP002 (LO threshold)
Entries 583328
TAC
QT Input Channel

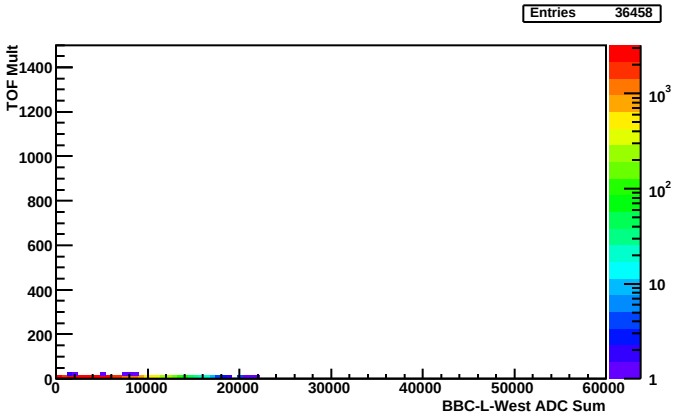

BBQ-VP003 (HI threshold)
Entries 583328
ADC
QT Input Channel

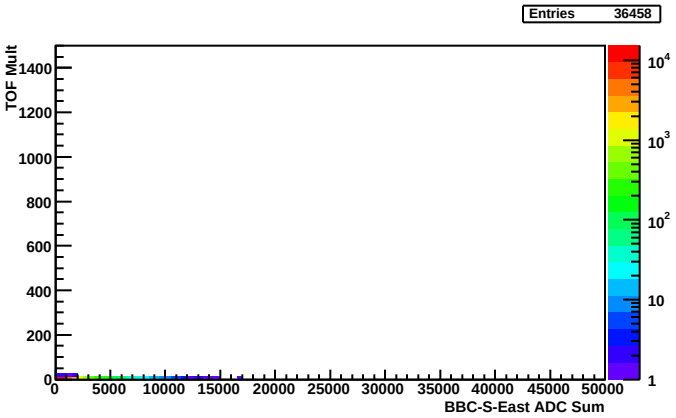

BBQ-VP003 (HI threshold)
Entries 583328
TAC
QT Input Channel

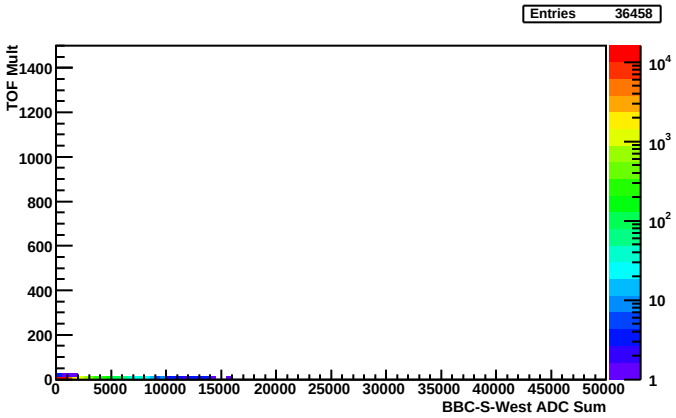

BBQ-VP004 (HI threshold)
Entries 583328
ADC
QT Input Channel

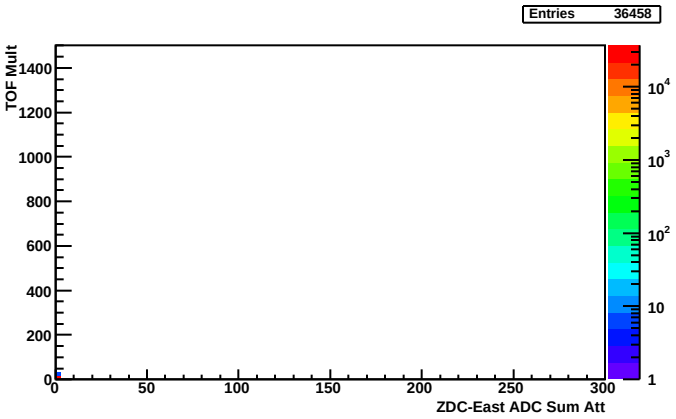

BBQ-VP004 (HI threshold)
Entries 583328
TAC
QT Input Channel

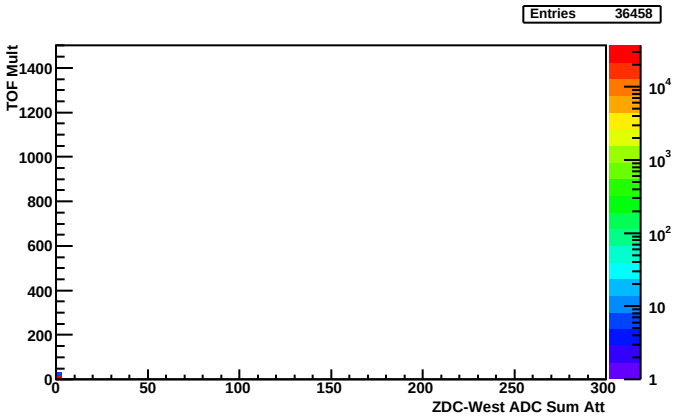

Entries 36458
TOF Mult
BBC-L-East ADC Sum

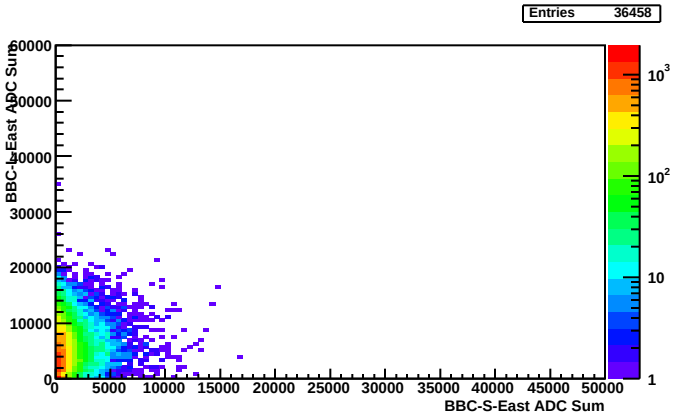

Entries 36458
TOF Mult
BBC-L-West ADC Sum

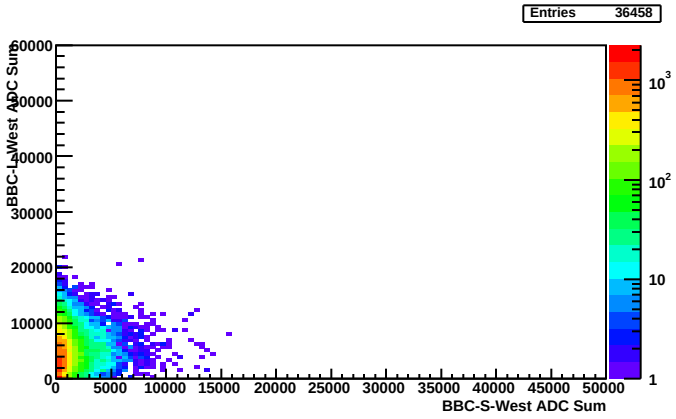

Entries 36458
TOF Mult
BBC-S-East ADC Sum

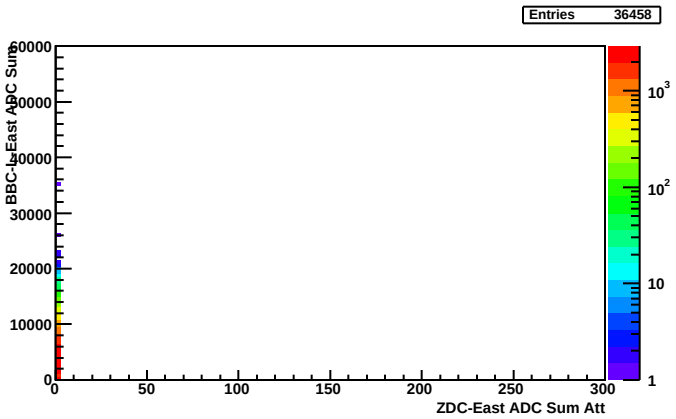

Entries 36458
TOF Mult
BBC-S-West ADC Sum

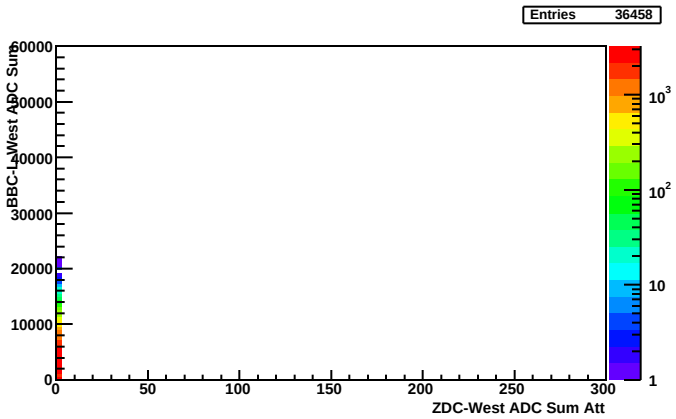

Entries 36458
TOF Mult
ZDC-East ADC Sum Att

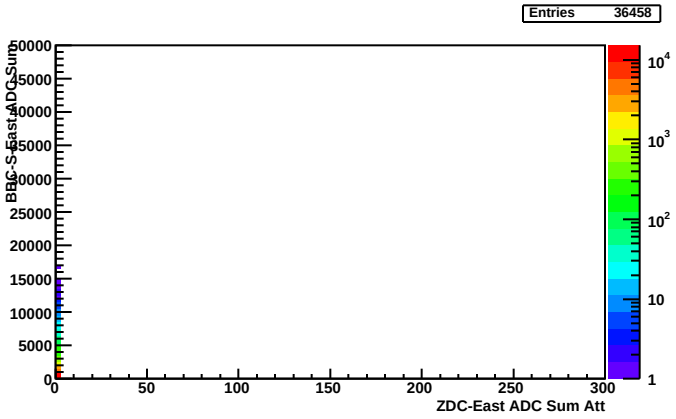

Entries 36458
TOF Mult
ZDC-West ADC Sum Att

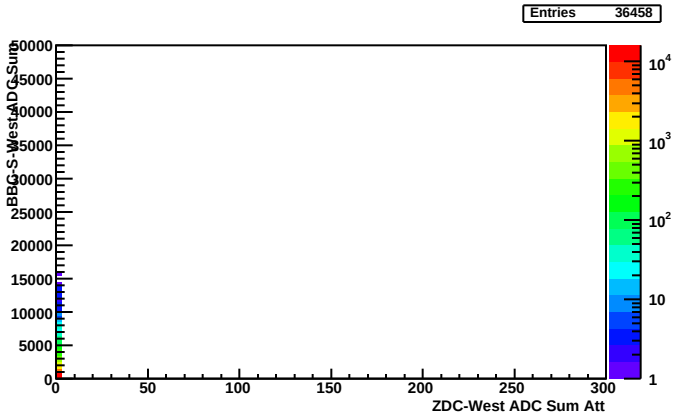

Entries 36458
BBC-L-East ADC Sum
BBC-S-East ADC Sum

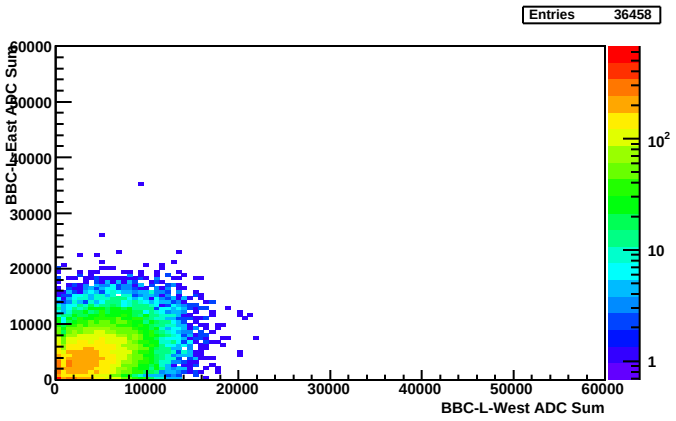

Entries 36458
BBC-L-West ADC Sum
BBC-S-West ADC Sum

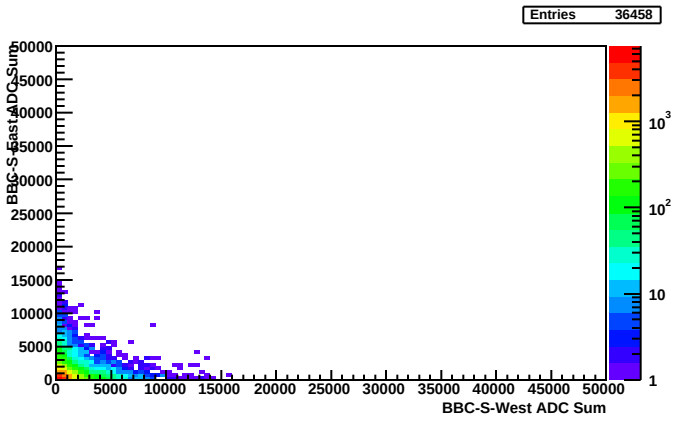

Entries 36458
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

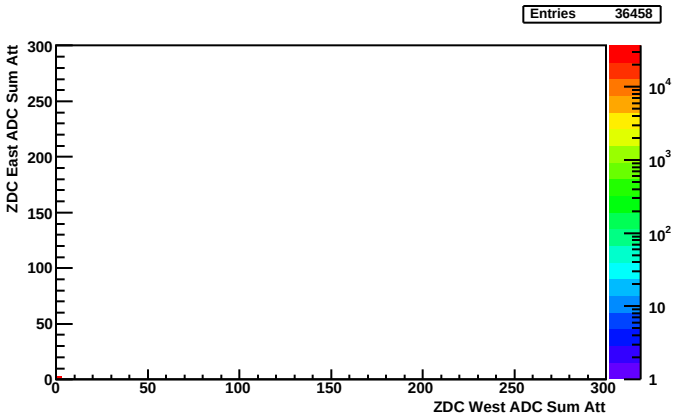

Entries 36458
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

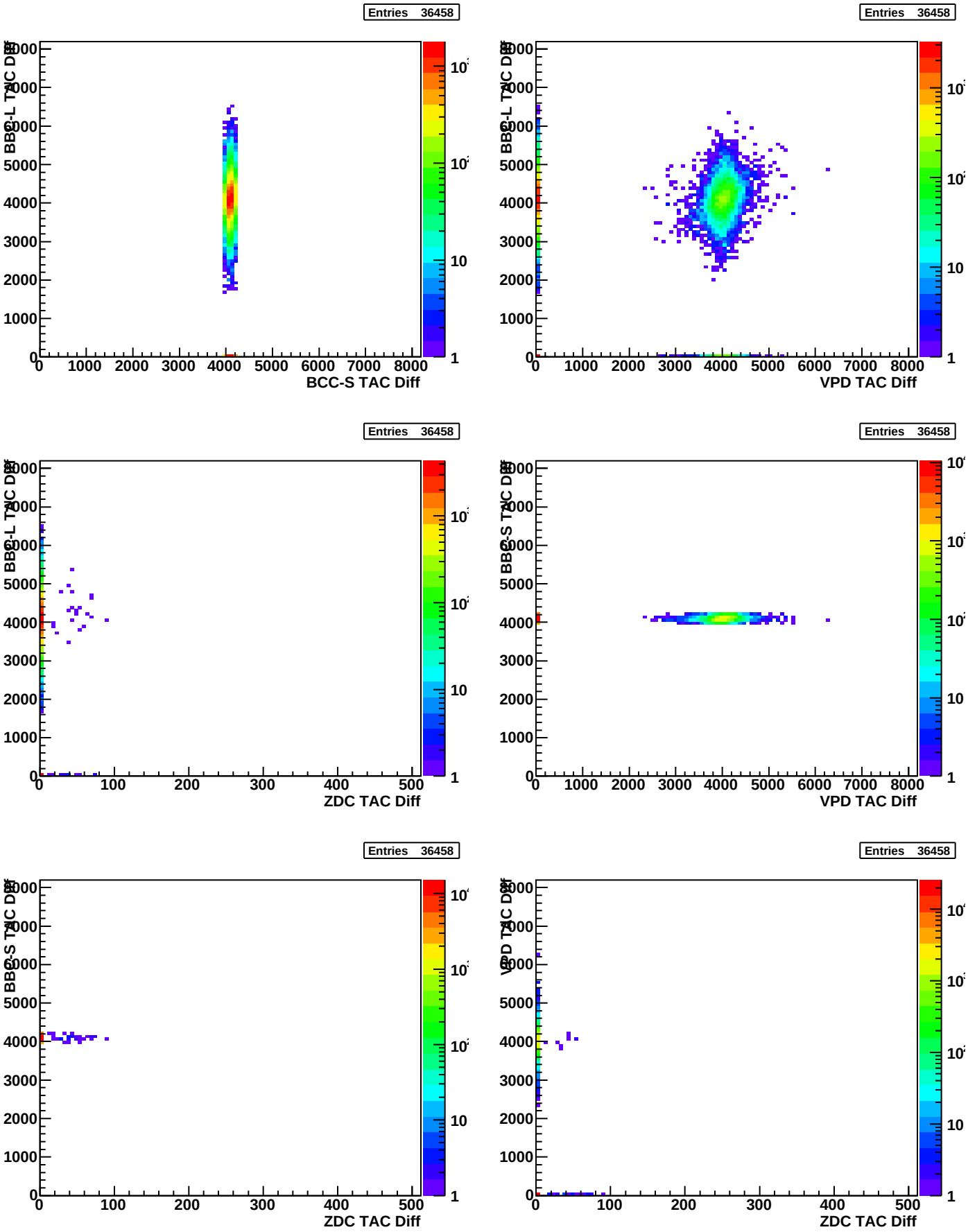

Entries 36458
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

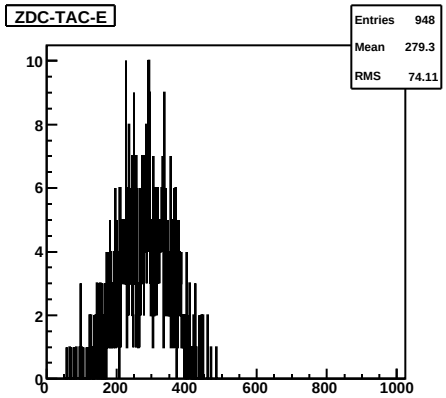

Entries 36458
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

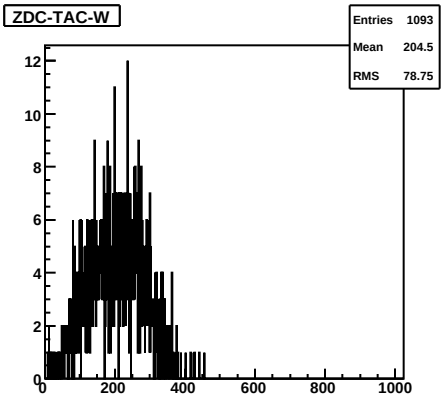

Entries 36458
BBC-L-East ADC Sum
BBC-L-West ADC Sum

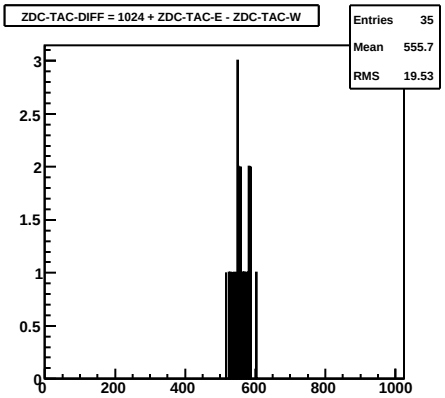

Entries 36458
BBC-S-East ADC Sum
BBC-S-West ADC Sum

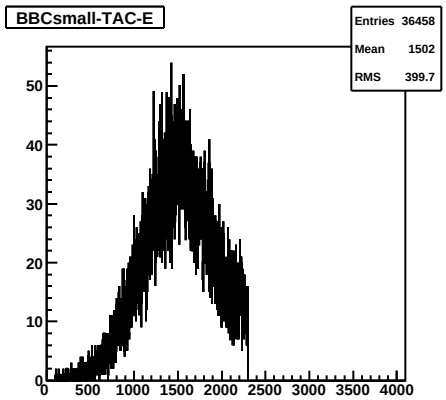

Entries 36458
ZDC East ADC Sum Att
ZDC West ADC Sum Att

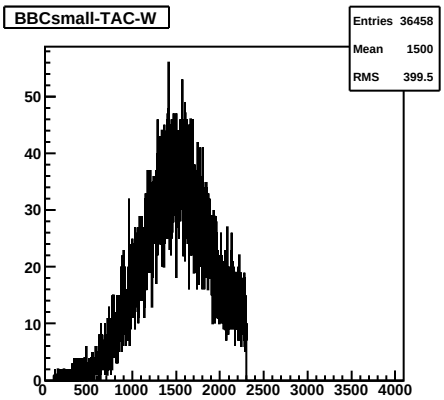

Entries 36458
BBC-L TAC Diff
BCC-S TAC Diff
Entries 36458
BBC-L TAC Diff
VPD TAC Diff
Entries 36458
BBC-L TAC Diff
ZDC TAC Diff
Entries 36458
BBC-S TAC Diff
VPD TAC Diff
Entries 36458
BBC-S TAC Diff
ZDC TAC Diff
Entries 36458
VPD TAC Diff
ZDC TAC Diff

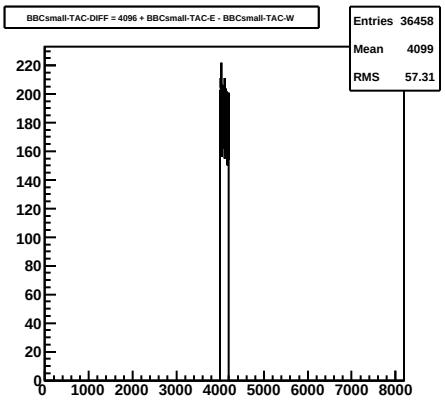

ZDC-TAC-E
Entries 948
Mean 279.3
RMS 74.11

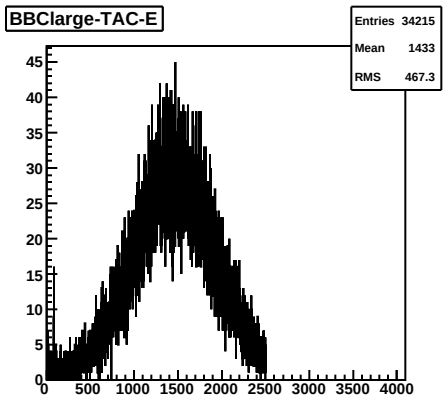

ZDC-TAC-W
Entries 1093
Mean 204.5
RMS 78.75

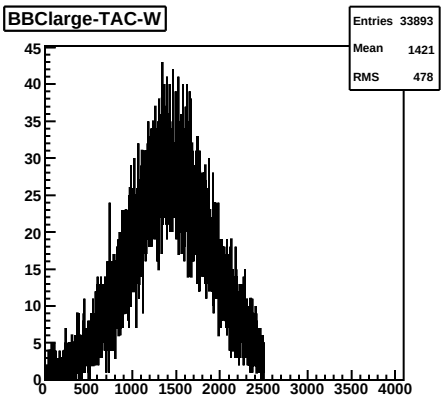

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 35
Mean 555.7
RMS 19.53

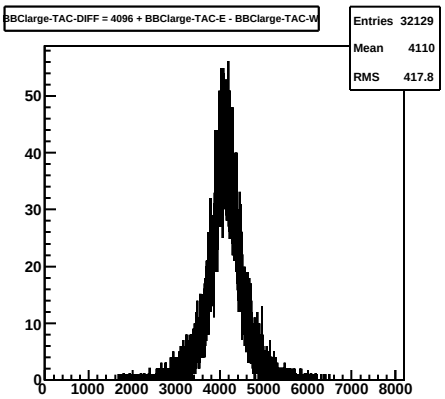

BBCsmall-TAC-E
Entries 36458
Mean 1502
RMS 399.7

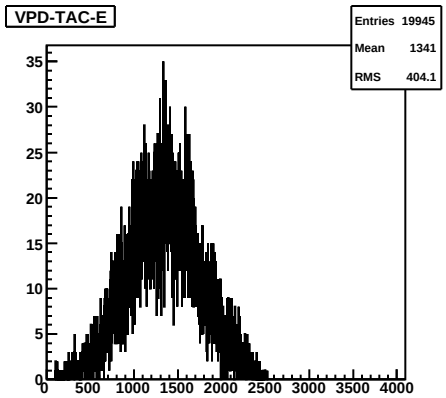

BBCsmall-TAC-W
Entries 36458
Mean 1500
RMS 399.5

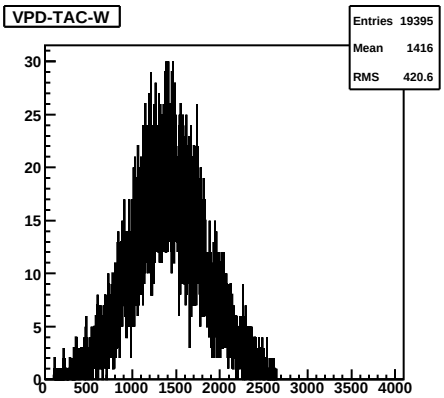

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 36458
Mean 4099
RMS 57.31

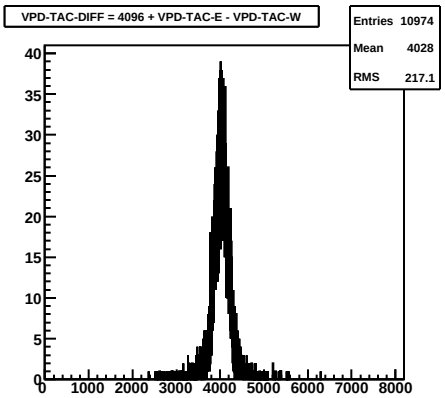

BBClarge-TAC-E
Entries 34215
Mean 1433
RMS 467.3

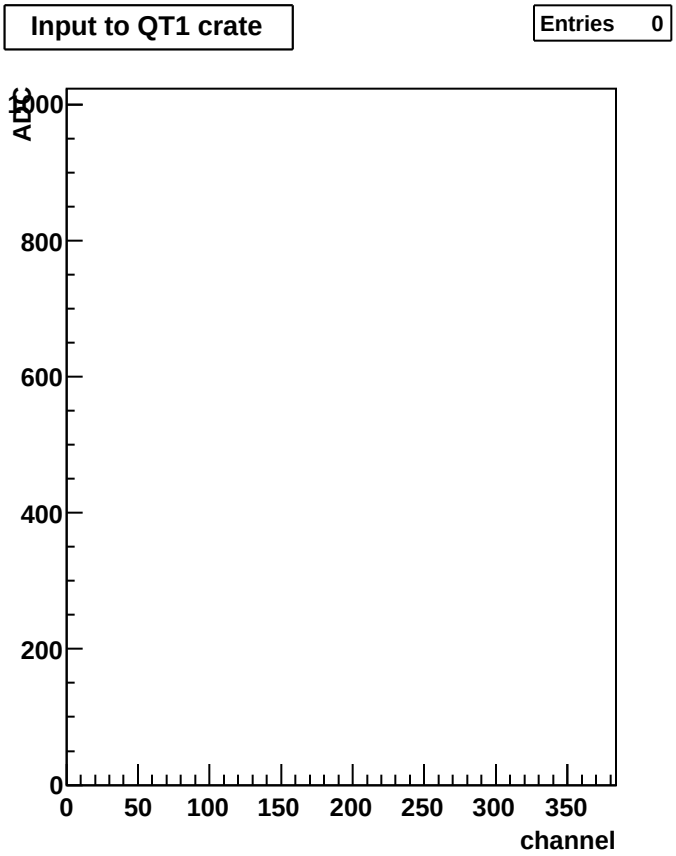

BBClarge-TAC-W
Entries 33893
Mean 1421
RMS 478

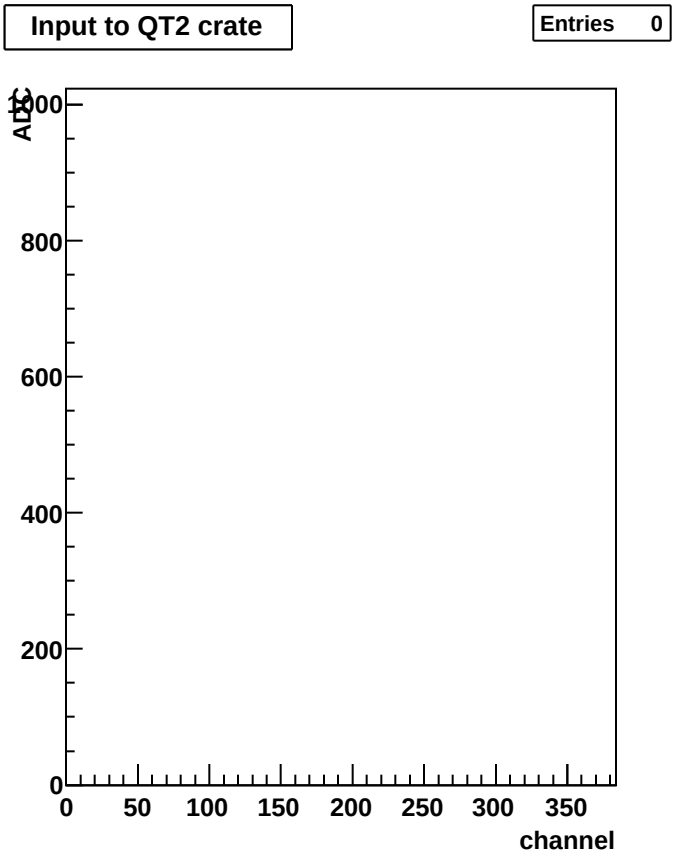

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 32129
Mean 4110
RMS 417.8

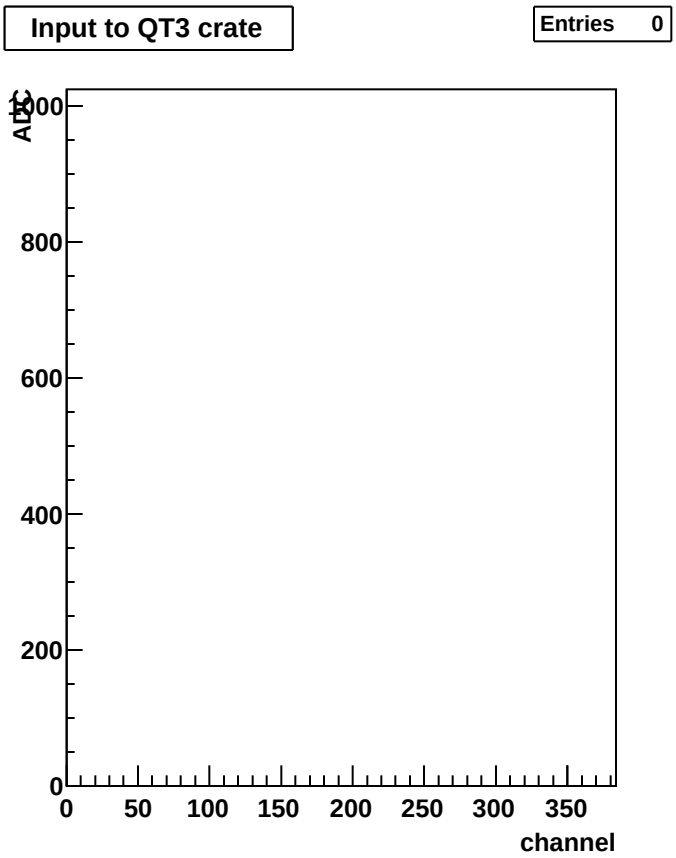

VPD-TAC-E
Entries 19945
Mean 1341
RMS 404.1

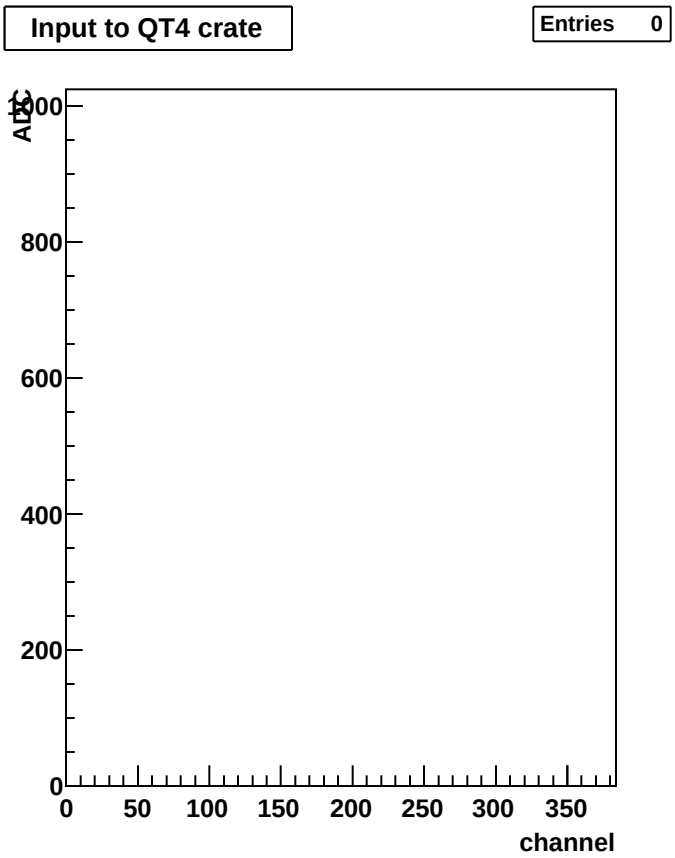

VPD-TAC-W
Entries 19395
Mean 1416
RMS 420.6

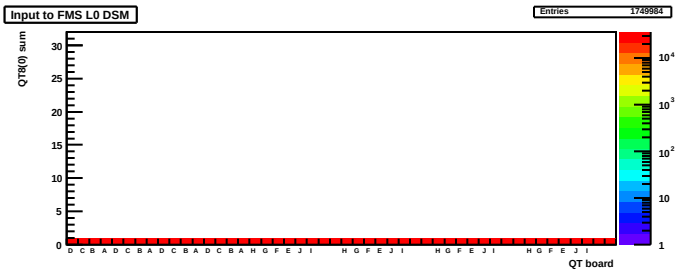

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 10974
Mean 4028
RMS 217.1

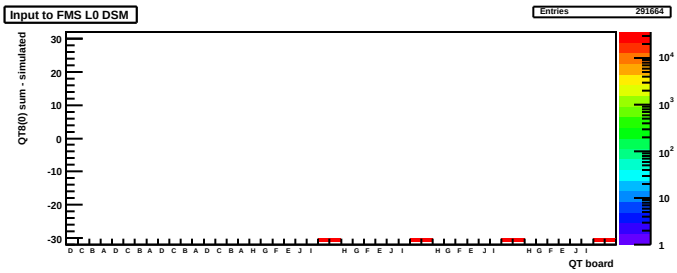

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

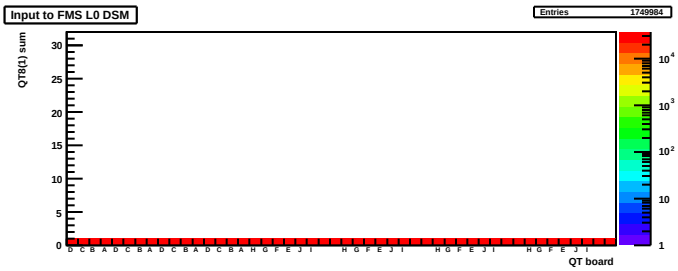

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

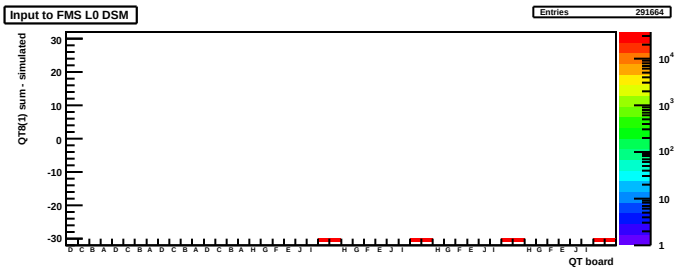

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

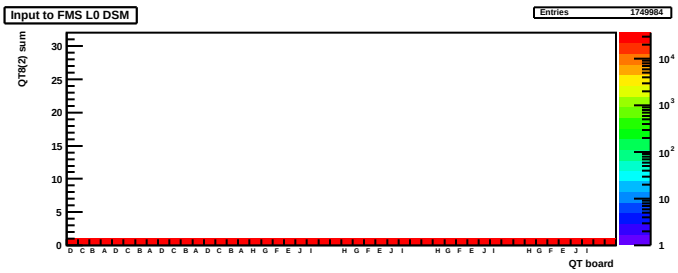

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

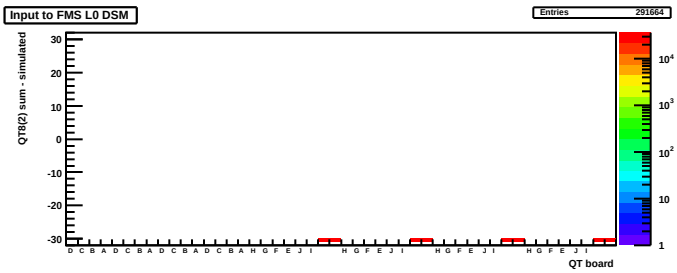

Input to FMS L0 DSM
Entries 1749984
QT8(0) sum
QT board

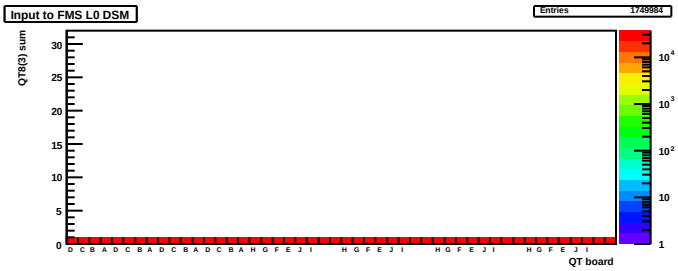

Input to FMS L0 DSM
Entries 291664
QT8(0) sum - simulated
QT board

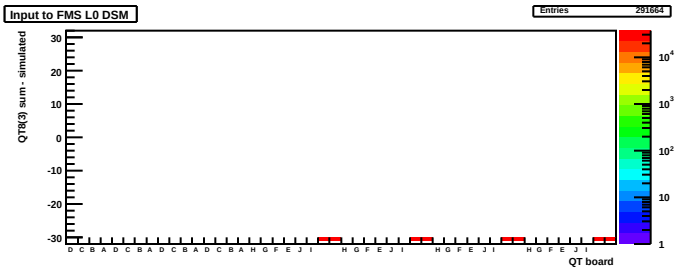

Input to FMS L0 DSM
Entries 1749984
QT8(1) sum
QT board

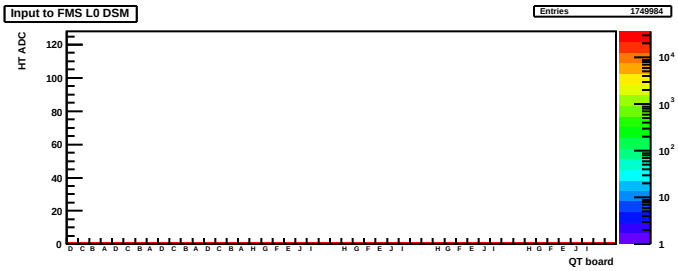

Input to FMS L0 DSM
Entries 291664
QT8(1) sum - simulated
QT board

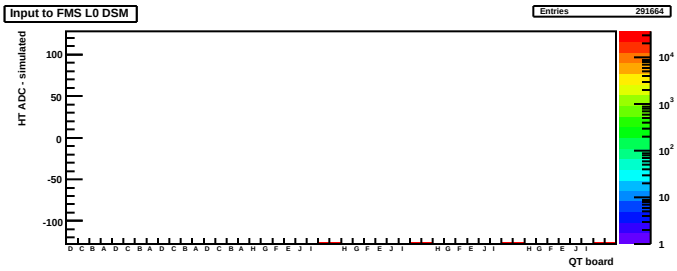

Input to FMS L0 DSM
Entries 1749984
QT8(2) sum
QT board

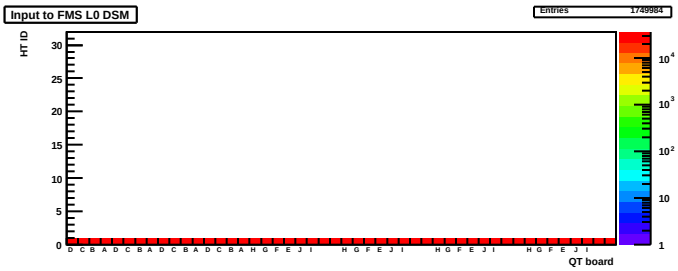

Input to FMS L0 DSM
Entries 291664
QT8(2) sum - simulated
QT board

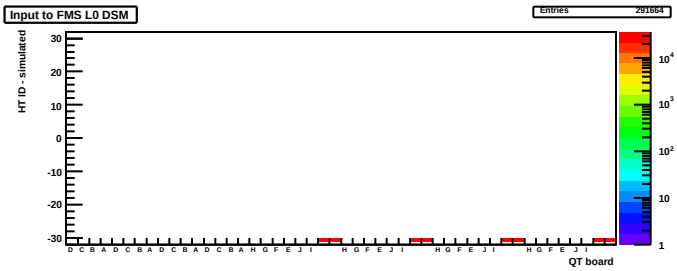

Input to FMS L0 DSM
Entries 1749984
QT8(3) sum
QT board

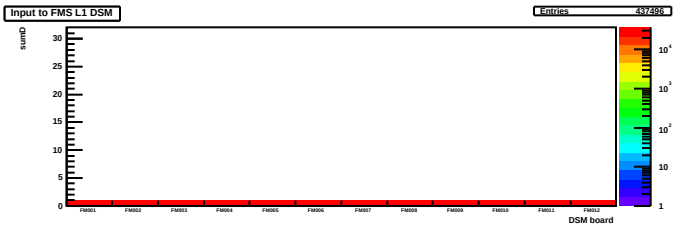

Input to FMS L0 DSM
Entries 291664
QT8(3) sum - simulated
QT board

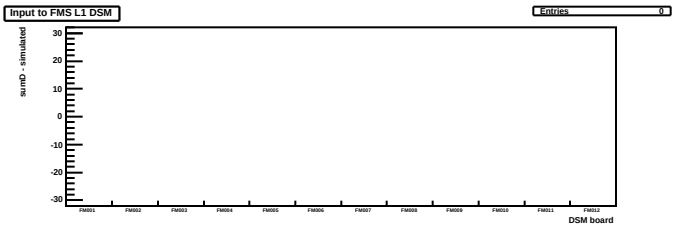

Input to FMS L0 DSM
Entries 1749984
HT ADC
QT board

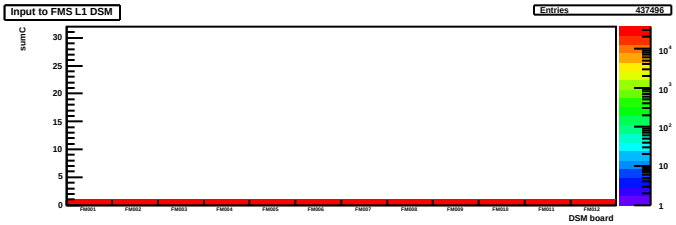

Input to FMS L0 DSM
Entries 291664
HT ADC - simulated
QT board

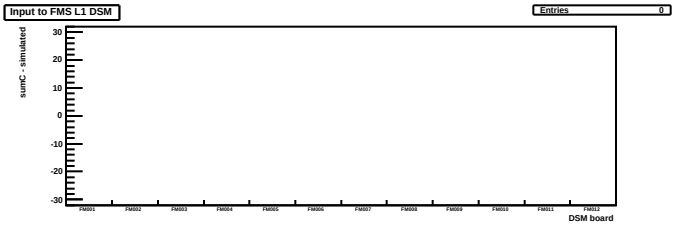

Input to FMS L0 DSM
Entries 1749984
HT ID
QT board

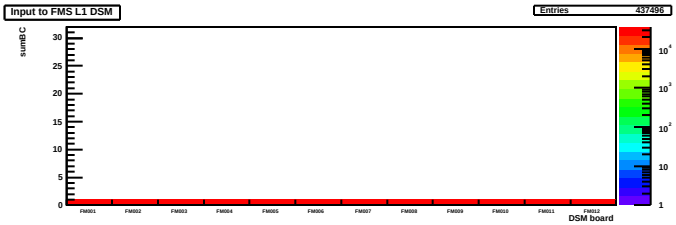

Input to FMS L0 DSM
Entries 291664
HT ID - simulated
QT board

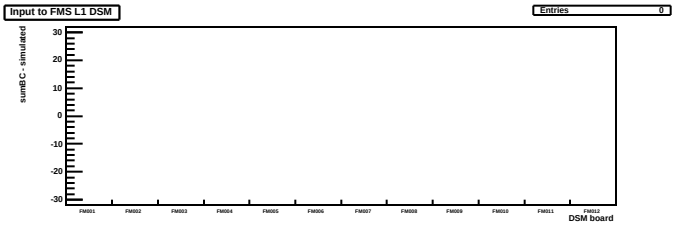

Input to FMS L1 DSM
Entries 437496
sumD
DSM board

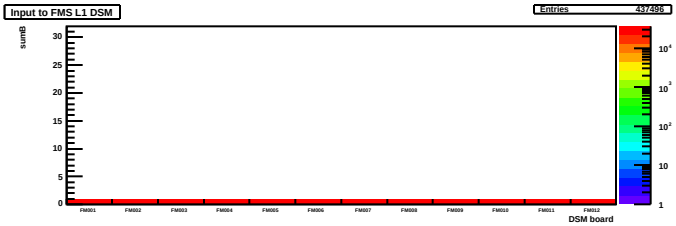

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

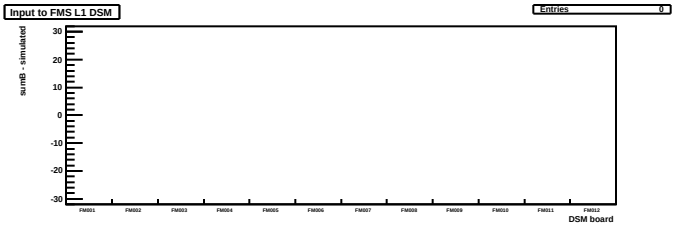

Input to FMS L1 DSM
Entries 437496
sumC
DSM board

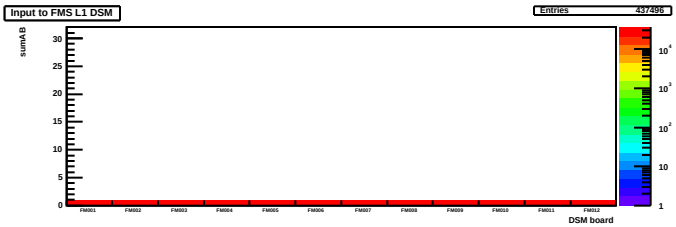

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

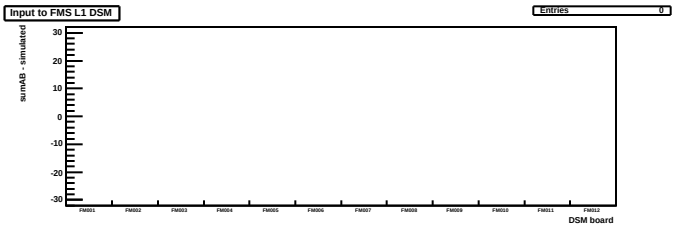

Input to FMS L1 DSM
Entries 437496
sumBC
DSM board

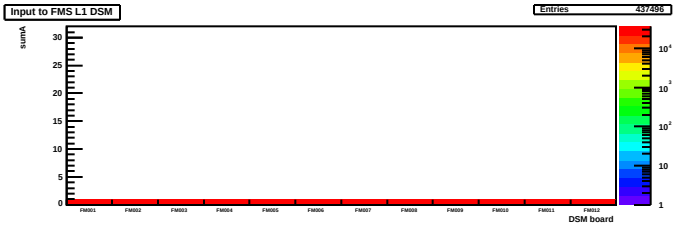

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

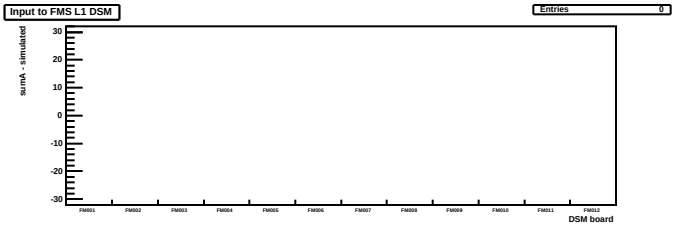

Input to FMS L1 DSM
Entries 437496
sumB
DSM board

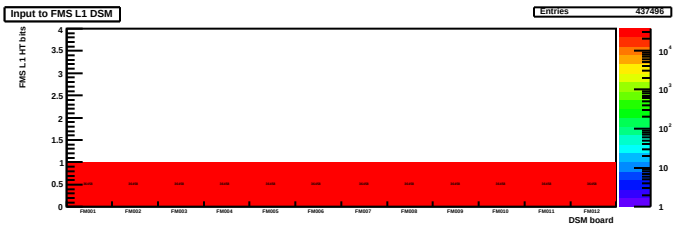

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

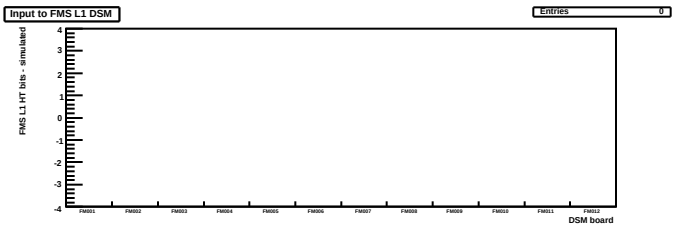

Input to FMS L1 DSM
Entries 437496
sumAB
DSM board

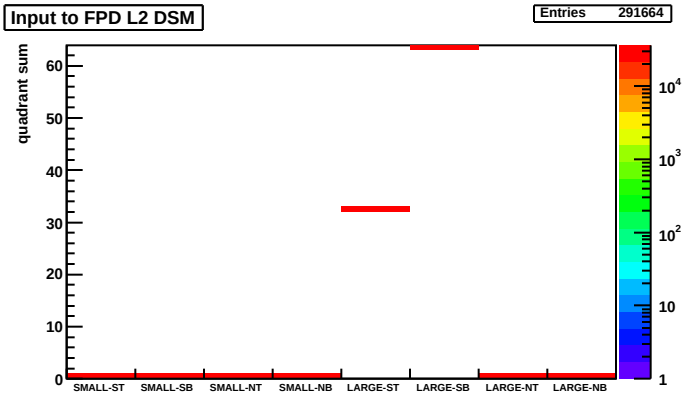

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

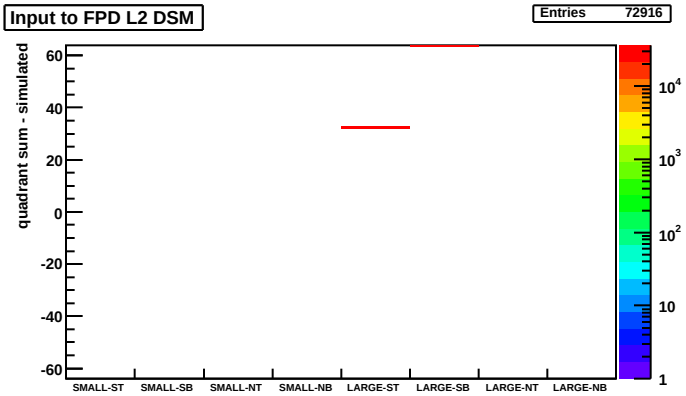

Input to FMS L1 DSM
Entries 437496
sumA
DSM board

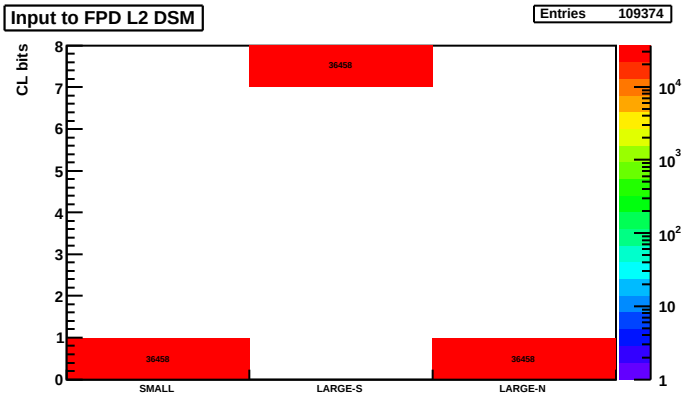

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

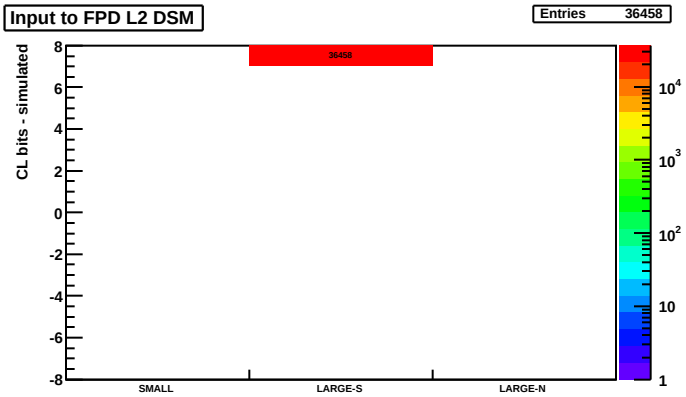

Input to FMS L1 DSM
Entries 437496
FMS L1 HT bits
DSM board

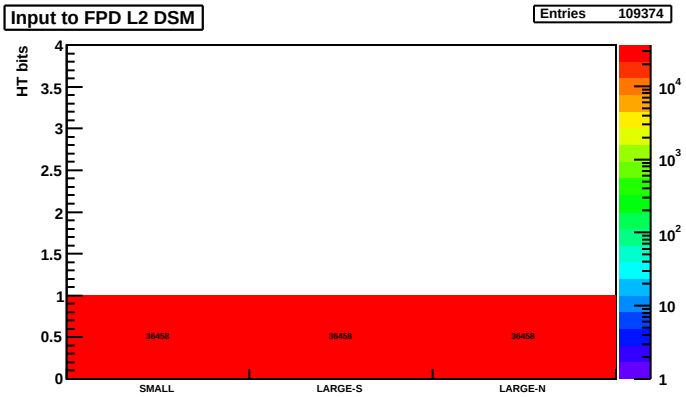

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

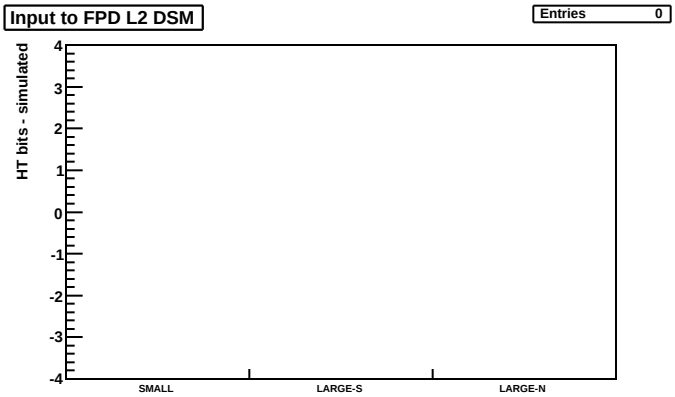

Input to FPD L2 DSM
Entries 291664
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

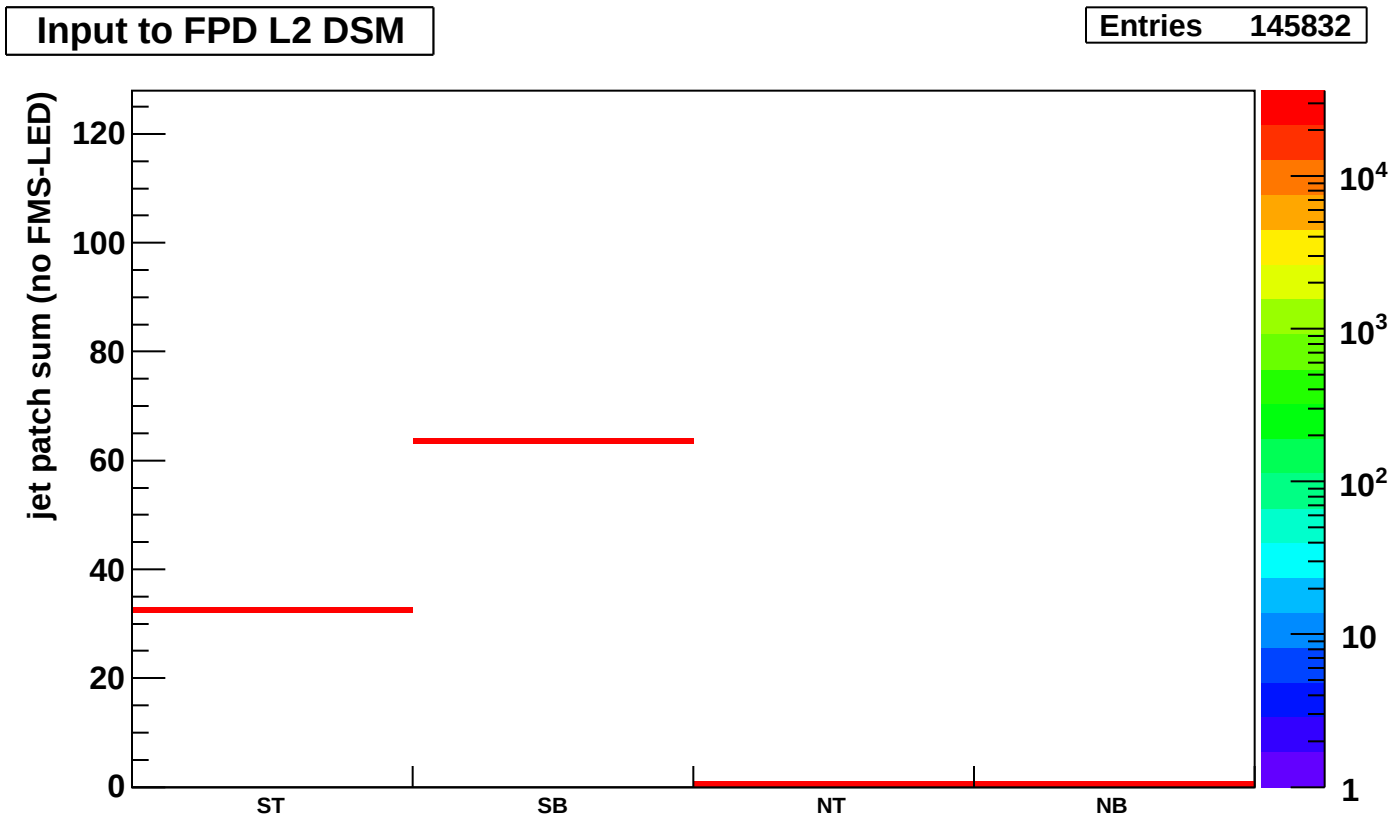

Input to FPD L2 DSM
Entries 72916
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

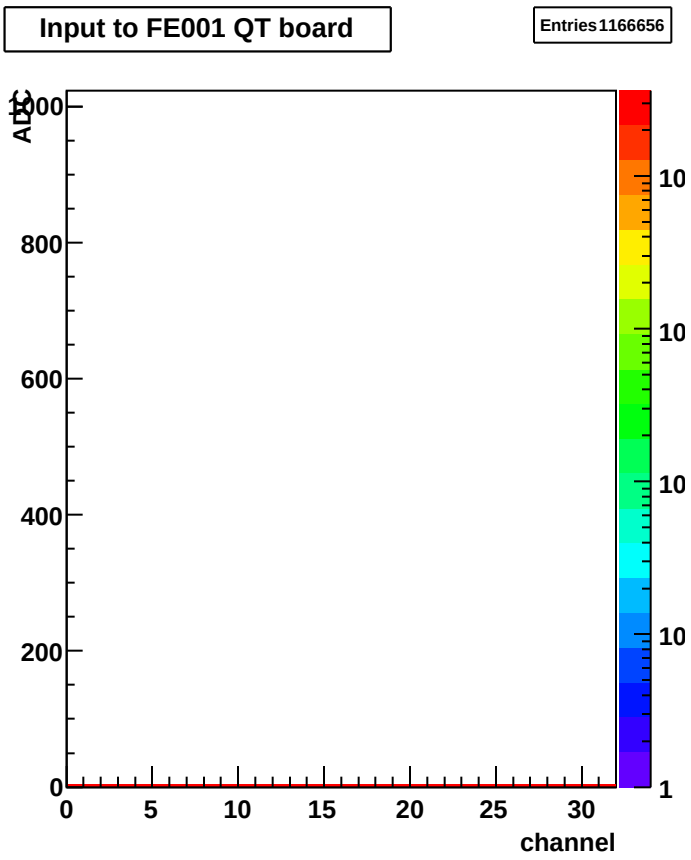

Input to FPD L2 DSM
Entries 109374
CL bits
36458
36458
36458
SMALL
LARGE-S
LARGE-N

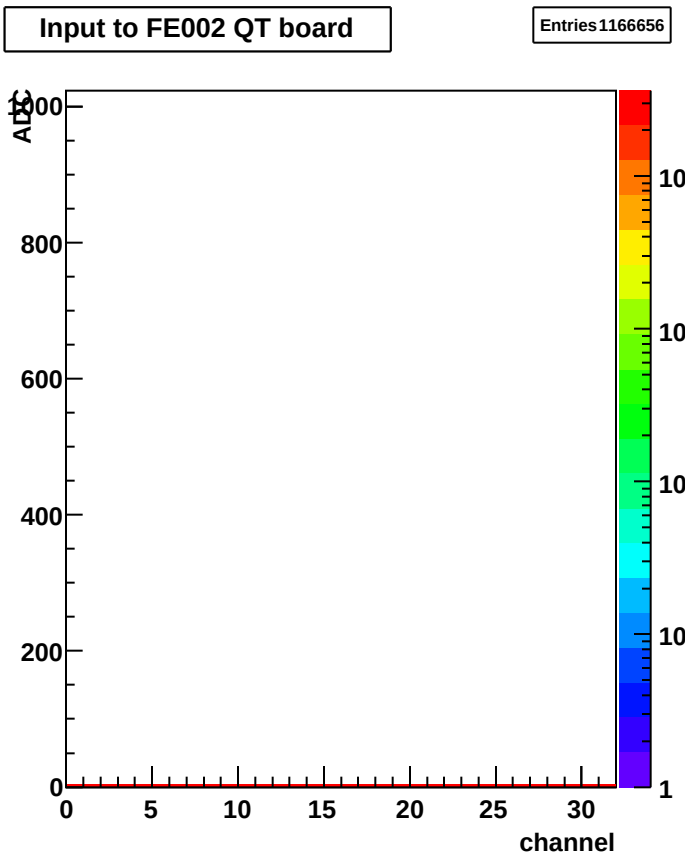

Input to FPD L2 DSM
Entries 36458
CL bits - simulated
36458
SMALL
LARGE-S
LARGE-N

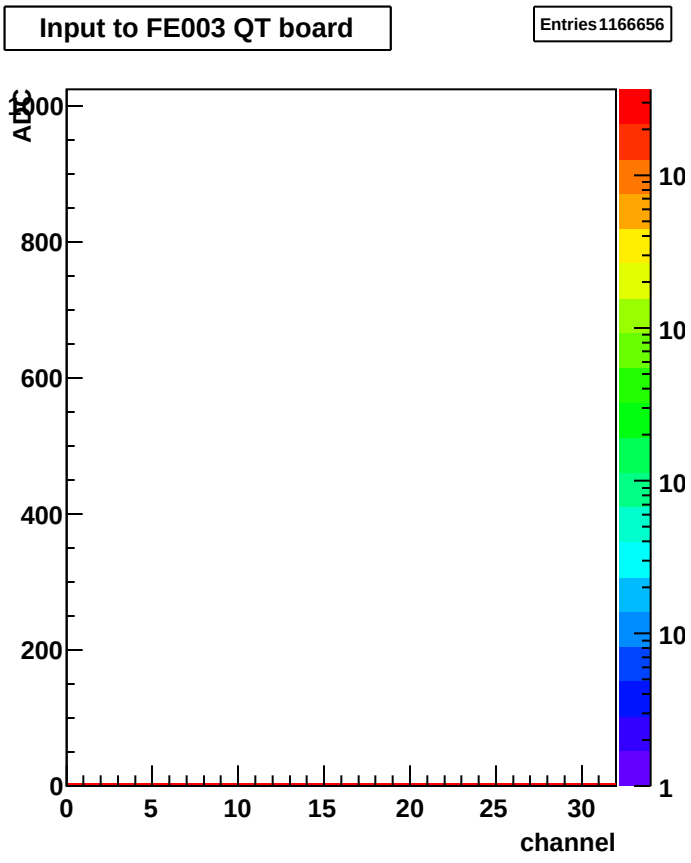

Input to FPD L2 DSM
Entries 109374
HT bits
36458
36458
36458
SMALL
LARGE-S
LARGE-N

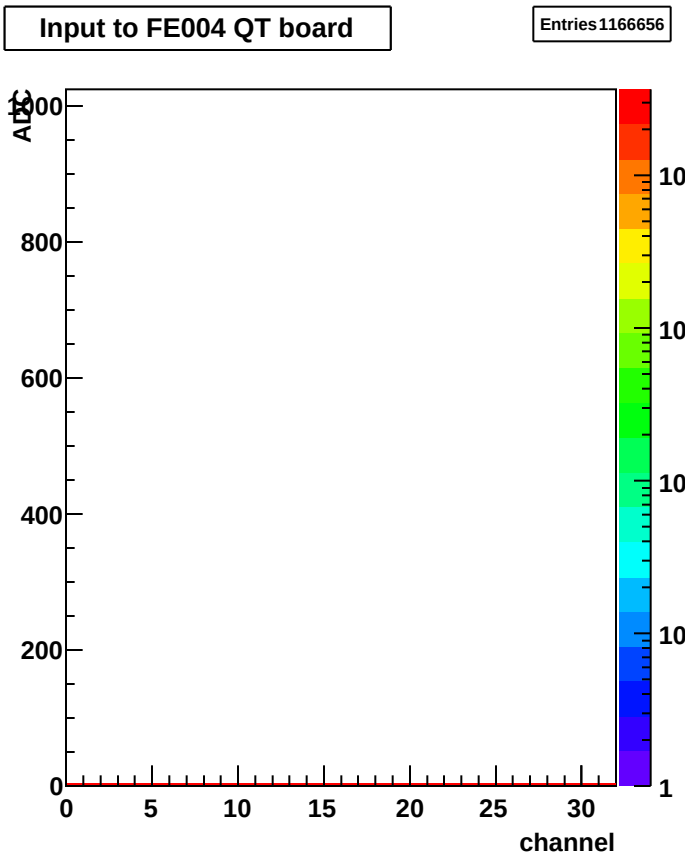

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

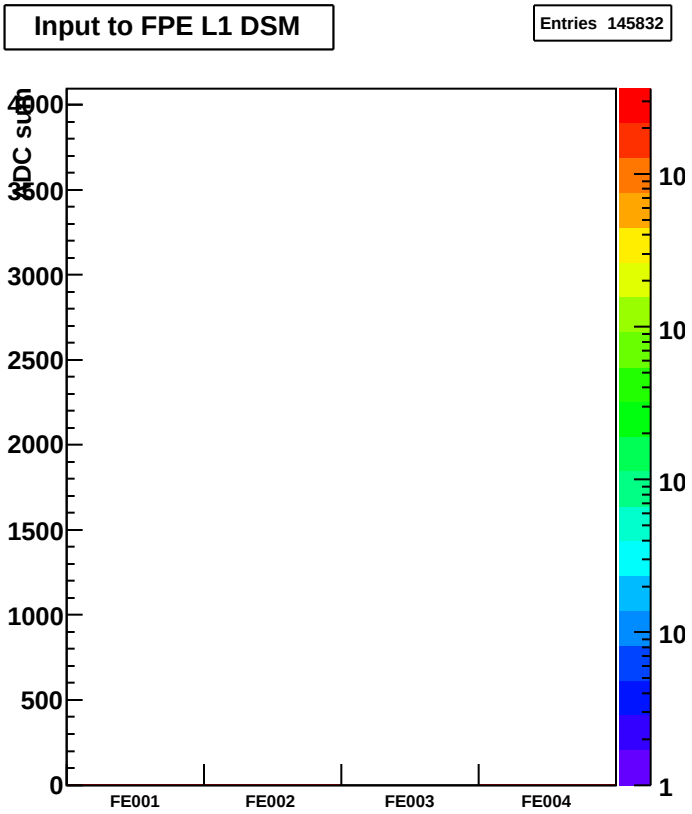

Input to FPD L2 DSM
Entries 145832
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

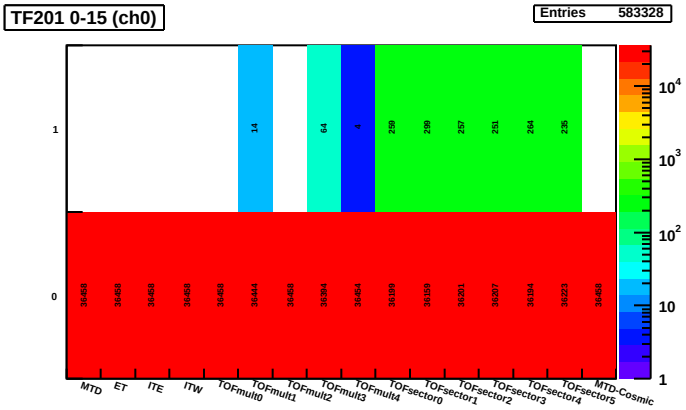

Input to FE001 QT board
Entries 1166656
ADC
channel

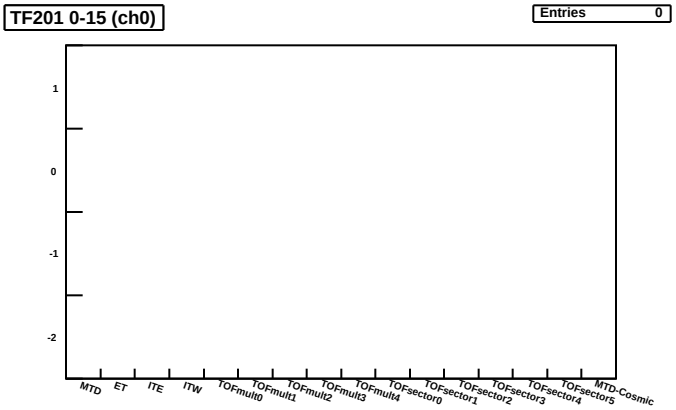

Input to FE002 QT board
Entries 1166656
ADC
channel

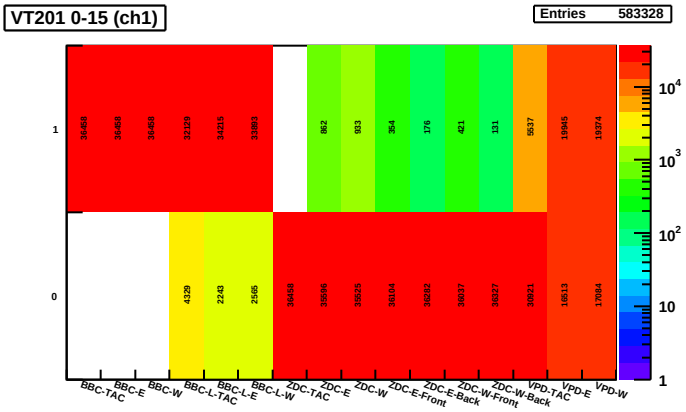

Input to FE003 QT board
Entries 1166656
ADC
channel

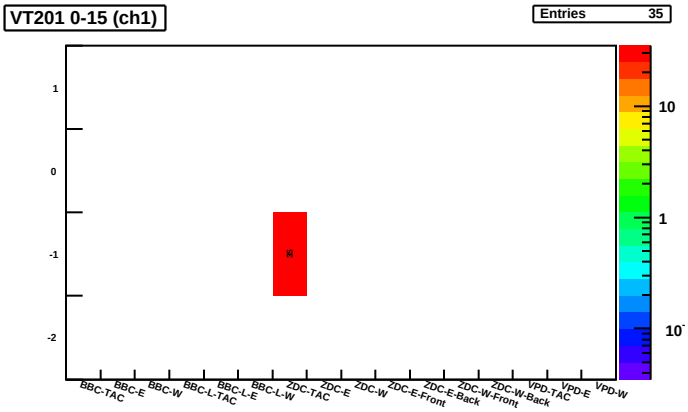

Input to FE004 QT board
Entries 1166656
ADC
channel

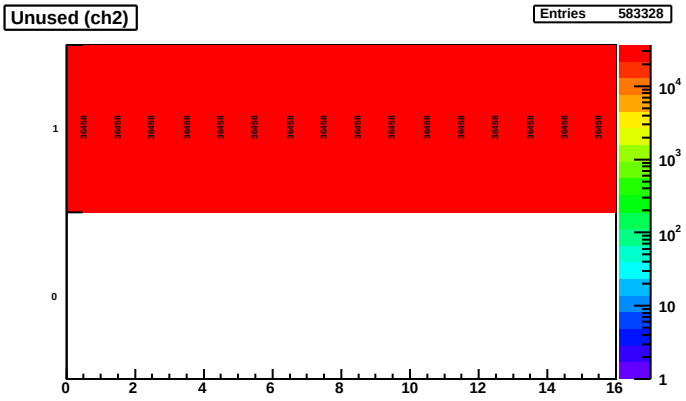

Input to FPE L1 DSM
Entries 145832
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

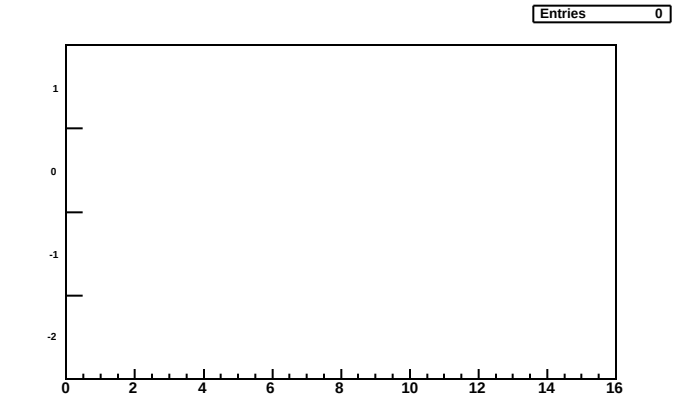

TF201 0-15 (ch0)
Entries 583328
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

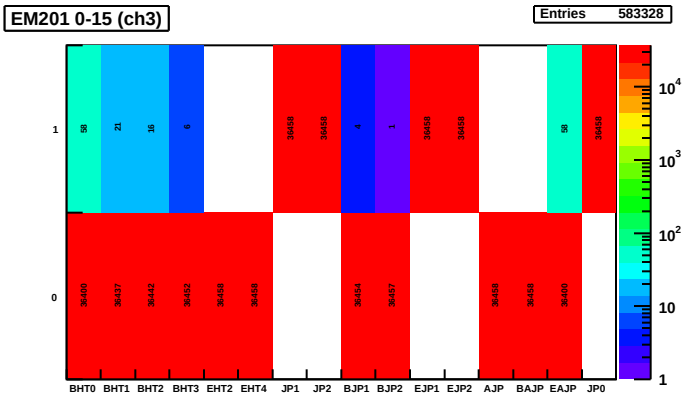

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

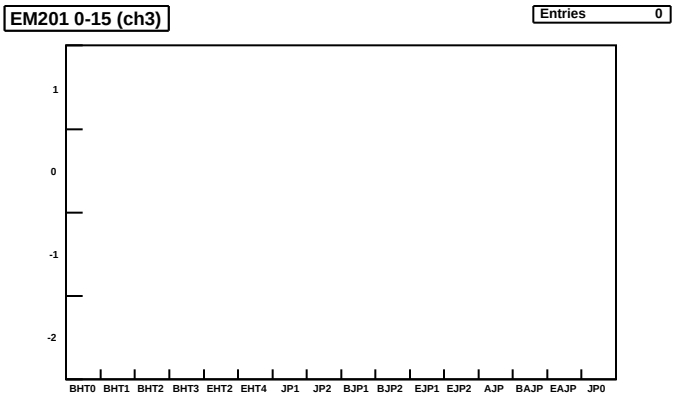

VT201 0-15 (ch1)
Entries 583328
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 35
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

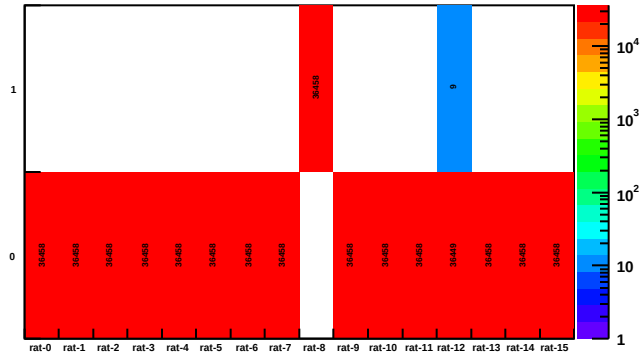

Unused (ch2)
Entries 583328

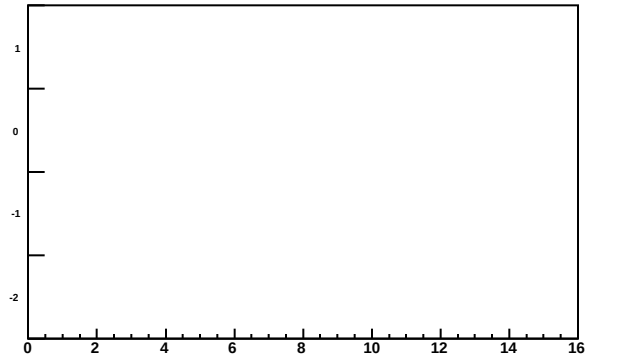

Entries 0


EM201 0-15 (ch3)
Entries 583328
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

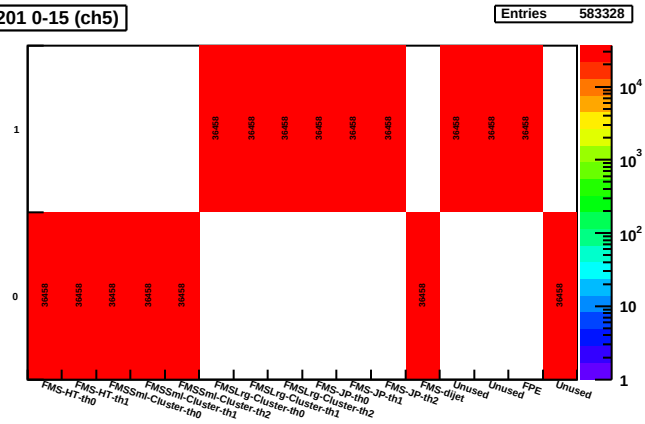

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

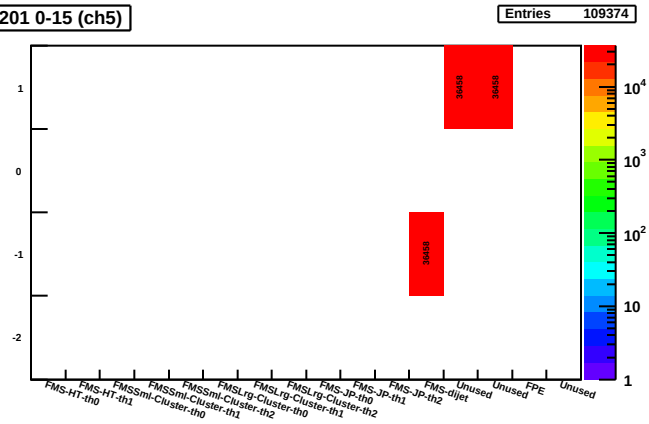




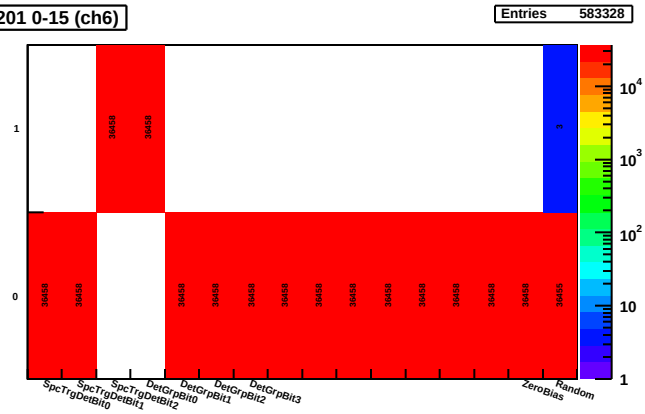
FP201 0-15 (ch5)

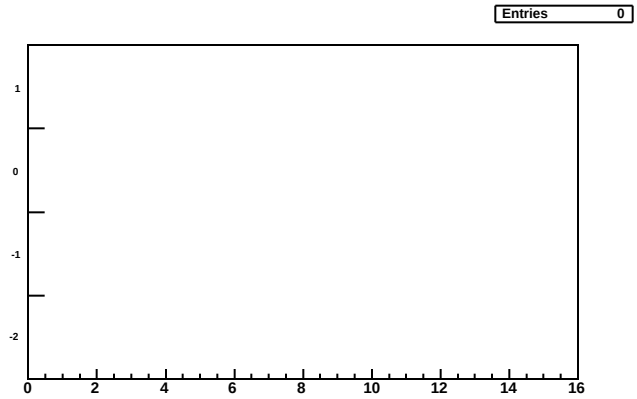
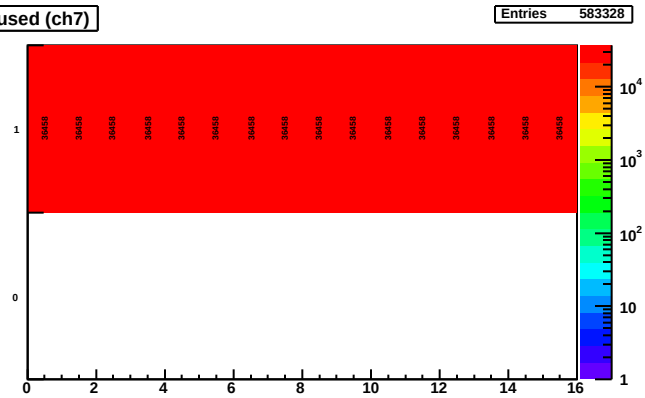
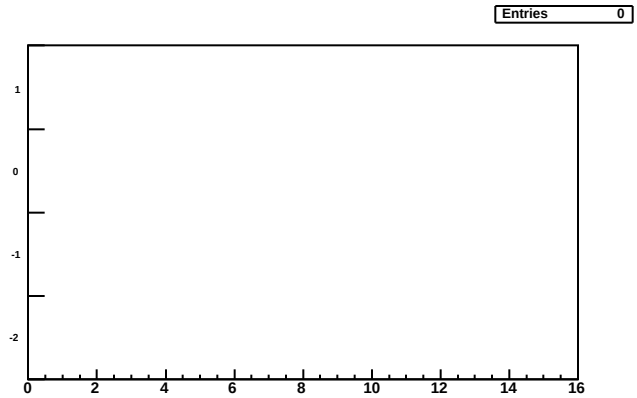


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)