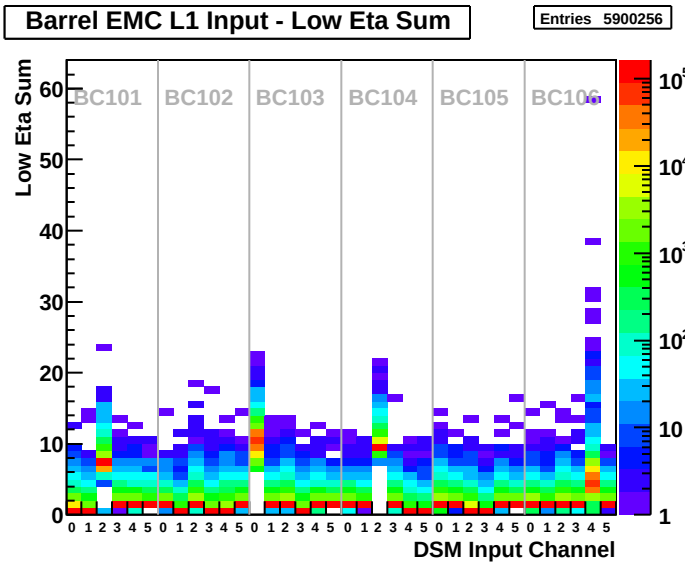
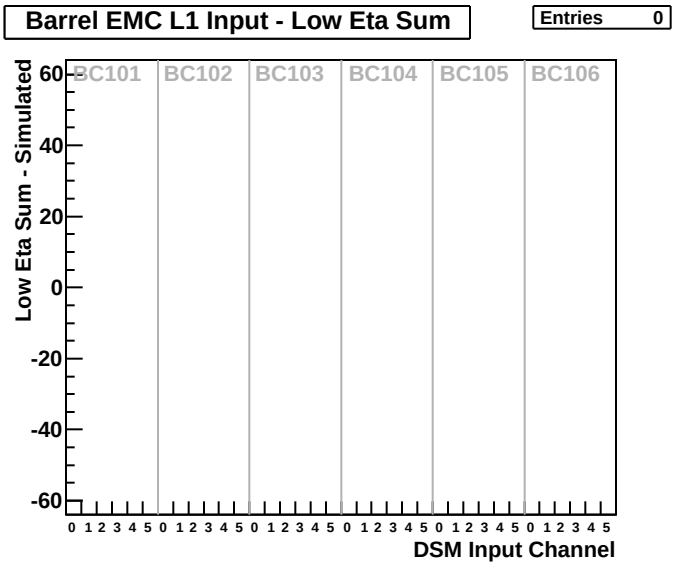
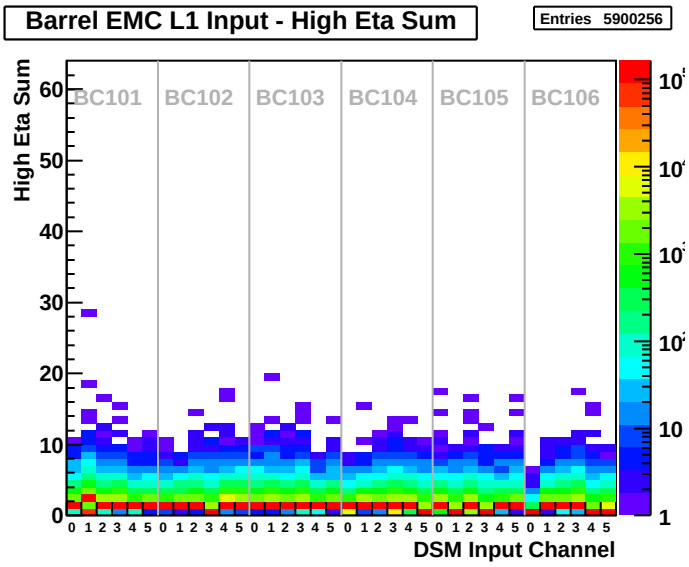
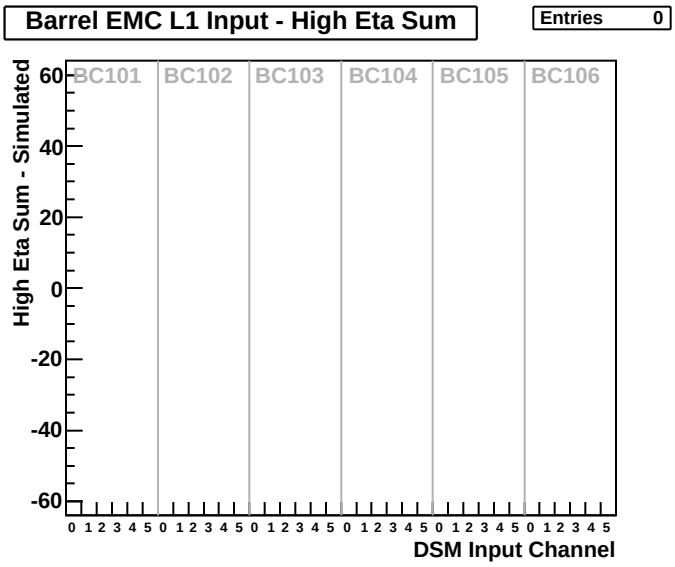
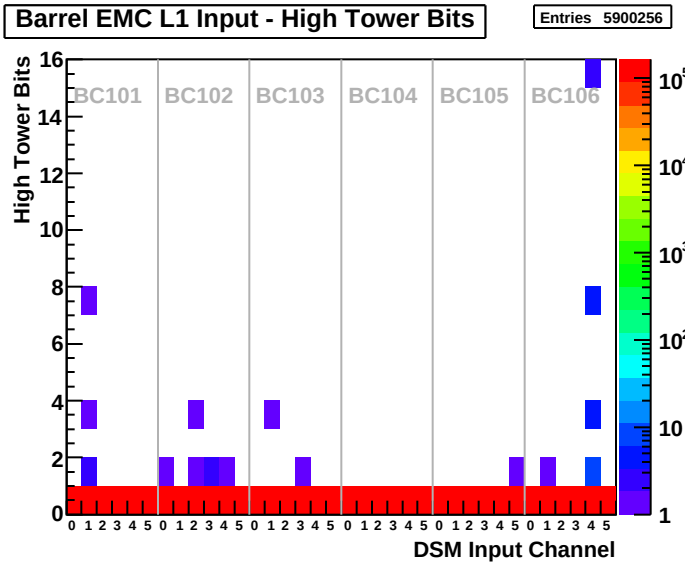
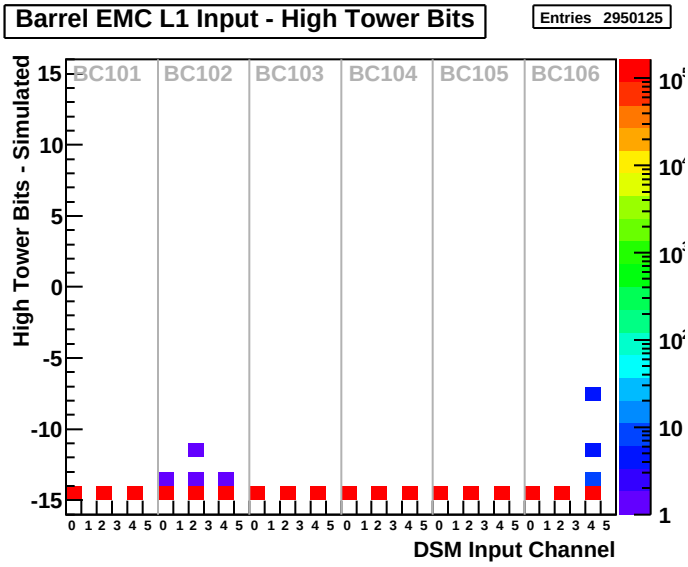

Barrel EMC L1 Input - Low Eta Sum
Entries 5900256
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

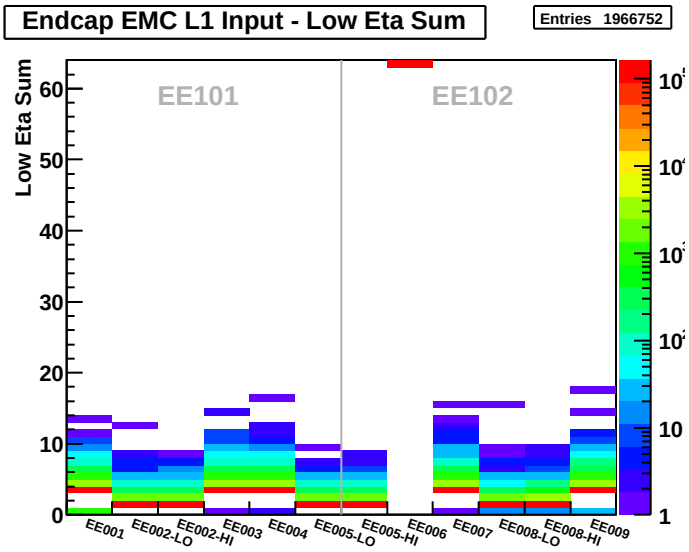

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

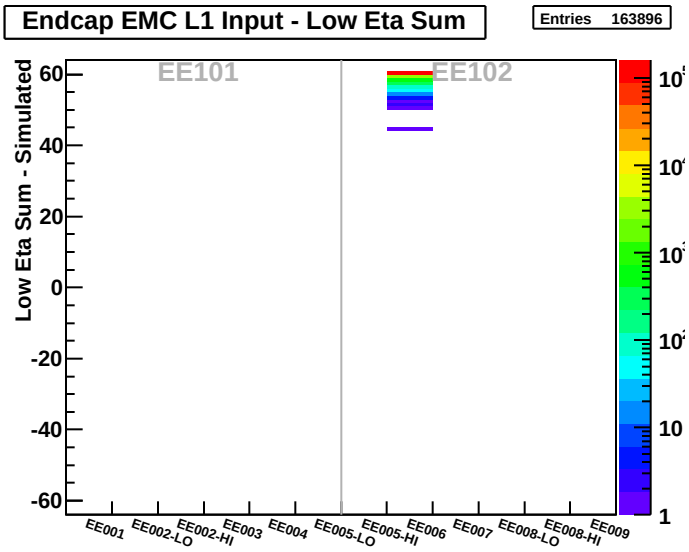

Barrel EMC L1 Input - High Eta Sum
Entries 5900256
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

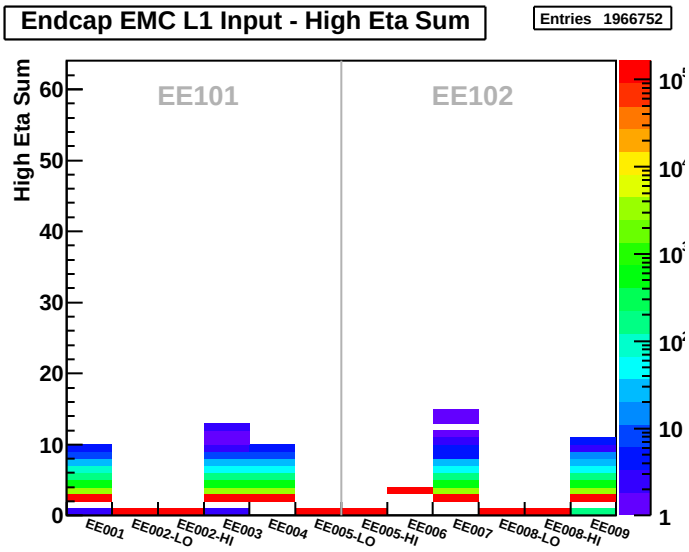

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

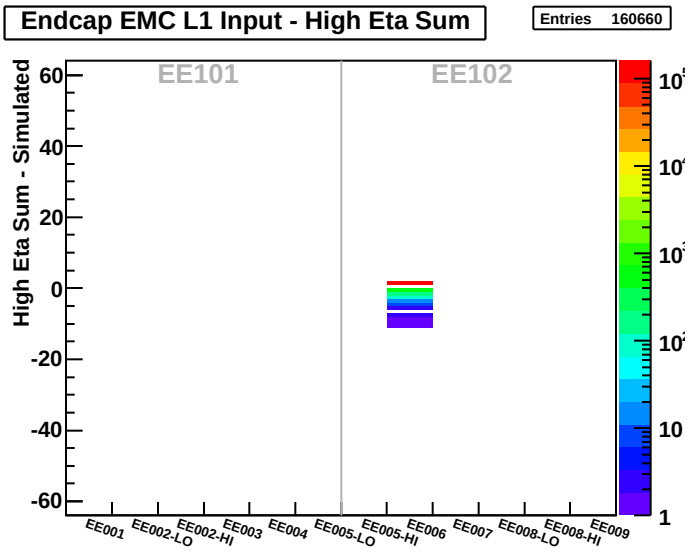

Barrel EMC L1 Input - High Tower Bits
Entries 5900256
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

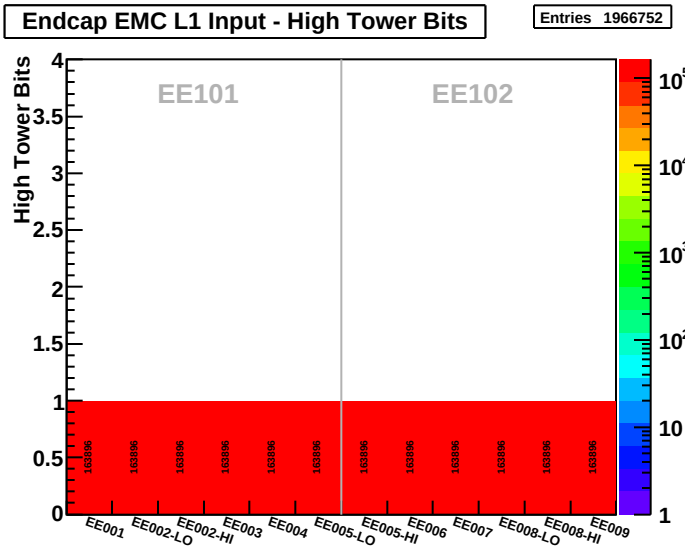

Barrel EMC L1 Input - High Tower Bits
Entries 2950125
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

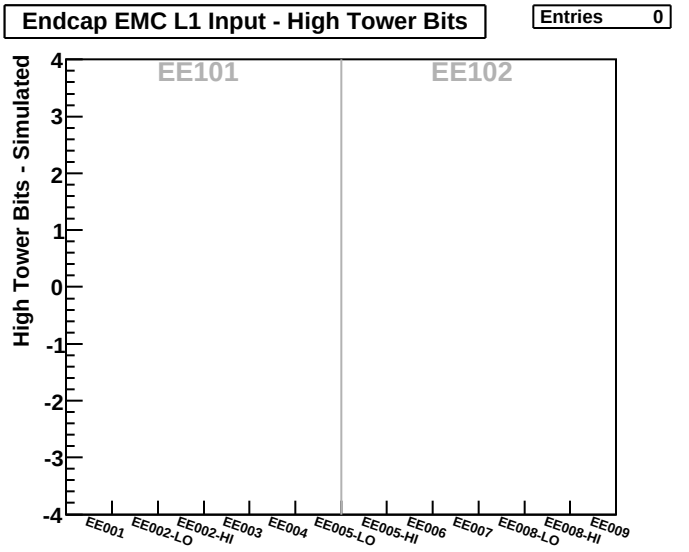

Endcap EMC L1 Input - Low Eta Sum
Entries 1966752
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

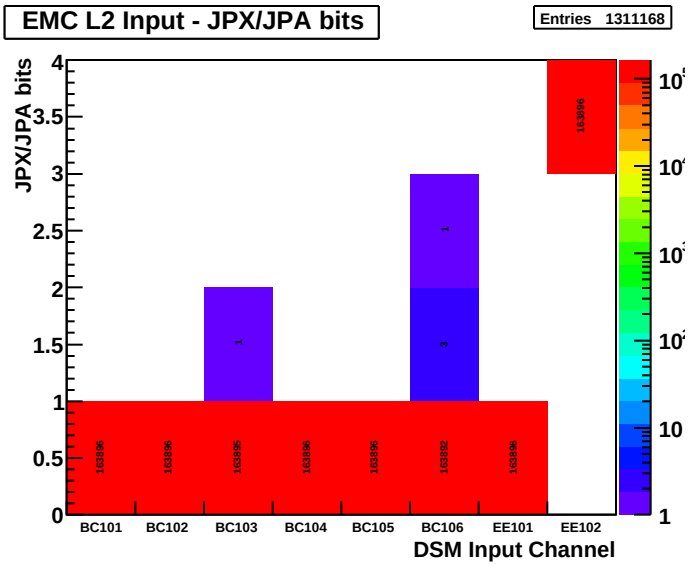

Endcap EMC L1 Input - Low Eta Sum
Entries 163896
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

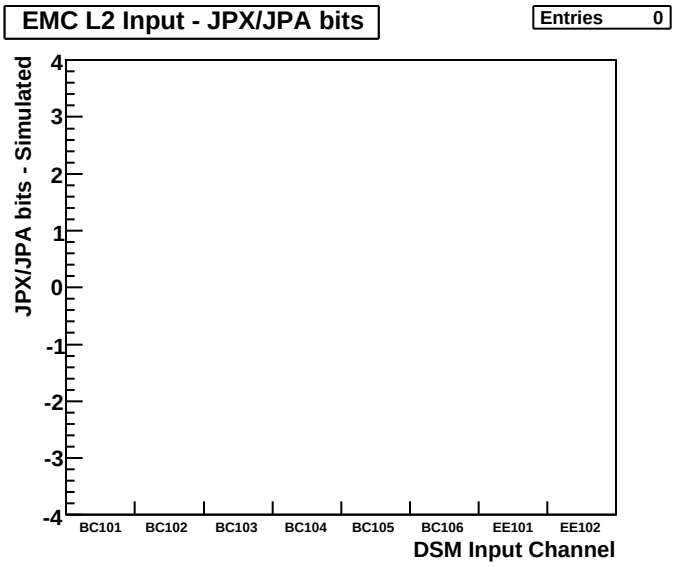

Endcap EMC L1 Input - High Eta Sum
Entries 1966752
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

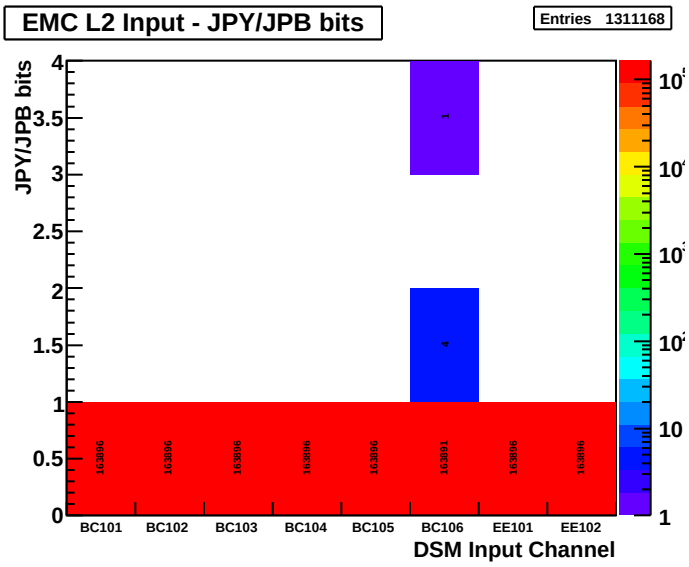

Endcap EMC L1 Input - High Eta Sum
Entries 160660
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

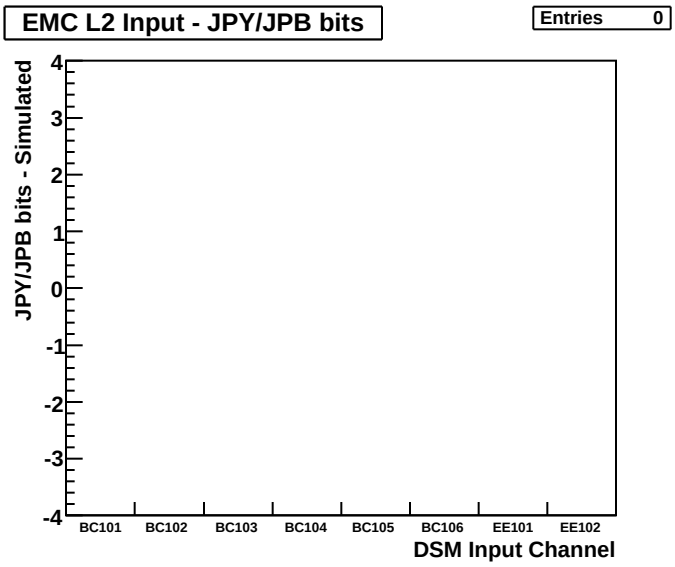

Endcap EMC L1 Input - High Tower Bits
Entries 1966752
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

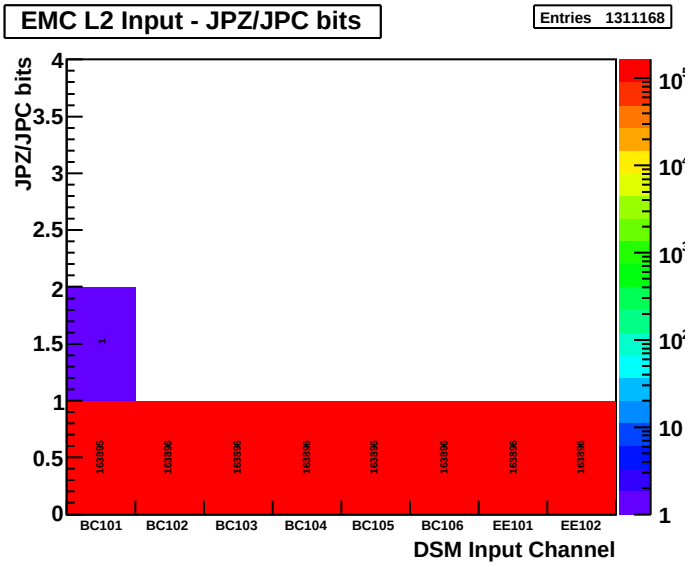

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

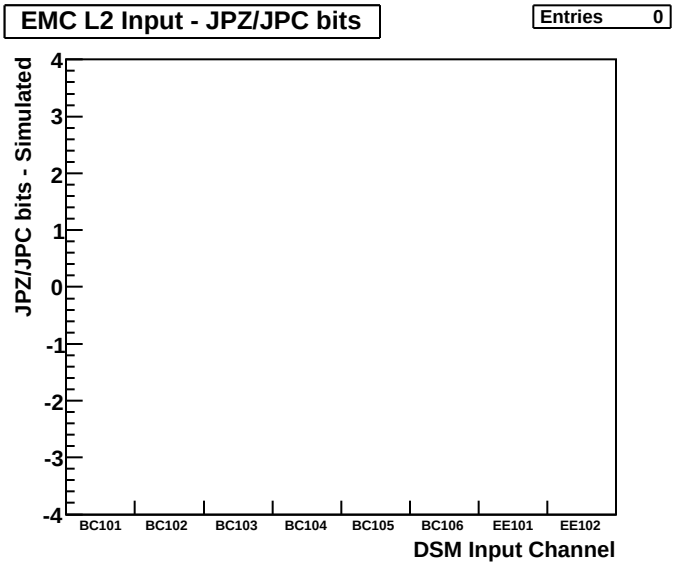

EMC L2 Input - JPX/JPA bits
Entries 1311168
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

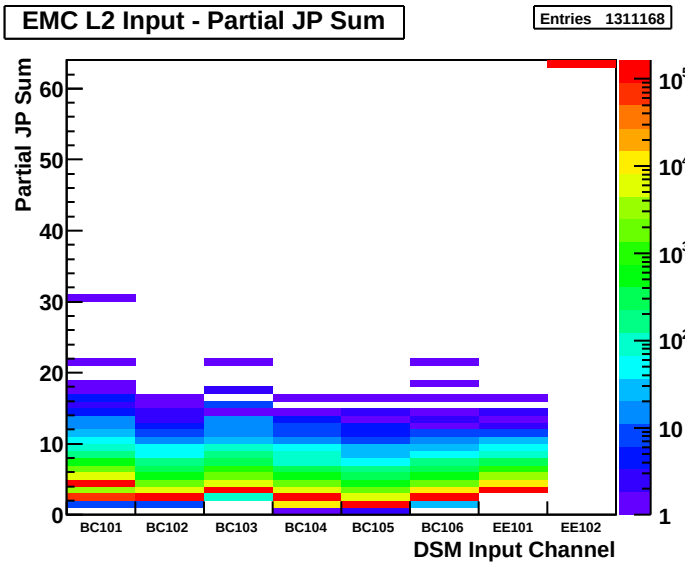

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

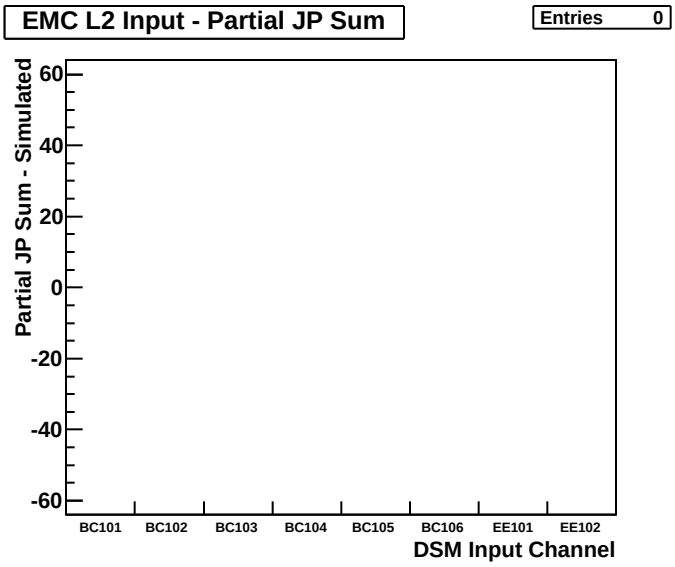

EMC L2 Input - JPY/JPB bits
Entries 1311168
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

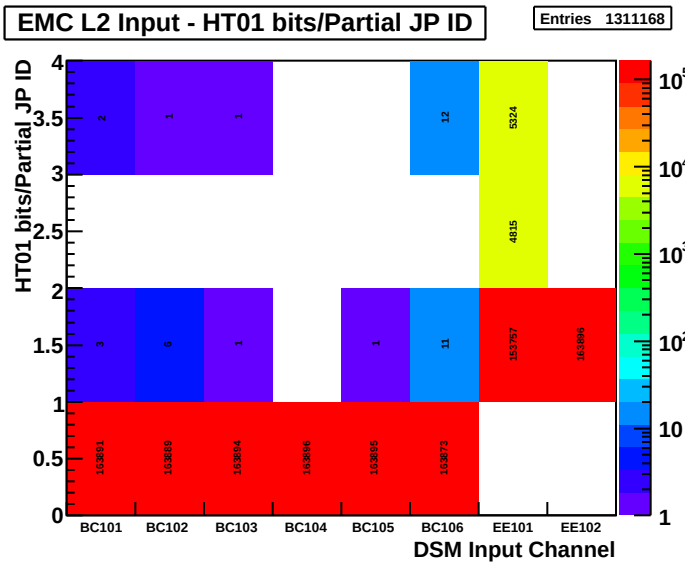

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

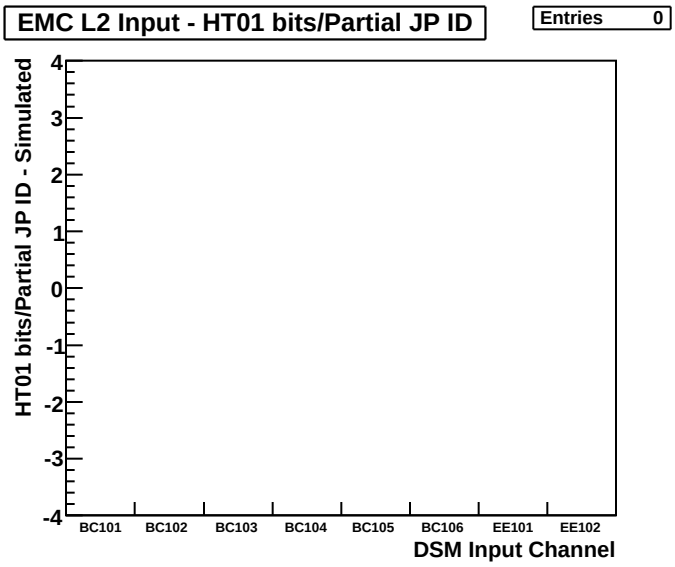

EMC L2 Input - JPZ/JPC bits
Entries 1311168
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

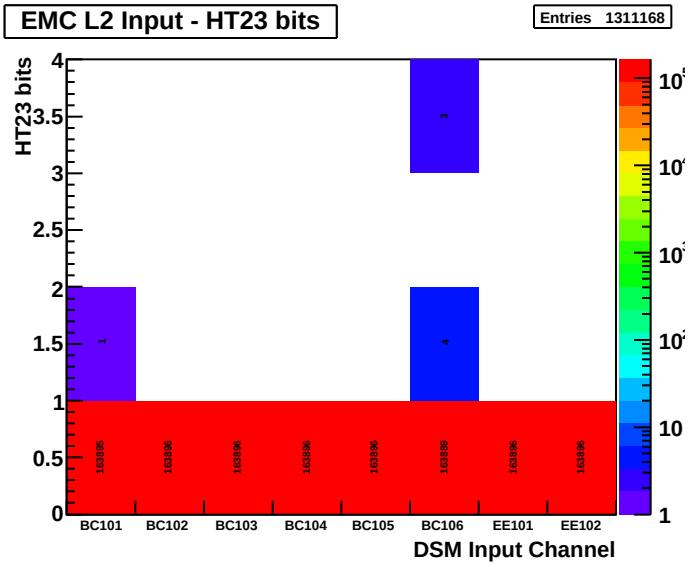

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

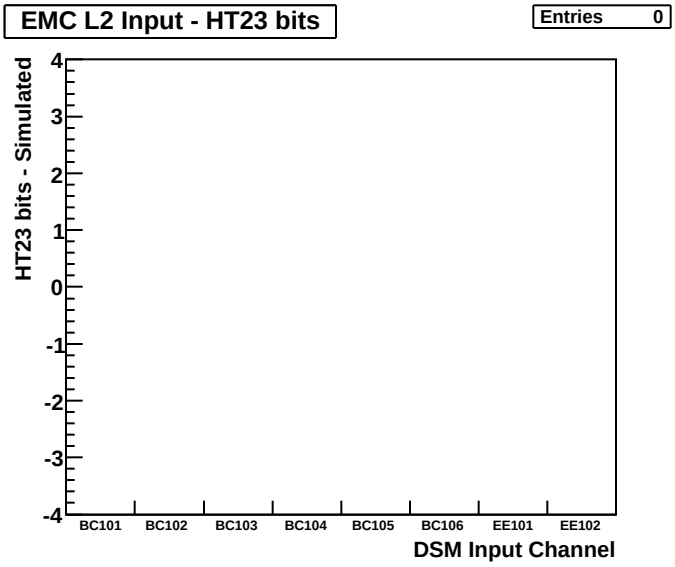

EMC L2 Input - Partial JP Sum
Entries 1311168
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

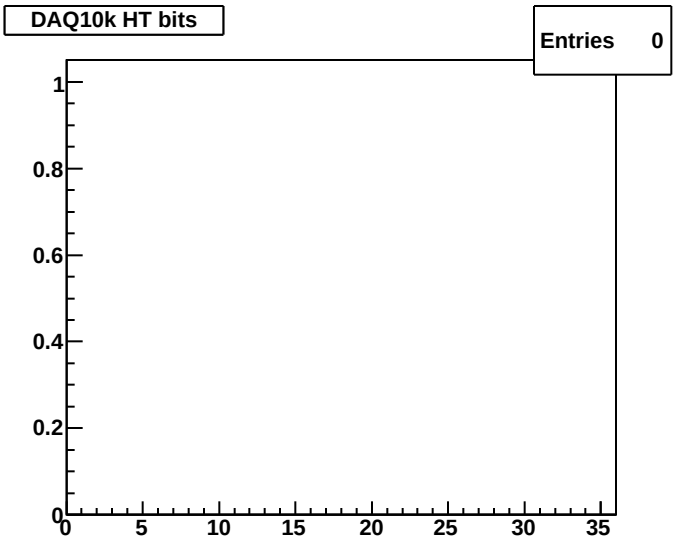

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

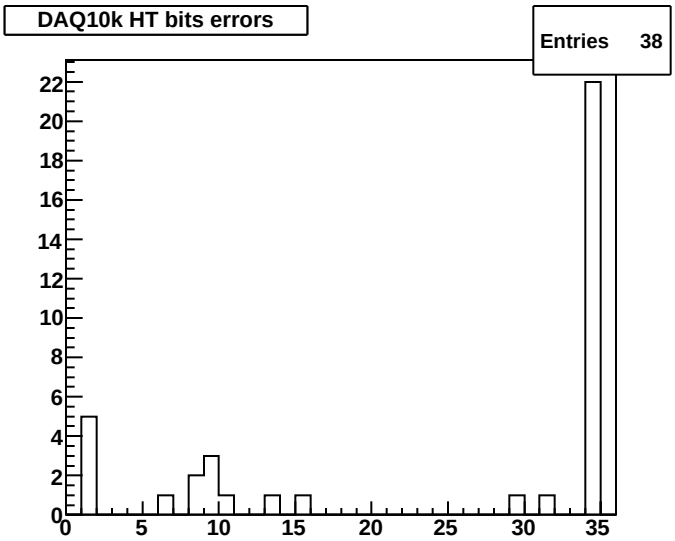

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1311168
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

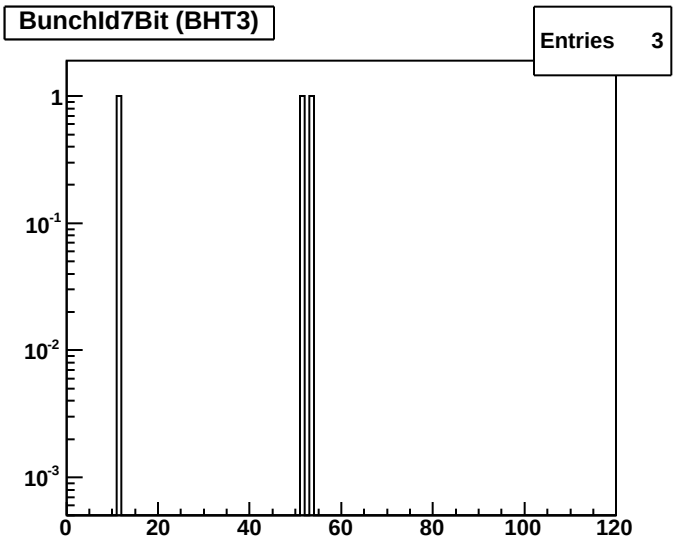

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

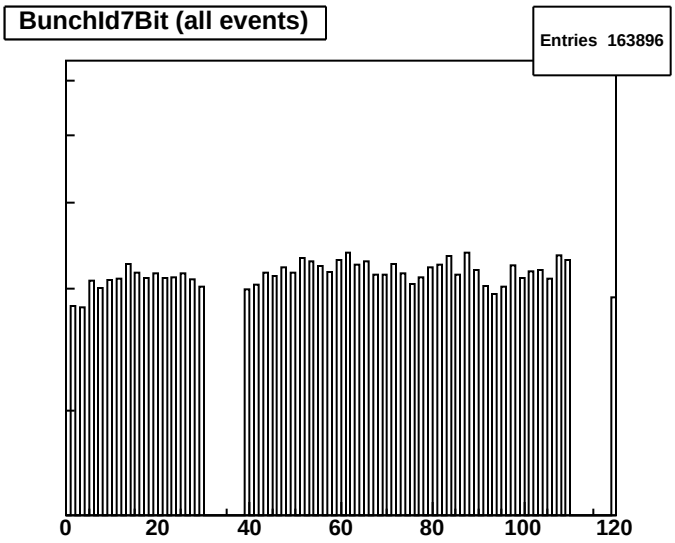

EMC L2 Input - HT23 bits
Entries 1311168
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

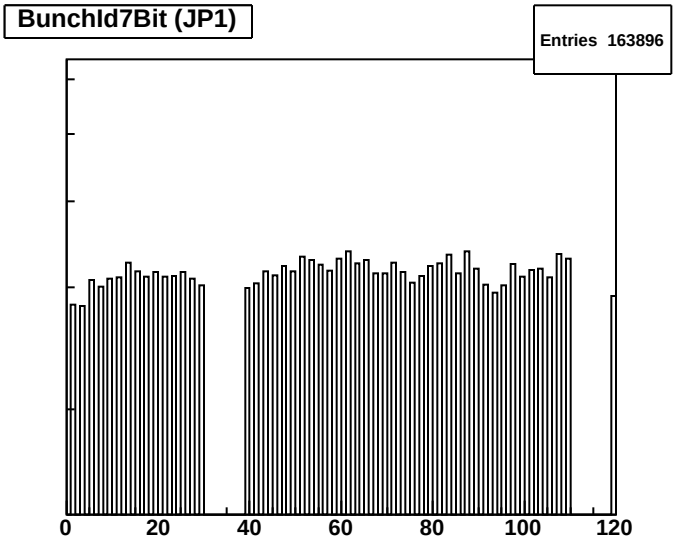

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

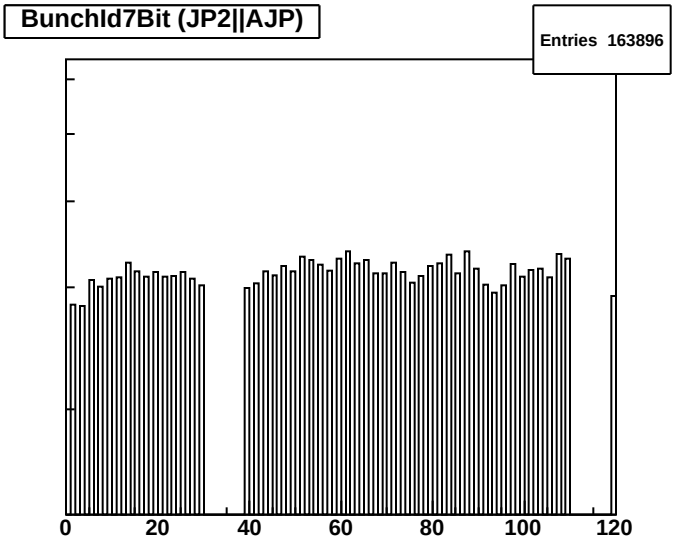

DAQ10k HT bits
Entries 0
1
0.8
0.6
0.4
0.2
0
0 5 10 15 20 25 30 35

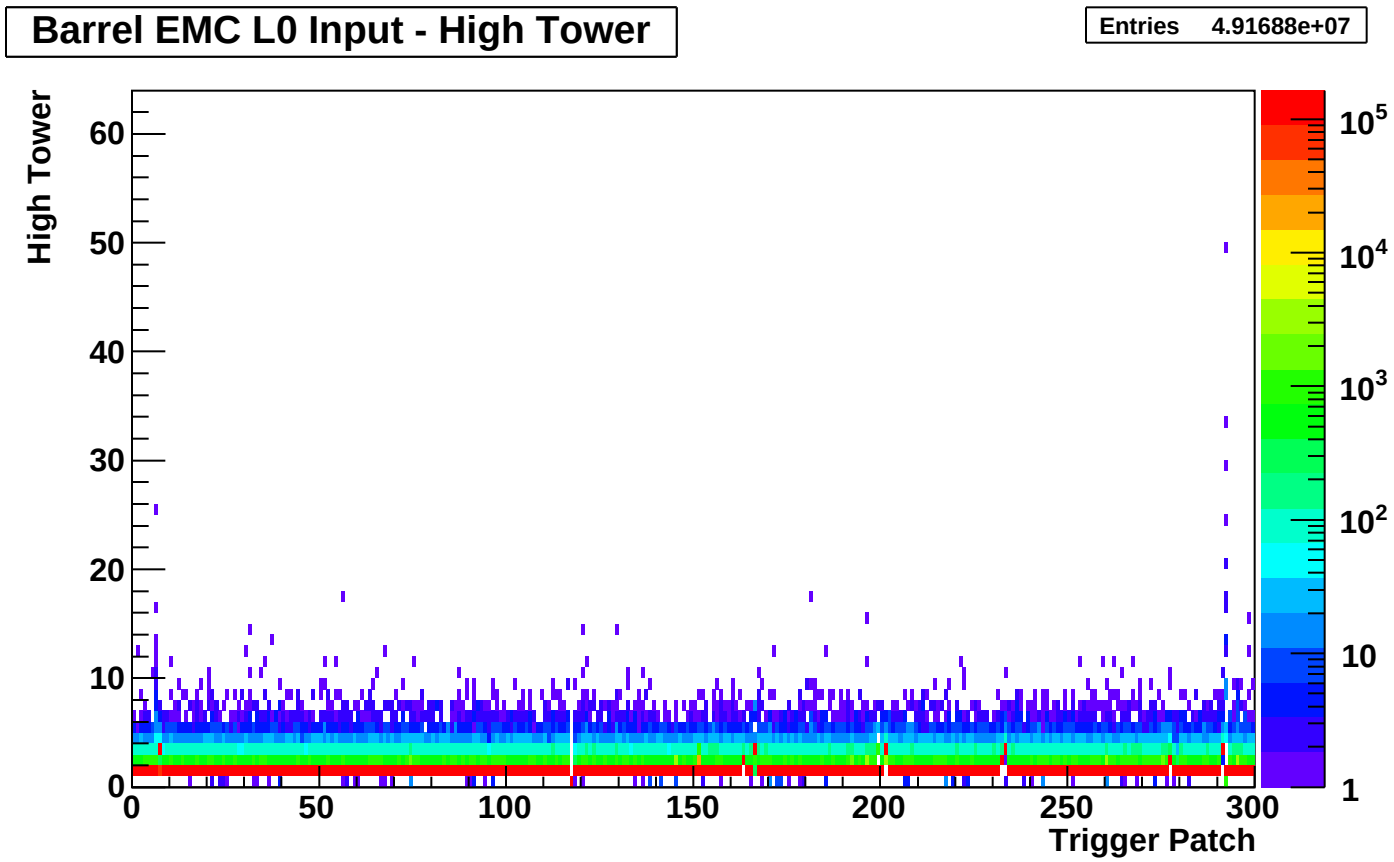

DAQ10k HT bits errors
Entries 38
22
20
18
16
14
12
10
8
6
4
2
0
0 5 10 15 20 25 30 35

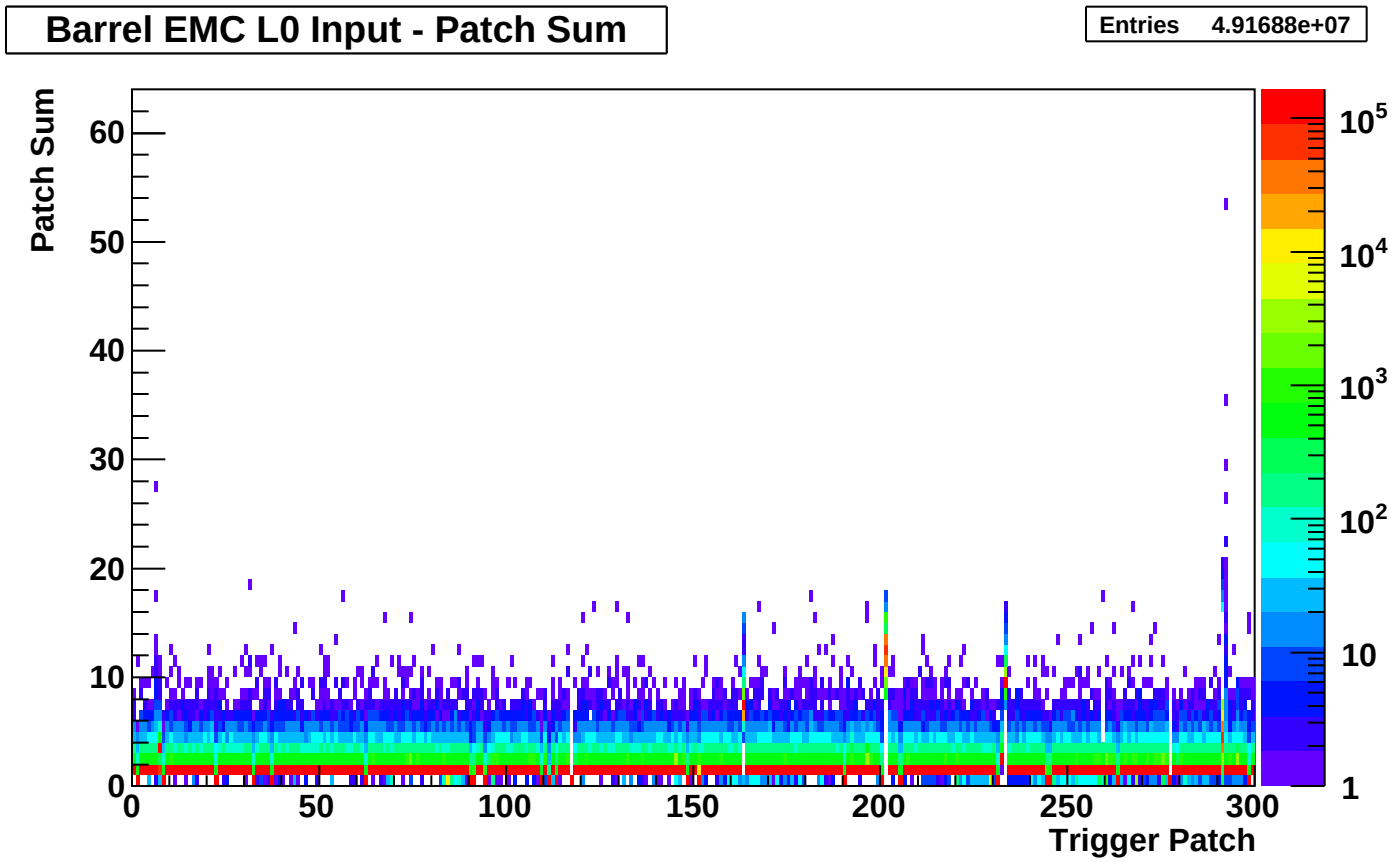

BunchId7Bit (BHT3)
Entries 3
1
10-1
10-2
10-3
0 20 40 60 80 100 120

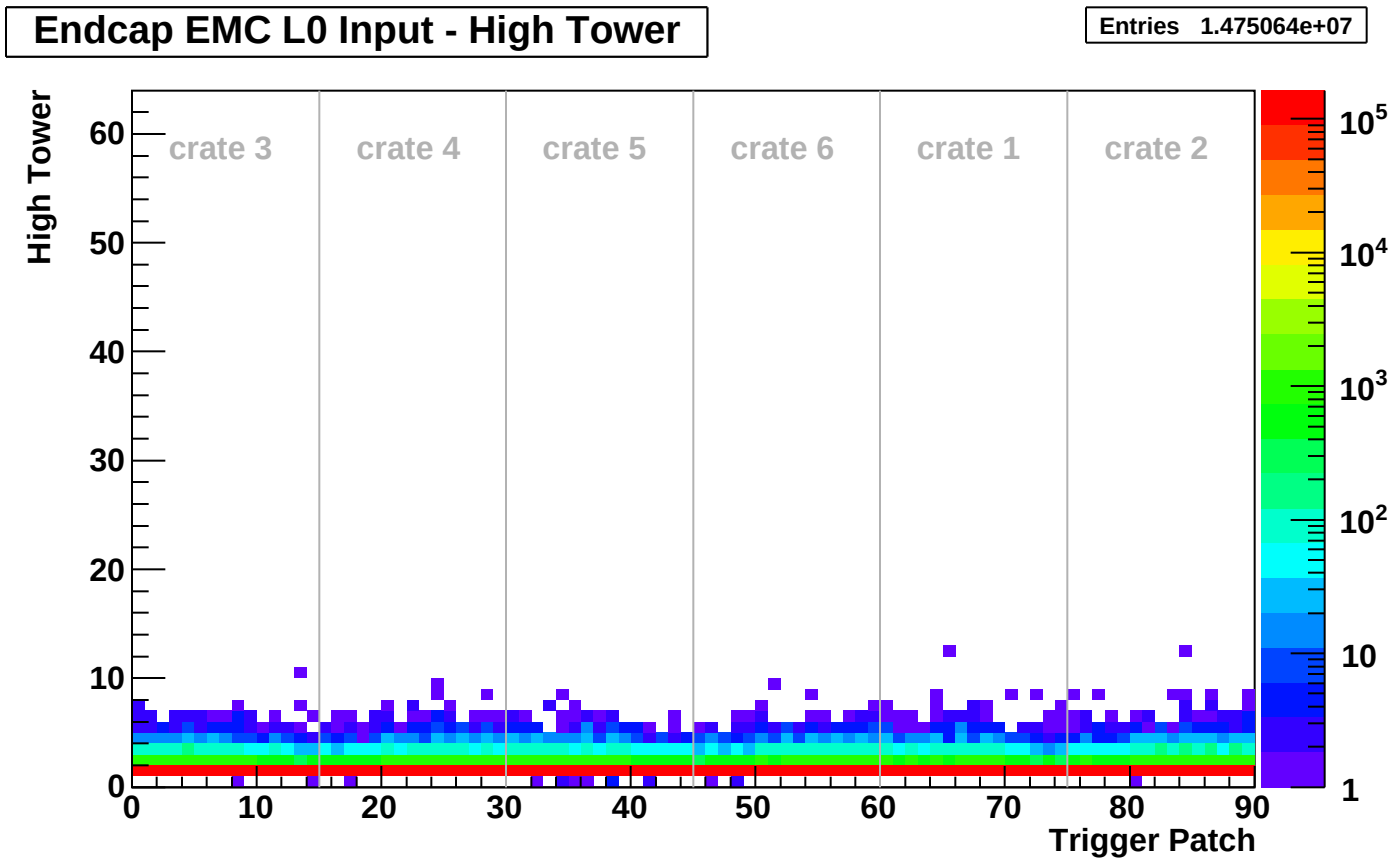

BunchId7Bit (all events)
Entries 163896
0 20 40 60 80 100 120

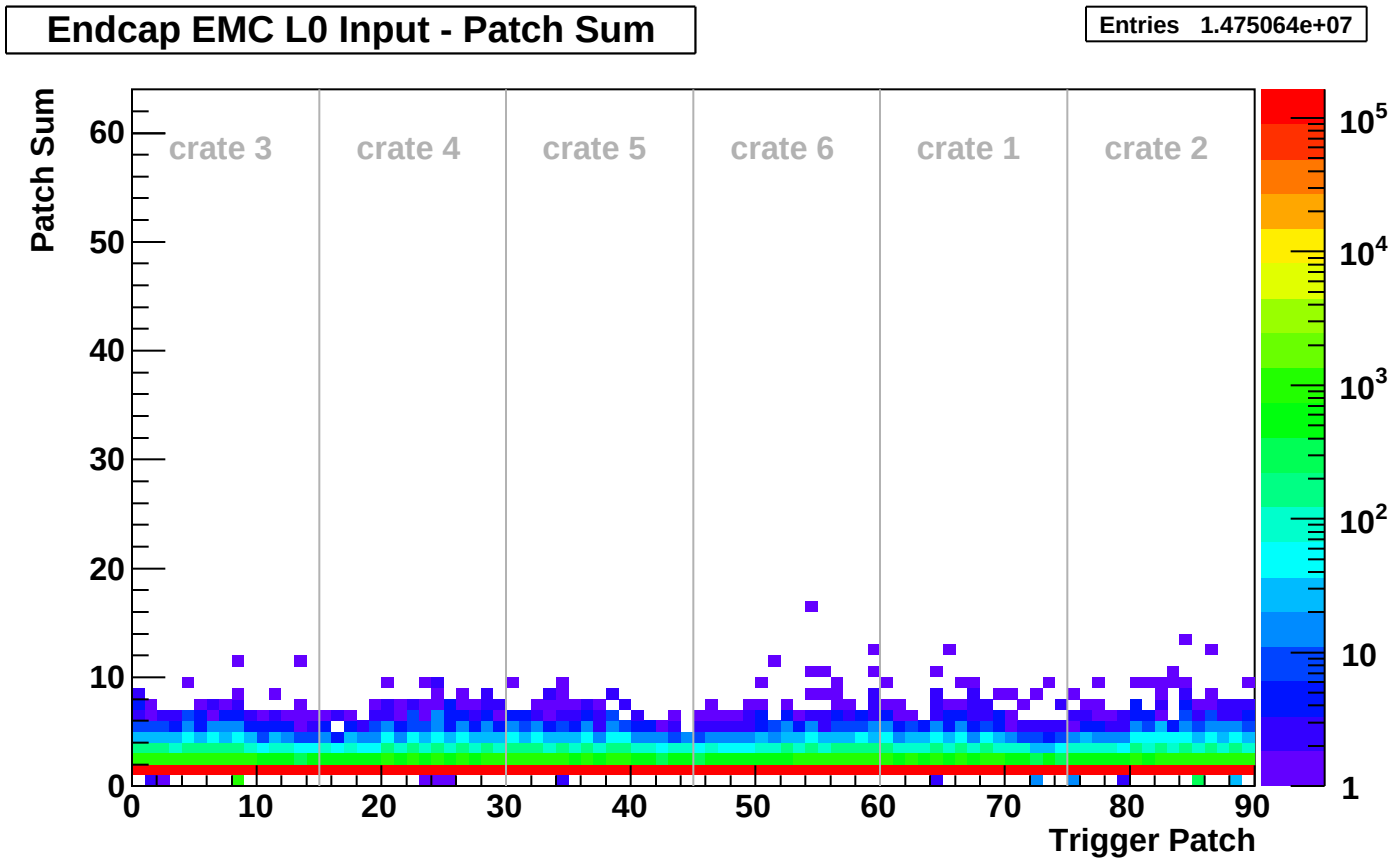

BunchId7Bit (JP1)
Entries 163896
0 20 40 60 80 100 120

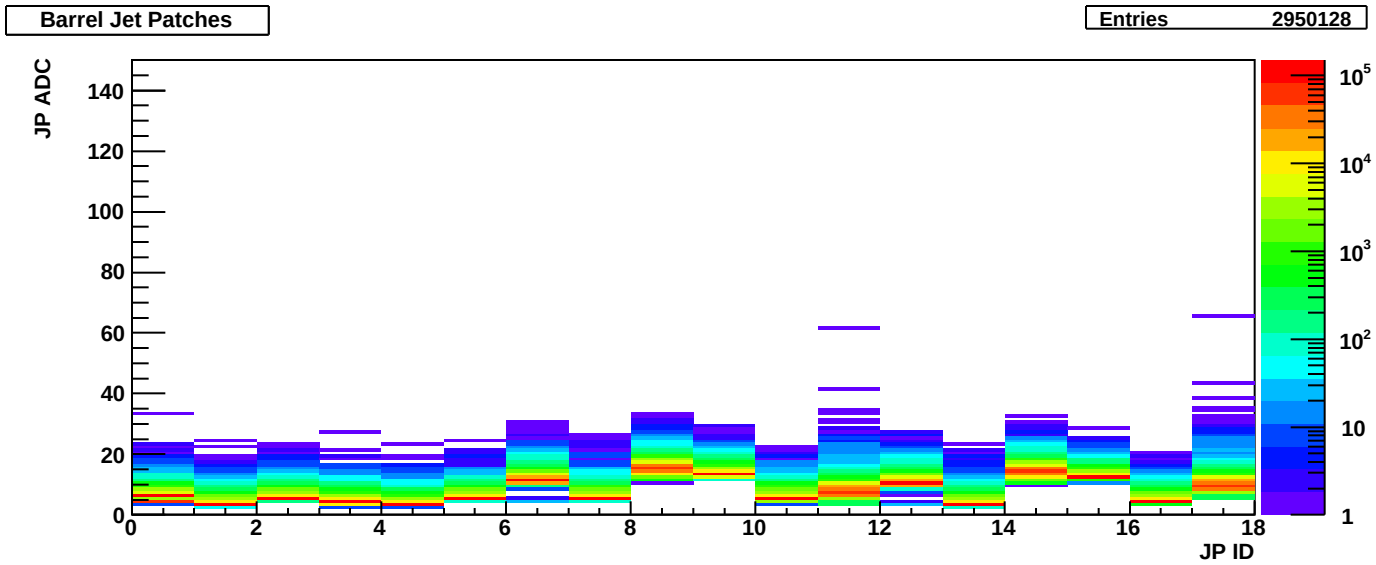

BunchId7Bit (JP2||AJP)
Entries 163896
0 20 40 60 80 100 120

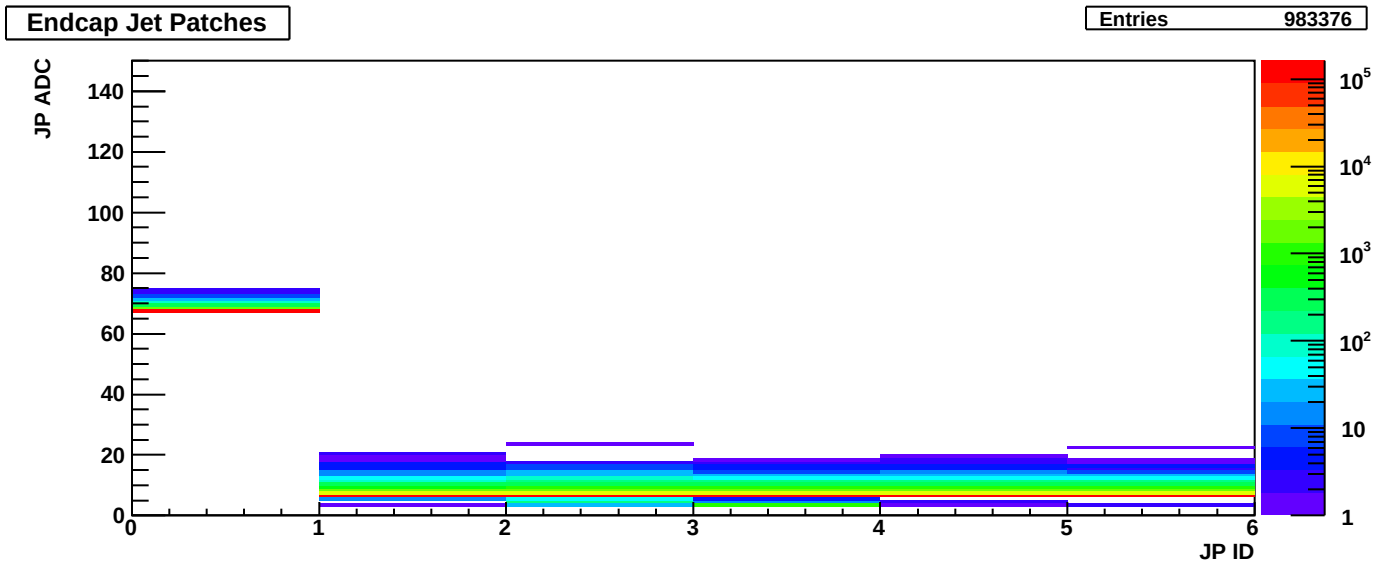

Barrel EMC L0 Input - High Tower
Entries 4.91688e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

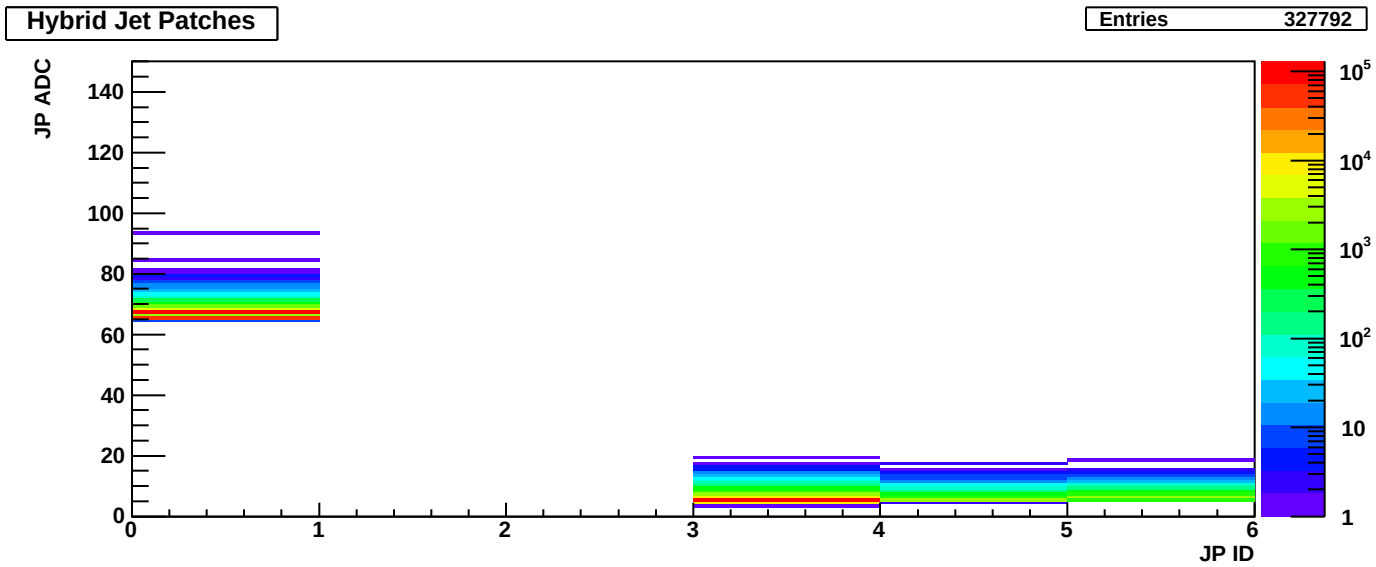

Barrel EMC L0 Input - Patch Sum
Entries 4.91688e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

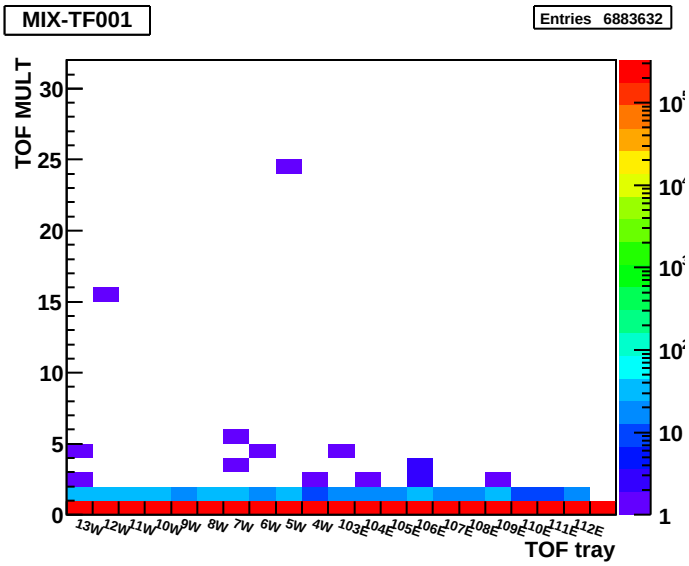

Endcap EMC L0 Input - High Tower
Entries 1.475064e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

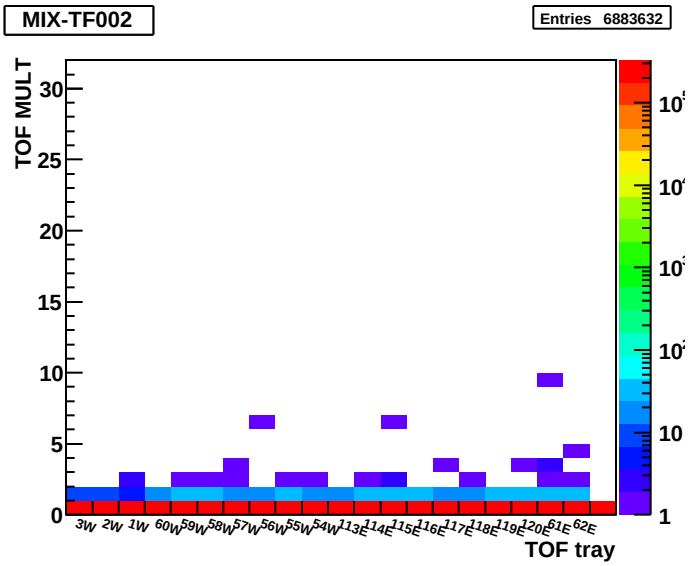

Endcap EMC L0 Input - Patch Sum
Entries 1.475064e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

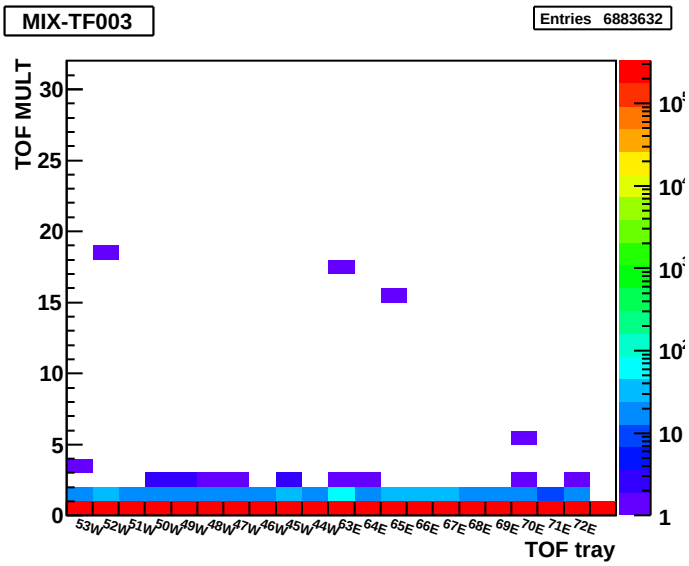

Barrel Jet Patches
Entries 2950128
JP ADC
JP ID

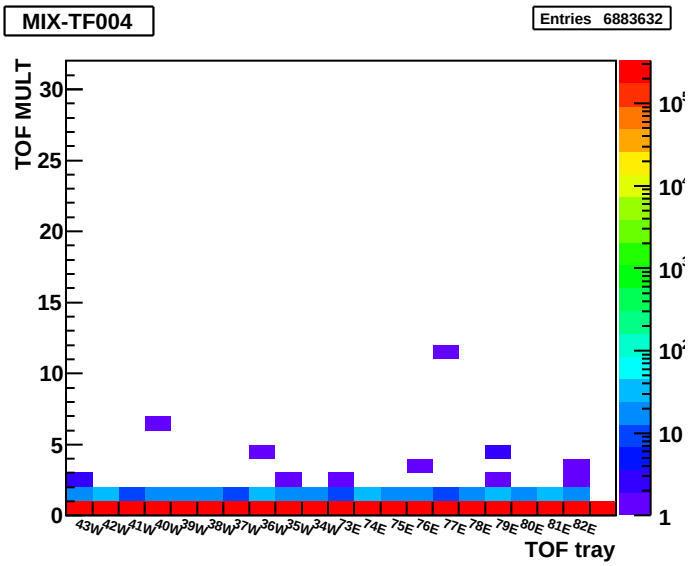

Endcap Jet Patches
Entries 983376
JP ADC
JP ID

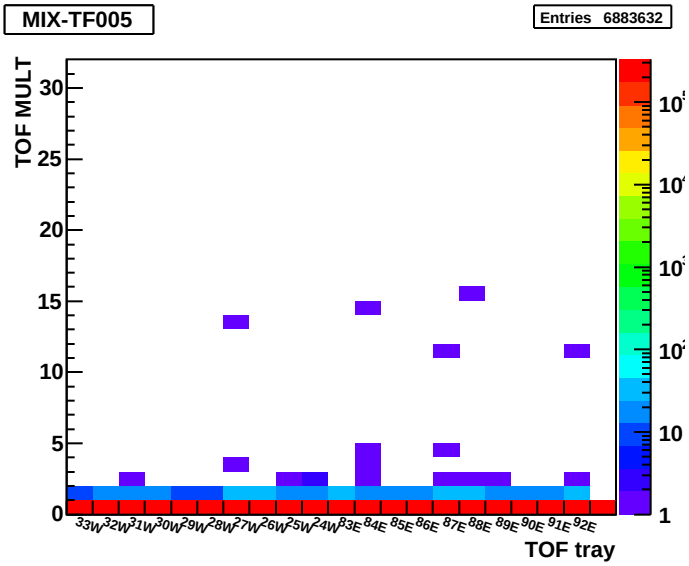

Hybrid Jet Patches
Entries 327792
JP ADC
JP ID

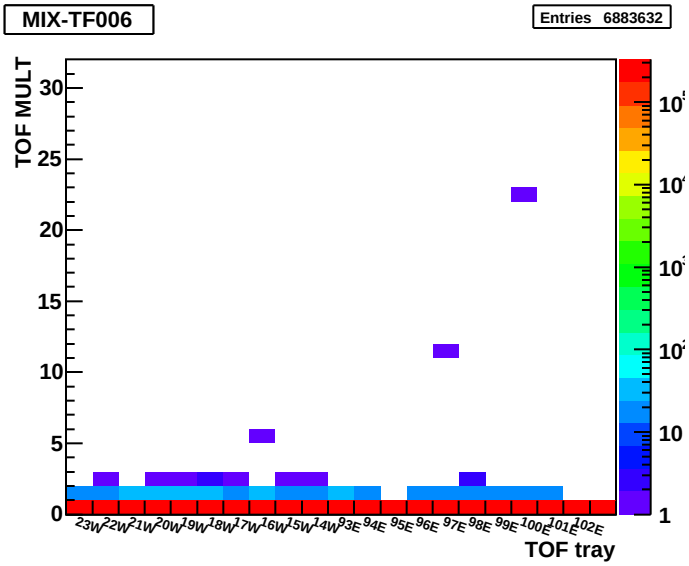

MIX-TF001
Entries 6883632
TOF MULT
TOF tray

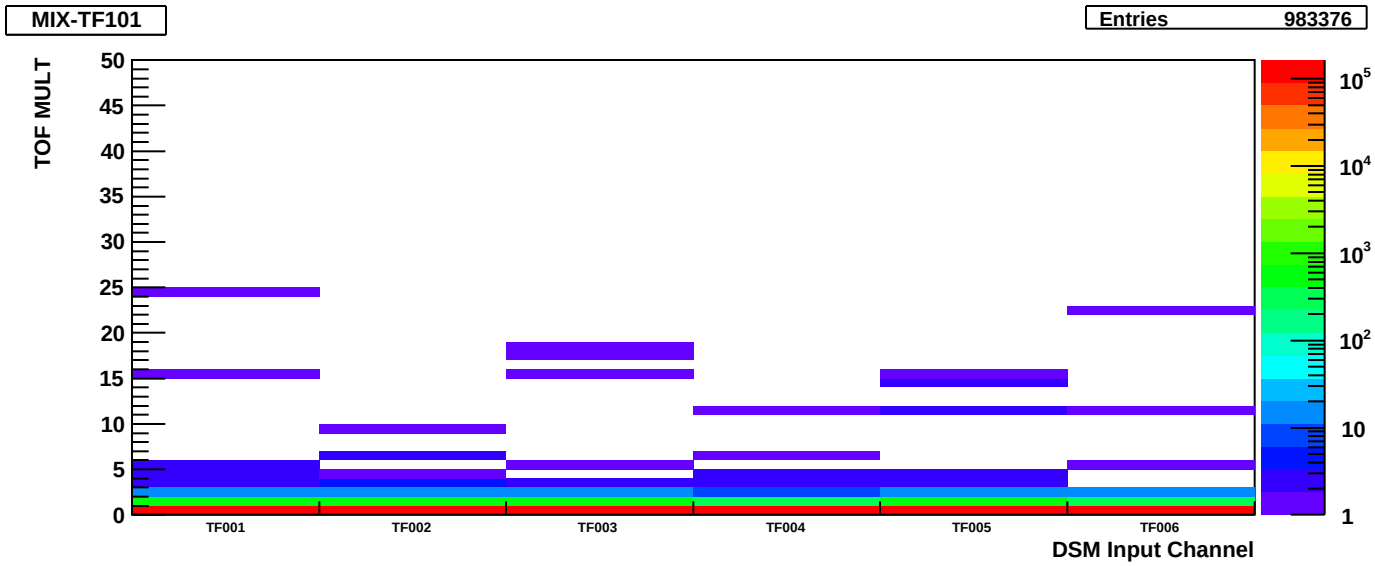

MIX-TF002
Entries 6883632
TOF MULT
TOF tray

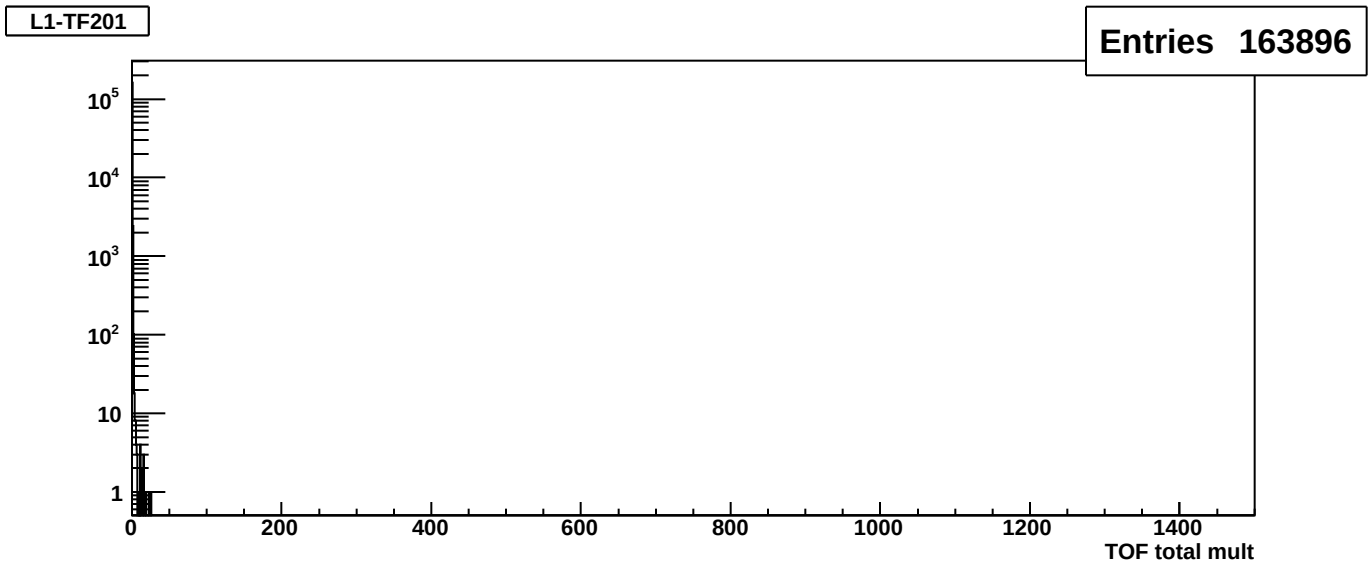

MIX-TF003
Entries 6883632
TOF MULT
TOF tray

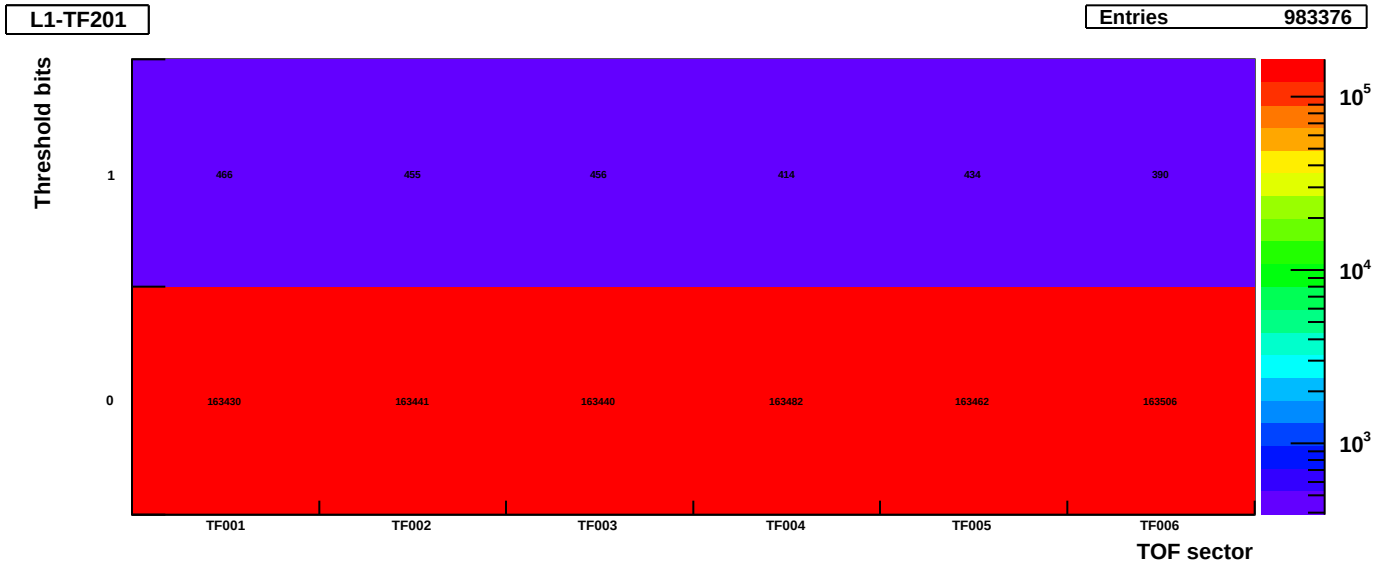

MIX-TF004
Entries 6883632
TOF MULT
TOF tray

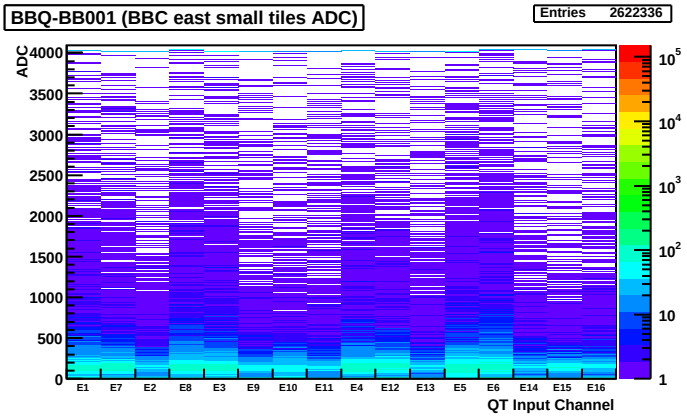

MIX-TF005
Entries 6883632
TOF MULT
TOF tray

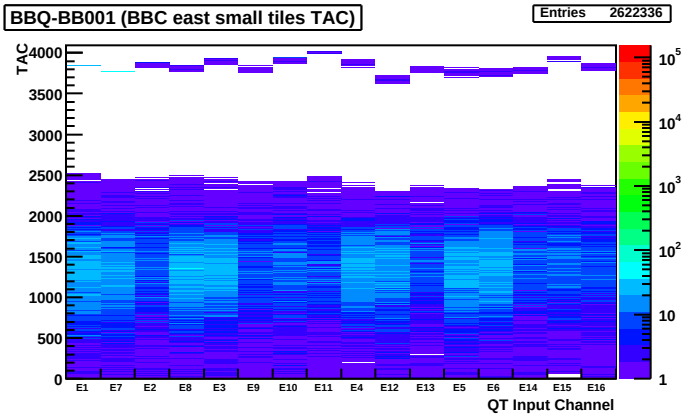

MIX-TF006
Entries 6883632
TOF MULT
TOF tray

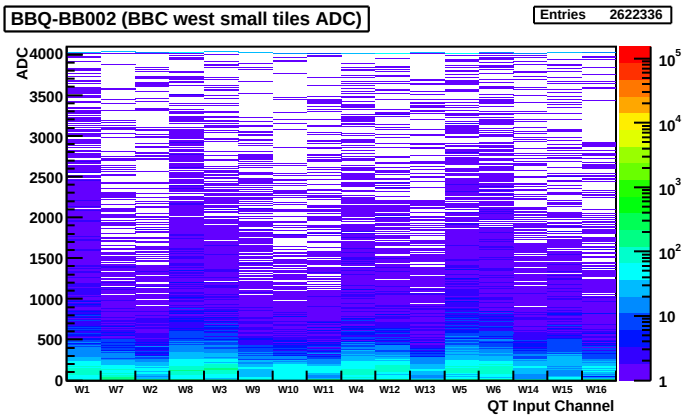

MIX-TF101
Entries 983376
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

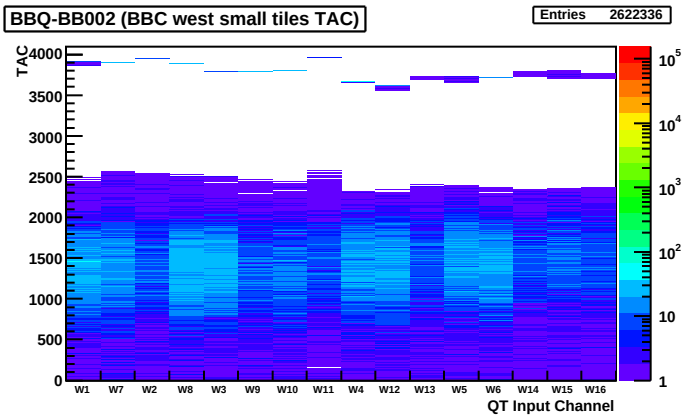

L1-TF201
Entries 163896
TOF total mult

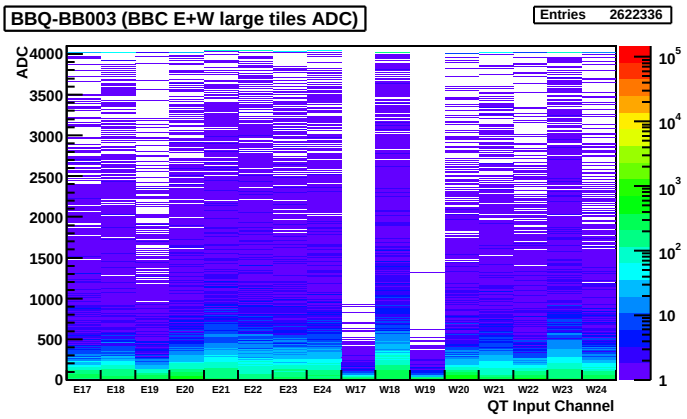

L1-TF201
Entries 983376
Threshold bits
466
455
456
414
434
390
163430
163441
163440
163482
163462
163506
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

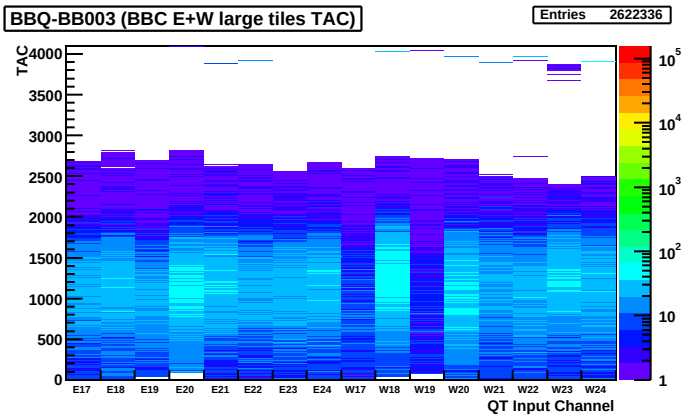

BBQ-BB001 (BBC east small tiles ADC)
Entries 2622336
ADC
QT Input Channel

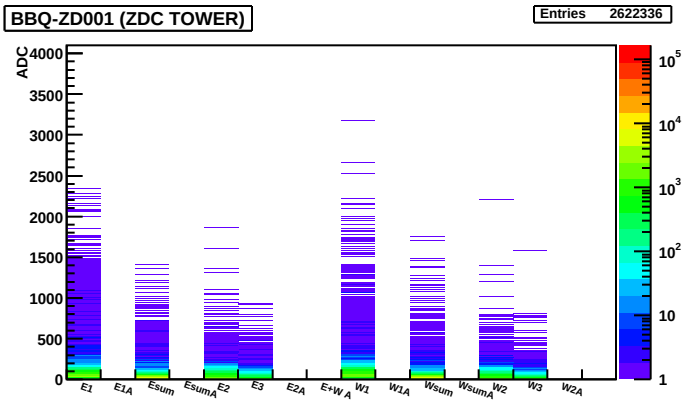

BBQ-BB001 (BBC east small tiles TAC)
Entries 2622336
TAC
QT Input Channel

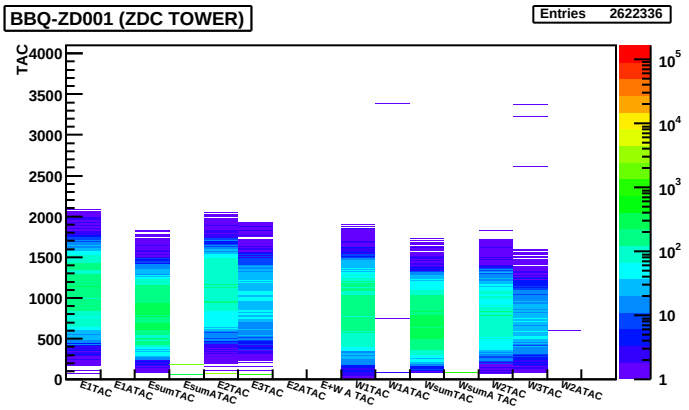

BBQ-BB002 (BBC west small tiles ADC)
Entries 2622336
ADC
QT Input Channel

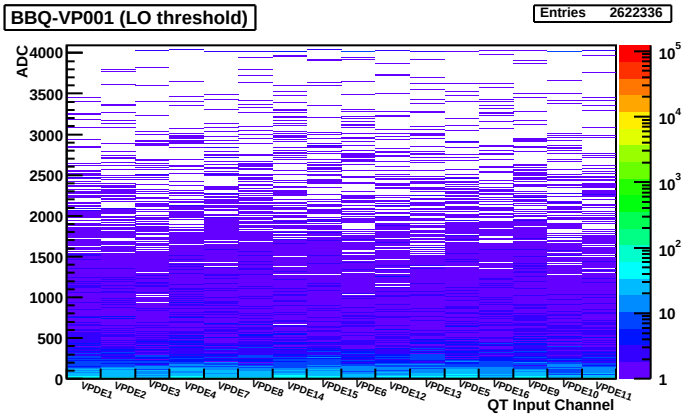

BBQ-BB002 (BBC west small tiles TAC)
Entries 2622336
TAC
QT Input Channel

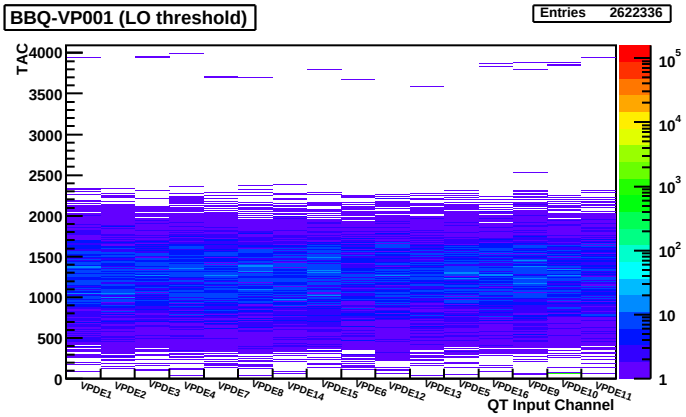

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2622336
ADC
QT Input Channel

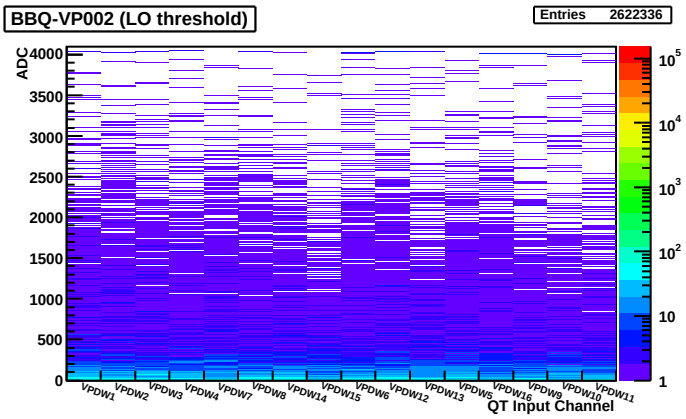

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2622336
TAC
QT Input Channel

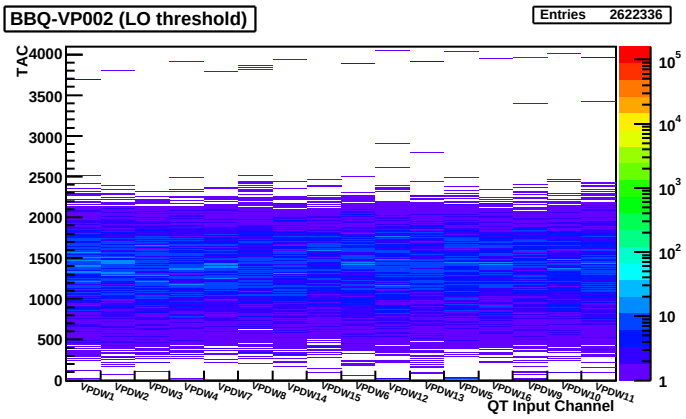

BBQ-ZD001 (ZDC TOWER)
Entries 2622336
ADC

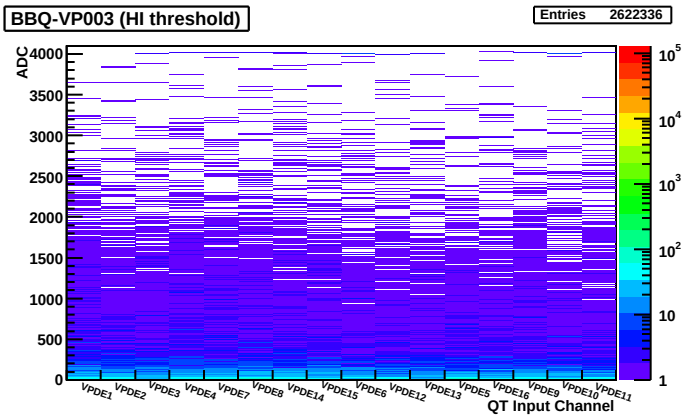

BBQ-ZD001 (ZDC TOWER)
Entries 2622336
TAC

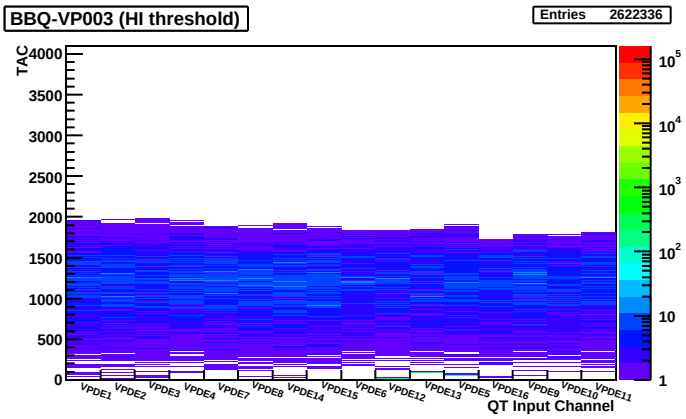

BBQ-VP001 (LO threshold)
Entries 2622336
ADC
QT Input Channel

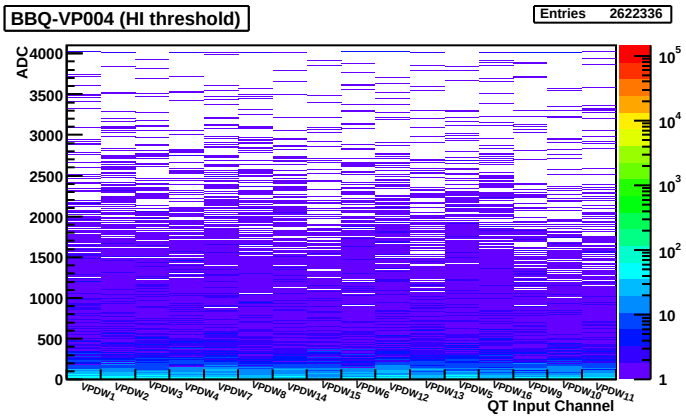

BBQ-VP001 (LO threshold)
Entries 2622336
TAC
QT Input Channel

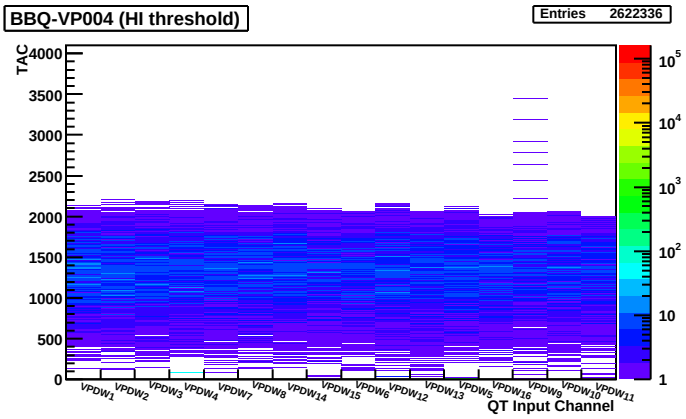

BBQ-VP002 (LO threshold)
Entries 2622336
ADC
QT Input Channel

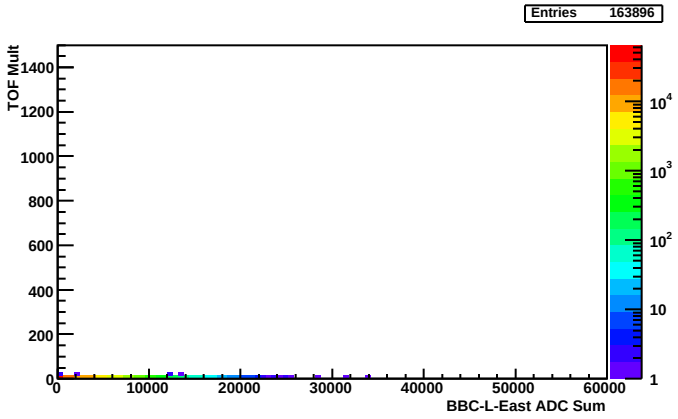

BBQ-VP002 (LO threshold)
Entries 2622336
TAC
QT Input Channel

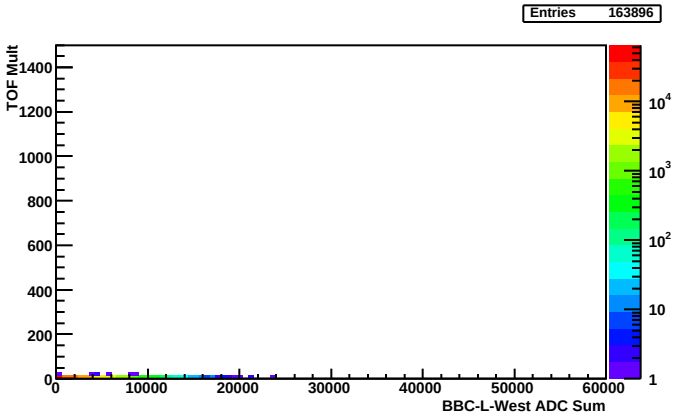

BBQ-VP003 (HI threshold)
Entries 2622336
ADC
QT Input Channel

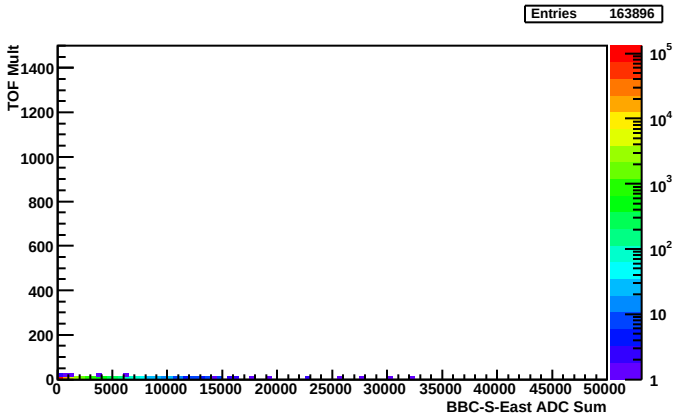

BBQ-VP003 (HI threshold)
Entries 2622336
TAC
QT Input Channel

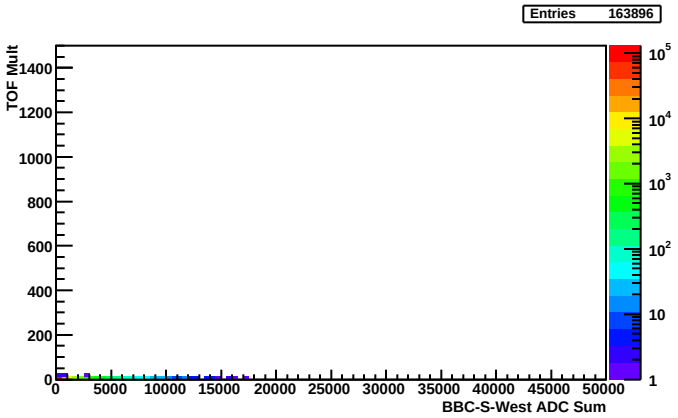

BBQ-VP004 (HI threshold)
Entries 2622336
ADC
QT Input Channel

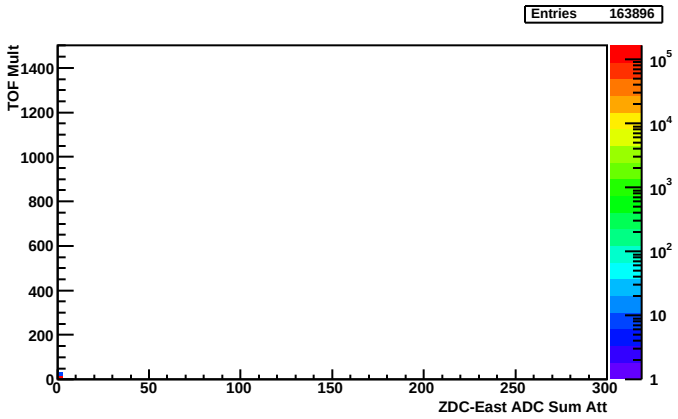

BBQ-VP004 (HI threshold)
Entries 2622336
TAC
QT Input Channel

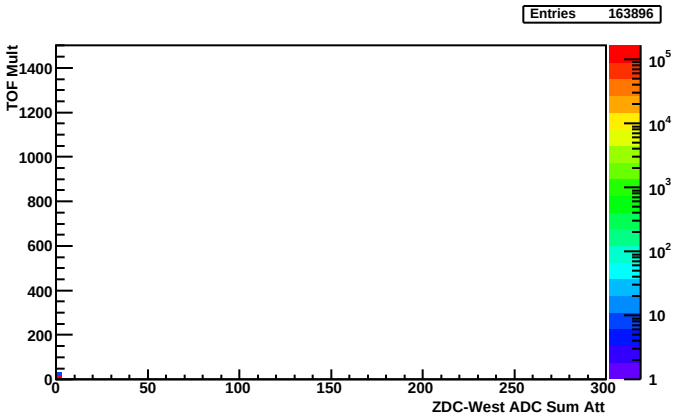

Entries 163896
TOF Mult
BBC-L-East ADC Sum

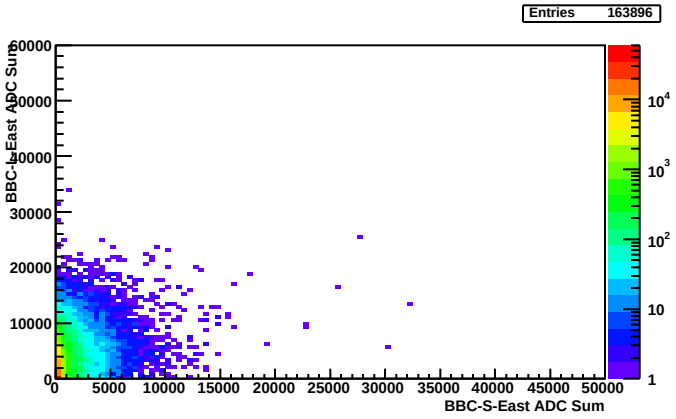

Entries 163896
TOF Mult
BBC-L-West ADC Sum

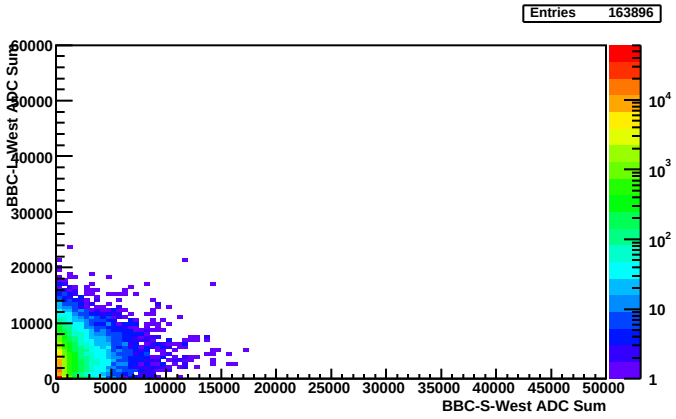

Entries 163896
TOF Mult
BBC-S-East ADC Sum

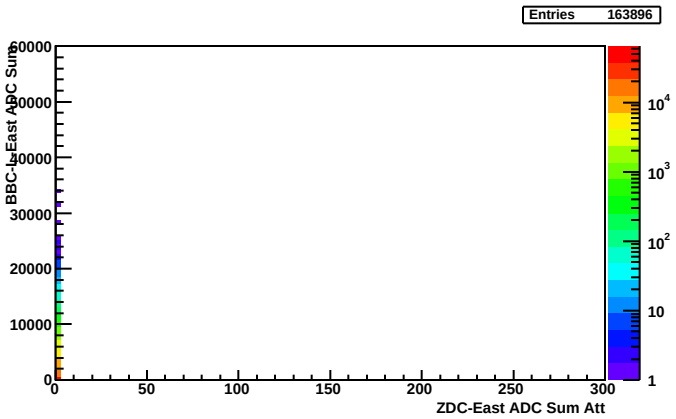

Entries 163896
TOF Mult
BBC-S-West ADC Sum

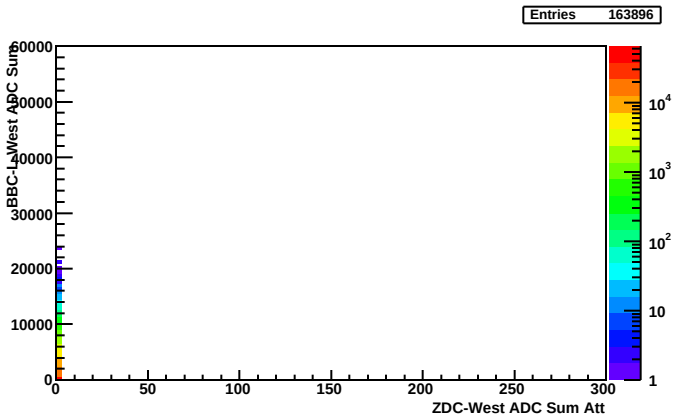

Entries 163896
TOF Mult
ZDC-East ADC Sum Att

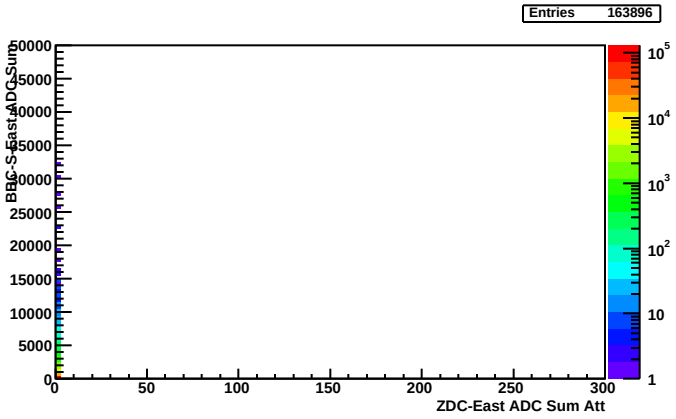

Entries 163896
TOF Mult
ZDC-West ADC Sum Att

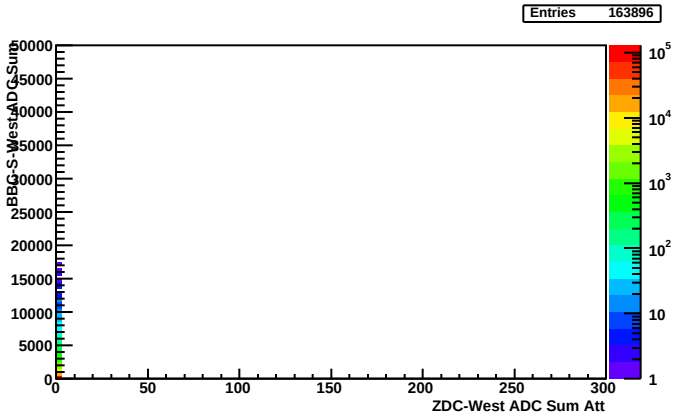

Entries 163896
BBC-L-East ADC Sum
BBC-S-East ADC Sum

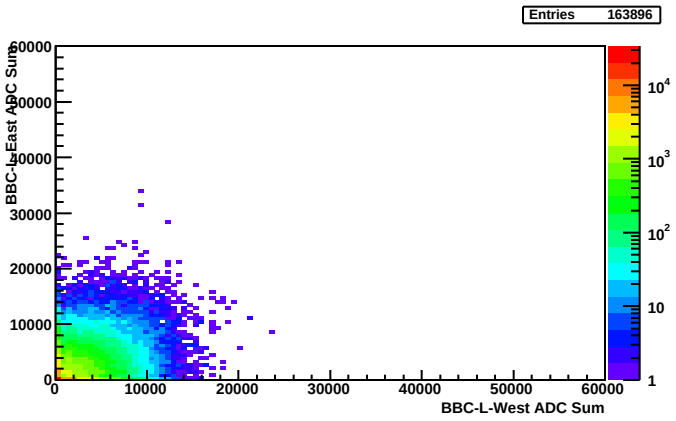

Entries 163896
BBC-L-West ADC Sum
BBC-S-West ADC Sum

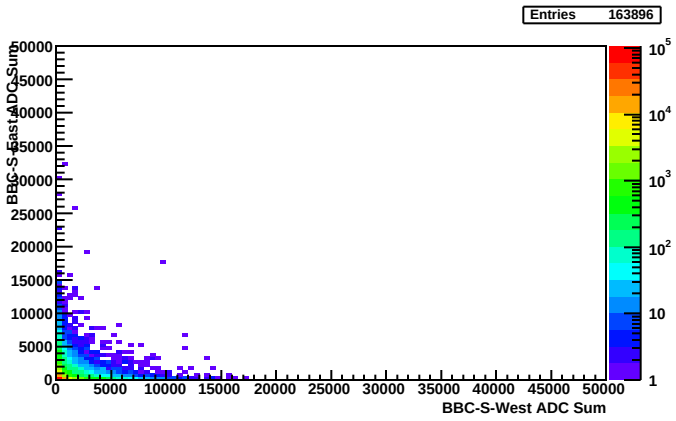

Entries 163896
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

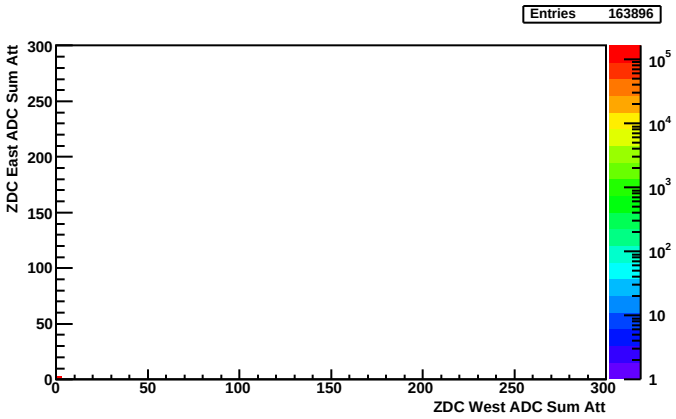

Entries 163896
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

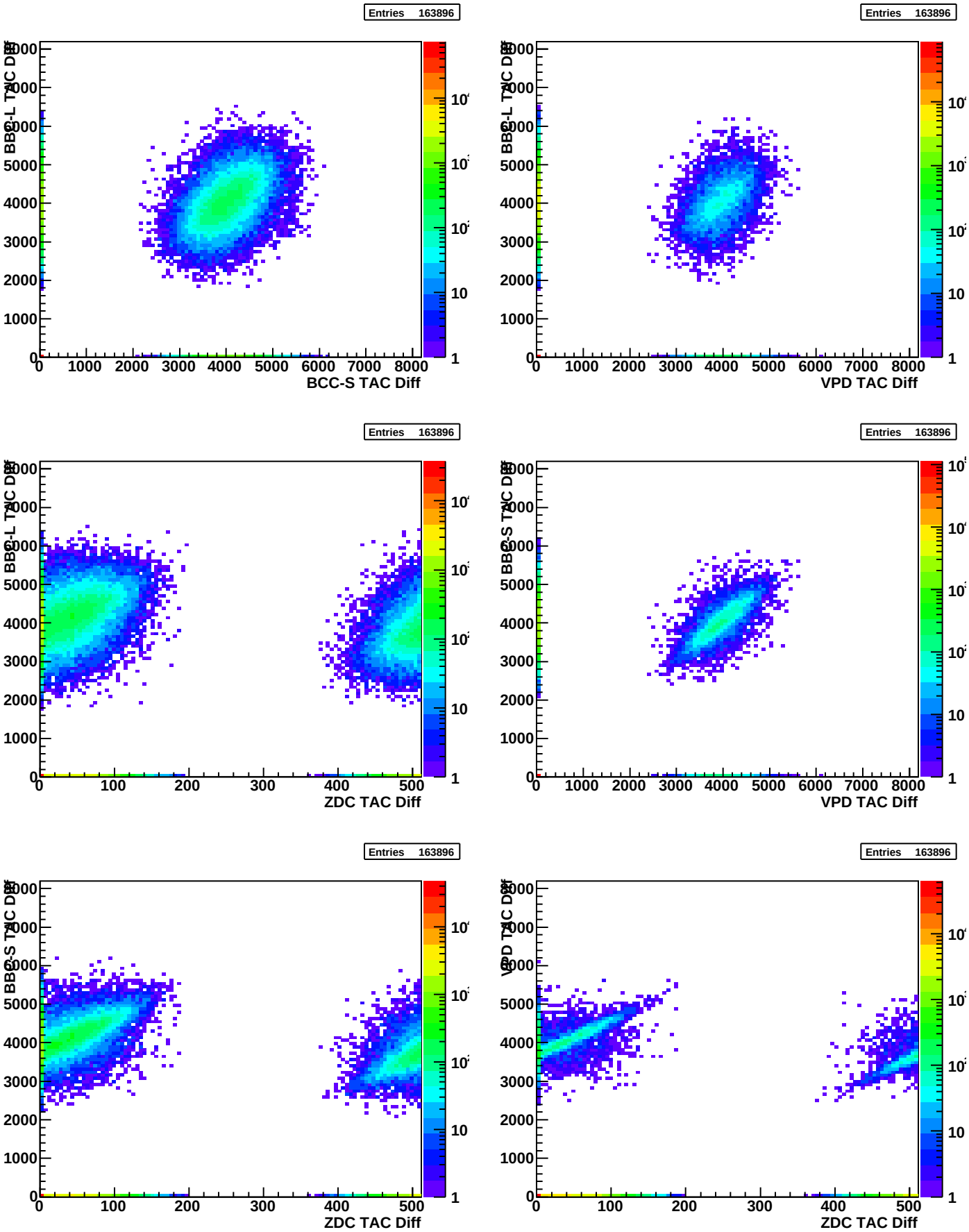

Entries 163896
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

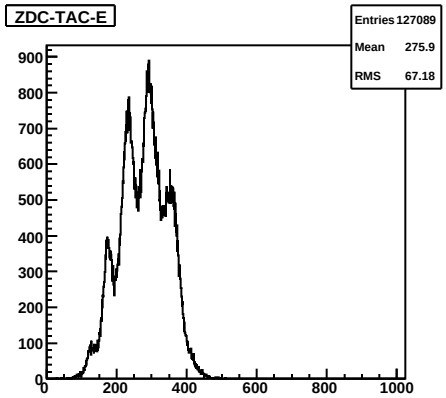

Entries 163896
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

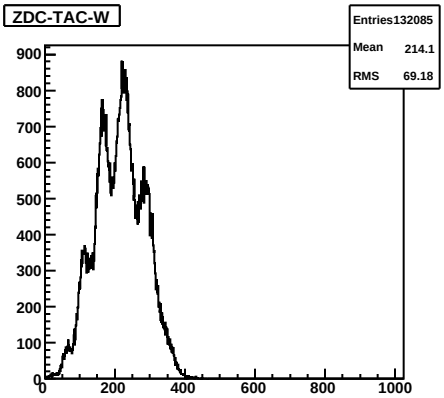

Entries 163896
BBC-L-East ADC Sum
BBC-L-West ADC Sum

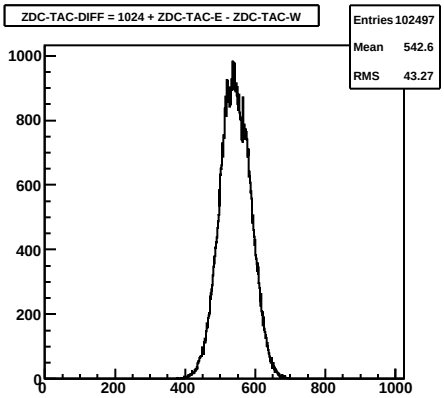

Entries 163896
BBC-S-East ADC Sum
BBC-S-West ADC Sum

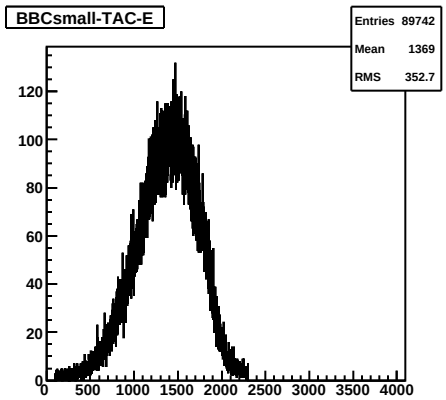

Entries 163896
ZDC East ADC Sum Att
ZDC West ADC Sum Att

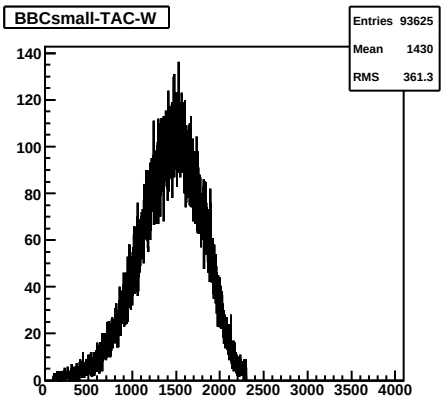

Entries 163896
BBC-L TAC Diff
BCC-S TAC Diff
Entries 163896
BBC-L TAC Diff
VPD TAC Diff
Entries 163896
BBC-L TAC Diff
ZDC TAC Diff
Entries 163896
BBC-S TAC Diff
VPD TAC Diff
Entries 163896
BBC-S TAC Diff
ZDC TAC Diff
Entries 163896
VPD TAC Diff
ZDC TAC Diff

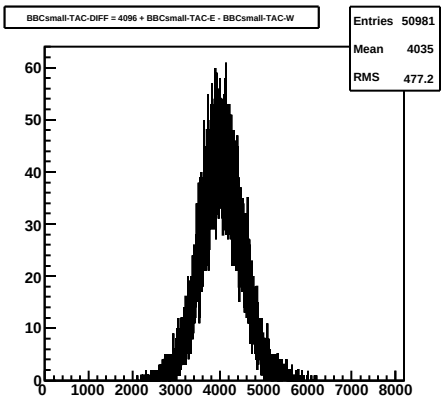

ZDC-TAC-E
Entries 127089
Mean 275.9
RMS 67.18

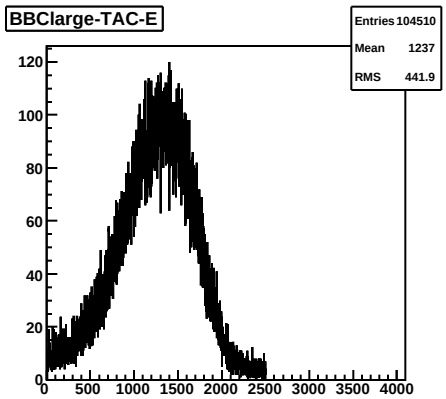

ZDC-TAC-W
Entries 132085
Mean 214.1
RMS 69.18

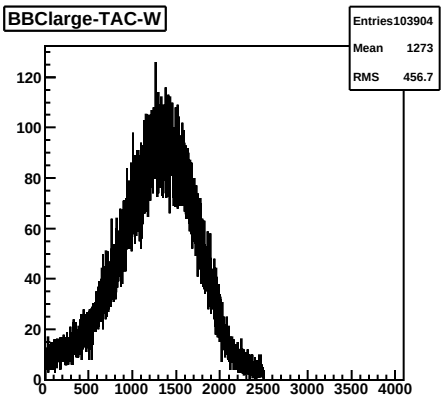

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 102497
Mean 542.6
RMS 43.27

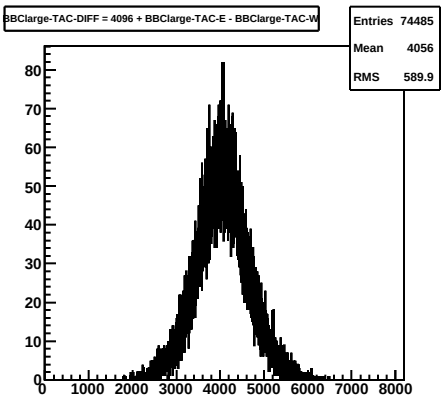

BBCsmall-TAC-E
Entries 89742
Mean 1369
RMS 352.7

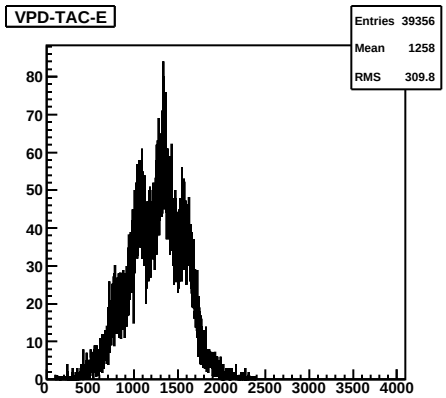

BBCsmall-TAC-W
Entries 93625
Mean 1430
RMS 361.3

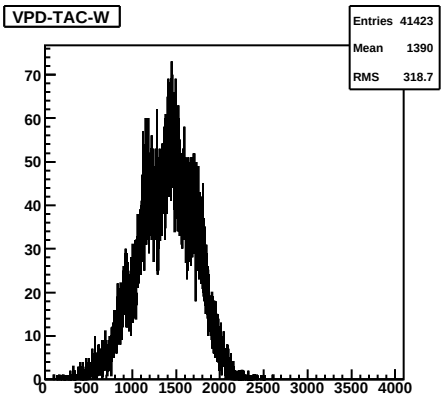

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 50981
Mean 4035
RMS 477.2

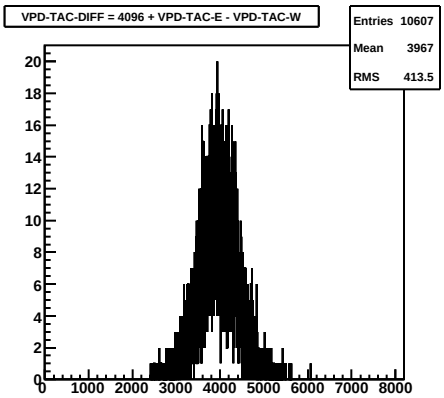

BBClarge-TAC-E
Entries 104510
Mean 1237
RMS 441.9

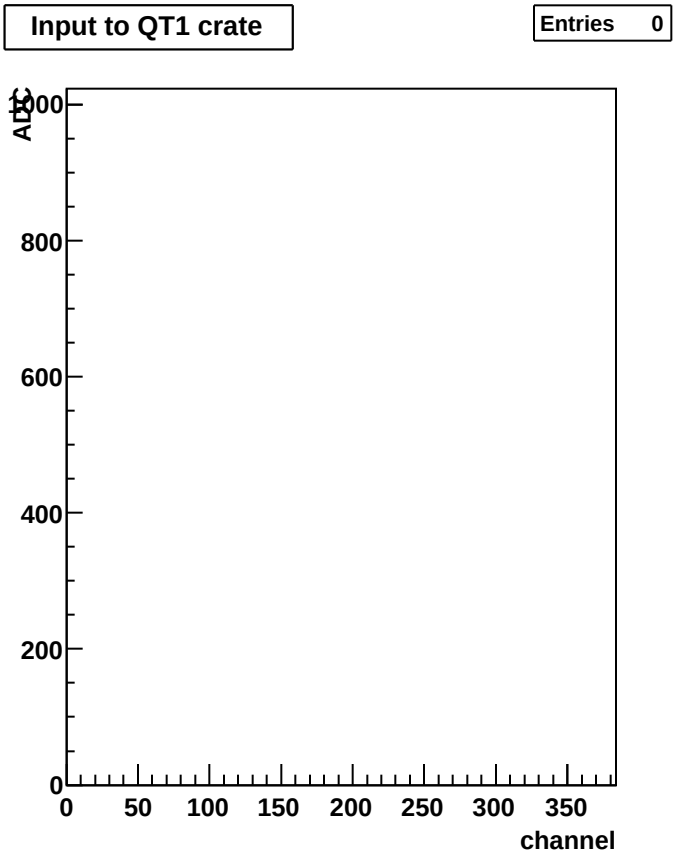

BBClarge-TAC-W
Entries 103904
Mean 1273
RMS 456.7

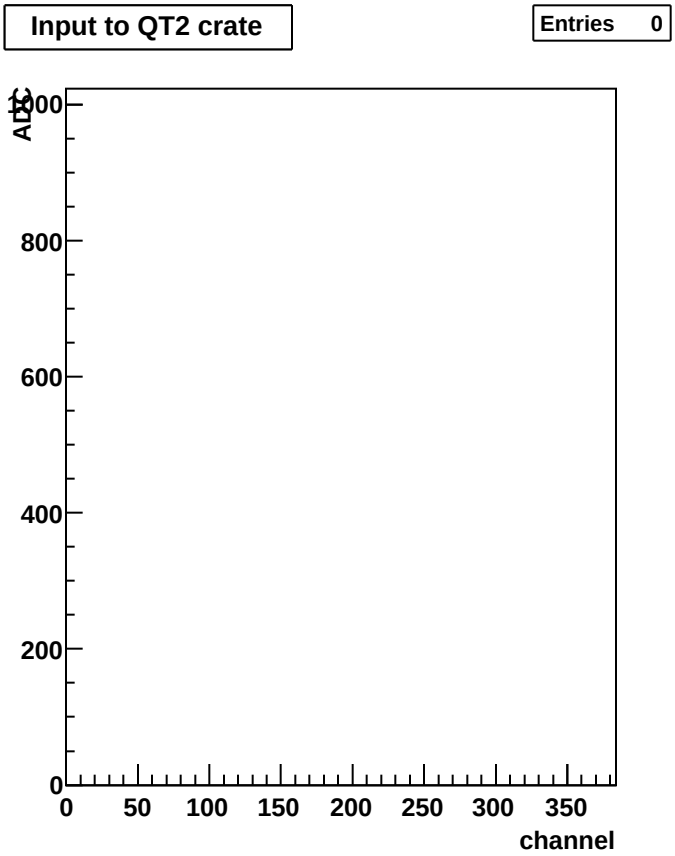

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 74485
Mean 4056
RMS 589.9

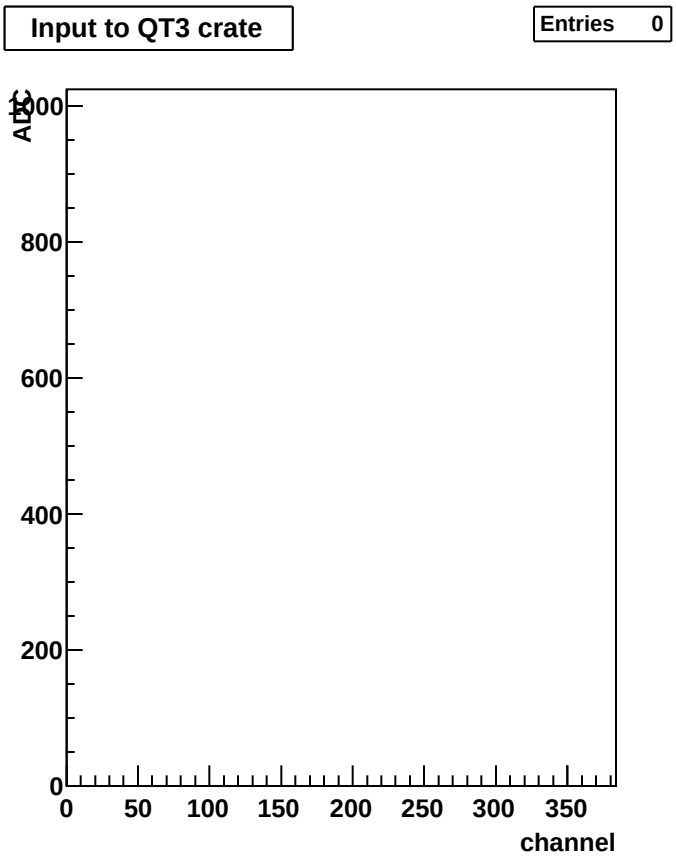

VPD-TAC-E
Entries 39356
Mean 1258
RMS 309.8

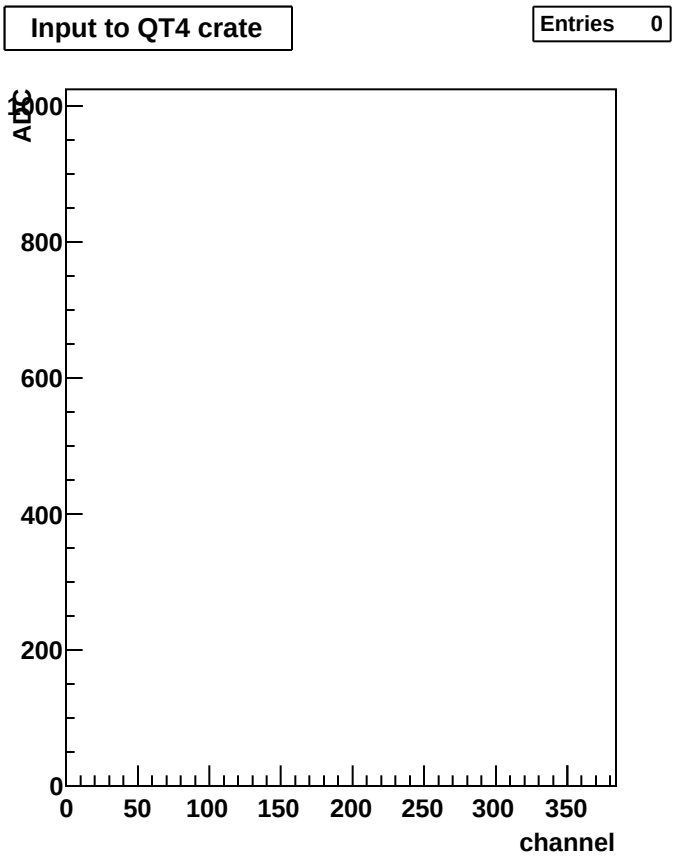

VPD-TAC-W
Entries 41423
Mean 1390
RMS 318.7

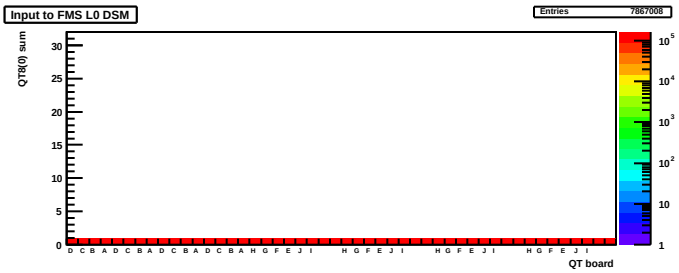

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 10607
Mean 3967
RMS 413.5

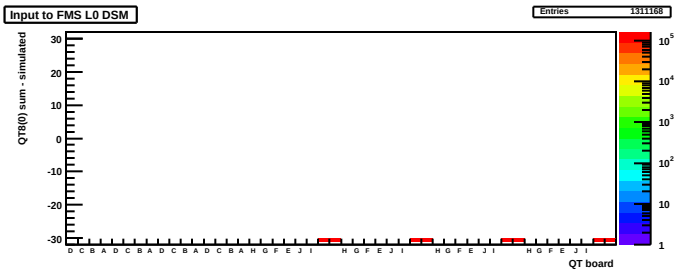

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

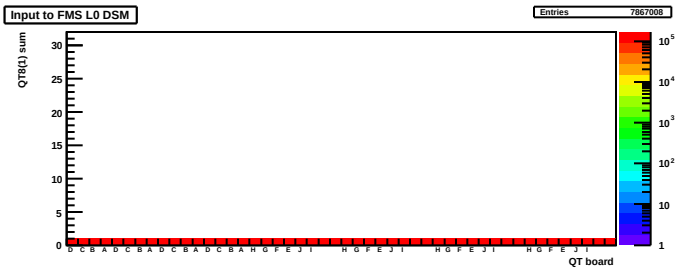

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

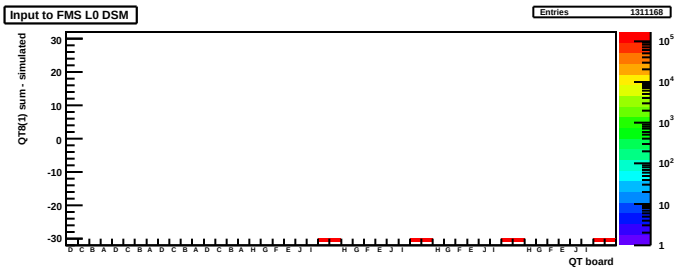

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

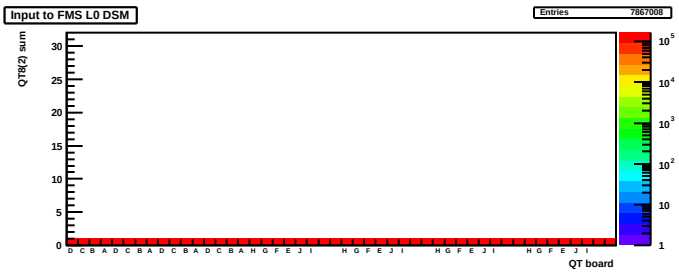

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

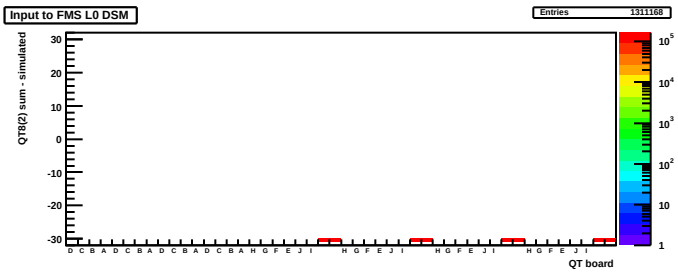

Input to FMS L0 DSM
Entries 7867008
QT8(0) sum
QT board

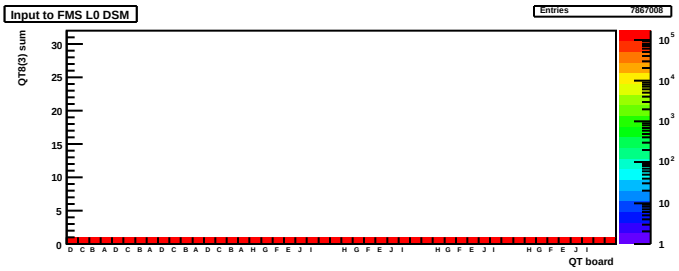

Input to FMS L0 DSM
Entries 1311168
QT8(0) sum - simulated
QT board

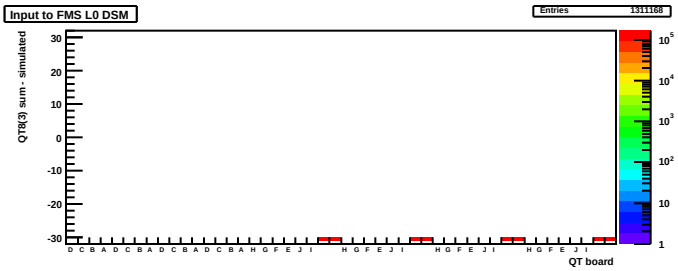

Input to FMS L0 DSM
Entries 7867008
QT8(1) sum
QT board

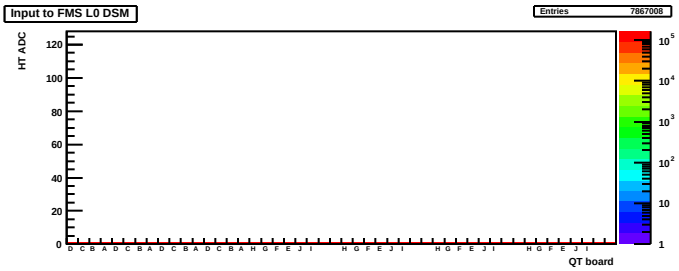

Input to FMS L0 DSM
Entries 1311168
QT8(1) sum - simulated
QT board

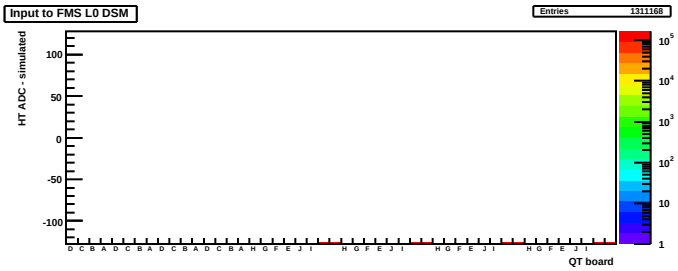

Input to FMS L0 DSM
Entries 7867008
QT8(2) sum
QT board

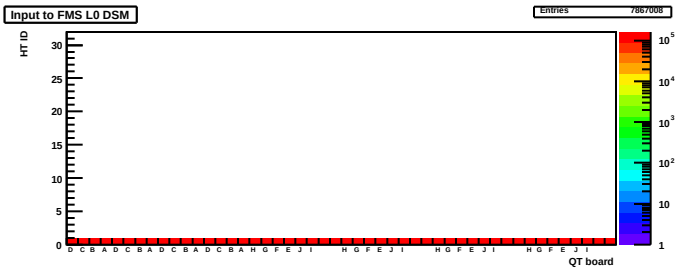

Input to FMS L0 DSM
Entries 1311168
QT8(2) sum - simulated
QT board

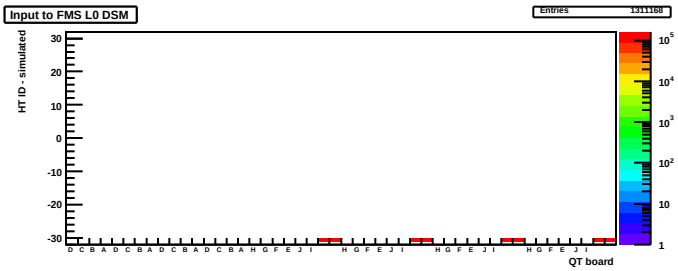

Input to FMS L0 DSM
Entries 7867008
QT8(3) sum
QT board

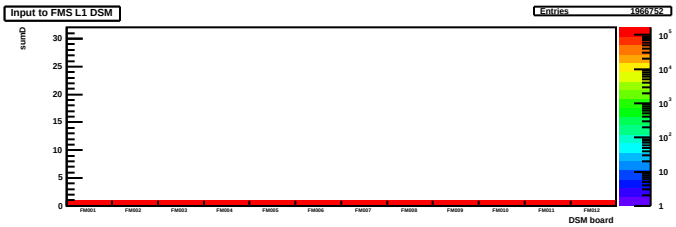

Input to FMS L0 DSM
Entries 1311168
QT8(3) sum - simulated
QT board

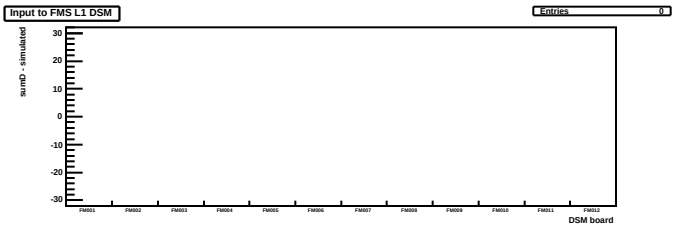

Input to FMS L0 DSM
Entries 7867008
HT ADC
QT board

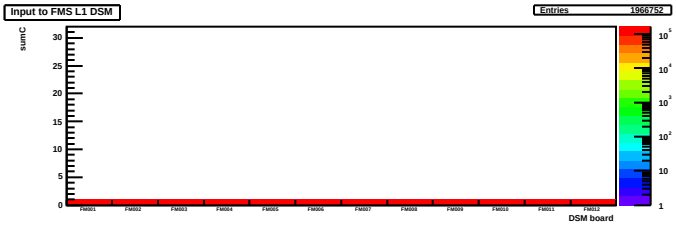

Input to FMS L0 DSM
Entries 1311168
HT ADC - simulated
QT board

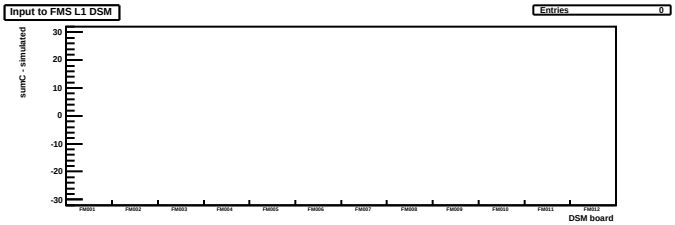

Input to FMS L0 DSM
Entries 7867008
HT ID
QT board

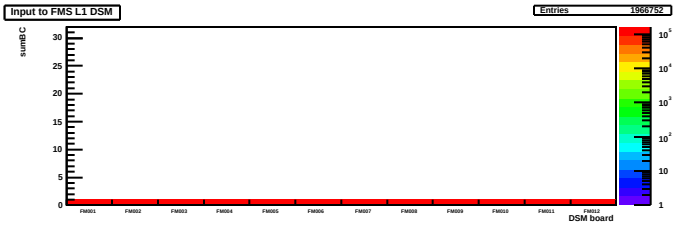

Input to FMS L0 DSM
Entries 1311168
HT ID - simulated
QT board

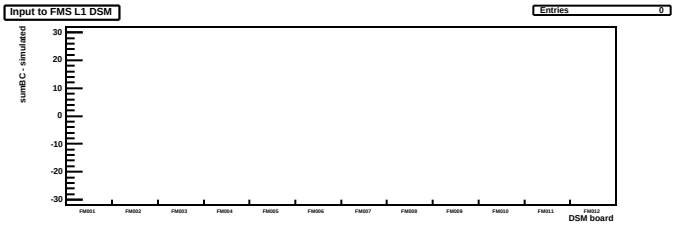

Input to FMS L1 DSM
Entries 1966752
sumD
DSM board

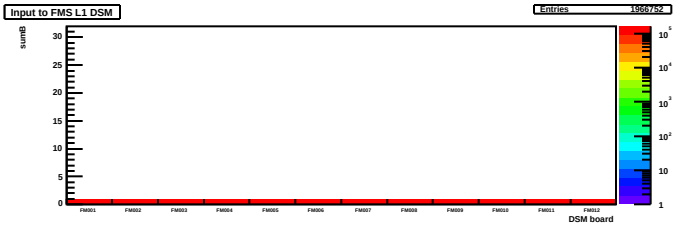

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

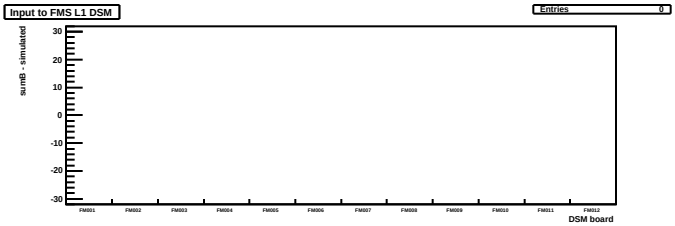

Input to FMS L1 DSM
Entries 1966752
sumC
DSM board

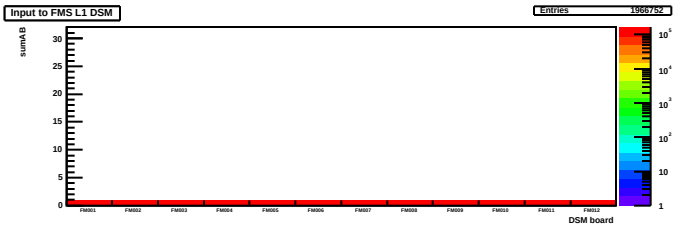

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

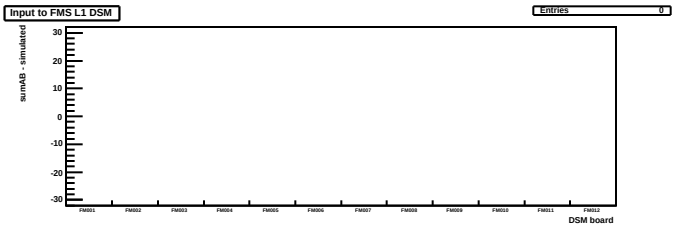

Input to FMS L1 DSM
Entries 1966752
sumBC
DSM board

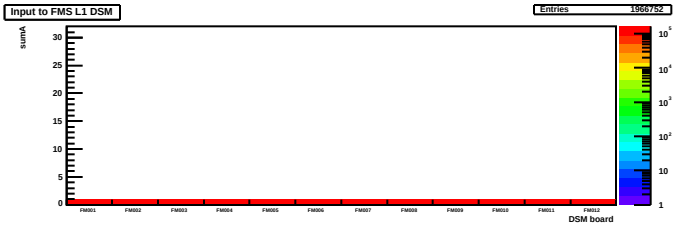

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

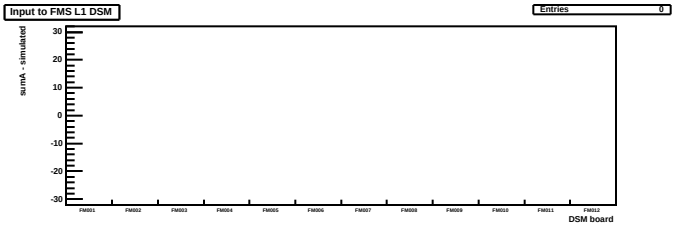

Input to FMS L1 DSM
Entries 1966752
sumB
DSM board

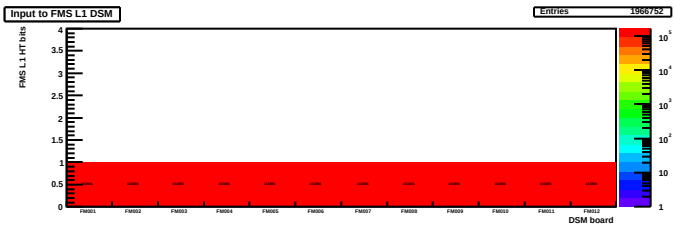

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

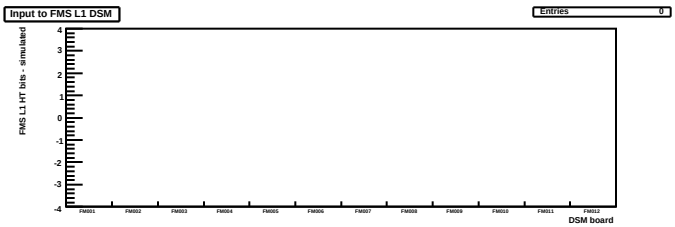

Input to FMS L1 DSM
Entries 1966752
sumAB
DSM board

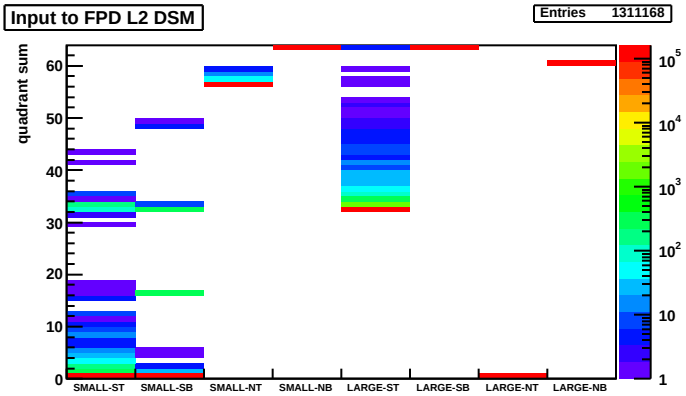

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

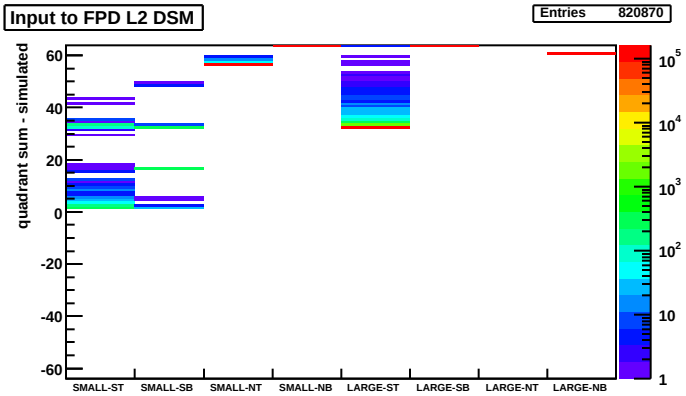

Input to FMS L1 DSM
Entries 1966752
sumA
DSM board

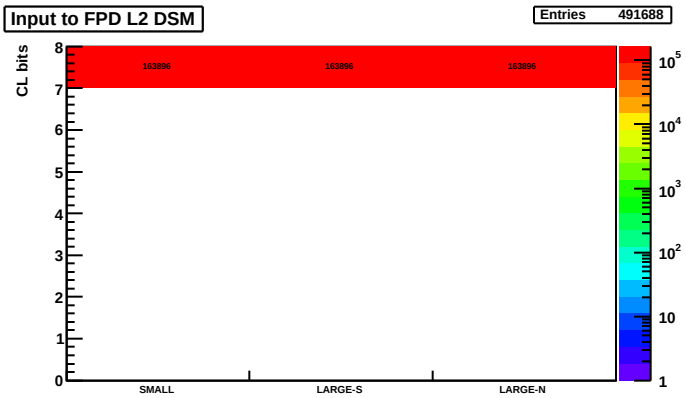

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

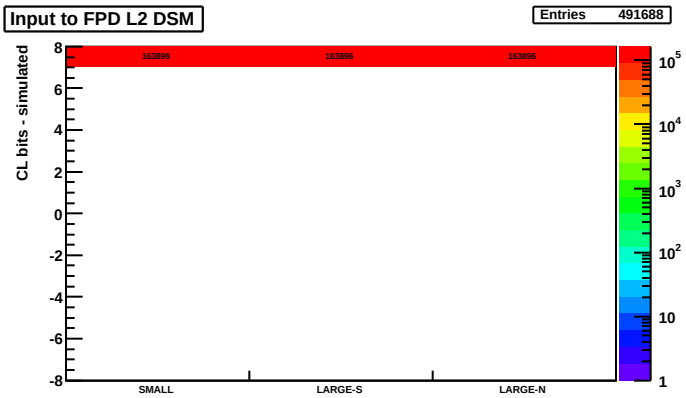

Input to FMS L1 DSM
Entries 1966752
FMS L1 HT bits
DSM board

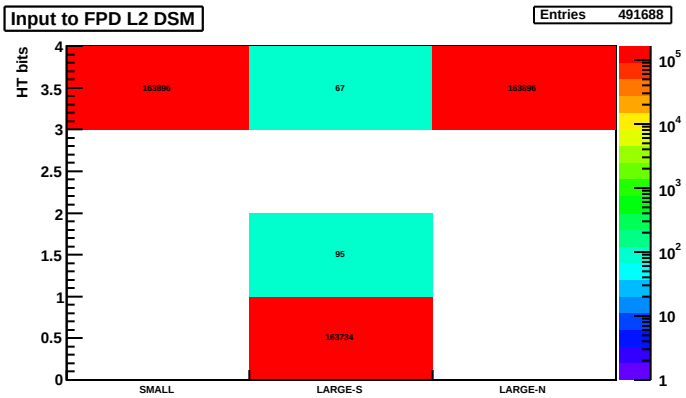

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

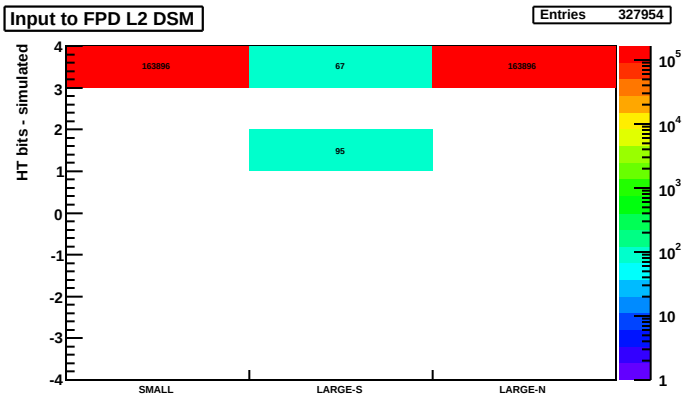

Input to FPD L2 DSM
Entries 1311168
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

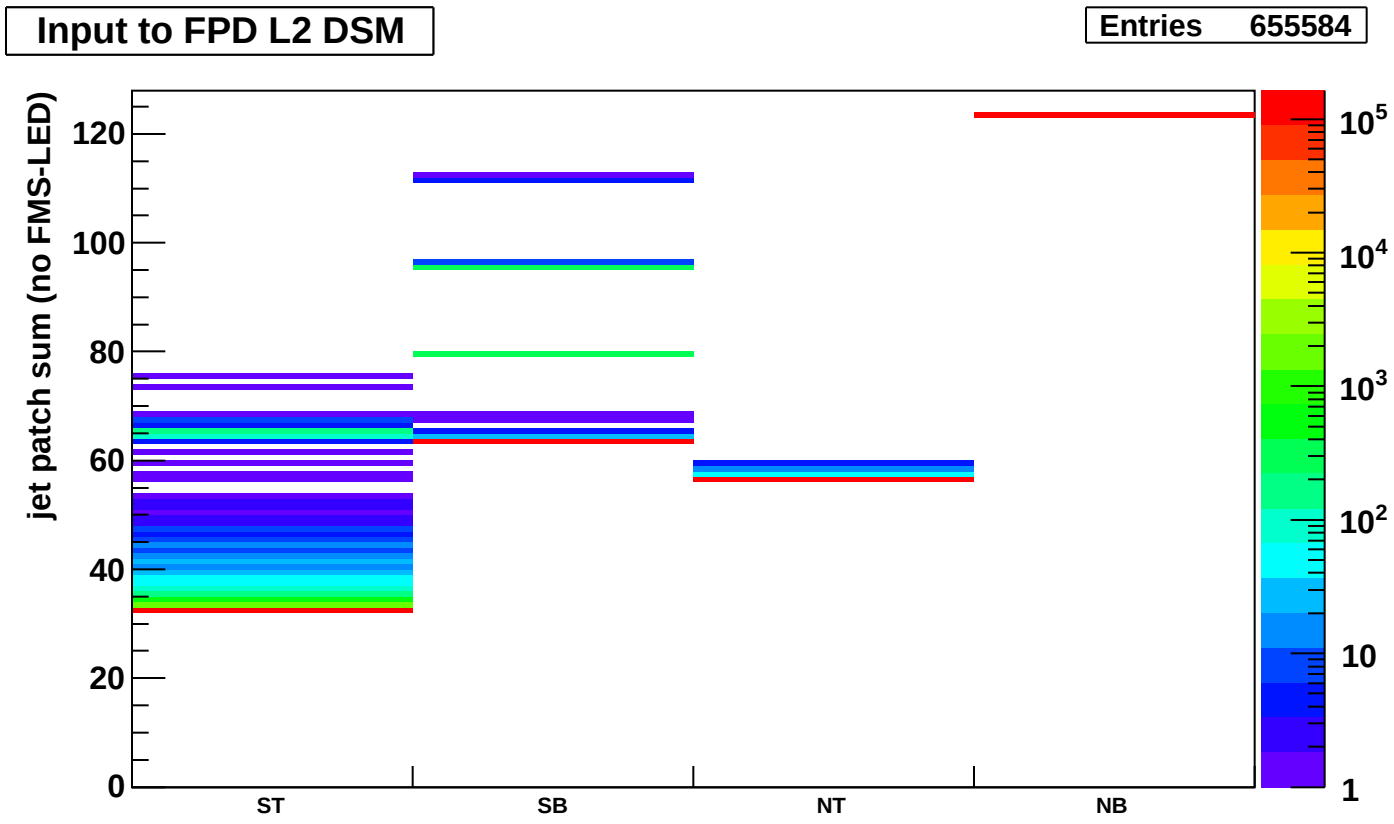

Input to FPD L2 DSM
Entries 820870
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

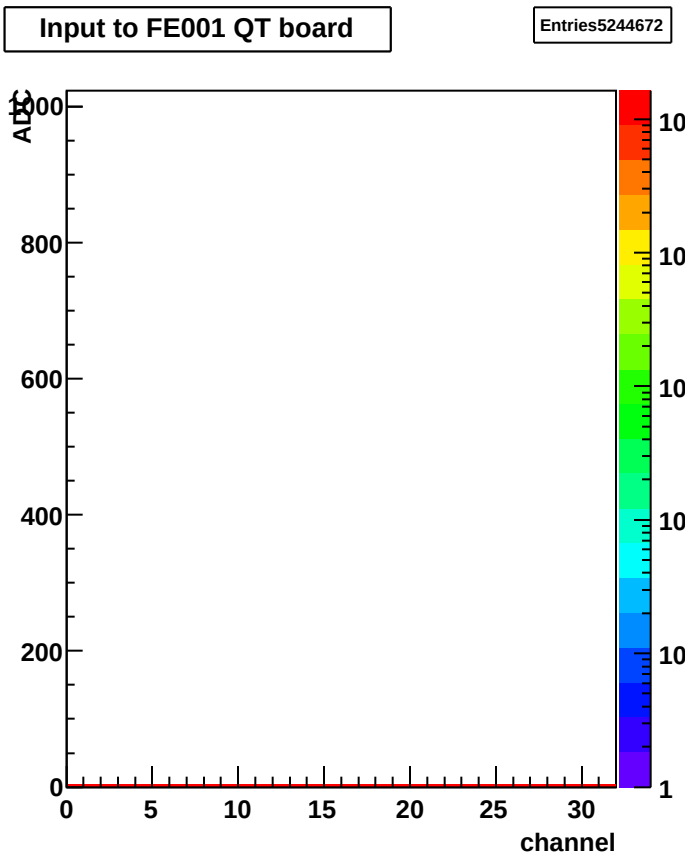

Input to FPD L2 DSM
Entries 491688
CL bits
163896
163896
163896
SMALL
LARGE-S
LARGE-N

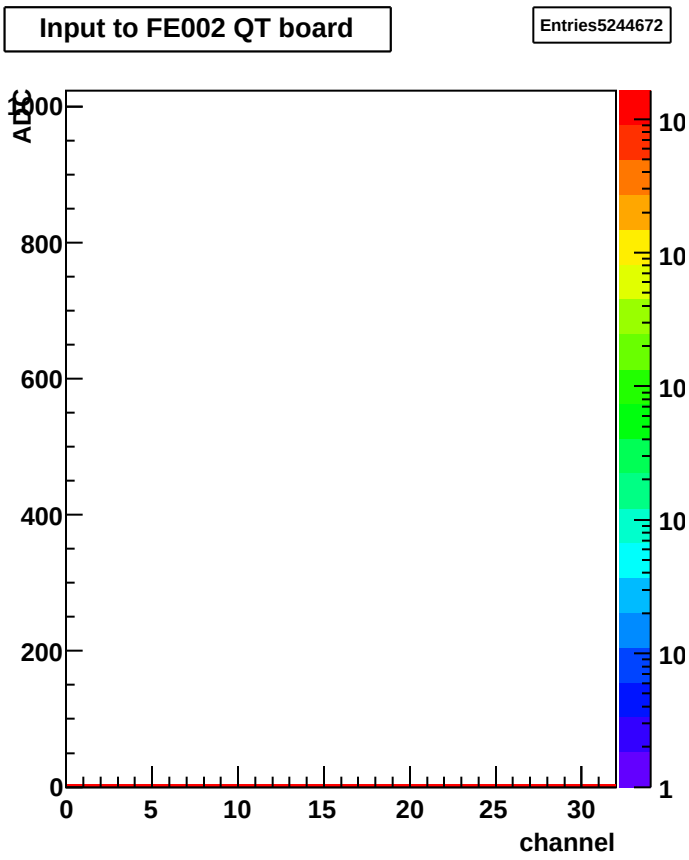

Input to FPD L2 DSM
Entries 491688
CL bits - simulated
163896
163896
163896
SMALL
LARGE-S
LARGE-N

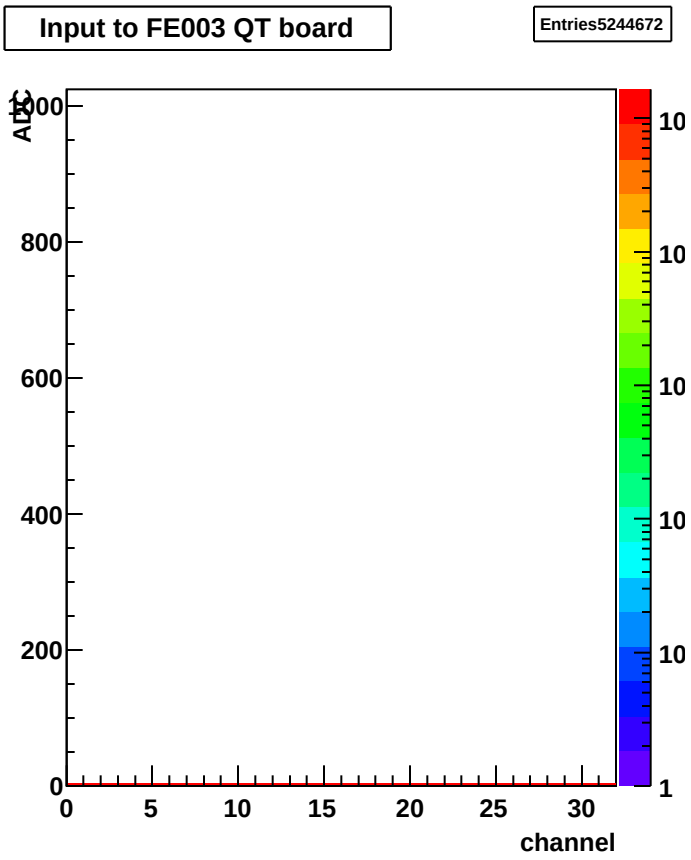

Input to FPD L2 DSM
Entries 491688
HT bits
163896
67
163896
95
163734
SMALL
LARGE-S
LARGE-N

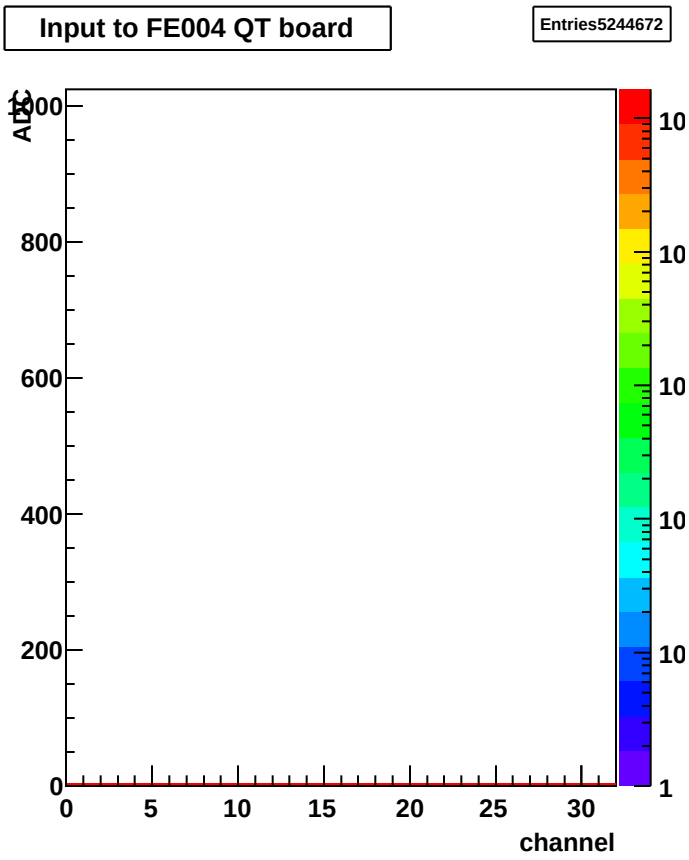

Input to FPD L2 DSM
Entries 327954
HT bits - simulated
163896
67
163896
95
SMALL
LARGE-S
LARGE-N

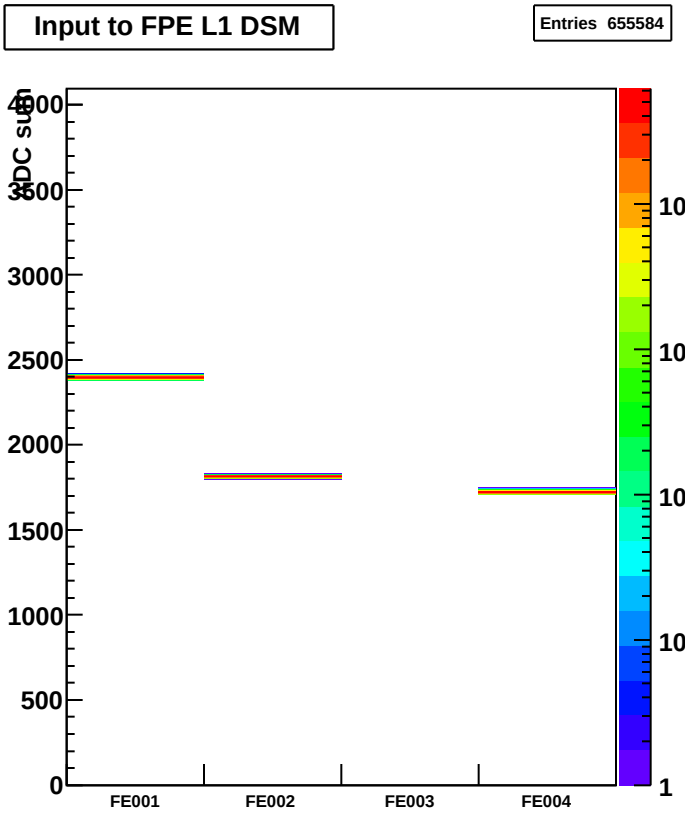

Input to FPD L2 DSM
Entries 655584
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

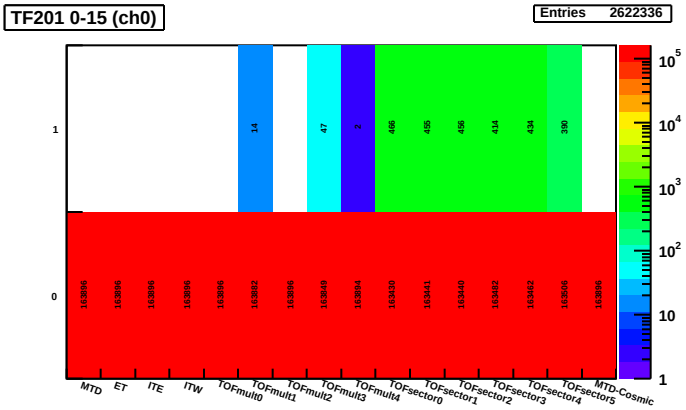

Input to FE001 QT board
Entries5244672
ADC
channel

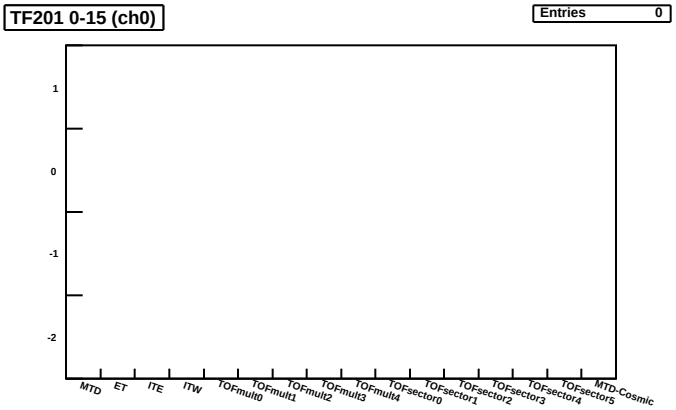

Input to FE002 QT board
Entries5244672
ADC
channel

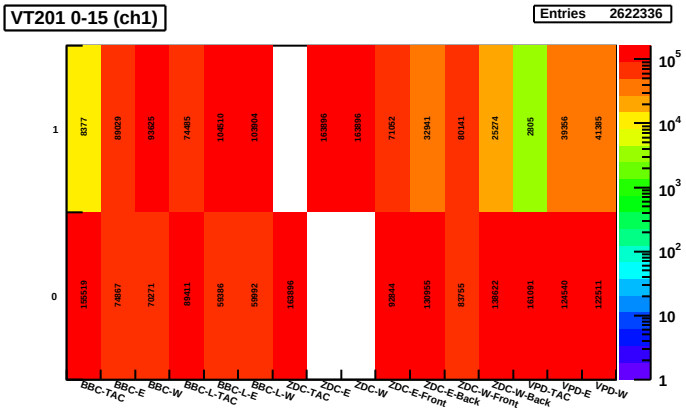

Input to FE003 QT board
Entries5244672
ADC
channel

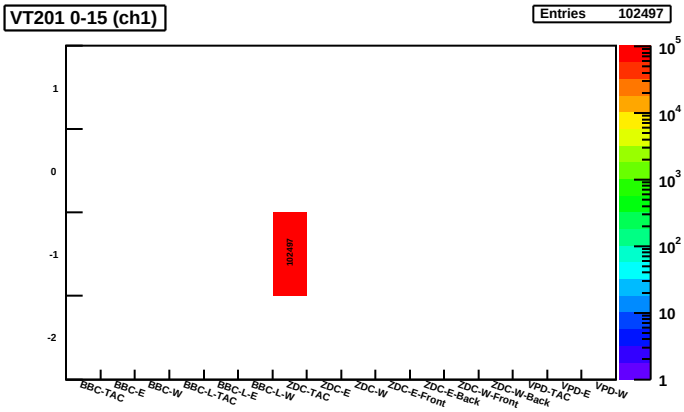

Input to FE004 QT board
Entries5244672
ADC
channel

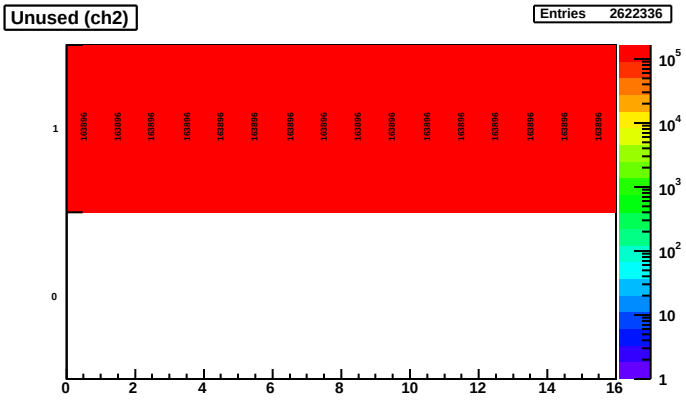

Input to FPE L1 DSM
Entries 655584
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

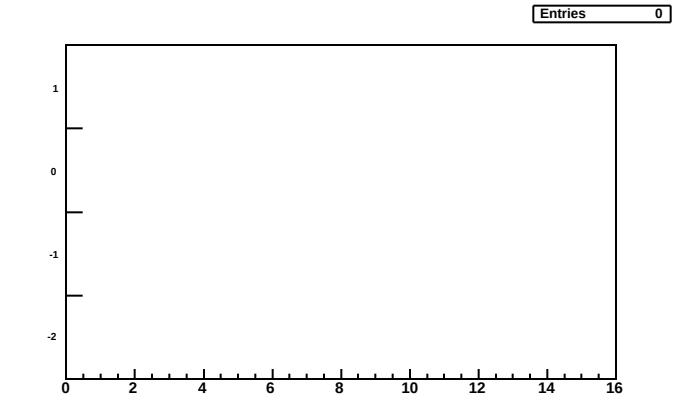

TF201 0-15 (ch0)
Entries 2622336
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

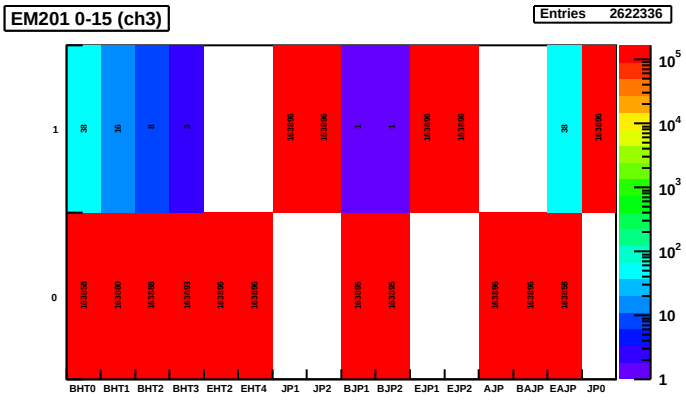

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

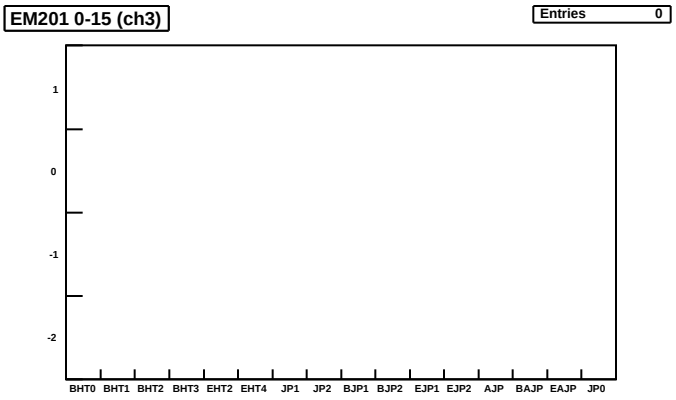

VT201 0-15 (ch1)
Entries 2622336
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 102497
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

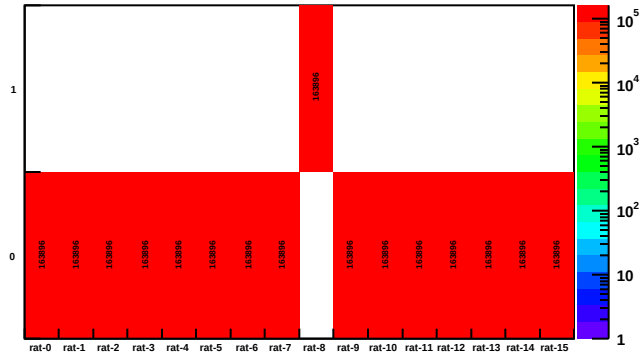

Unused (ch2)
Entries 2622336

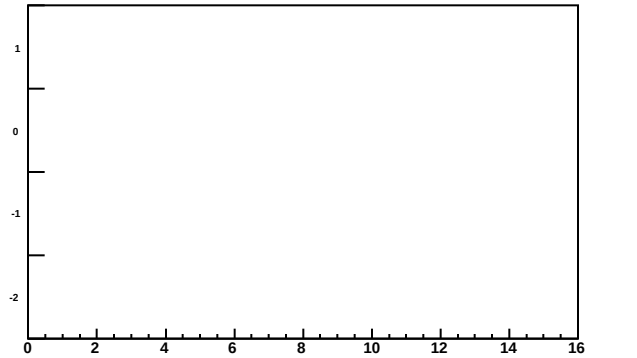

Entries 0


EM201 0-15 (ch3)
Entries 2622336
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

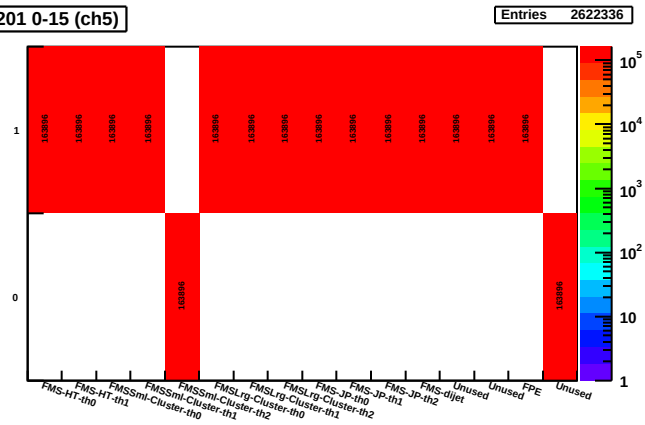

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

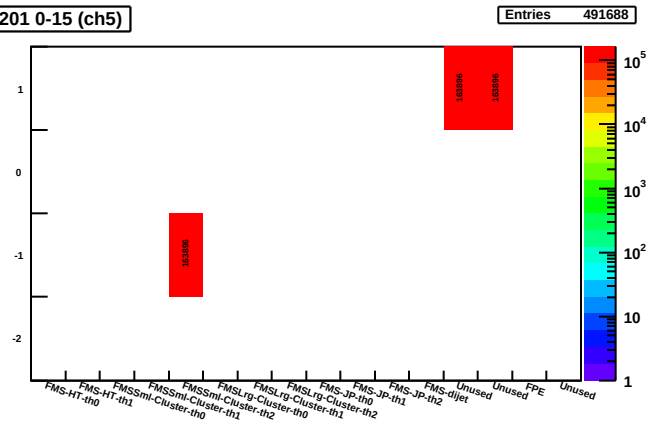




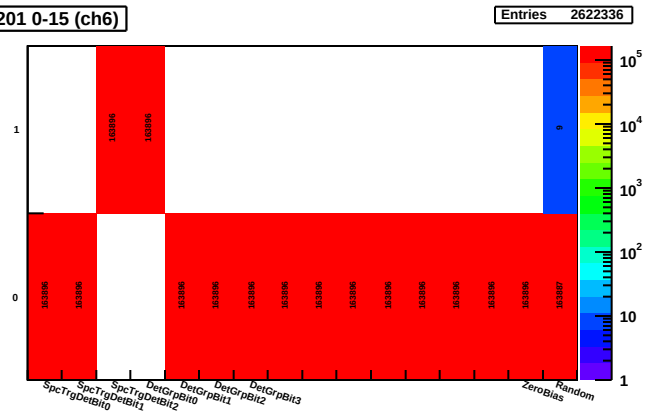
FP201 0-15 (ch5)

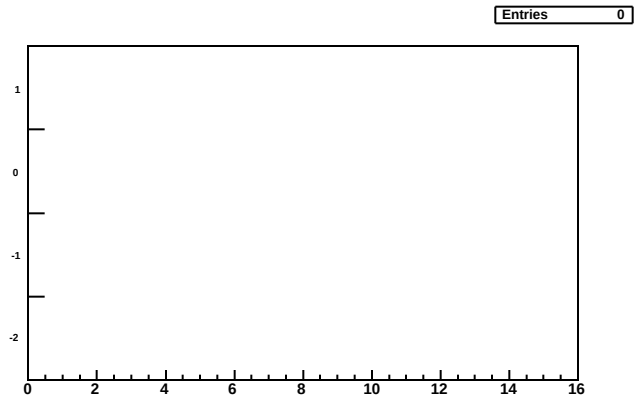
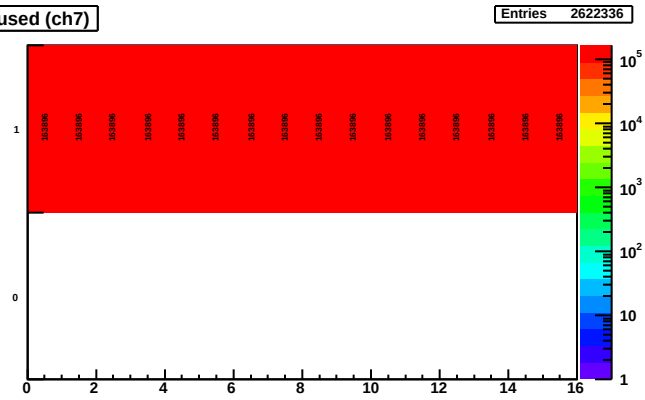
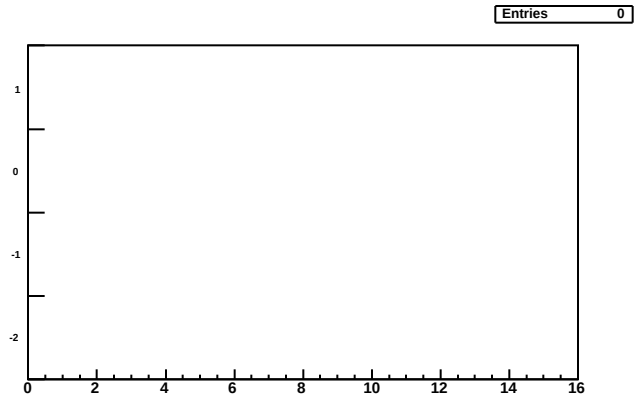


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)