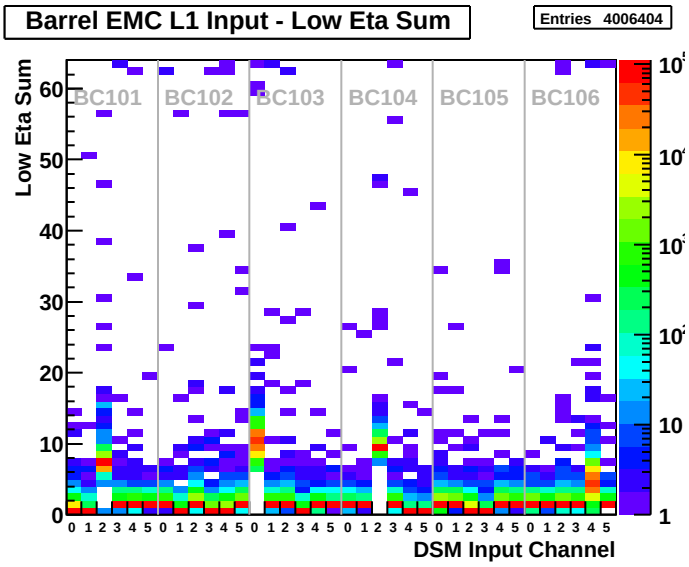
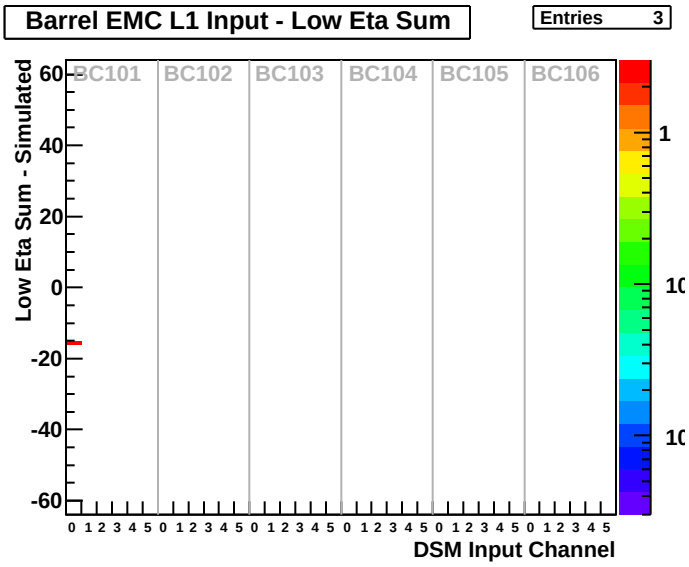
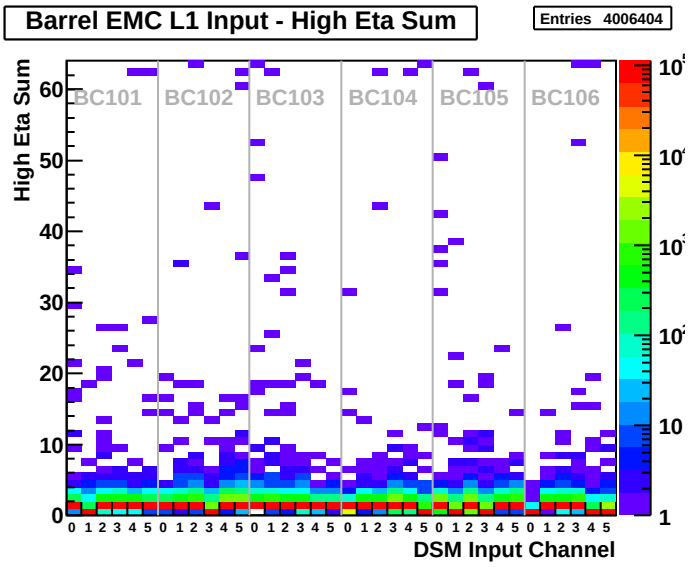
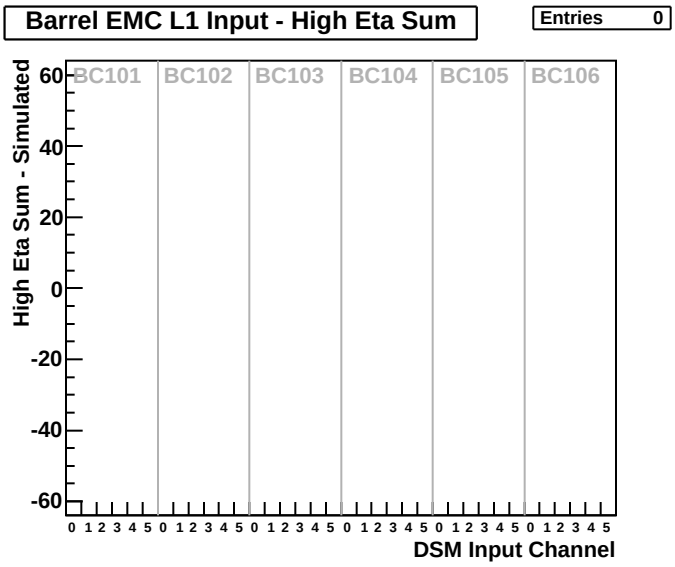
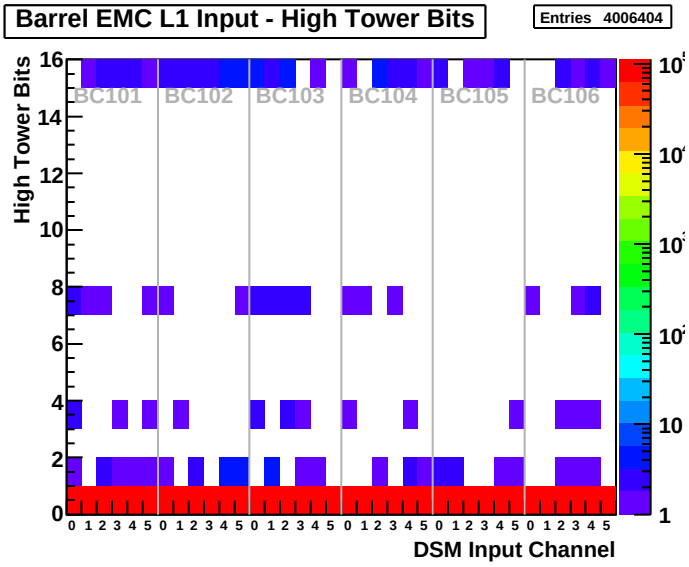
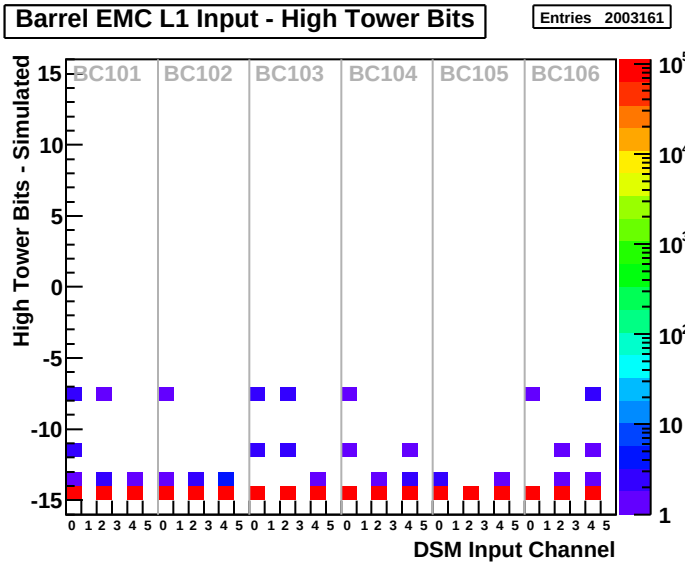

Barrel EMC L1 Input - Low Eta Sum
Entries 4006404
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

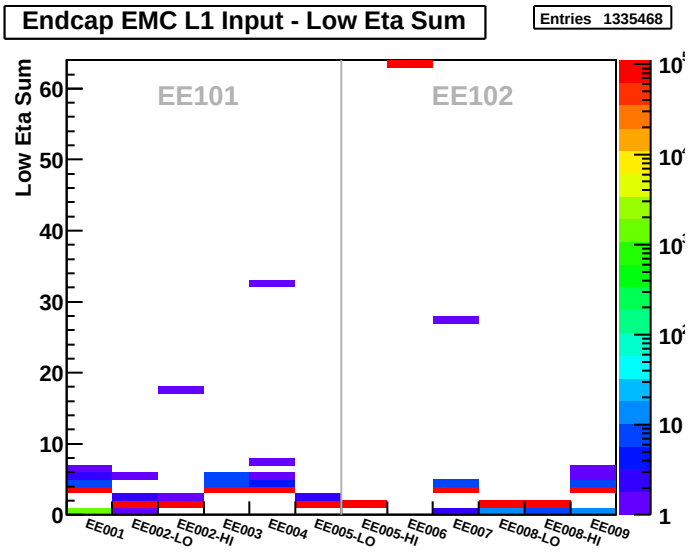

Barrel EMC L1 Input - Low Eta Sum
Entries 3
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

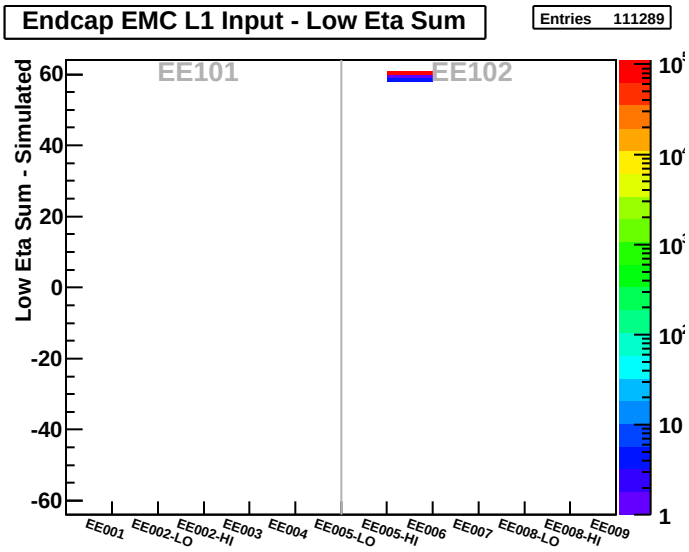

Barrel EMC L1 Input - High Eta Sum
Entries 4006404
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

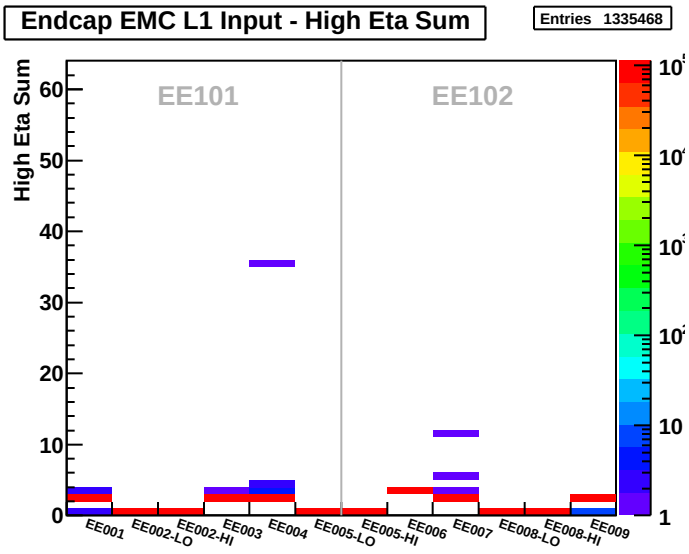

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

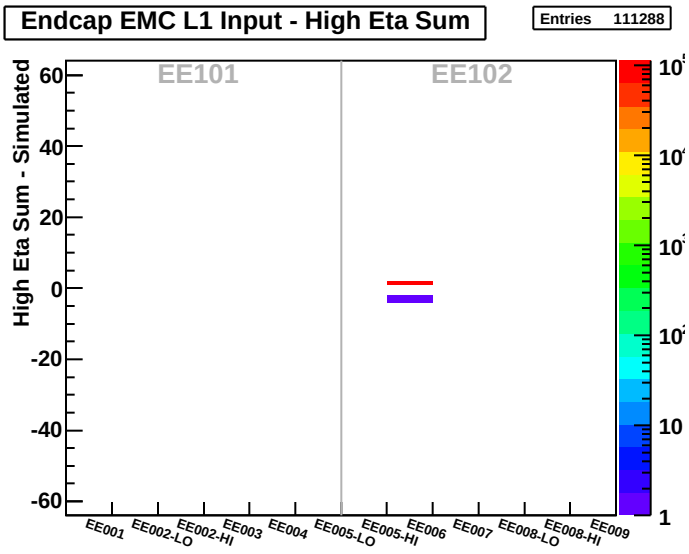

Barrel EMC L1 Input - High Tower Bits
Entries 4006404
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

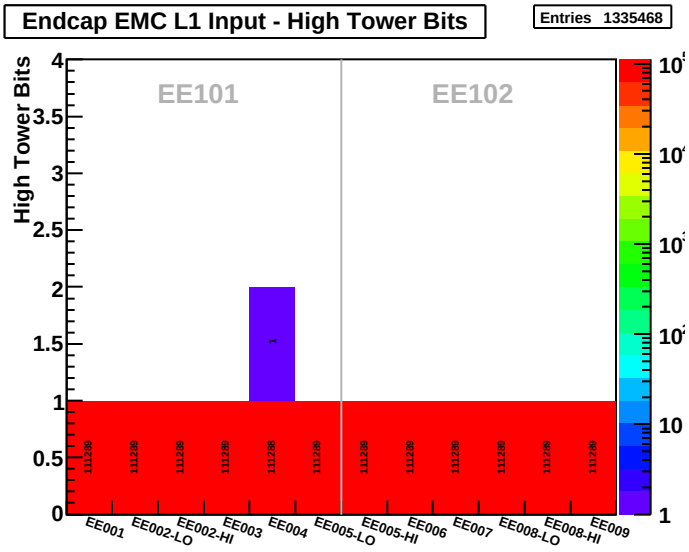

Barrel EMC L1 Input - High Tower Bits
Entries 2003161
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

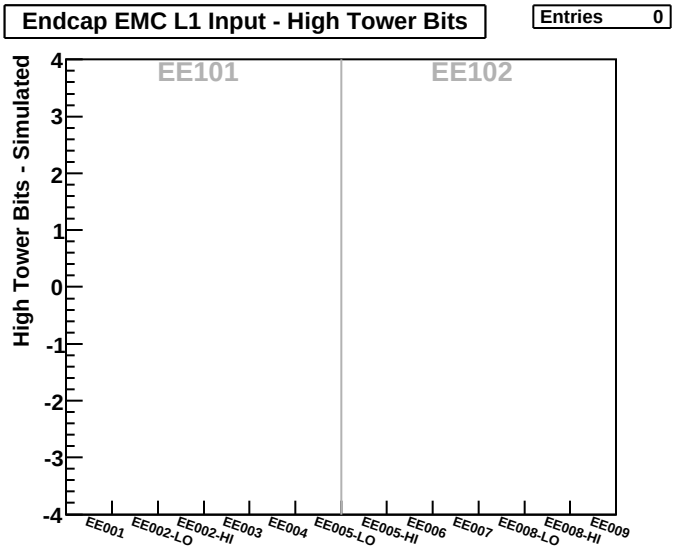

Endcap EMC L1 Input - Low Eta Sum
Entries 1335468
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

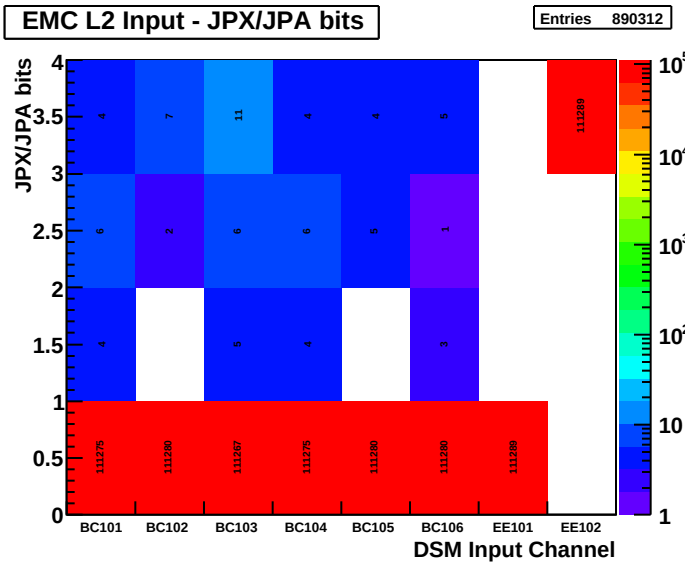

Endcap EMC L1 Input - Low Eta Sum
Entries 111289
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

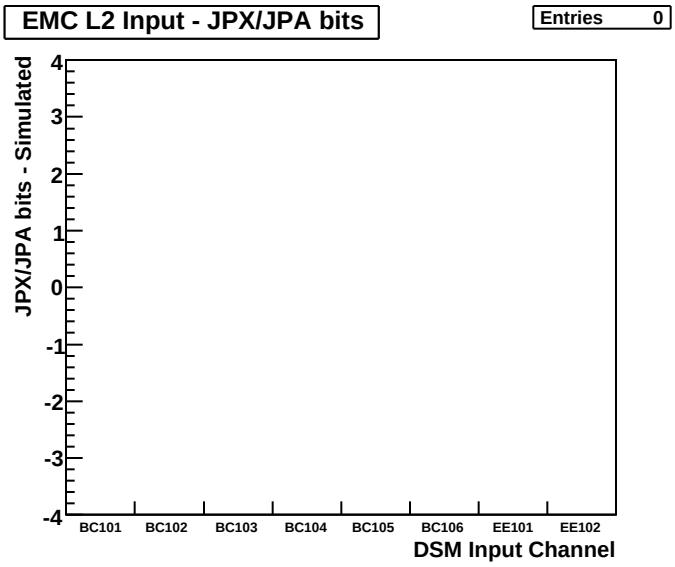

Endcap EMC L1 Input - High Eta Sum
Entries 1335468
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

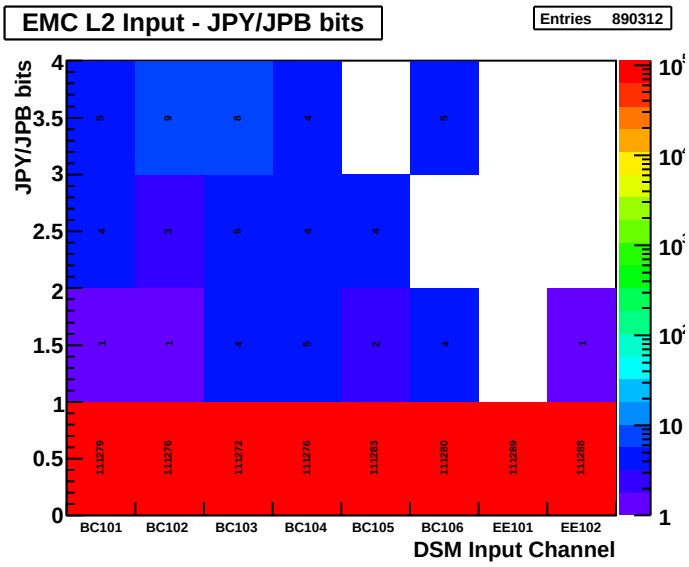

Endcap EMC L1 Input - High Eta Sum
Entries 111288
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

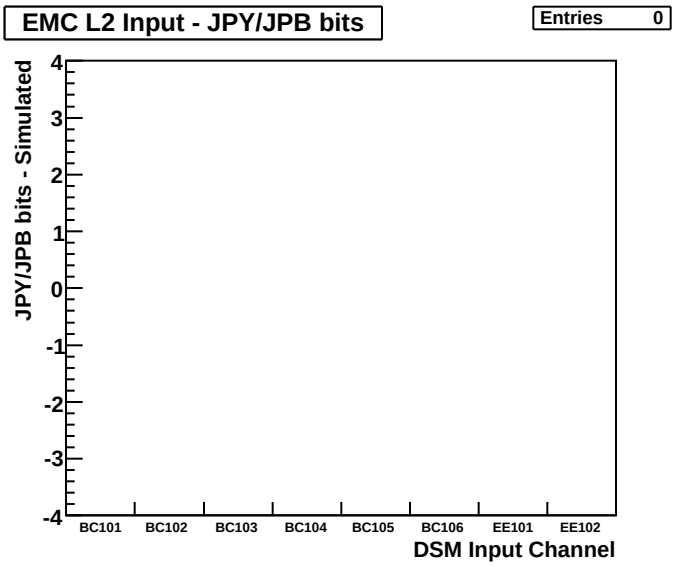

Endcap EMC L1 Input - High Tower Bits
Entries 1335468
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

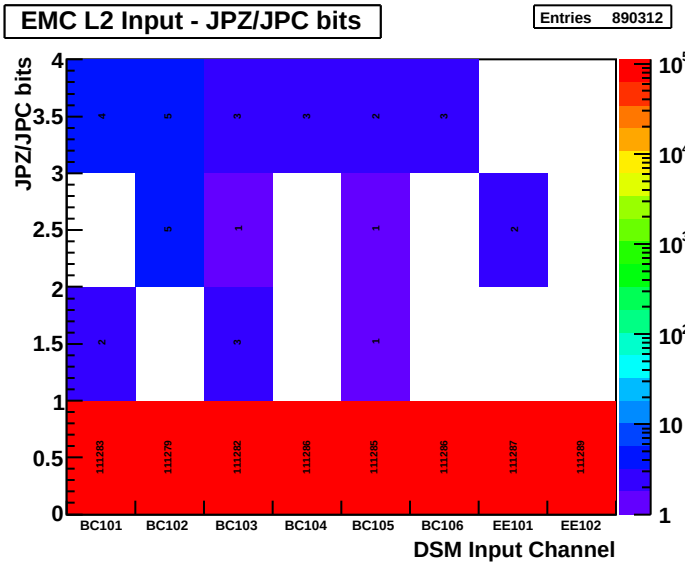

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

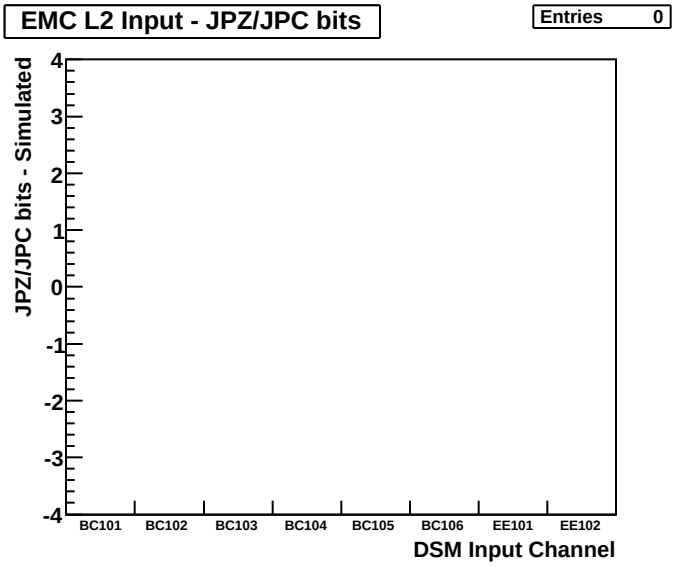

EMC L2 Input - JPX/JPA bits
Entries 890312
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

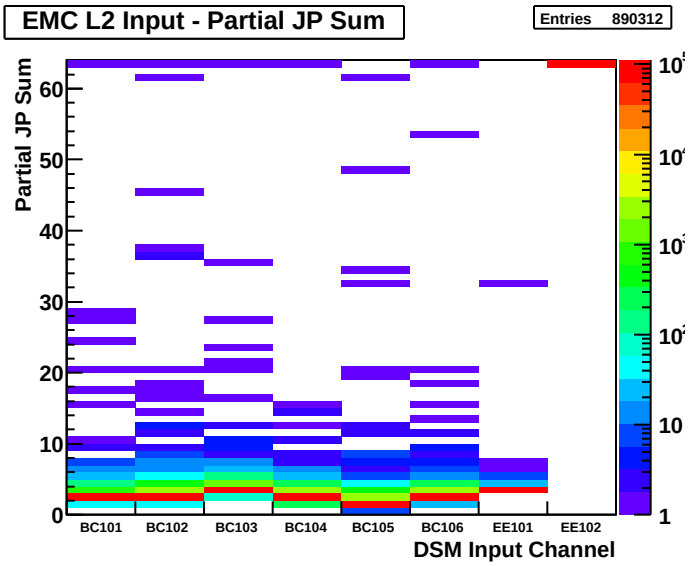

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

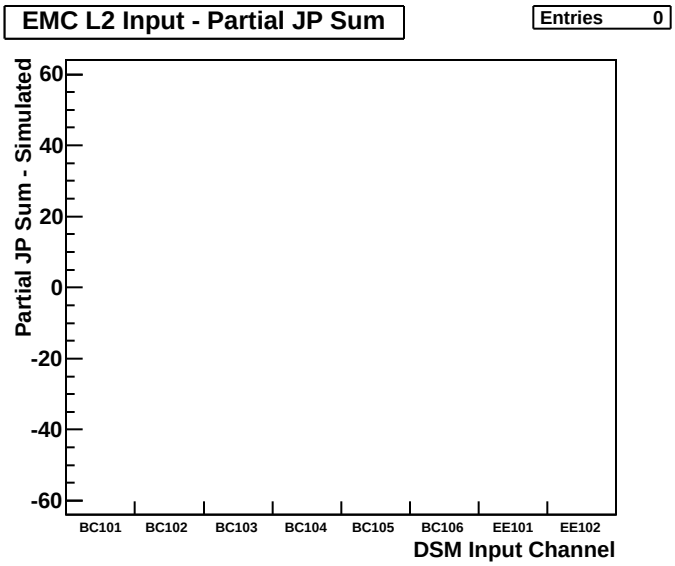

EMC L2 Input - JPY/JPB bits
Entries 890312
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

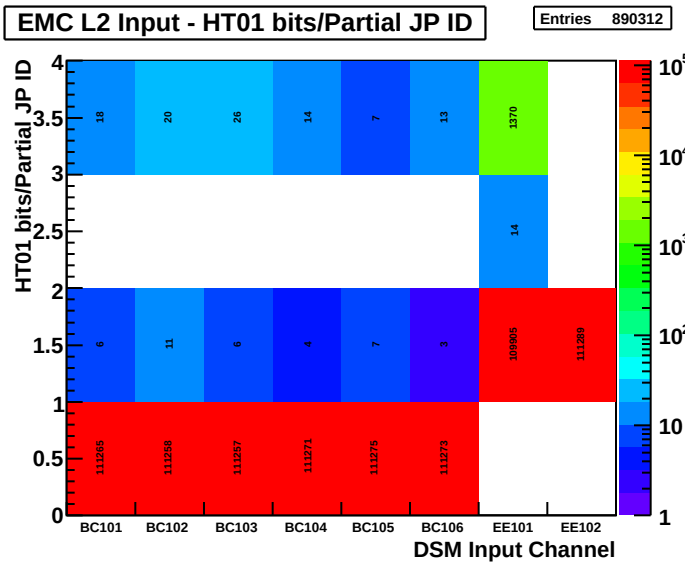

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

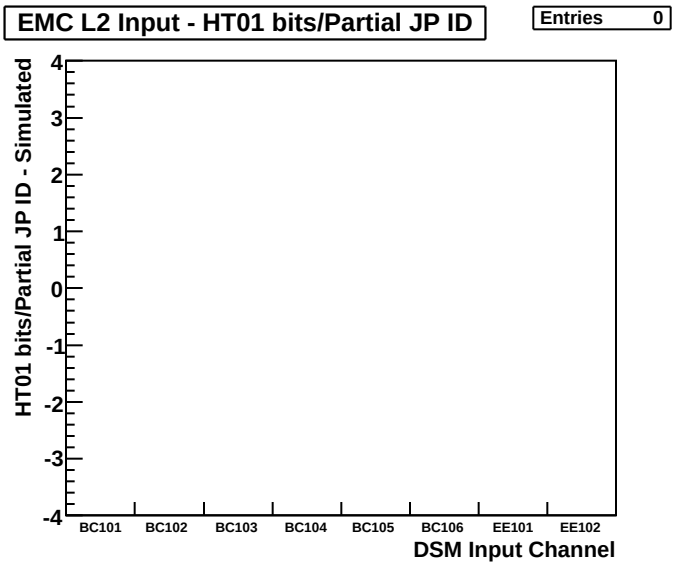

EMC L2 Input - JPZ/JPC bits
Entries 890312
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

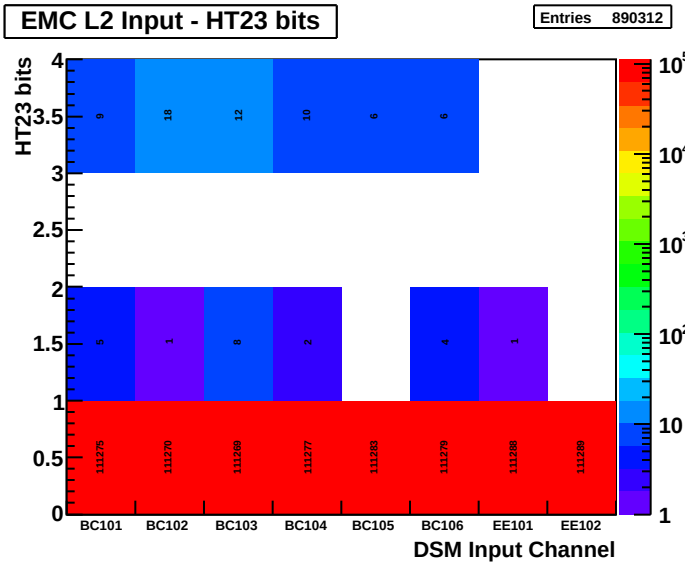

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

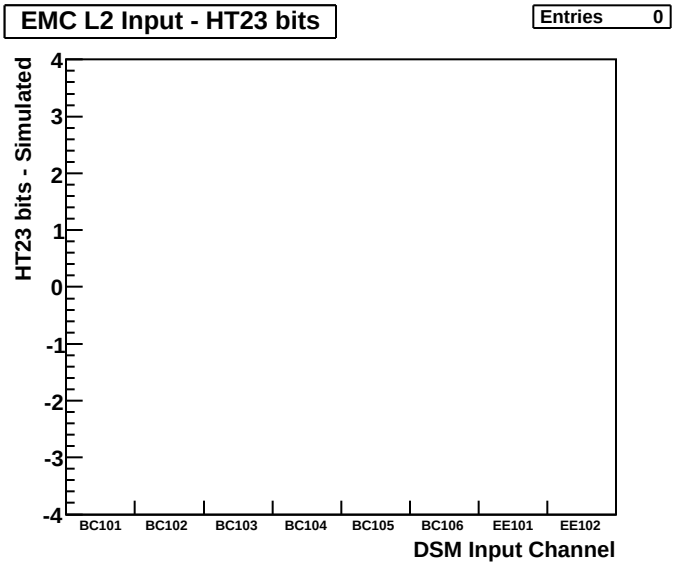

EMC L2 Input - Partial JP Sum
Entries 890312
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

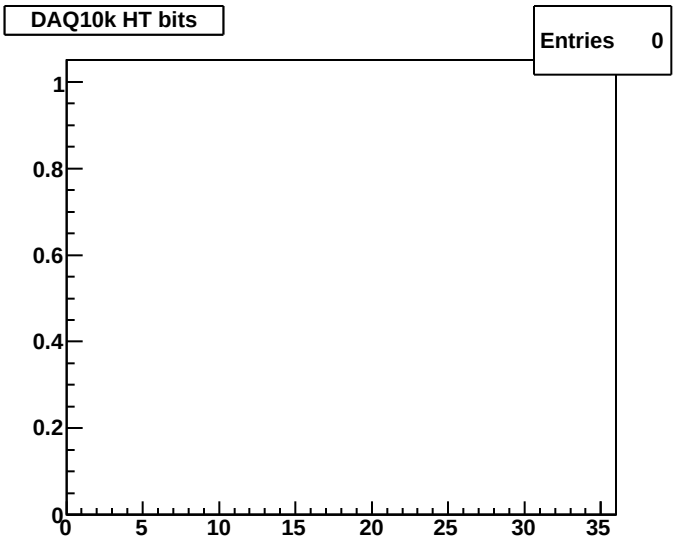

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

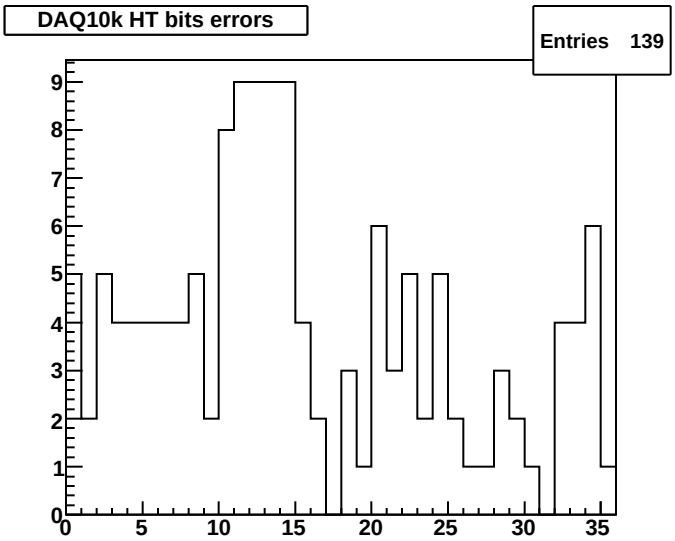

EMC L2 Input - HT01 bits/Partial JP ID
Entries 890312
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

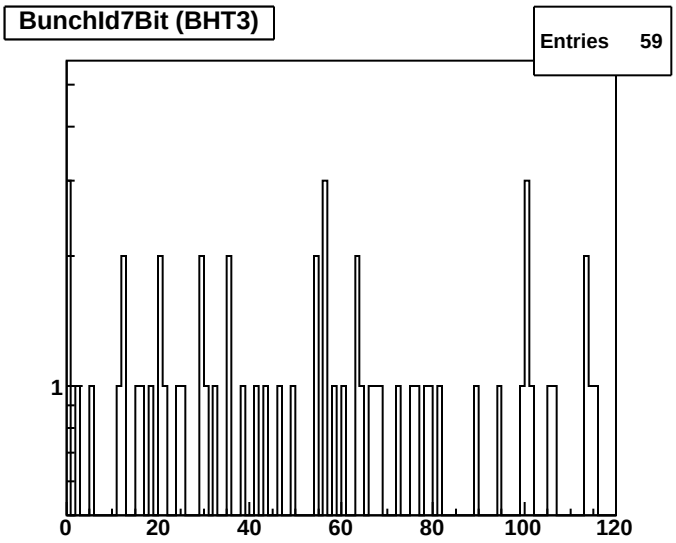

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

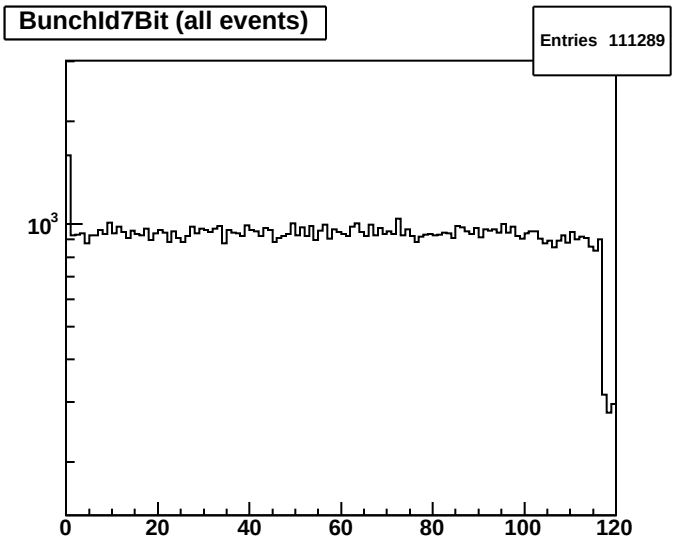

EMC L2 Input - HT23 bits
Entries 890312
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

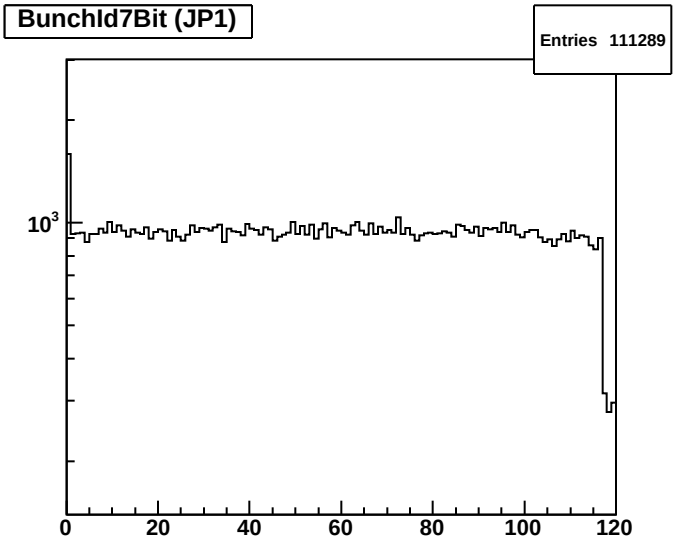

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

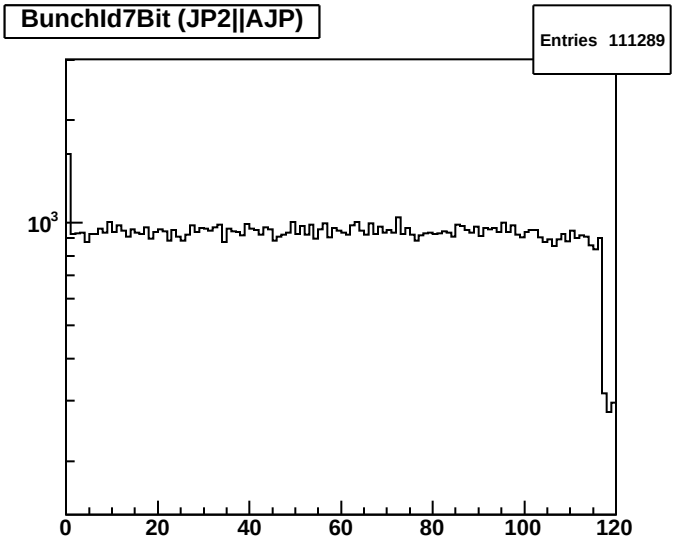

DAQ10k HT bits
Entries 0

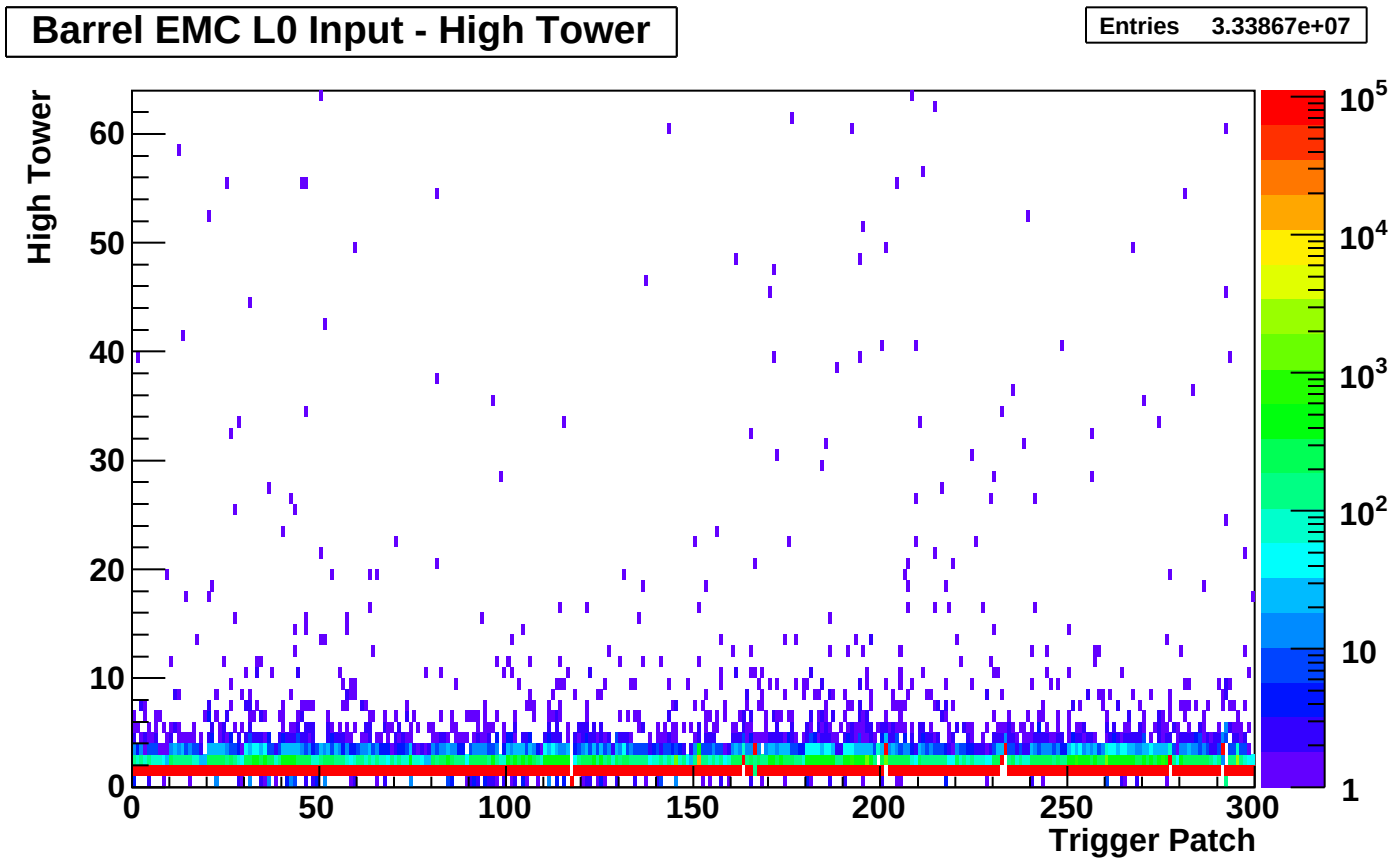

DAQ10k HT bits errors
Entries 139

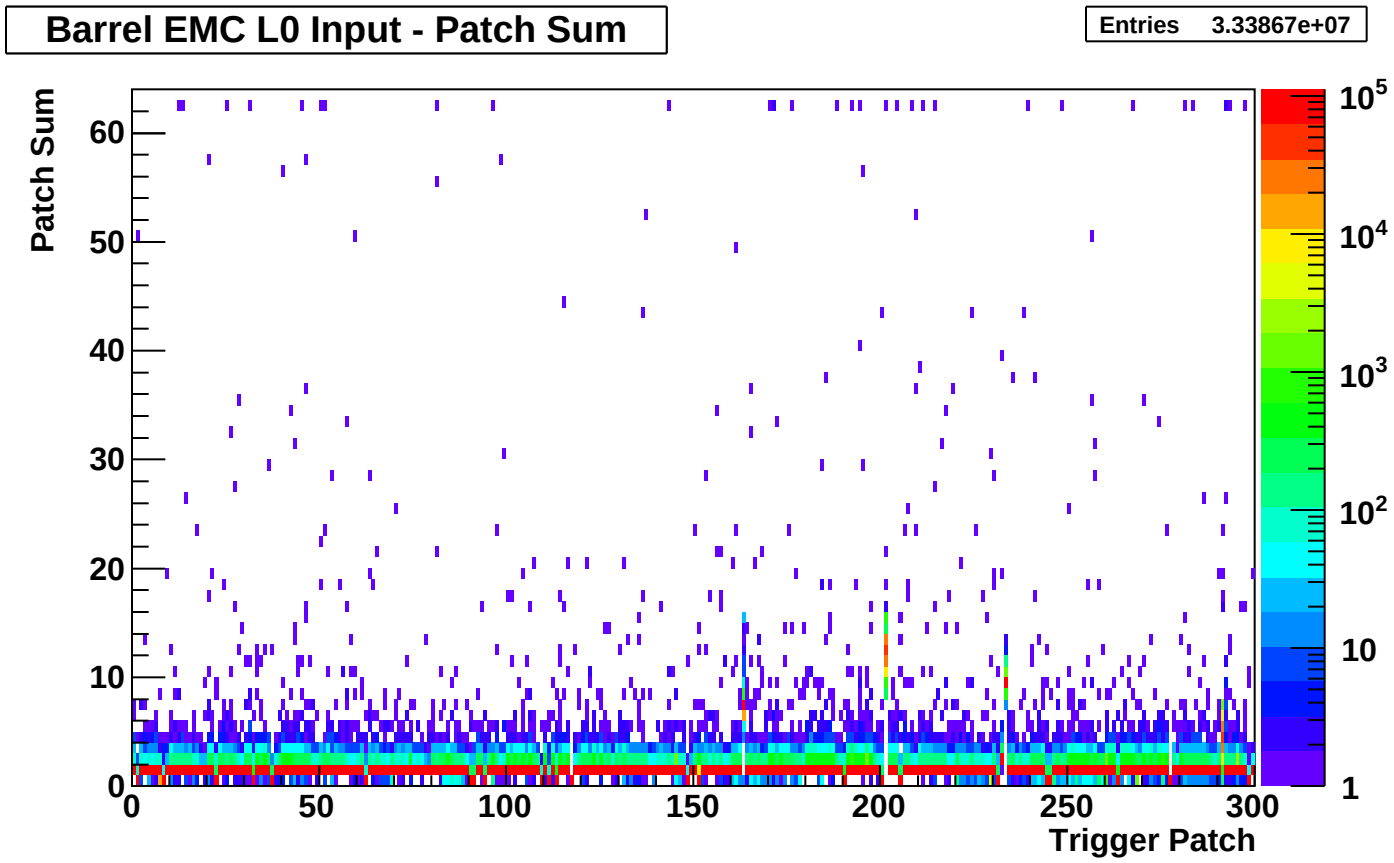

BunchId7Bit (BHT3)
Entries 59

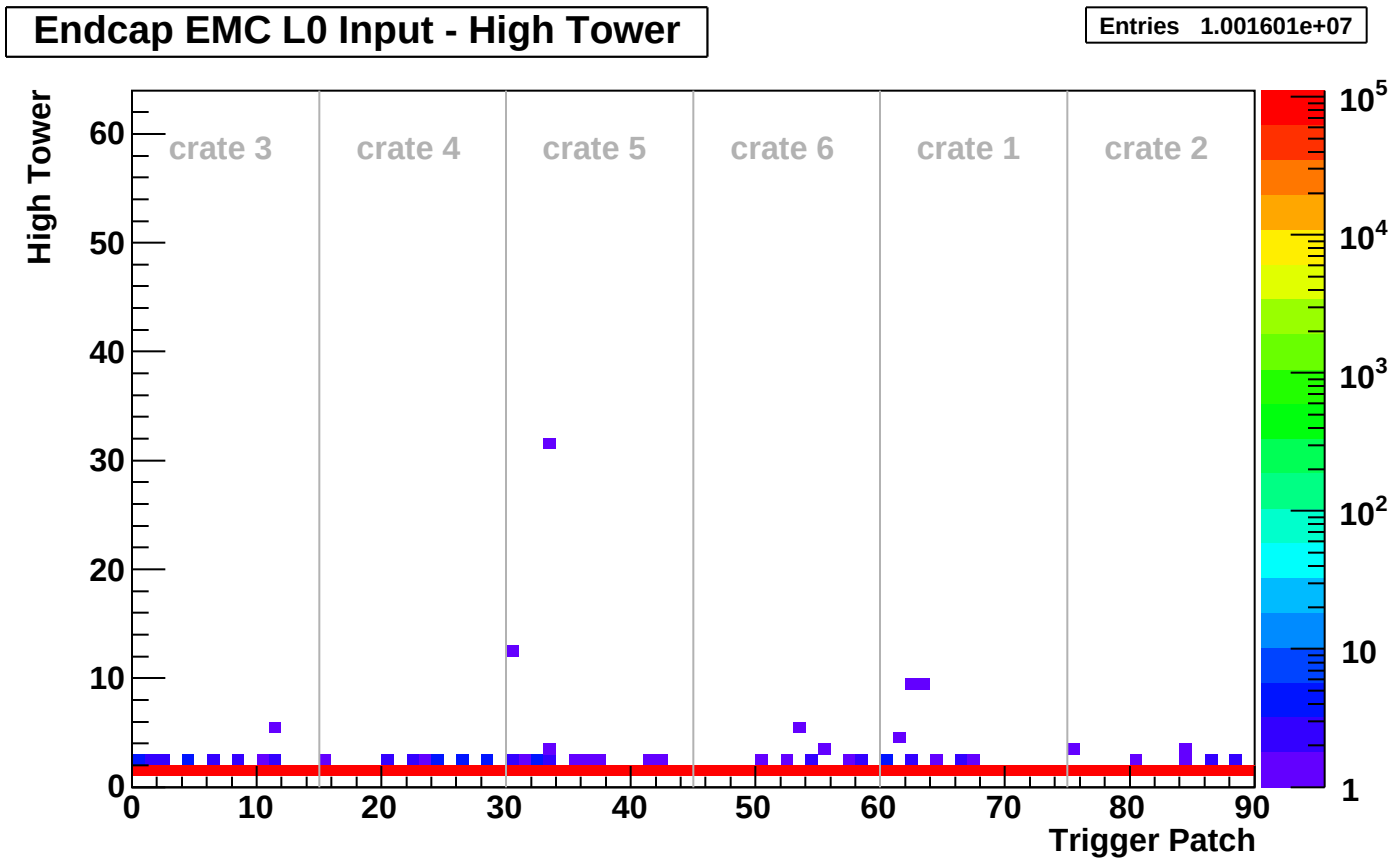

BunchId7Bit (all events)
Entries 111289

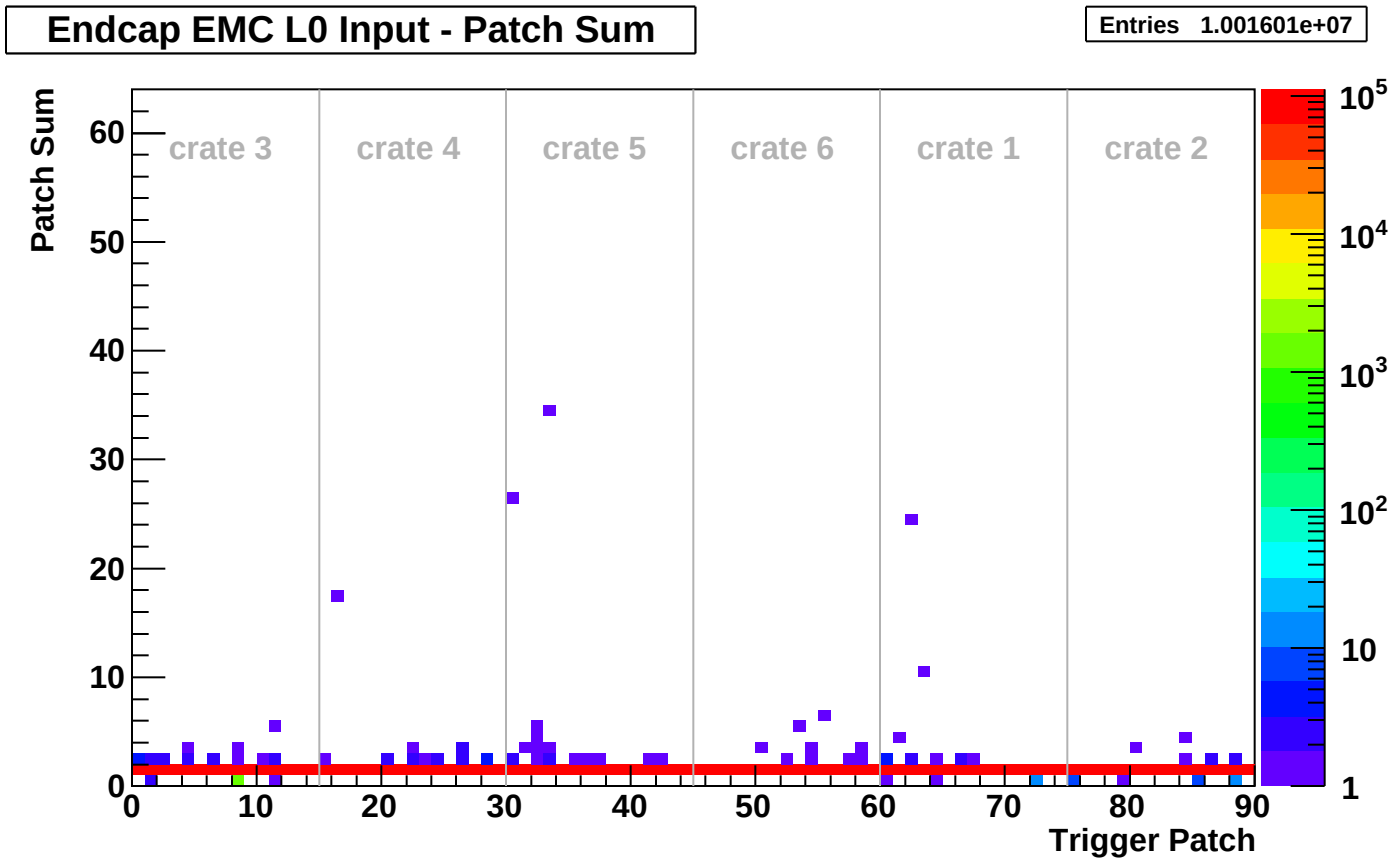

BunchId7Bit (JP1)
Entries 111289

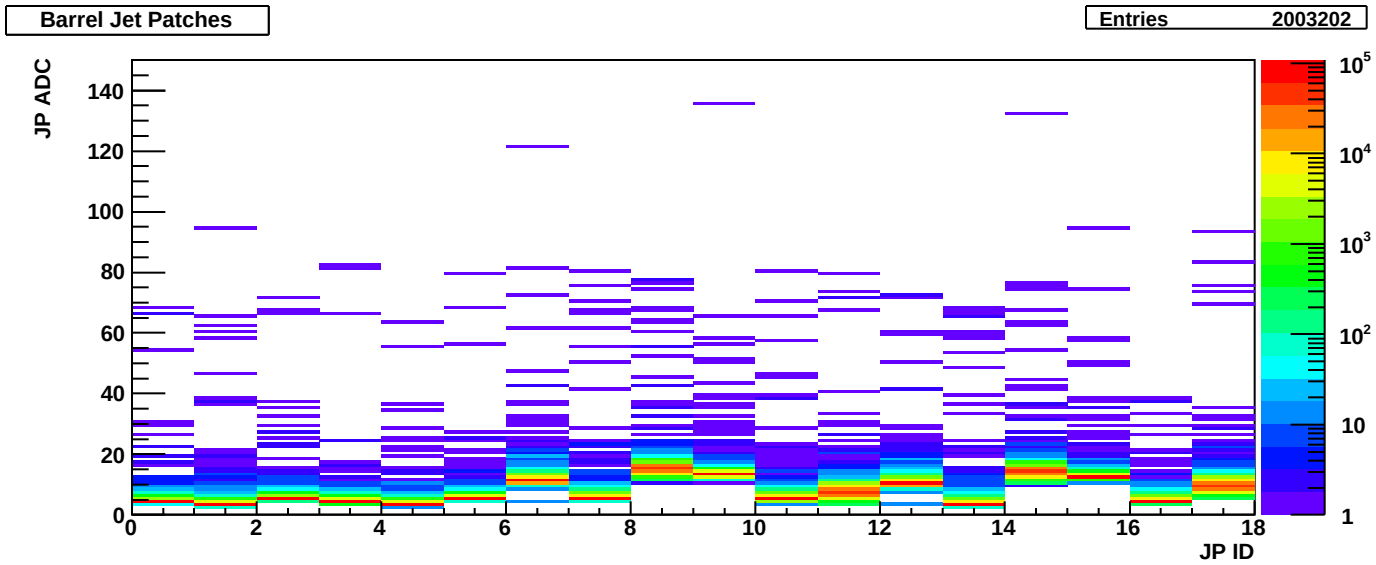

BunchId7Bit (JP2||AJP)
Entries 111289

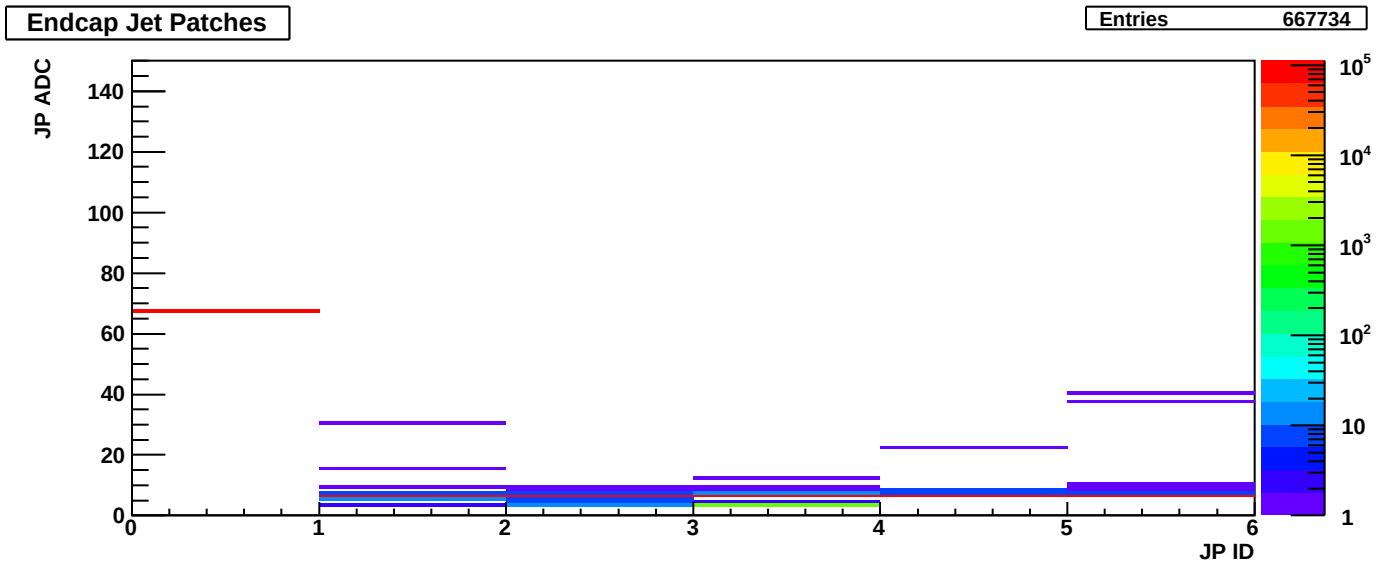

Barrel EMC L0 Input - High Tower
Entries 3.33867e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

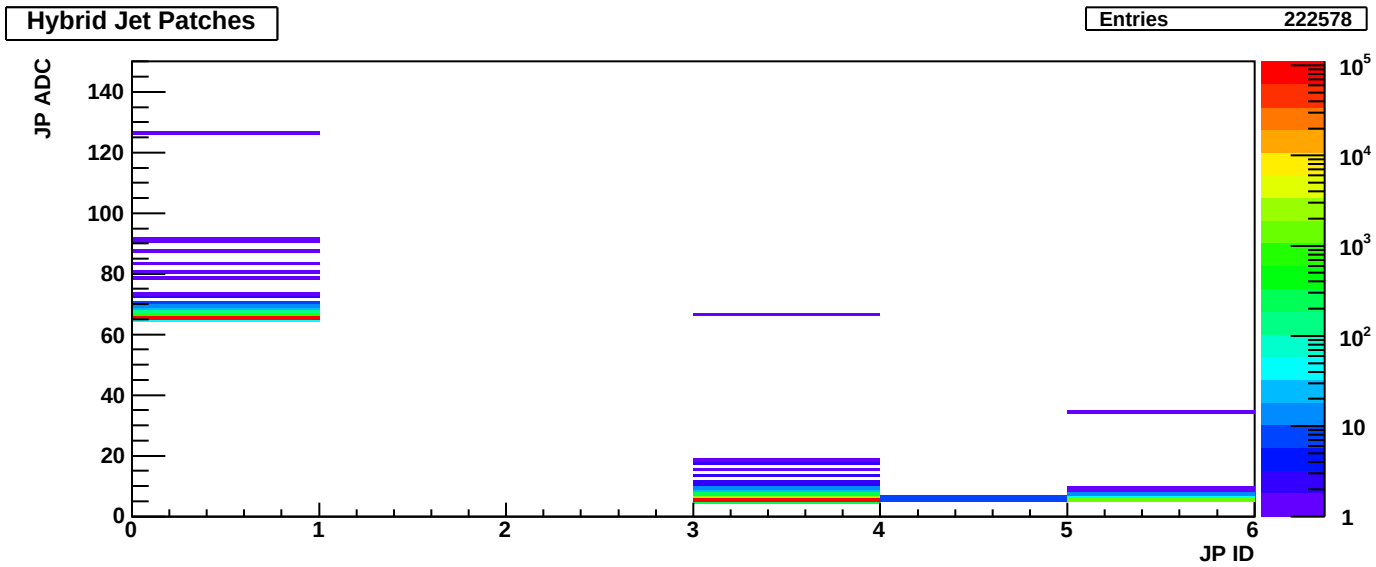

Barrel EMC L0 Input - Patch Sum
Entries 3.33867e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

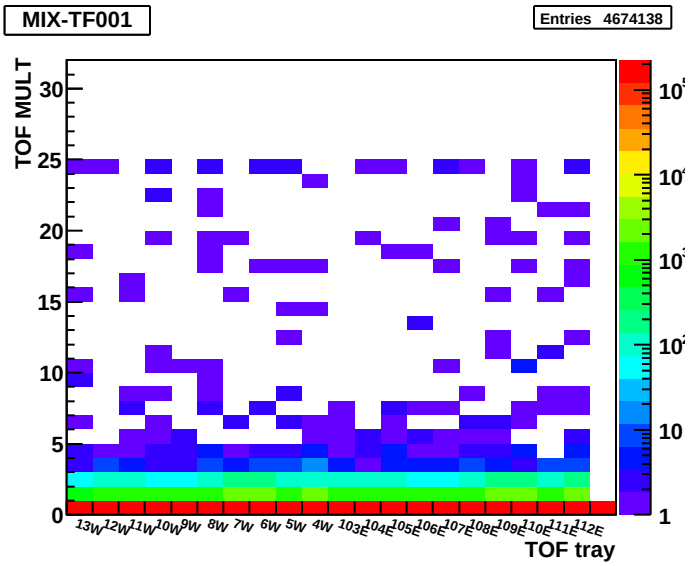

Endcap EMC L0 Input - High Tower
Entries 1.001601e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

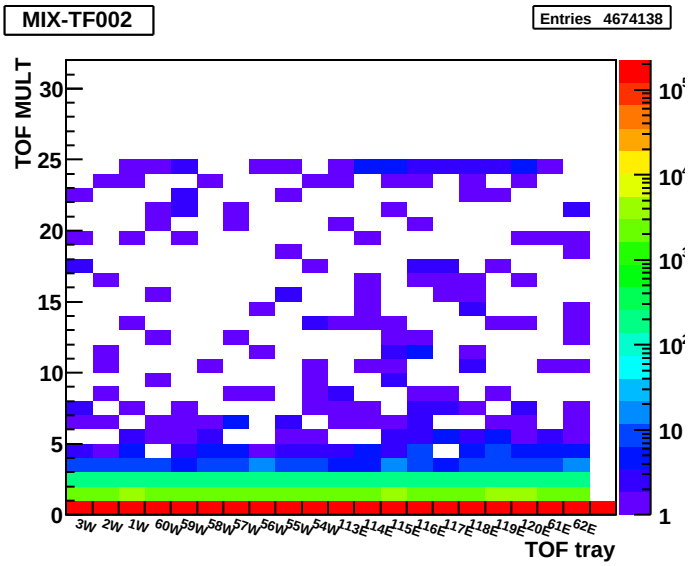

Endcap EMC L0 Input - Patch Sum
Entries 1.001601e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

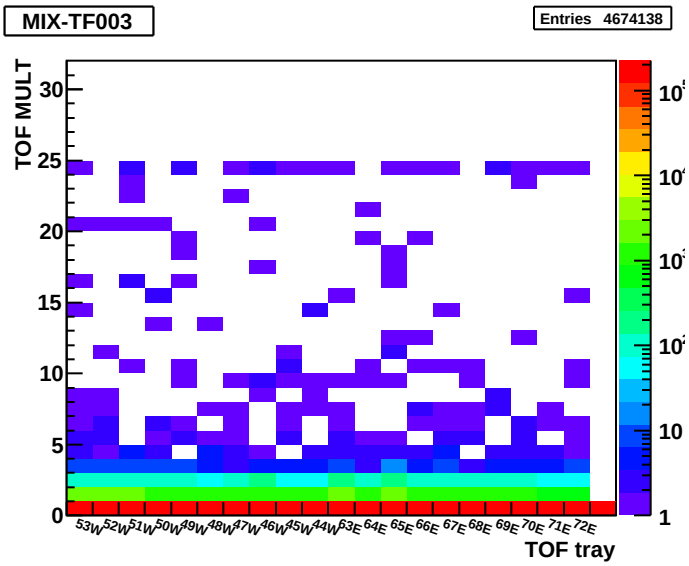

Barrel Jet Patches
Entries 2003202
JP ADC
JP ID

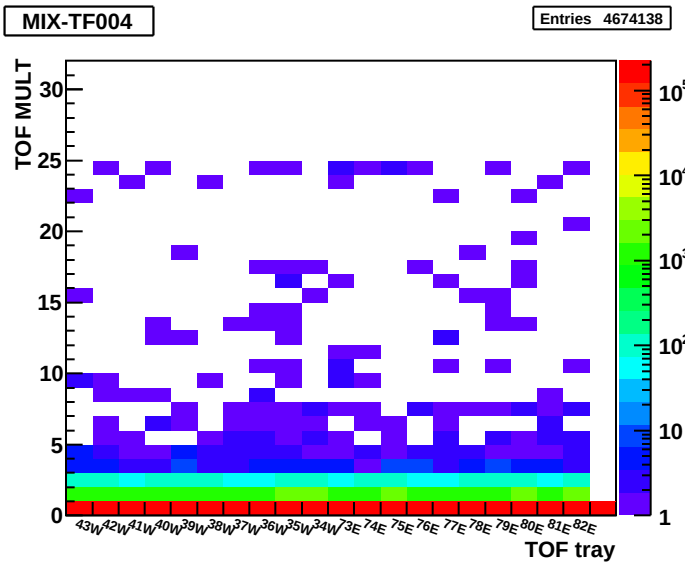

Endcap Jet Patches
Entries 667734
JP ADC
JP ID

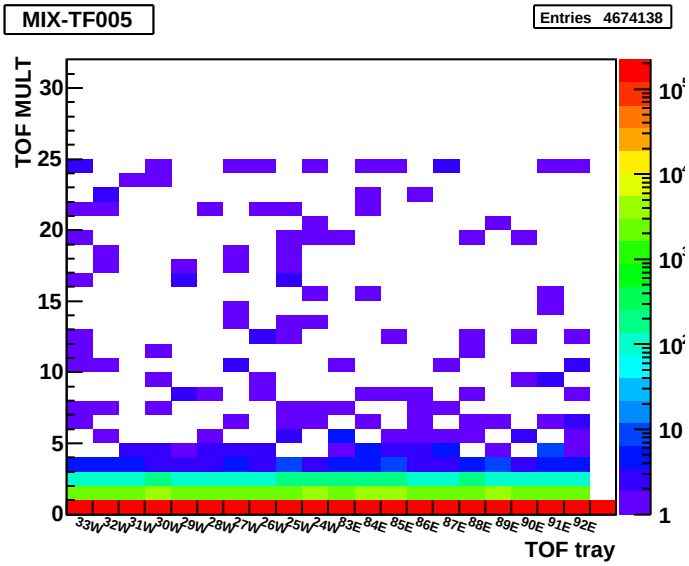

Hybrid Jet Patches
Entries 222578
JP ADC
JP ID

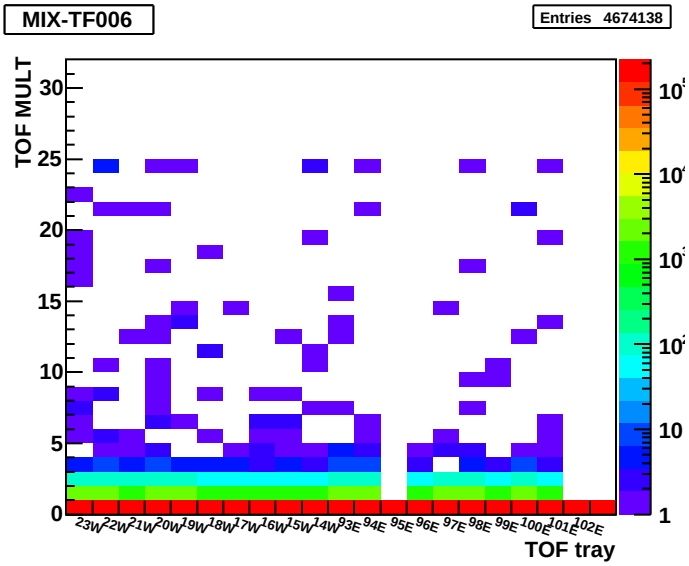

MIX-TF001
Entries 4674138
TOF MULT
TOF tray

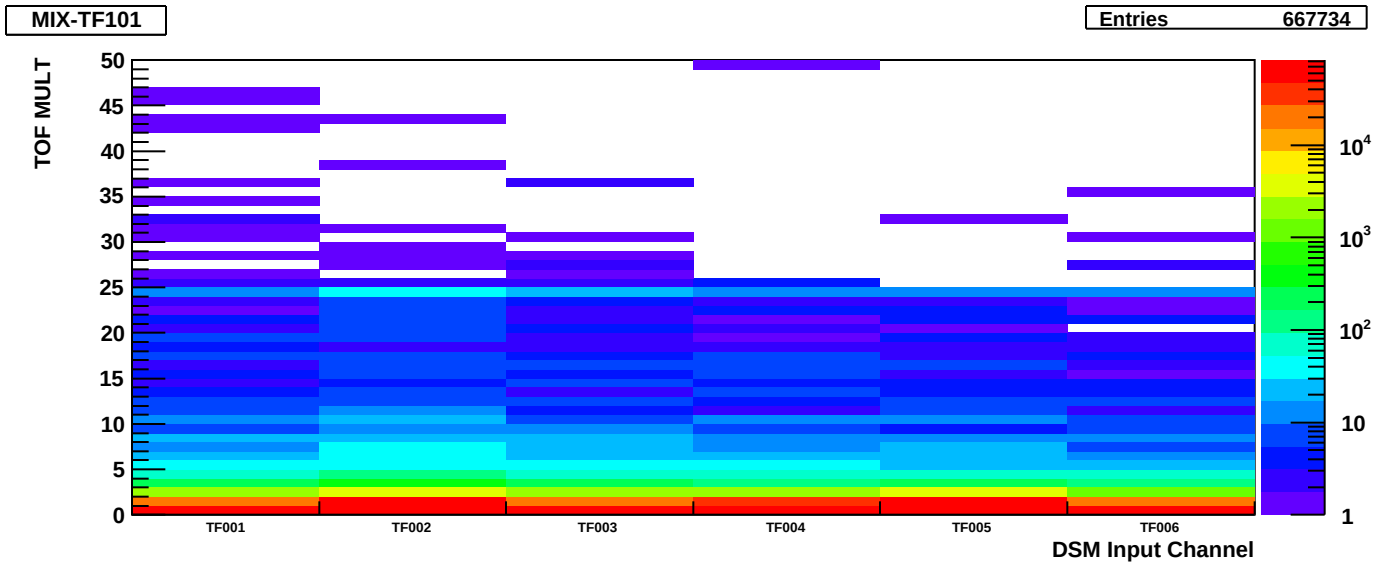

MIX-TF002
Entries 4674138
TOF MULT
TOF tray

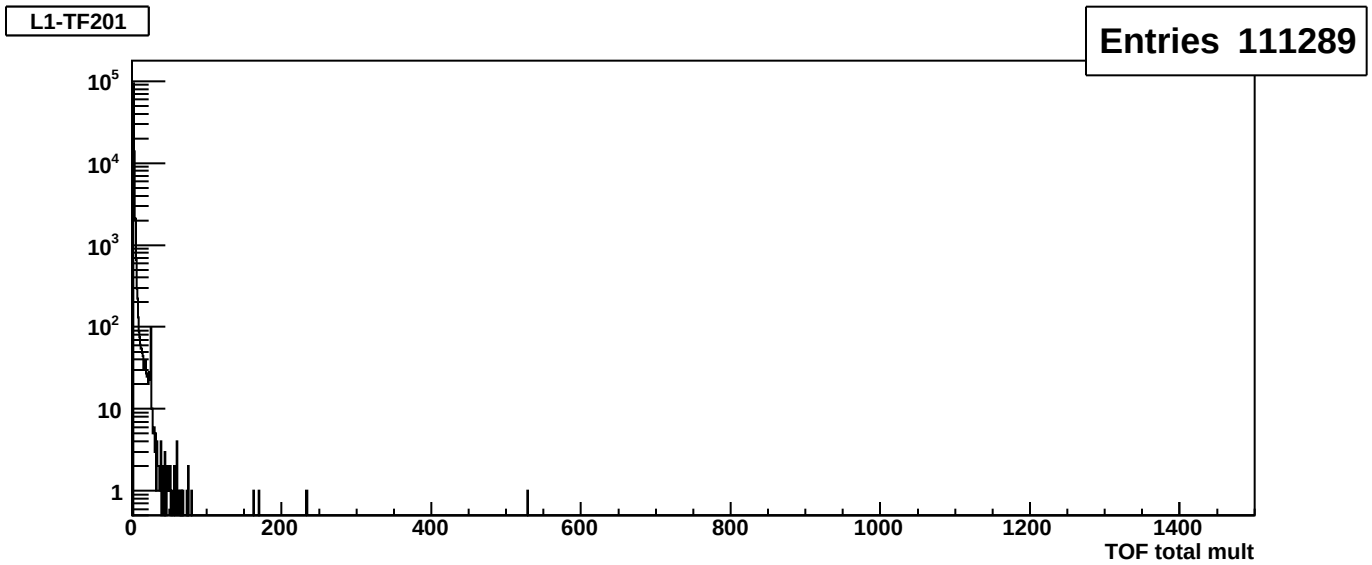

MIX-TF003
Entries 4674138
TOF MULT
TOF tray

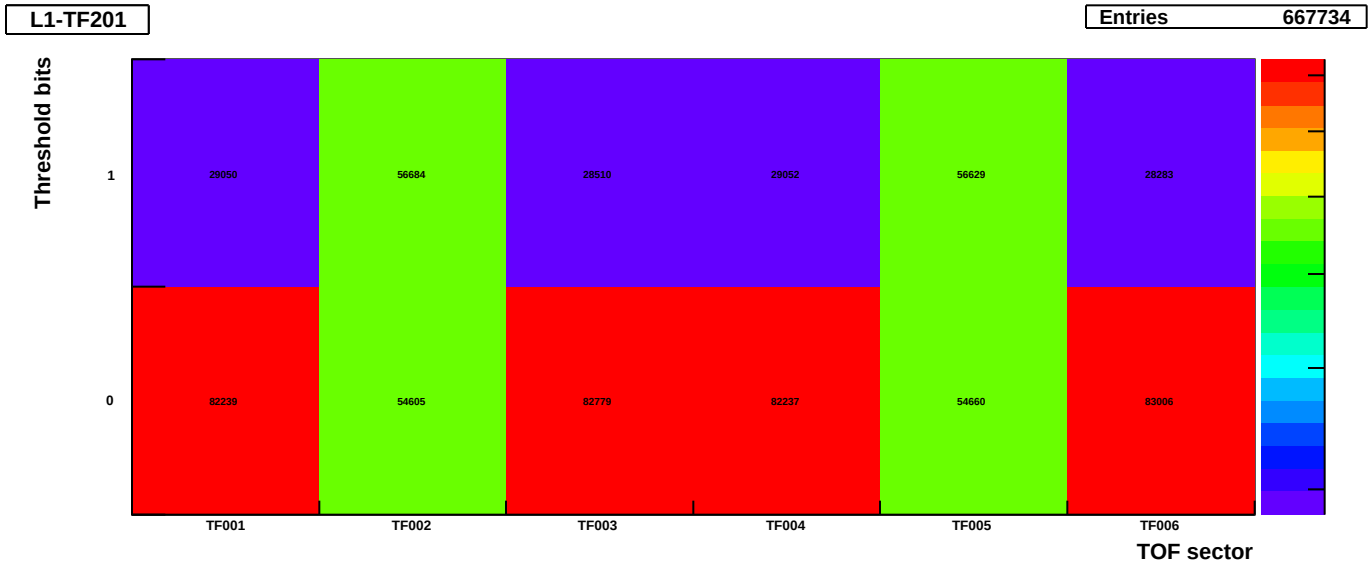

MIX-TF004
Entries 4674138
TOF MULT
TOF tray

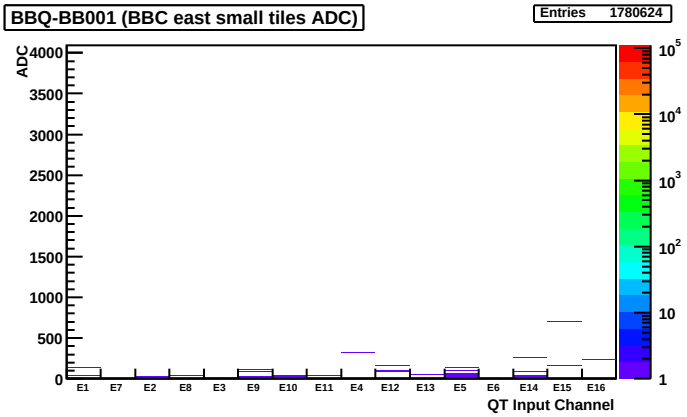

MIX-TF005
Entries 4674138
TOF MULT
TOF tray

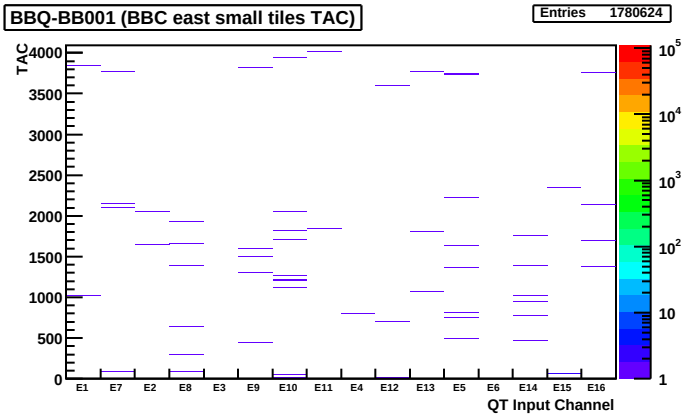

MIX-TF006
Entries 4674138
TOF MULT
TOF tray

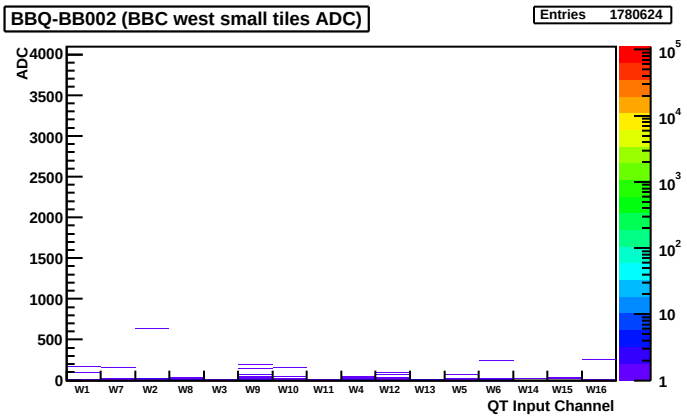

MIX-TF101
Entries 667734
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

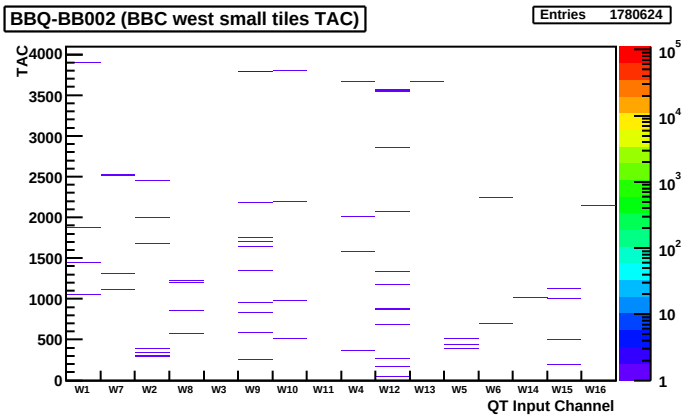

L1-TF201
Entries 111289
TOF total mult

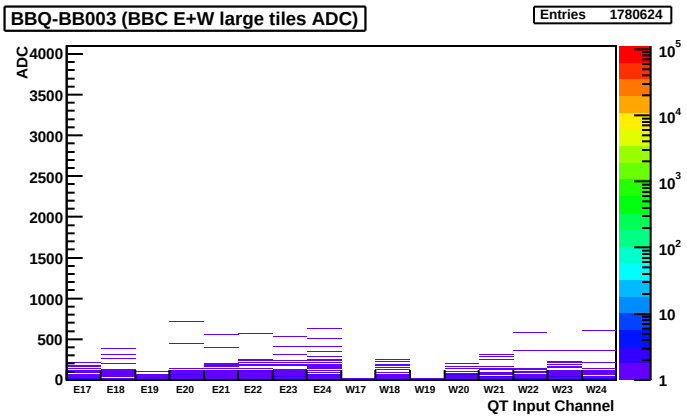

L1-TF201
Entries 667734
Threshold bits
29050
56684
28510
29052
56629
28283
82239
54605
82779
82237
54660
83006
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

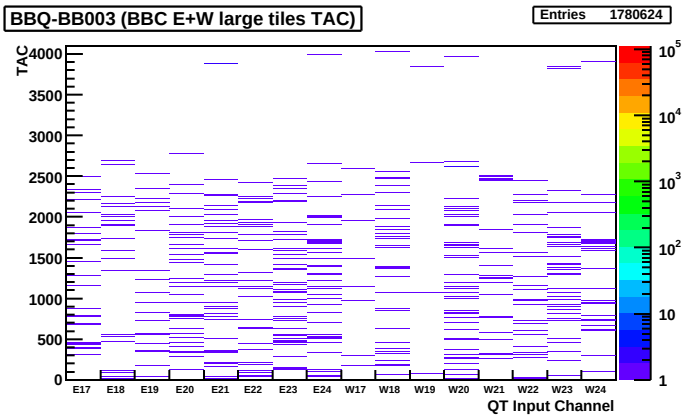

BBQ-BB001 (BBC east small tiles ADC)
Entries 1780624
ADC
QT Input Channel

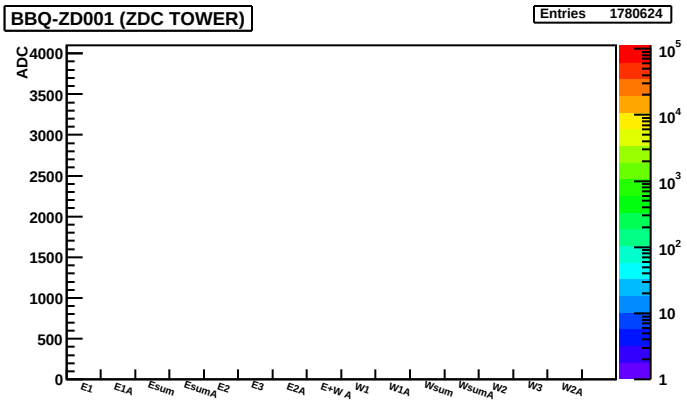

BBQ-BB001 (BBC east small tiles TAC)
Entries 1780624
TAC
QT Input Channel

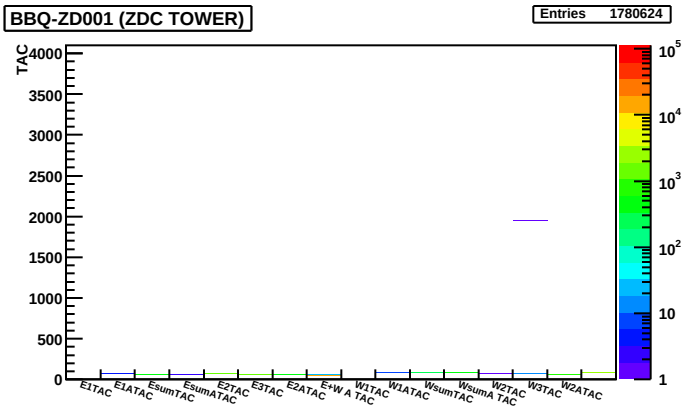

BBQ-BB002 (BBC west small tiles ADC)
Entries 1780624
ADC
QT Input Channel

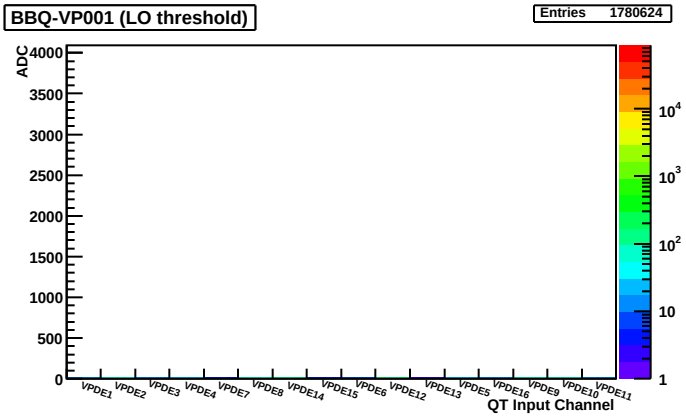

BBQ-BB002 (BBC west small tiles TAC)
Entries 1780624
TAC
QT Input Channel

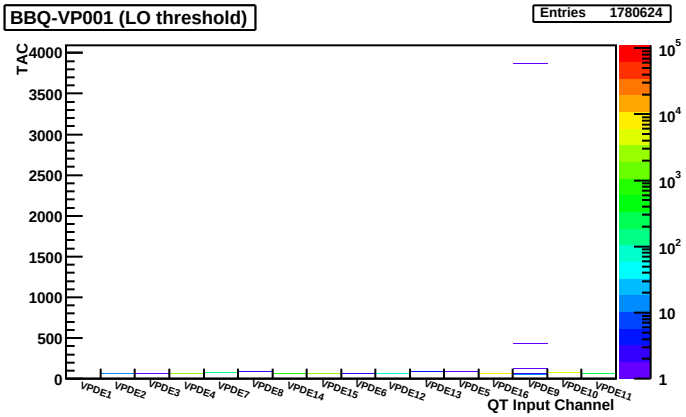

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1780624
ADC
QT Input Channel

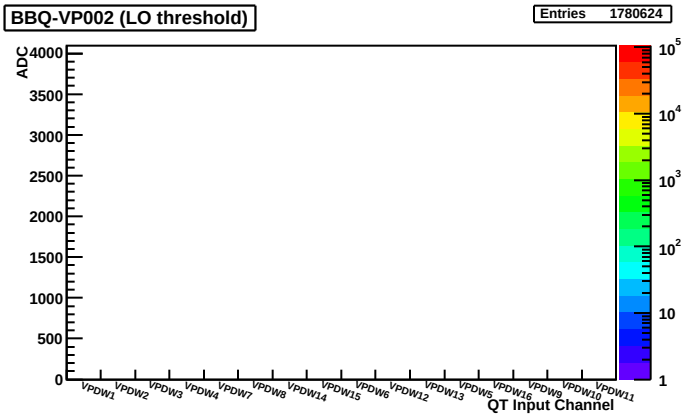

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1780624
TAC
QT Input Channel

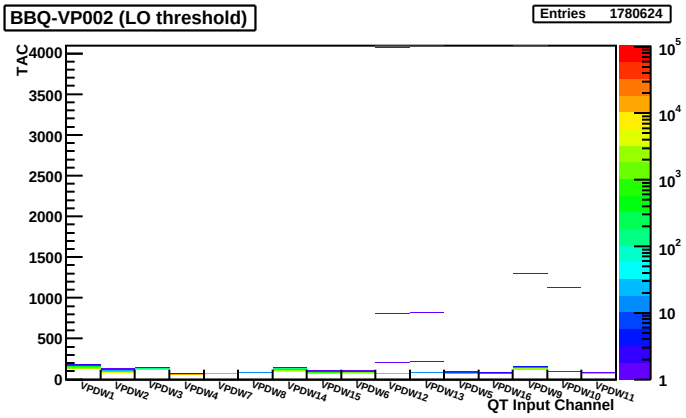

BBQ-ZD001 (ZDC TOWER)
Entries 1780624
ADC

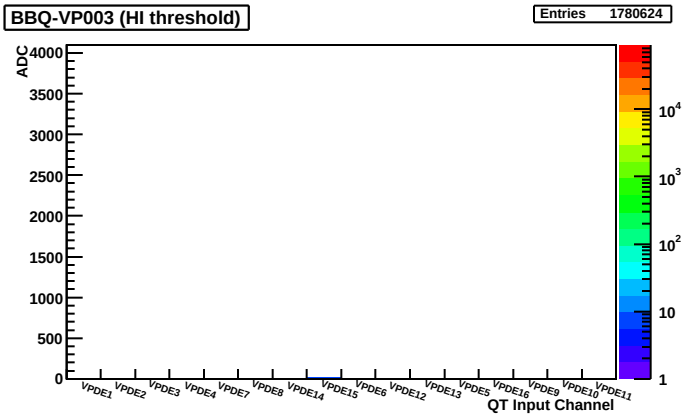

BBQ-ZD001 (ZDC TOWER)
Entries 1780624
TAC

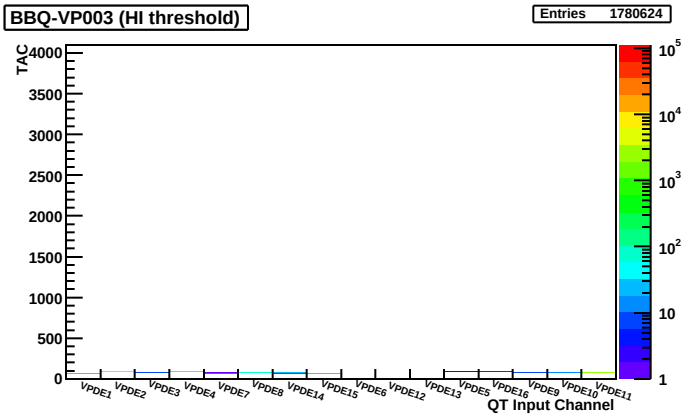

BBQ-VP001 (LO threshold)
Entries 1780624
ADC
QT Input Channel

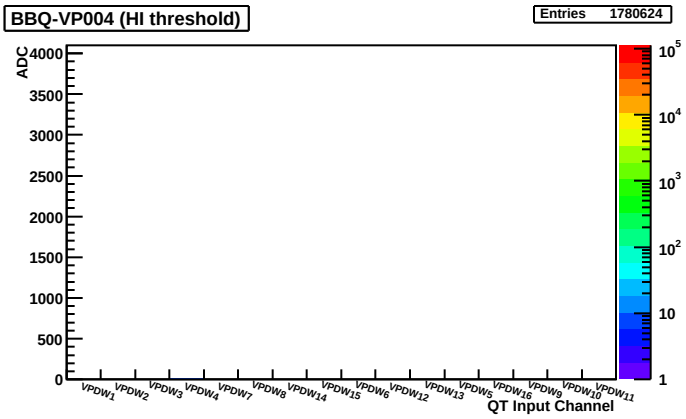

BBQ-VP001 (LO threshold)
Entries 1780624
TAC
QT Input Channel

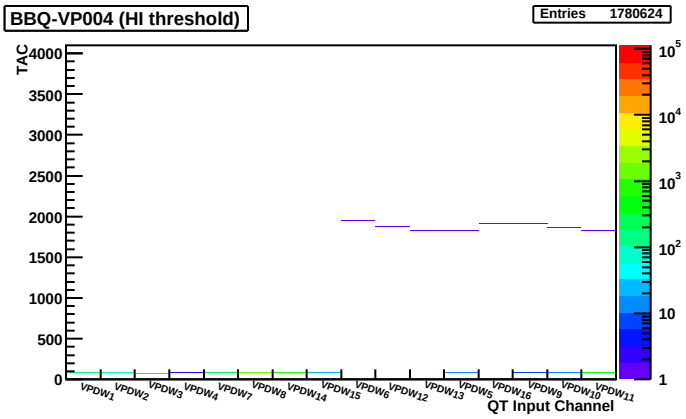

BBQ-VP002 (LO threshold)
Entries 1780624
ADC
QT Input Channel

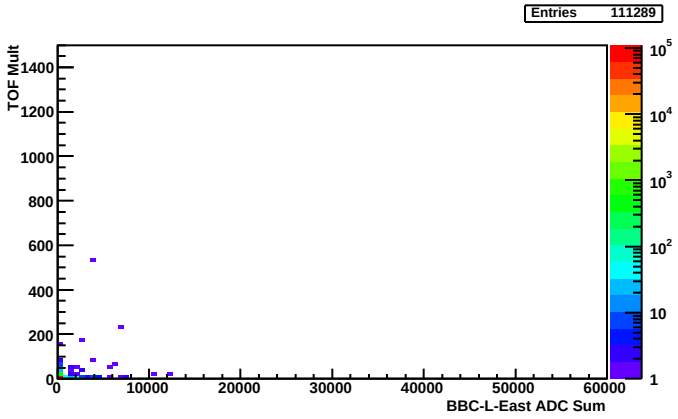

BBQ-VP002 (LO threshold)
Entries 1780624
TAC
QT Input Channel

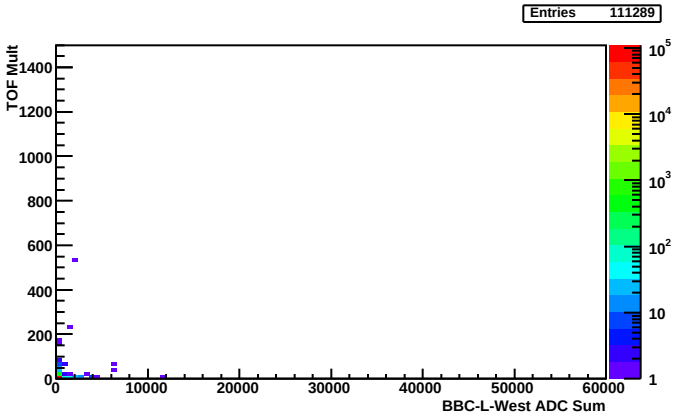

BBQ-VP003 (HI threshold)
Entries 1780624
ADC
QT Input Channel

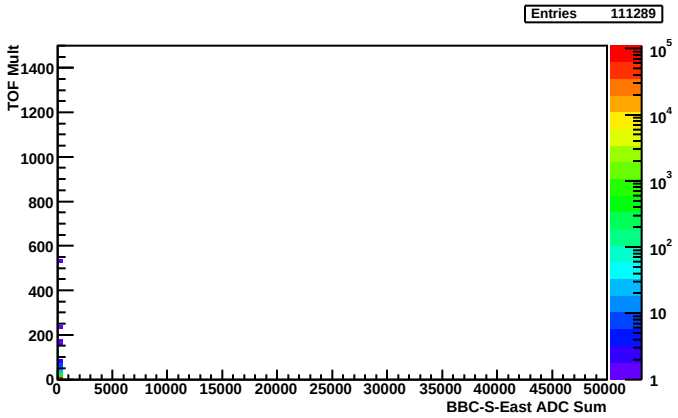

BBQ-VP003 (HI threshold)
Entries 1780624
TAC
QT Input Channel

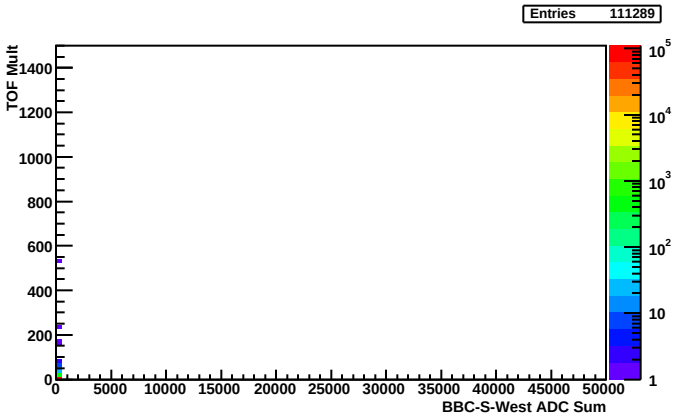

BBQ-VP004 (HI threshold)
Entries 1780624
ADC
QT Input Channel

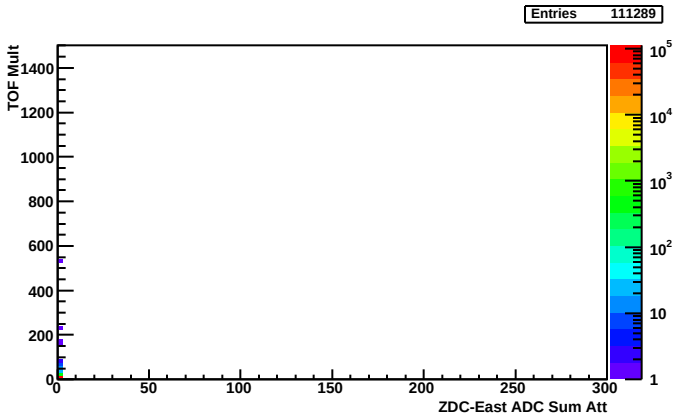

BBQ-VP004 (HI threshold)
Entries 1780624
TAC
QT Input Channel

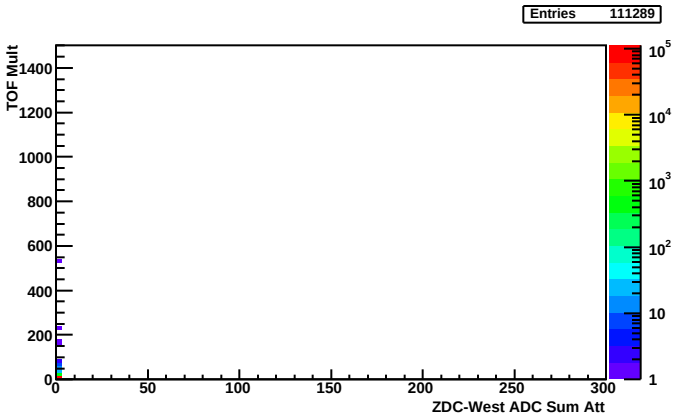

Entries 111289
TOF Mult
BBC-L-East ADC Sum

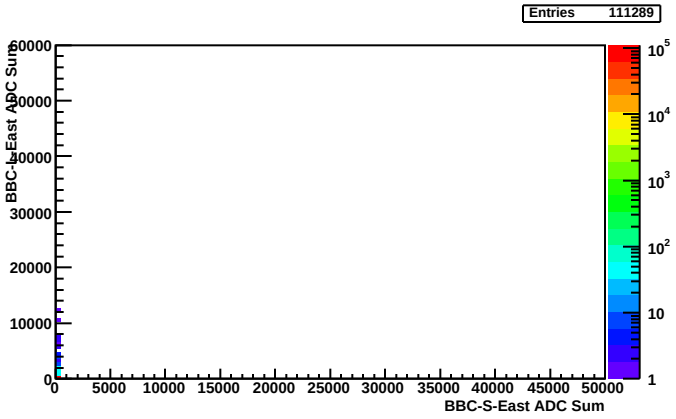

Entries 111289
TOF Mult
BBC-L-West ADC Sum

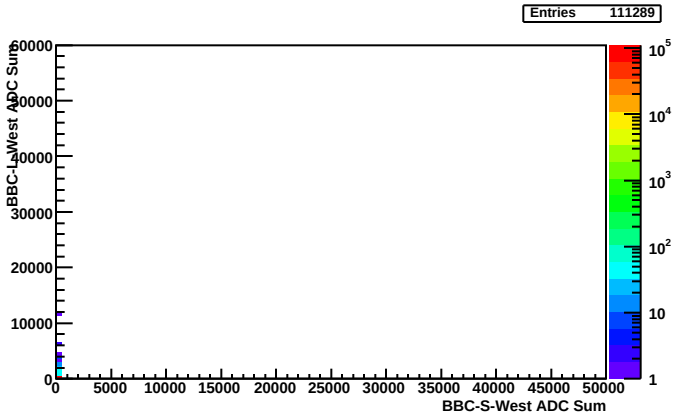

Entries 111289
TOF Mult
BBC-S-East ADC Sum

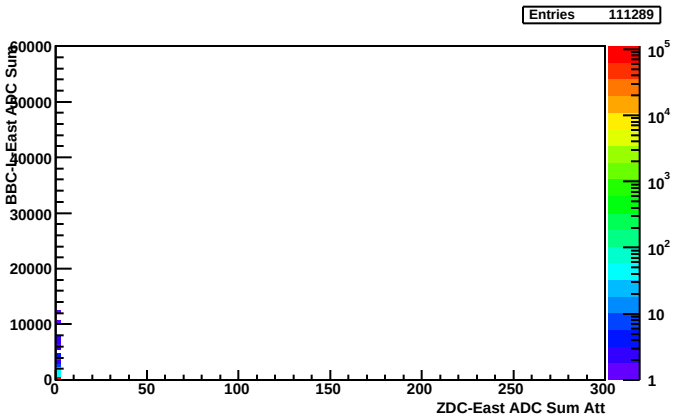

Entries 111289
TOF Mult
BBC-S-West ADC Sum

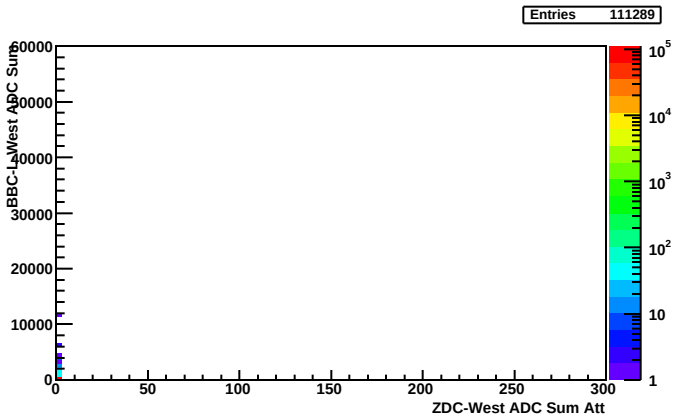

Entries 111289
TOF Mult
ZDC-East ADC Sum Att

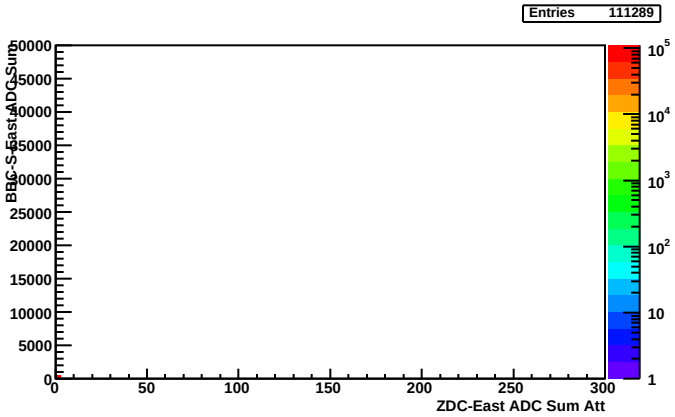

Entries 111289
TOF Mult
ZDC-West ADC Sum Att

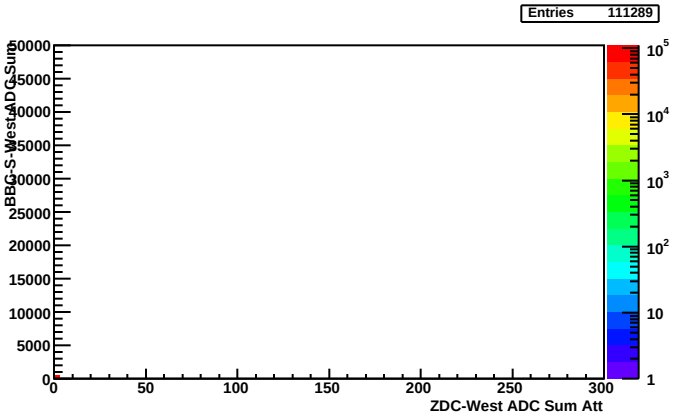

Entries 111289
BBC-L-East ADC Sum
BBC-S-East ADC Sum

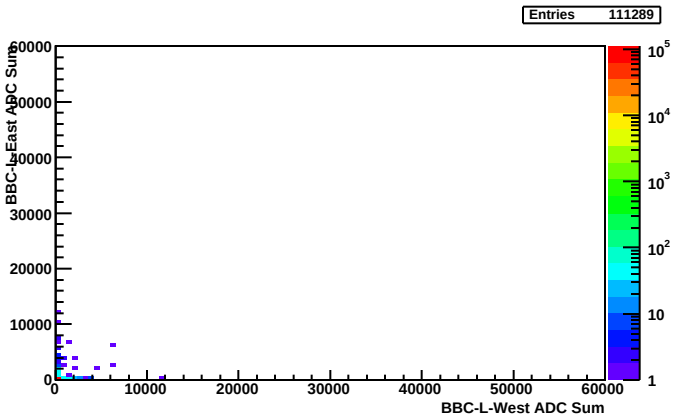

Entries 111289
BBC-L-West ADC Sum
BBC-S-West ADC Sum

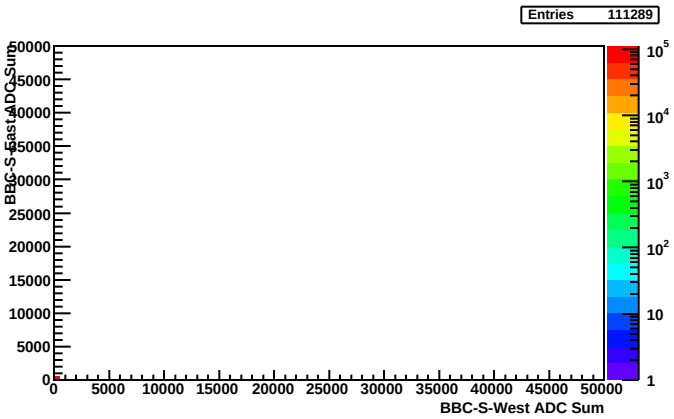

Entries 111289
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

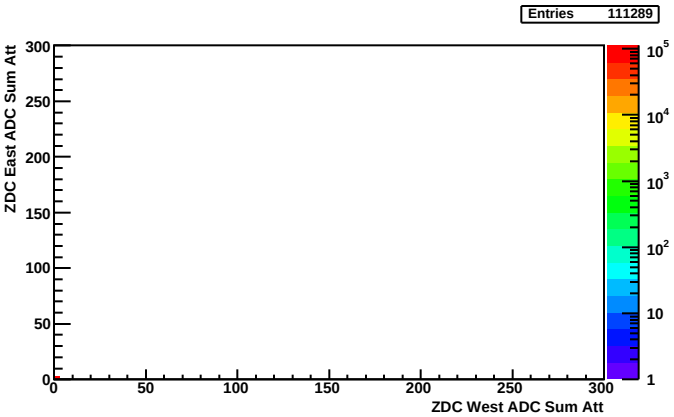

Entries 111289
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

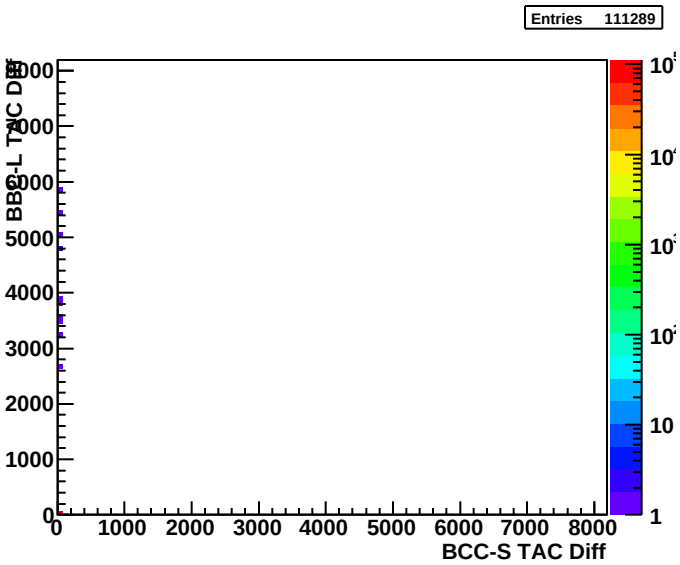

Entries 111289
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

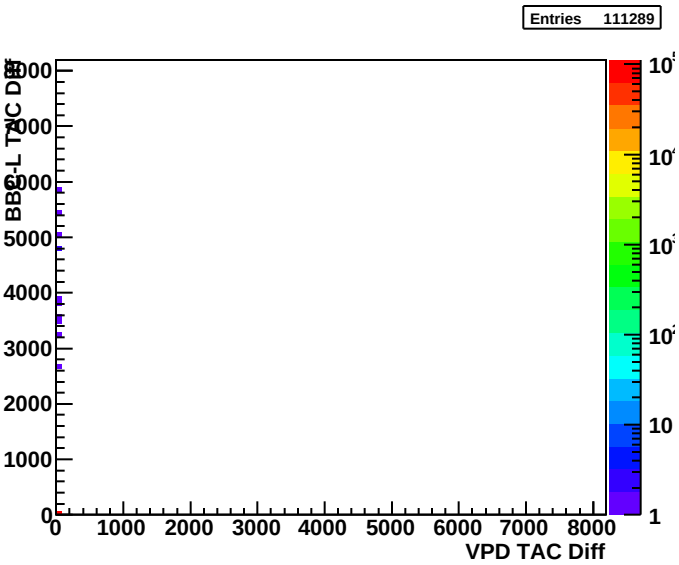

Entries 111289
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

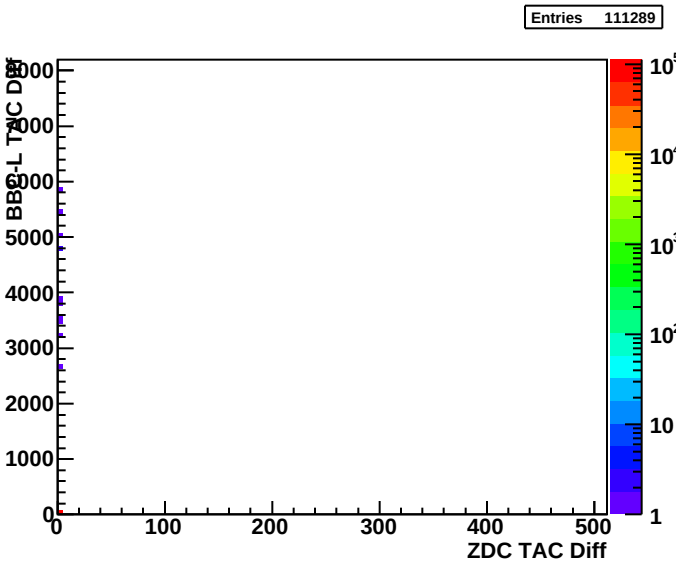

Entries 111289
BBC-L-East ADC Sum
BBC-L-West ADC Sum

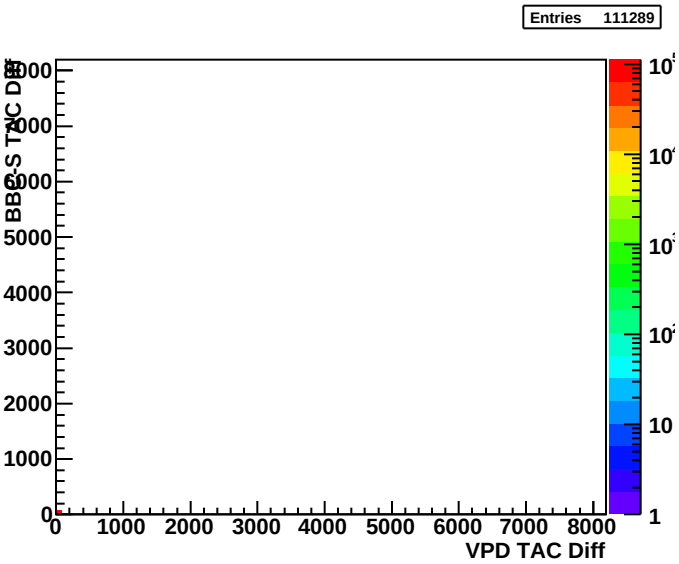

Entries 111289
BBC-S-East ADC Sum
BBC-S-West ADC Sum

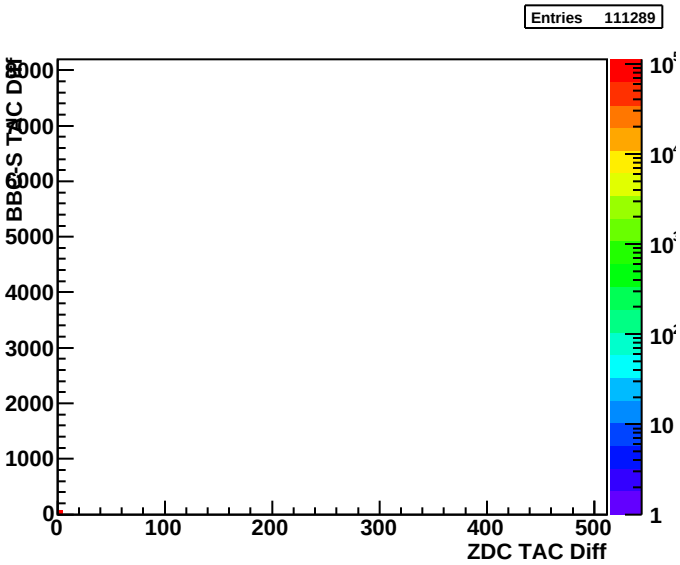

Entries 111289
ZDC East ADC Sum Att
ZDC West ADC Sum Att

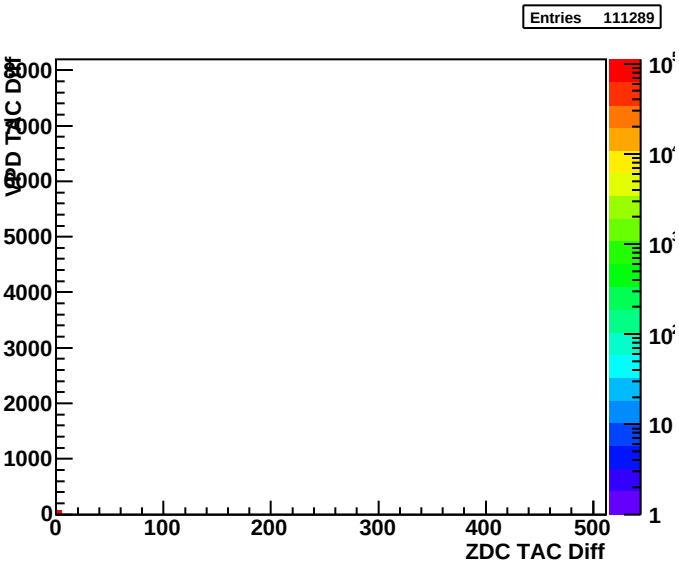

Entries 111289
BBC-L TAC Diff
BCC-S TAC Diff

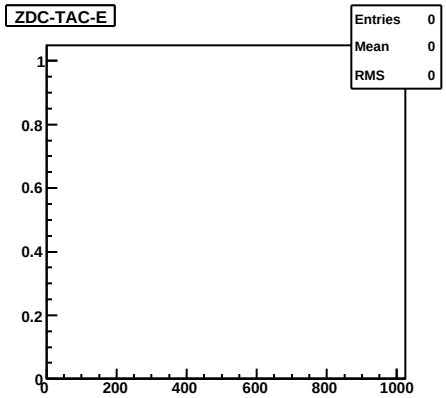

Entries 111289
BBC-L TAC Diff
VPD TAC Diff

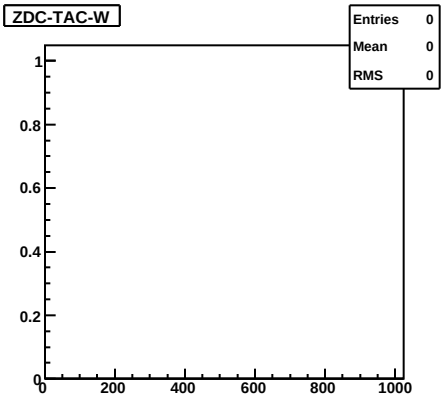

Entries 111289
BBC-L TAC Diff
ZDC TAC Diff

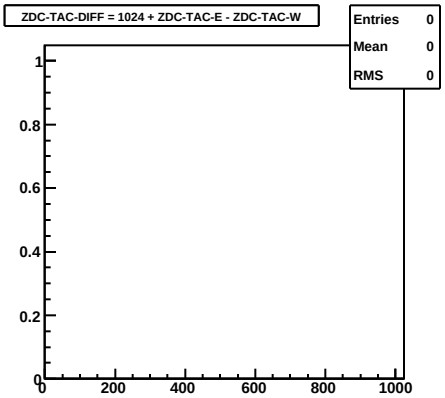

Entries 111289
BBC-S TAC Diff
VPD TAC Diff

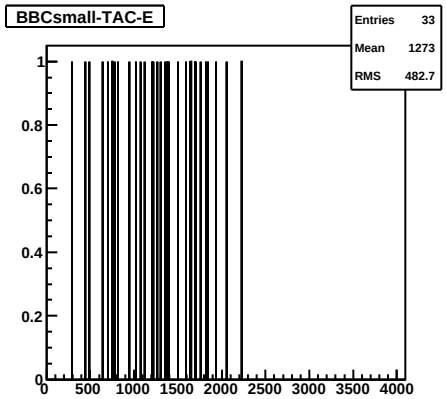

Entries 111289
BBC-S TAC Diff
ZDC TAC Diff

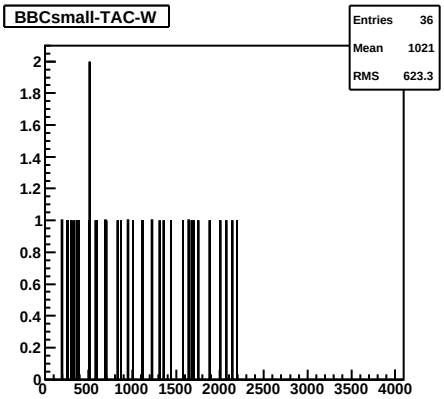

Entries 111289
VPD TAC Diff
ZDC TAC Diff

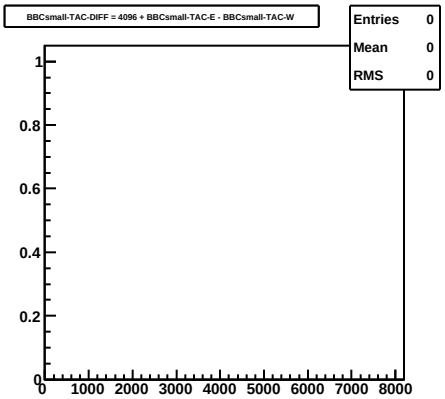

ZDC-TAC-E
Entries 0
Mean 0
RMS 0

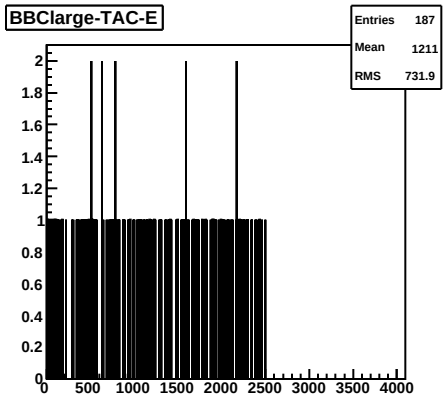

ZDC-TAC-W
Entries 0
Mean 0
RMS 0

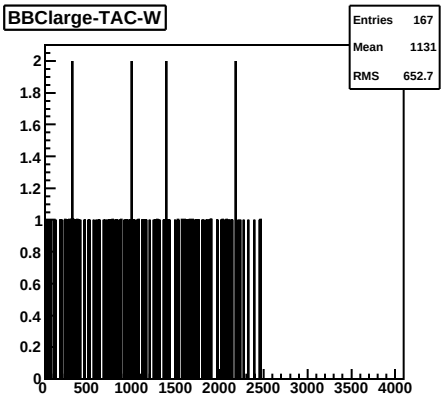

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 0
Mean 0
RMS 0

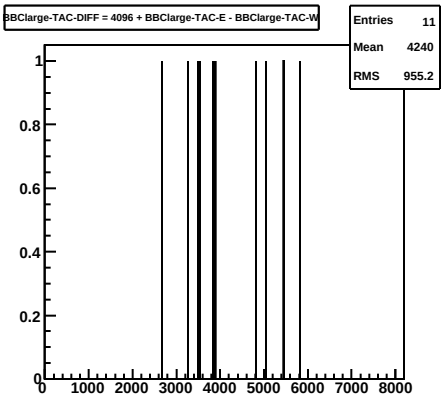

BBCsmall-TAC-E
Entries 33
Mean 1273
RMS 482.7

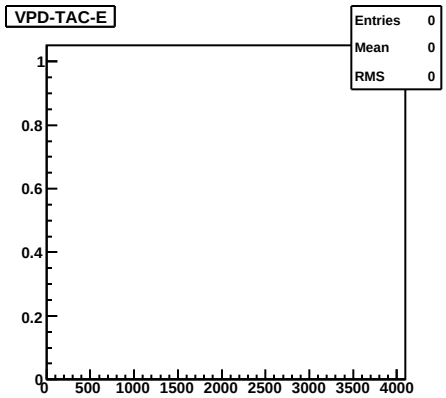

BBCsmall-TAC-W
Entries 36
Mean 1021
RMS 623.3

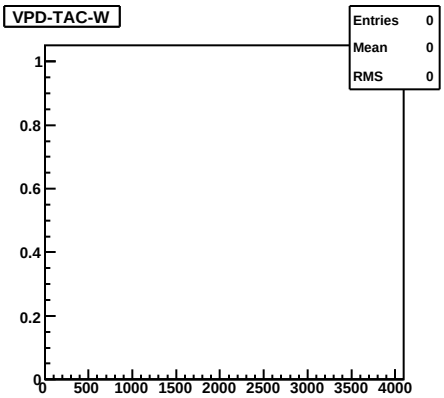

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 0
Mean 0
RMS 0

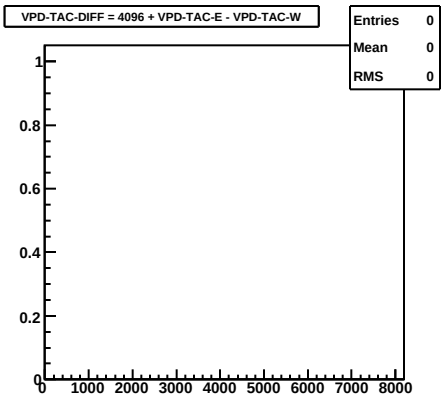

BBClarge-TAC-E
Entries 187
Mean 1211
RMS 731.9

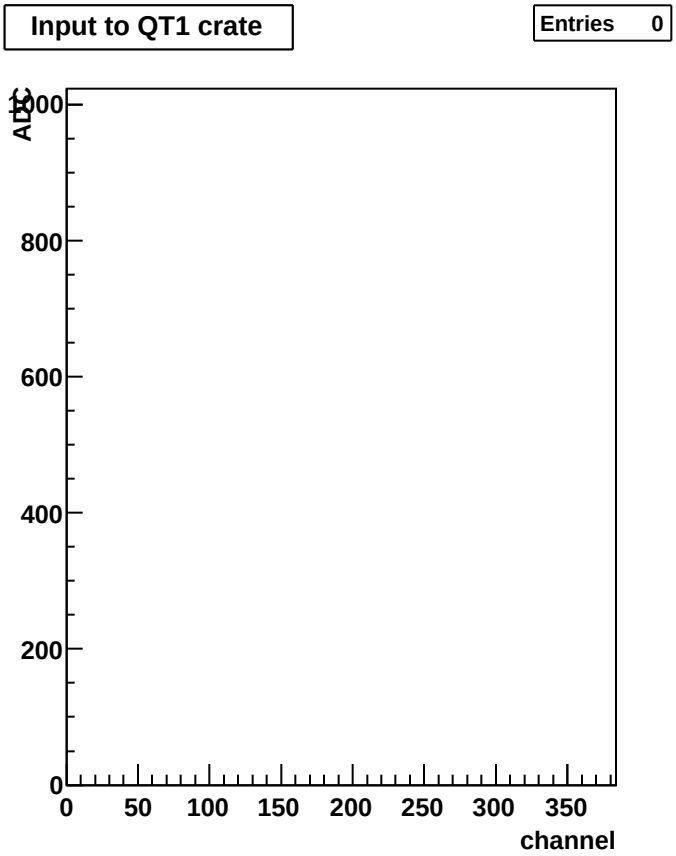

BBClarge-TAC-W
Entries 167
Mean 1131
RMS 652.7

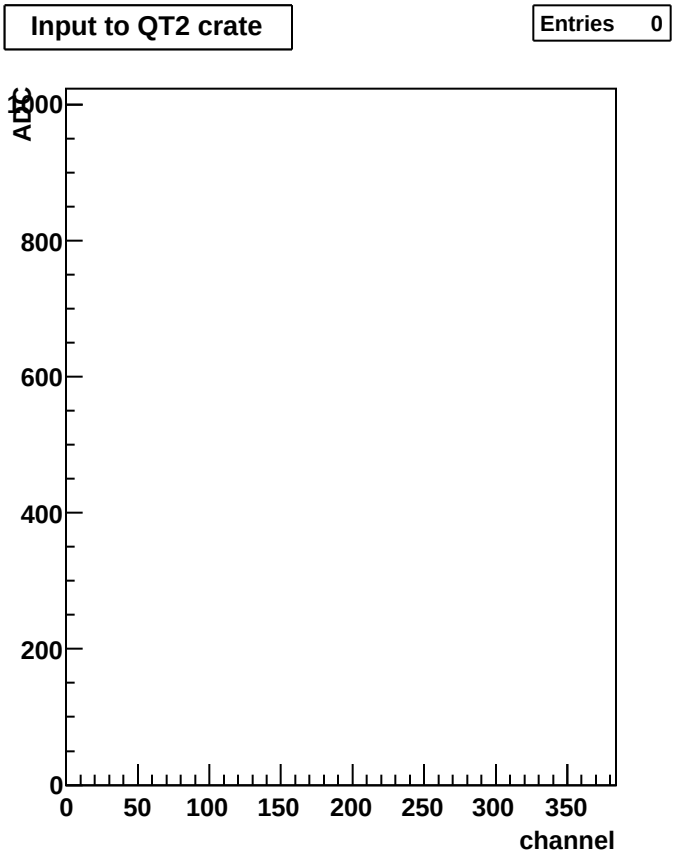

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 11
Mean 4240
RMS 955.2

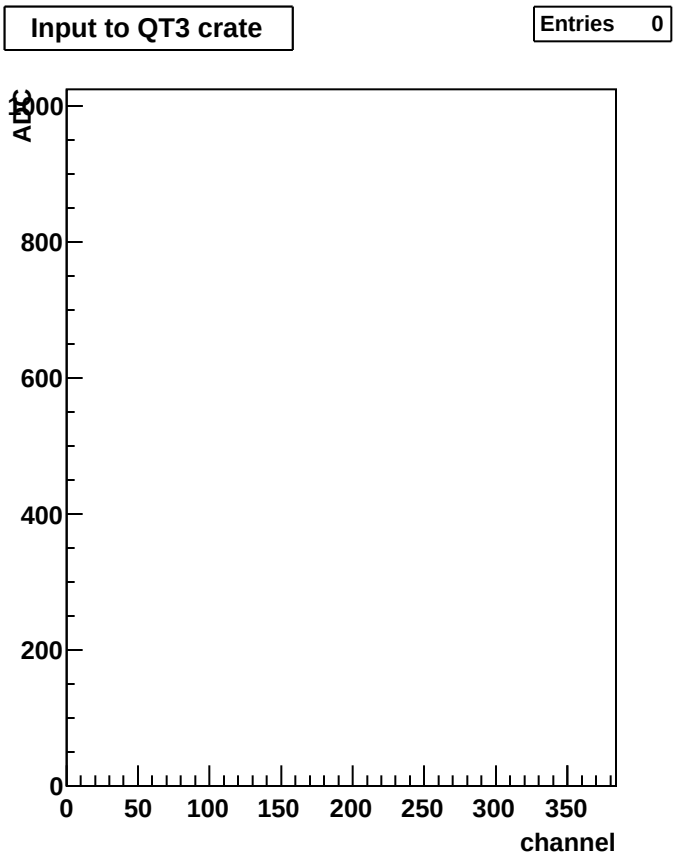

VPD-TAC-E
Entries 0
Mean 0
RMS 0

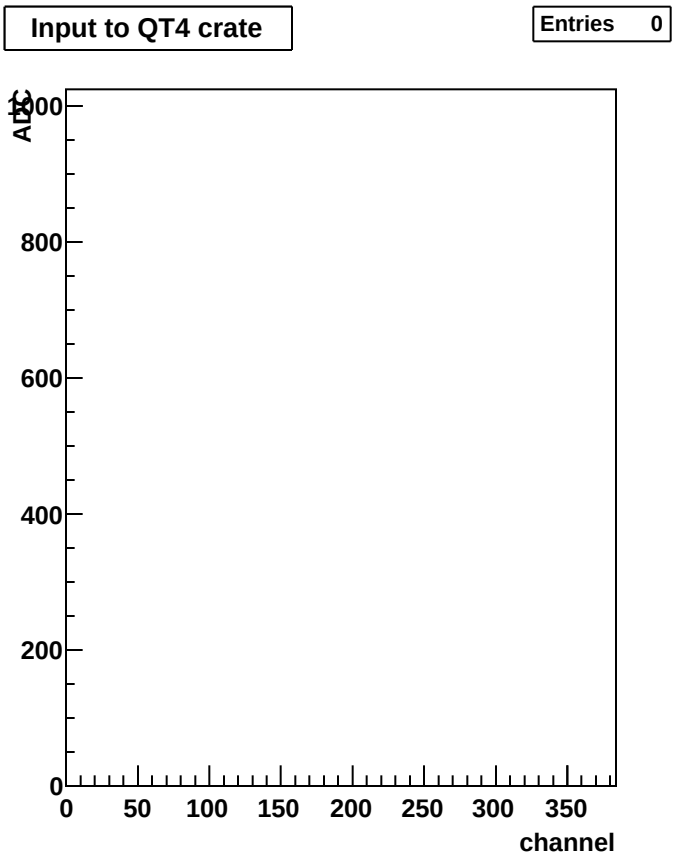

VPD-TAC-W
Entries 0
Mean 0
RMS 0

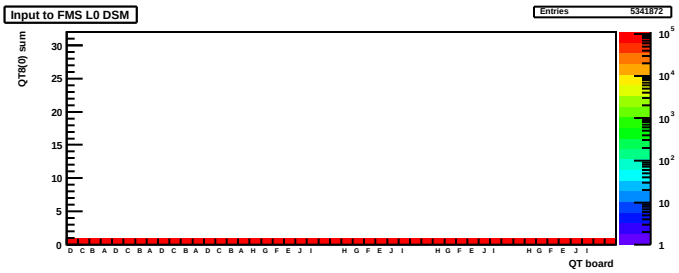

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 0
Mean 0
RMS 0

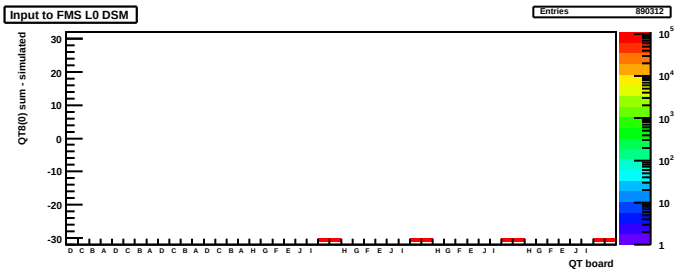

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

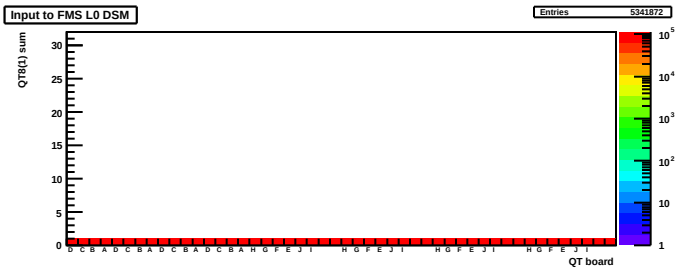

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

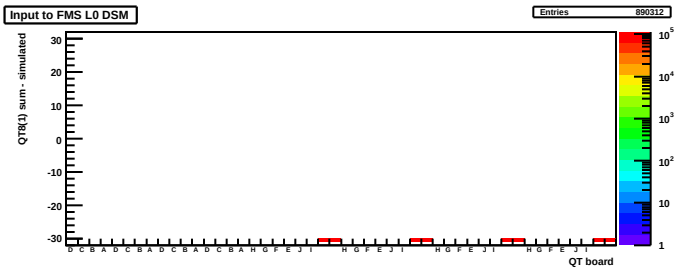

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

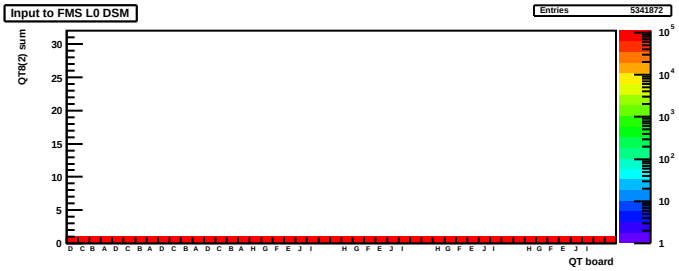

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

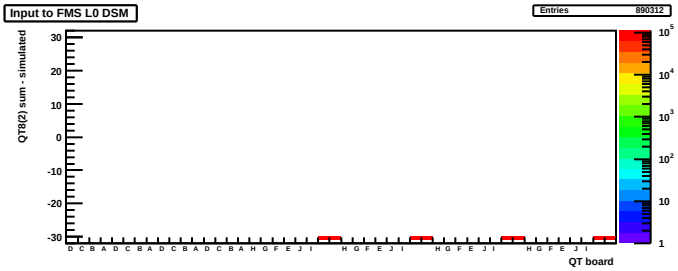

Input to FMS L0 DSM
Entries 5341872
QT8(0) sum
QT board

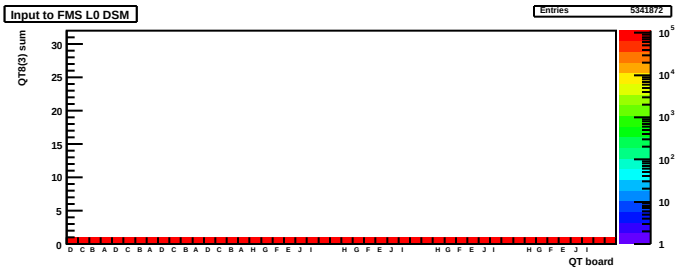

Input to FMS L0 DSM
Entries 890312
QT8(0) sum - simulated
QT board

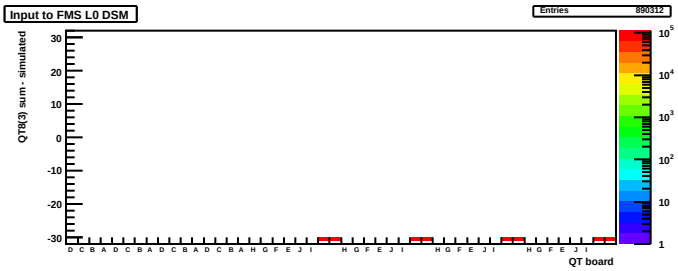

Input to FMS L0 DSM
Entries 5341872
QT8(1) sum
QT board

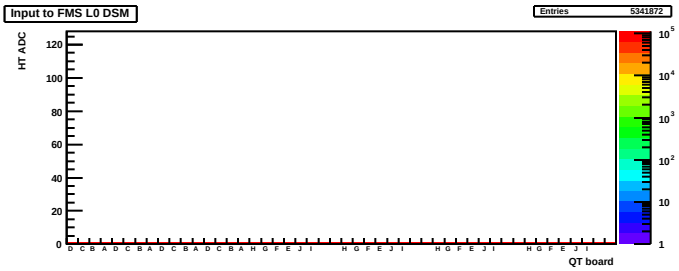

Input to FMS L0 DSM
Entries 890312
QT8(1) sum - simulated
QT board

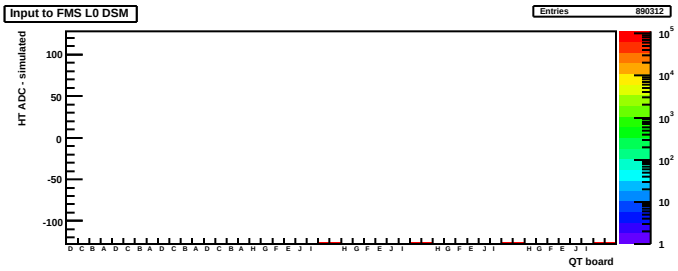

Input to FMS L0 DSM
Entries 5341872
QT8(2) sum
QT board

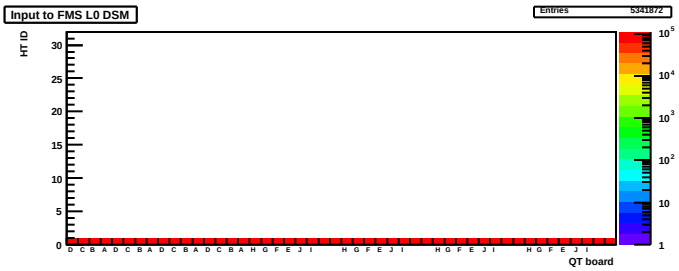

Input to FMS L0 DSM
Entries 890312
QT8(2) sum - simulated
QT board

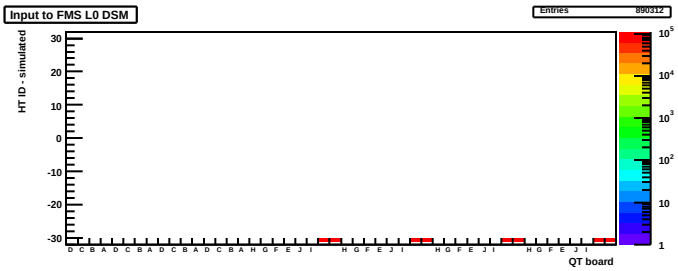

Input to FMS L0 DSM
Entries 5341872
QT8(3) sum
QT board

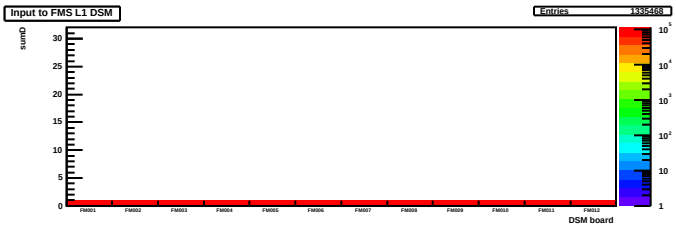

Input to FMS L0 DSM
Entries 890312
QT8(3) sum - simulated
QT board

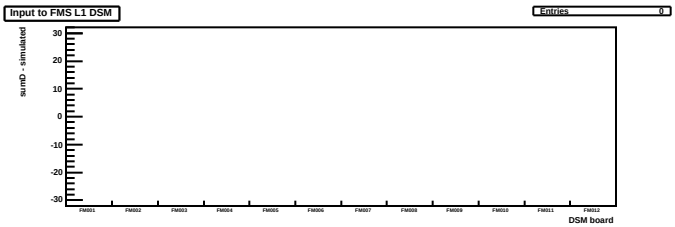

Input to FMS L0 DSM
Entries 5341872
HT ADC
QT board

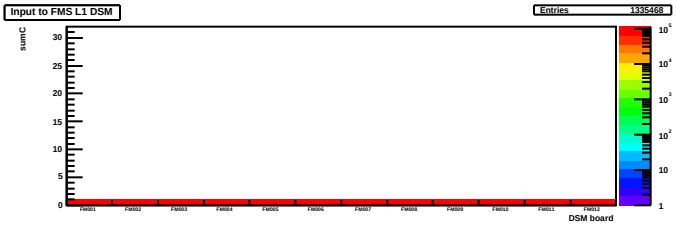

Input to FMS L0 DSM
Entries 890312
HT ADC - simulated
QT board

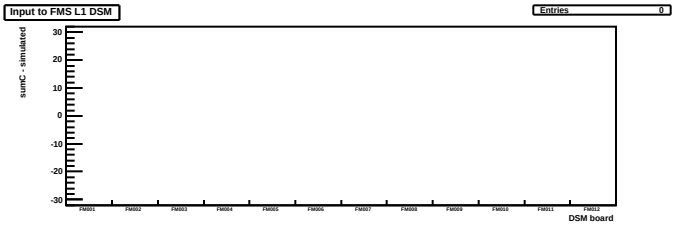

Input to FMS L0 DSM
Entries 5341872
HT ID
QT board

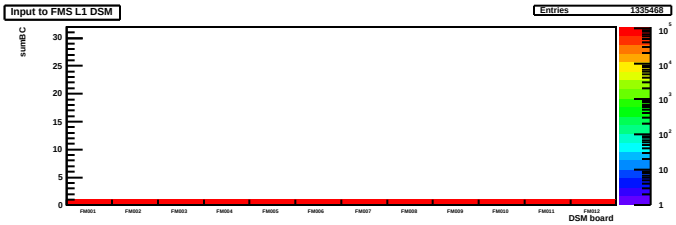

Input to FMS L0 DSM
Entries 890312
HT ID - simulated
QT board

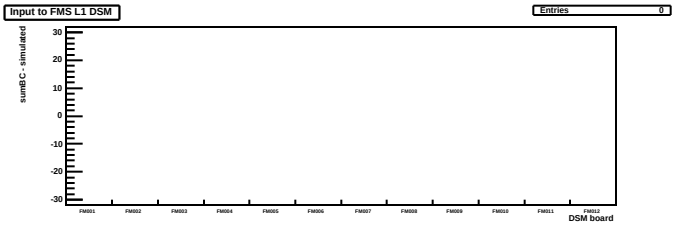

Input to FMS L1 DSM
Entries 1335468
sumD
DSM board

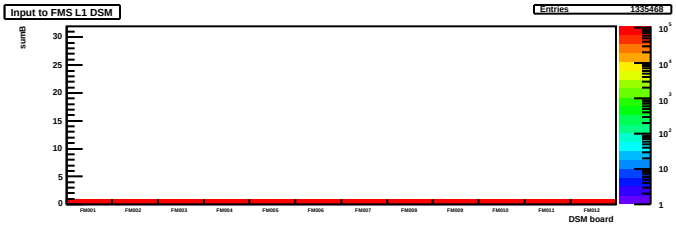

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

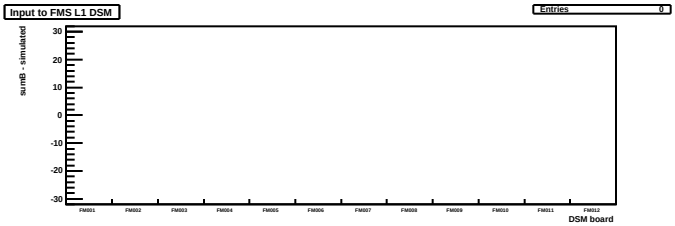

Input to FMS L1 DSM
Entries 1335468
sumC
DSM board

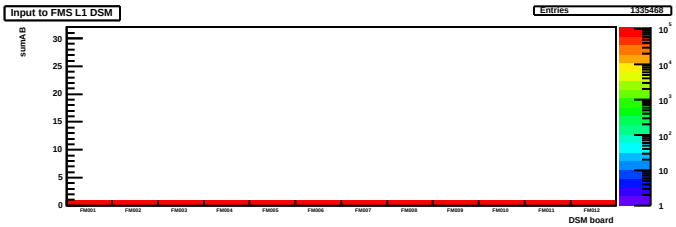

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

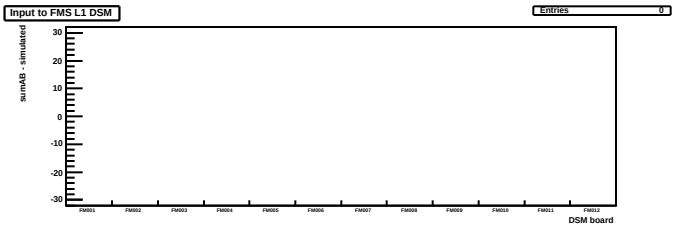

Input to FMS L1 DSM
Entries 1335468
sumBC
DSM board

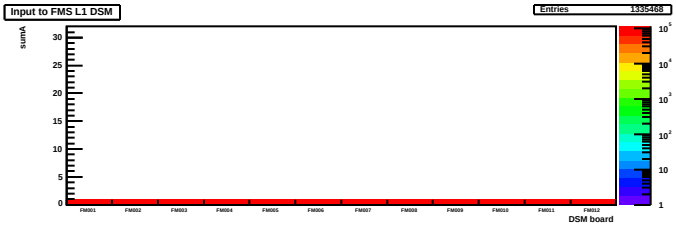

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

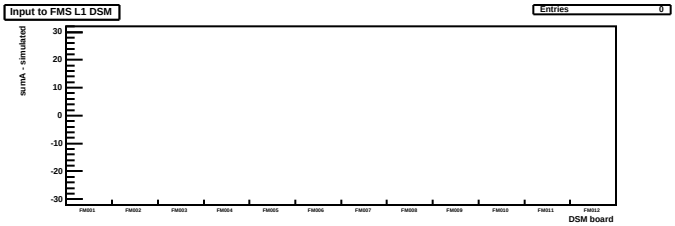

Input to FMS L1 DSM
Entries 1335468
sumB
DSM board

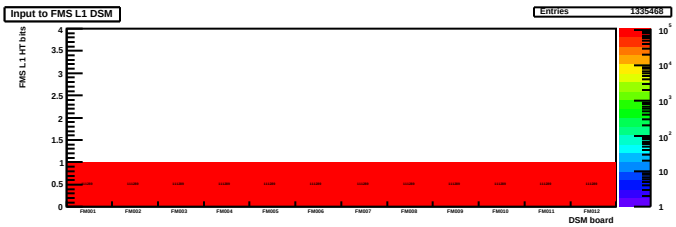

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

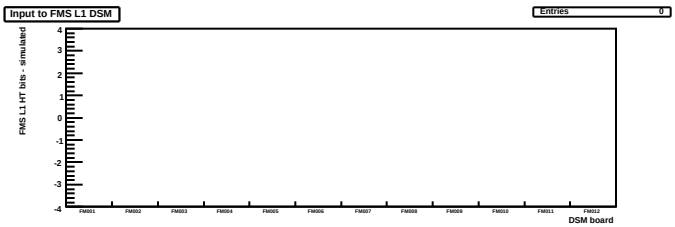

Input to FMS L1 DSM
Entries 1335468
sumAB
DSM board

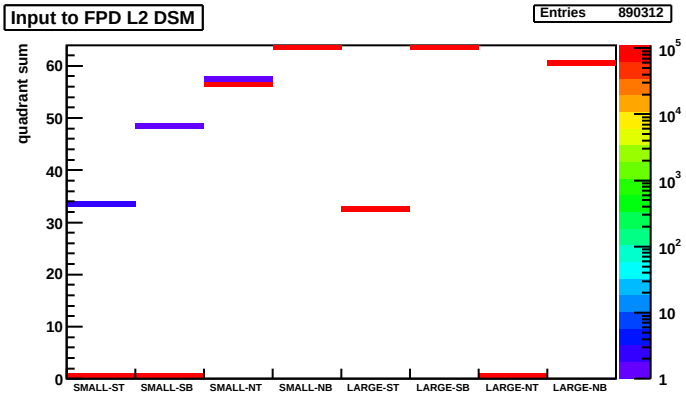

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

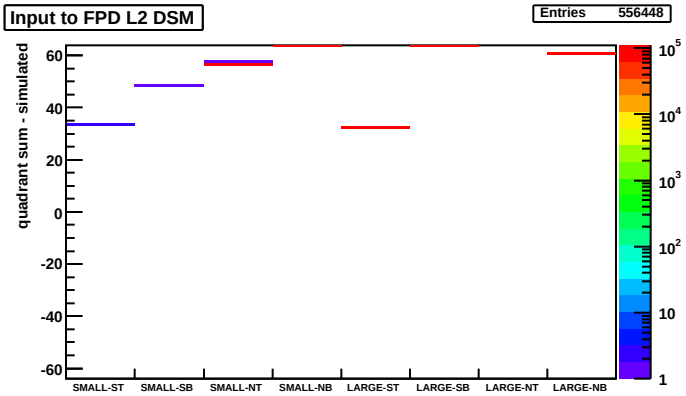

Input to FMS L1 DSM
Entries 1335468
sumA
DSM board

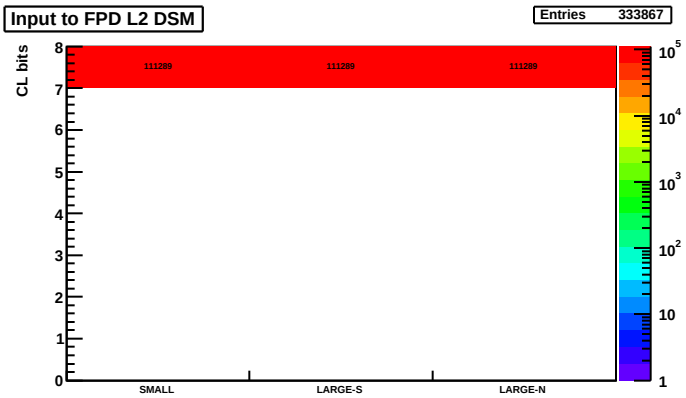

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

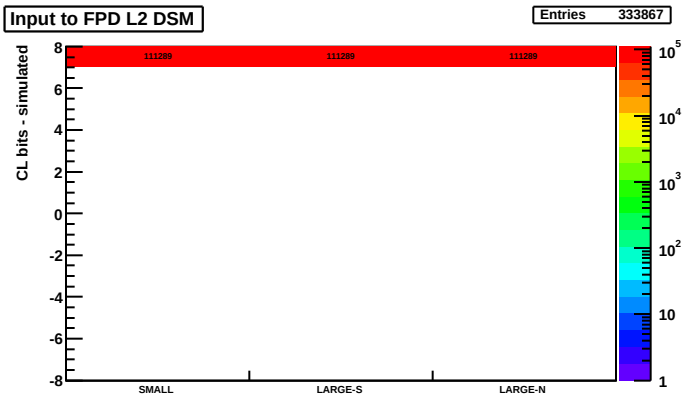

Input to FMS L1 DSM
Entries 1335468
FMS L1 HT bits
DSM board

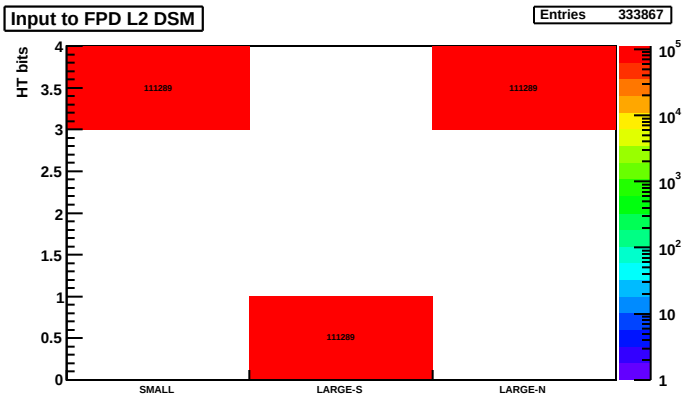

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

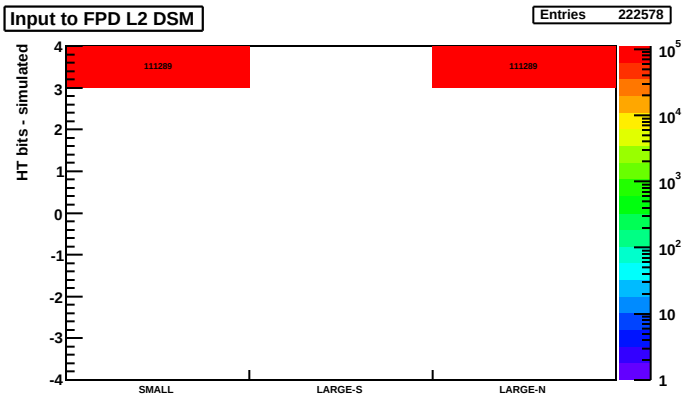

Input to FPD L2 DSM
Entries 890312
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

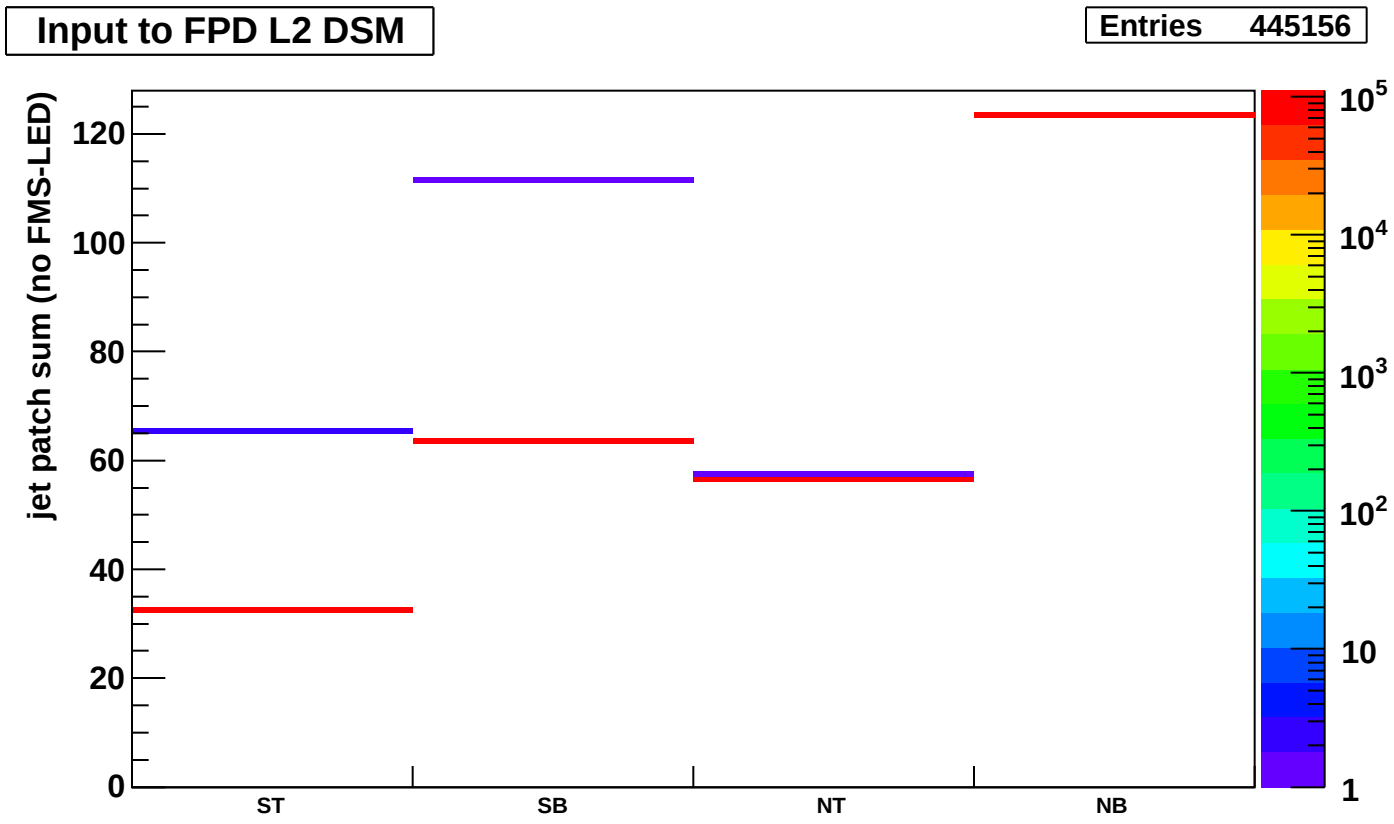

Input to FPD L2 DSM
Entries 556448
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

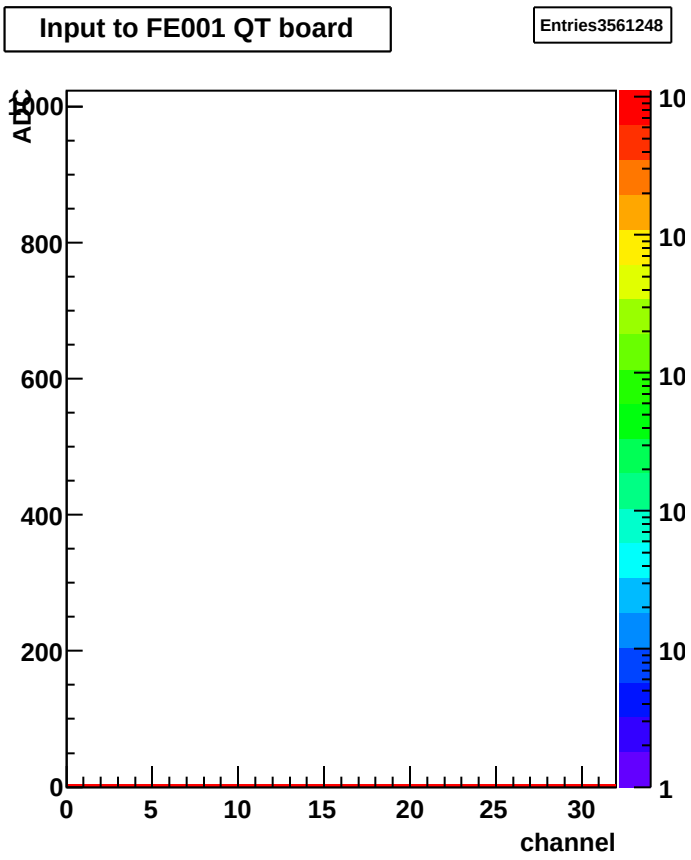

Input to FPD L2 DSM
Entries 333867
CL bits
111289
SMALL
LARGE-S
LARGE-N

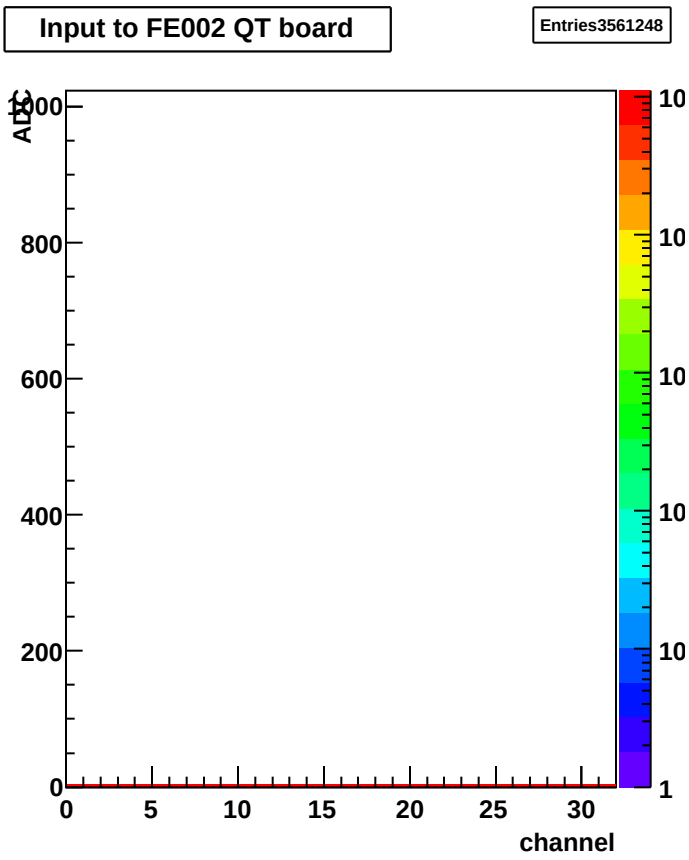

Input to FPD L2 DSM
Entries 333867
CL bits - simulated
111289
SMALL
LARGE-S
LARGE-N

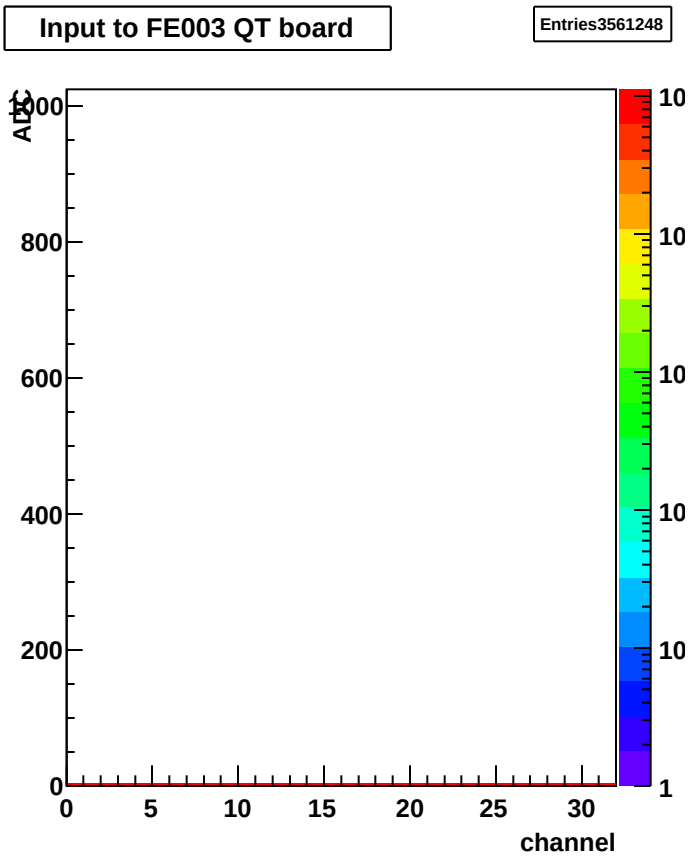

Input to FPD L2 DSM
Entries 333867
HT bits
111289
SMALL
LARGE-S
LARGE-N

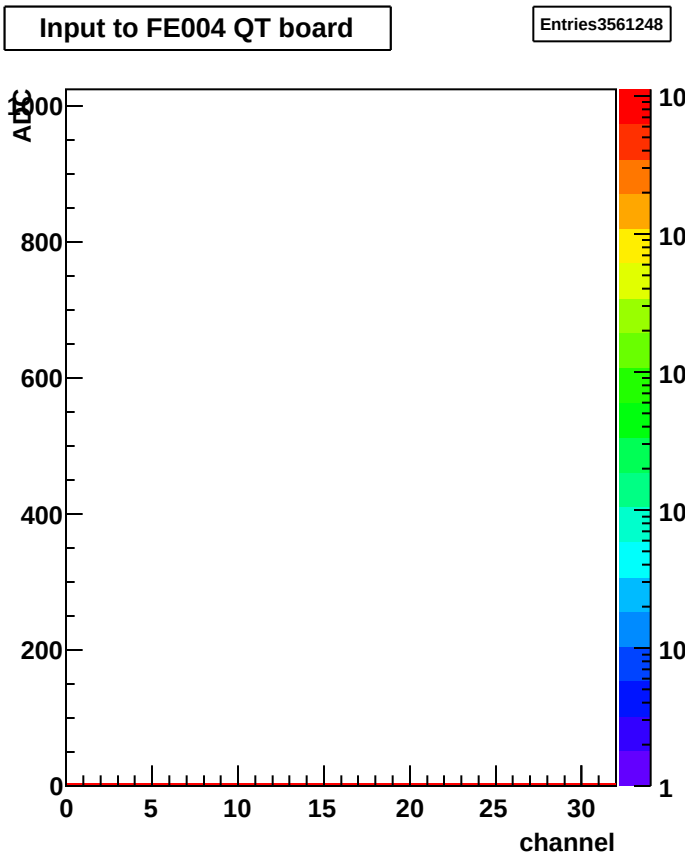

Input to FPD L2 DSM
Entries 222578
HT bits - simulated
111289
SMALL
LARGE-S
LARGE-N

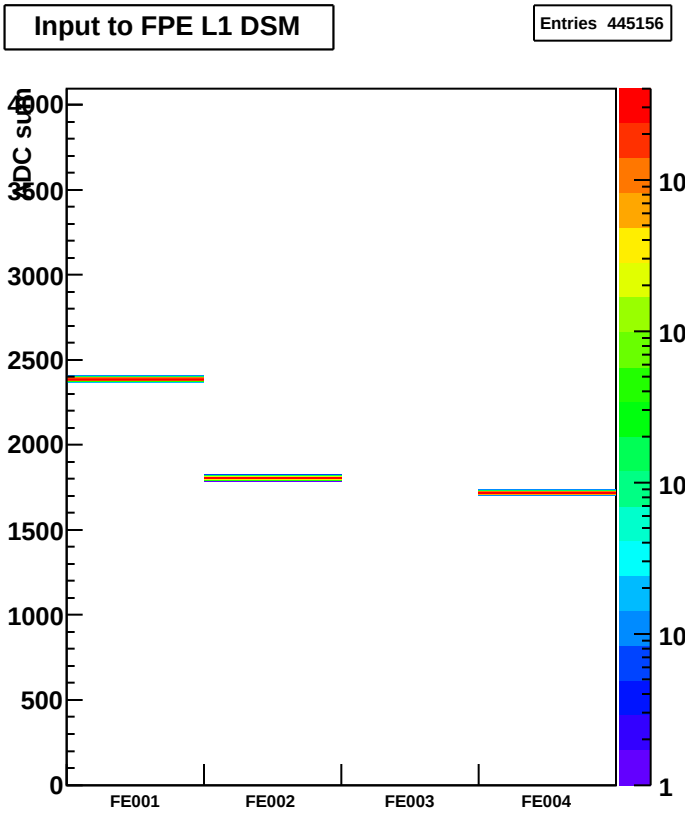

Input to FPD L2 DSM
Entries 445156
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

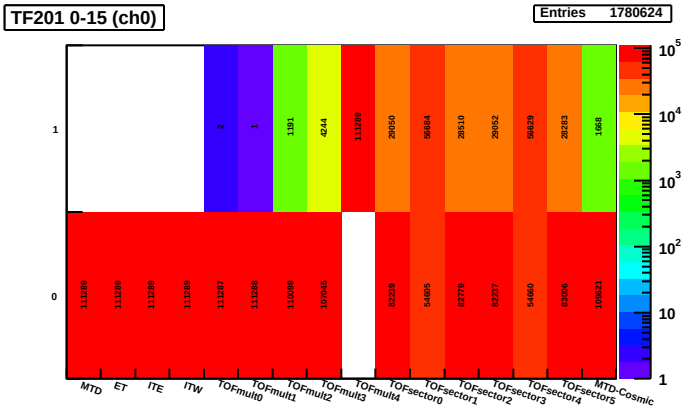

Input to FE001 QT board
Entries3561248
ADC
channel

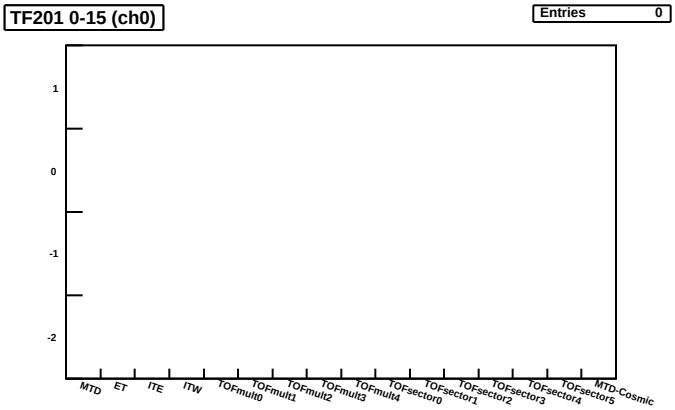

Input to FE002 QT board
Entries3561248
ADC
channel

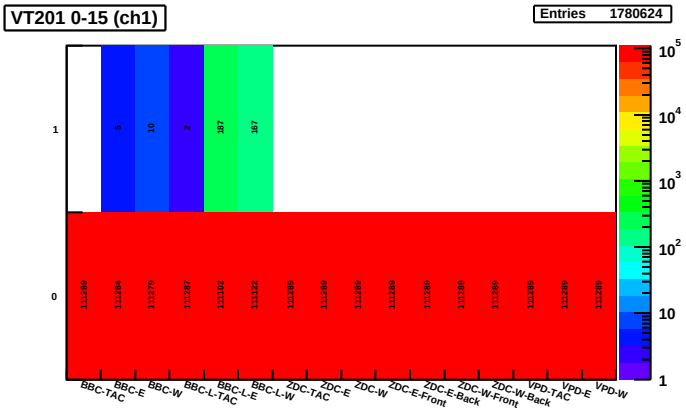

Input to FE003 QT board
Entries3561248
ADC
channel

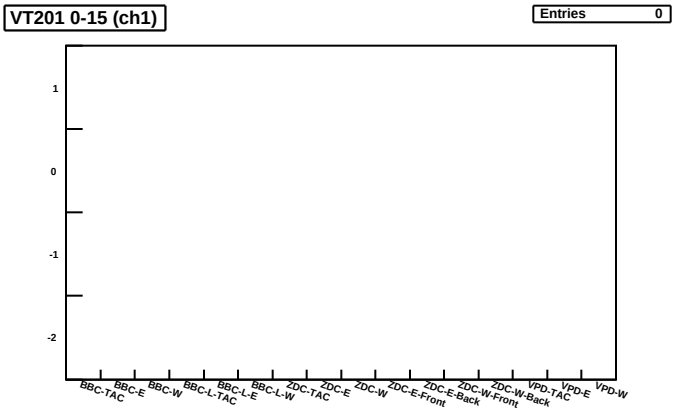

Input to FE004 QT board
Entries3561248
ADC
channel

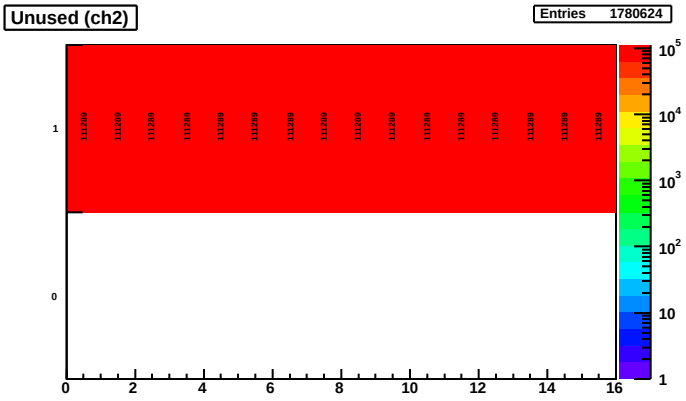

Input to FPE L1 DSM
Entries 445156
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

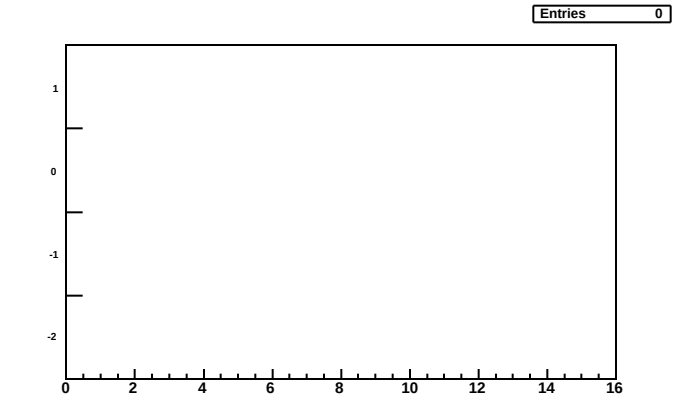

TF201 0-15 (ch0)
Entries 1780624
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

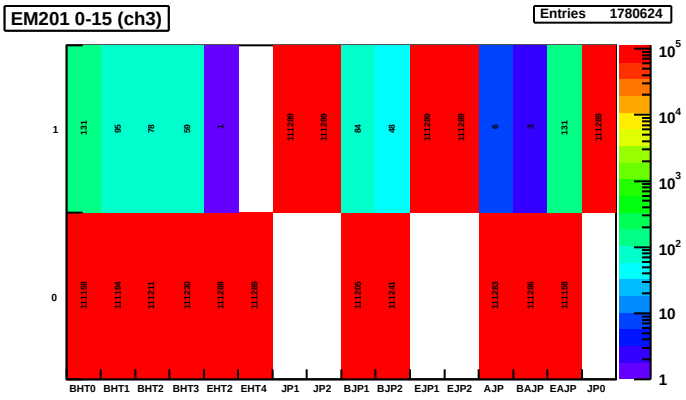

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

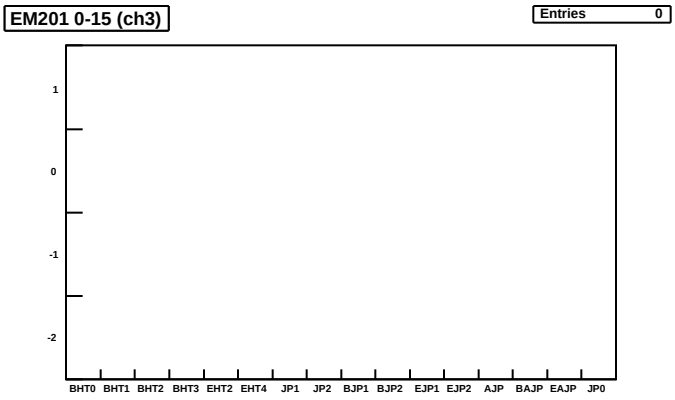

VT201 0-15 (ch1)
Entries 1780624
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

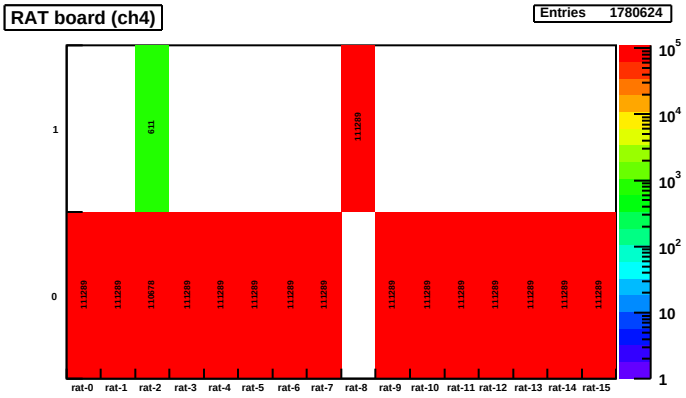

VT201 0-15 (ch1)
Entries 0
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

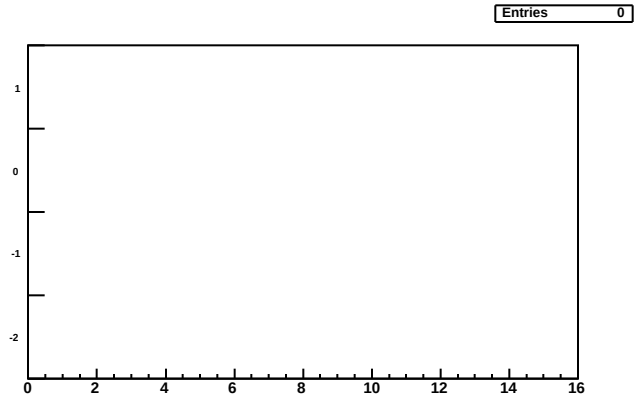

Unused (ch2)
Entries 1780624

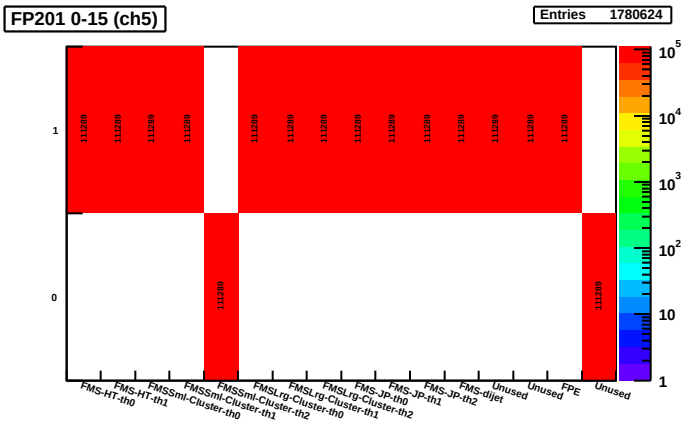

Entries 0


EM201 0-15 (ch3)
Entries 1780624
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0


EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0


RAT board (ch4)
Entries 1780624
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15


Entries 0


FP201 0-15 (ch5)
Entries 1780624
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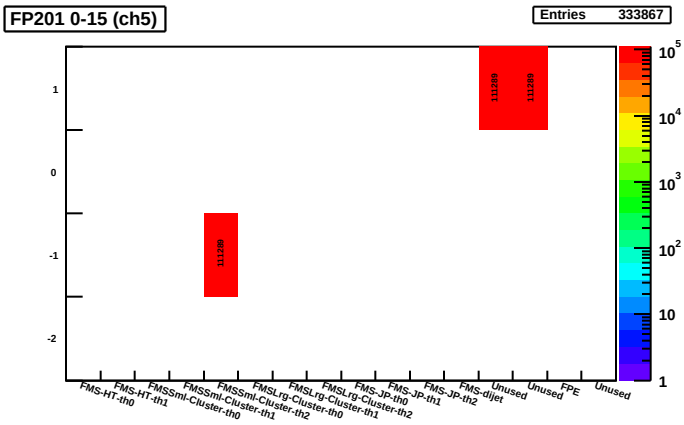
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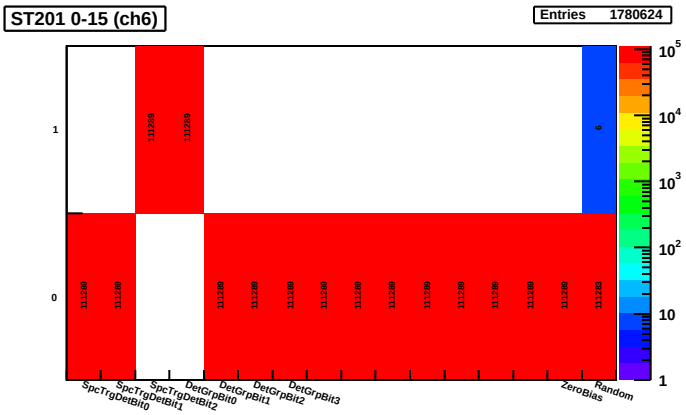
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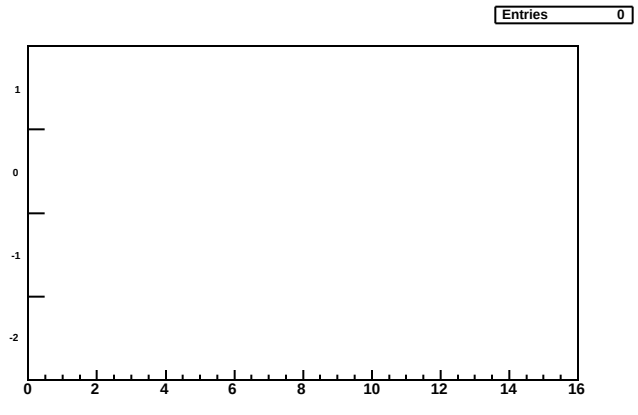
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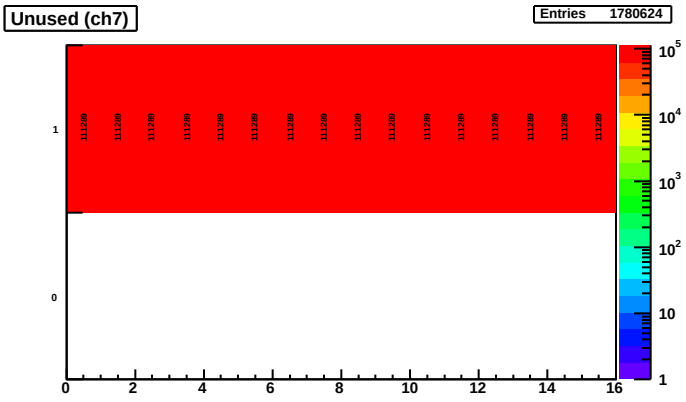
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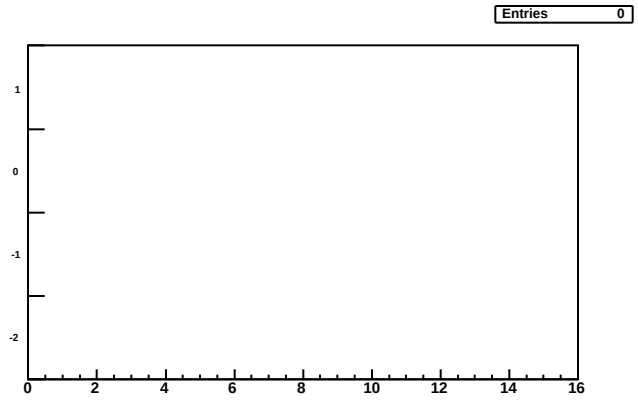
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