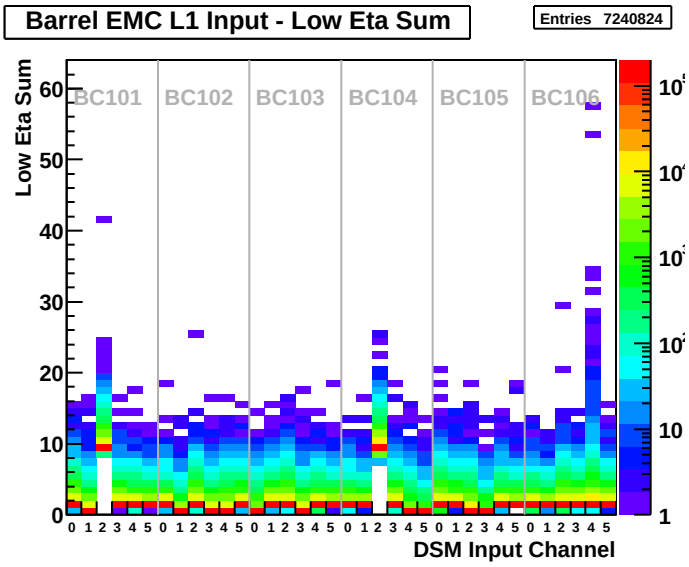
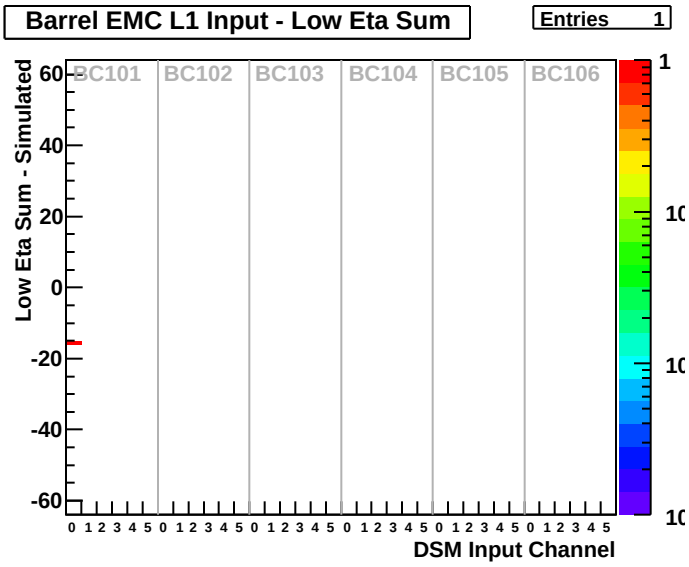
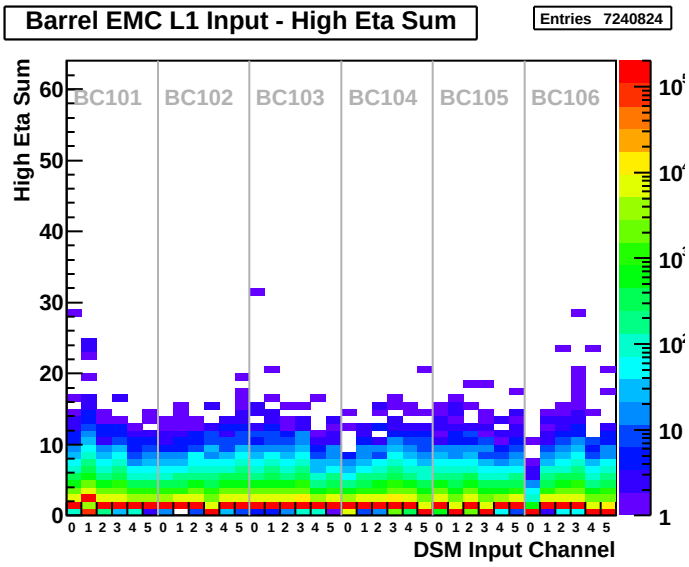
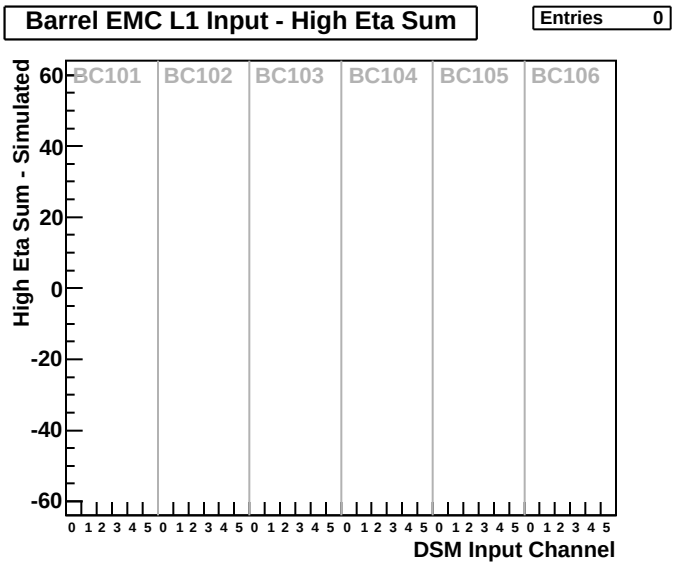
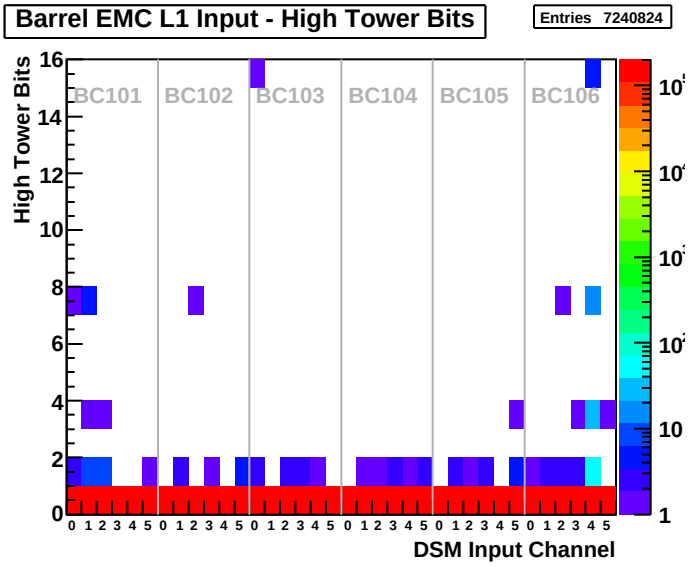
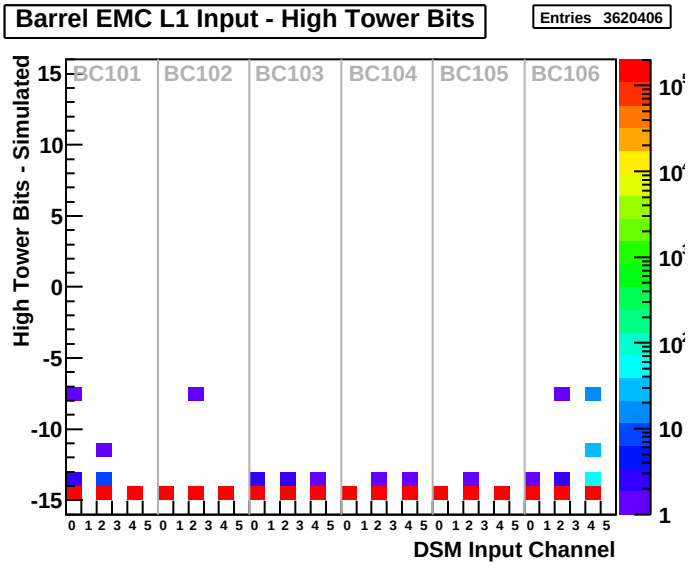

Barrel EMC L1 Input - Low Eta Sum
Entries 7240824
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

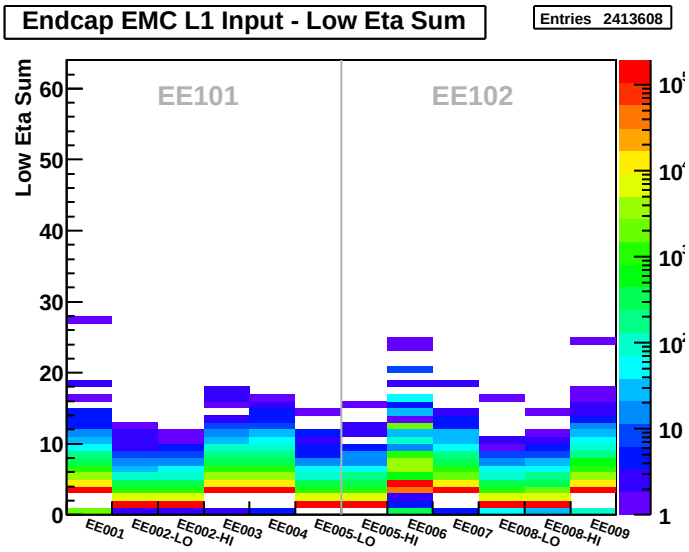

Barrel EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

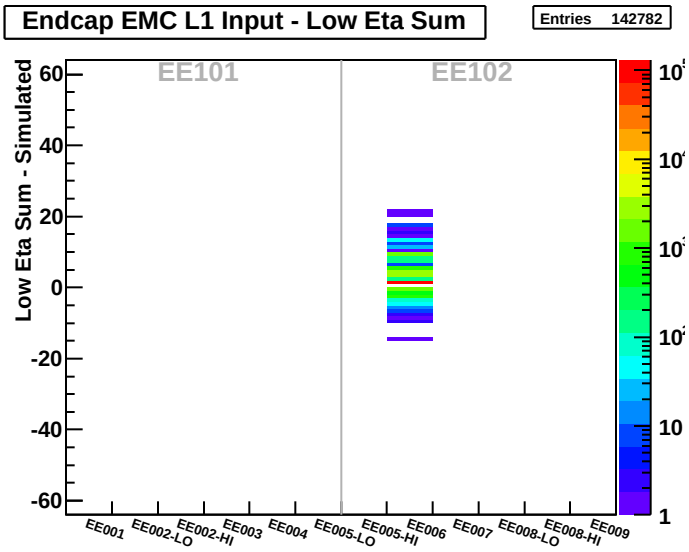

Barrel EMC L1 Input - High Eta Sum
Entries 7240824
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

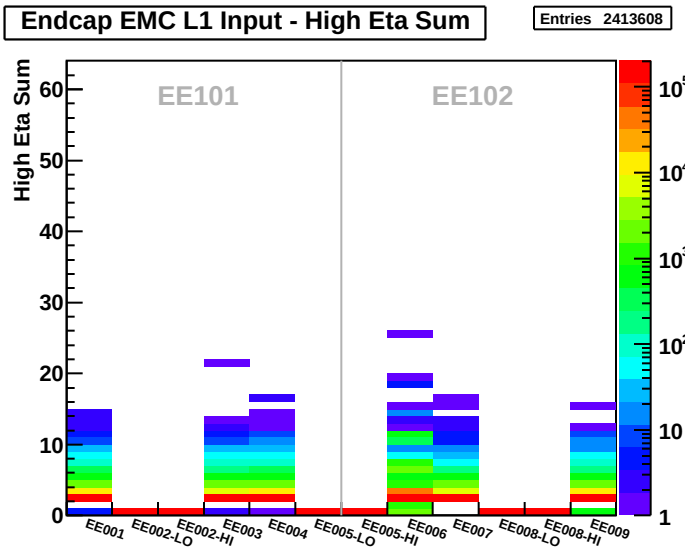

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

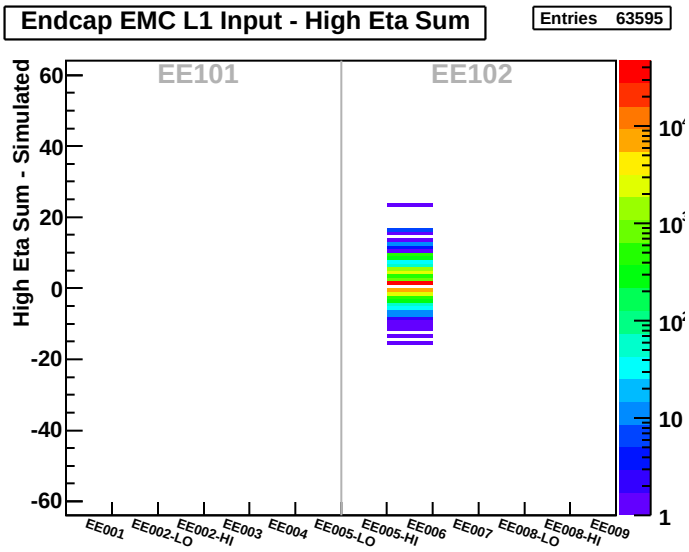

Barrel EMC L1 Input - High Tower Bits
Entries 7240824
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

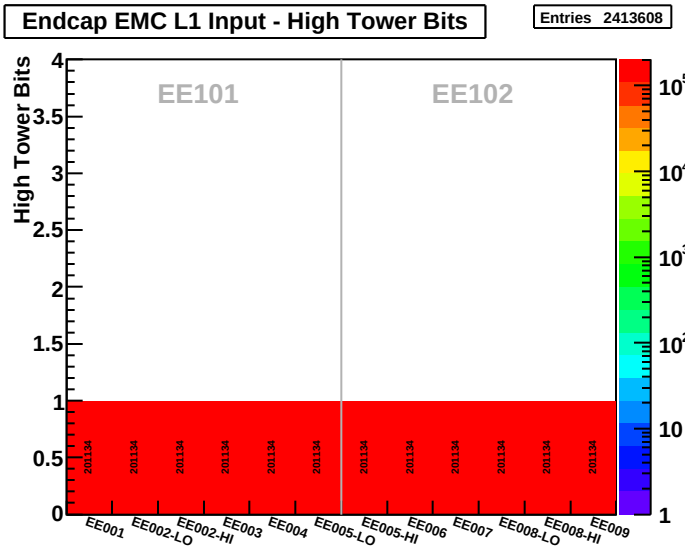

Barrel EMC L1 Input - High Tower Bits
Entries 3620406
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

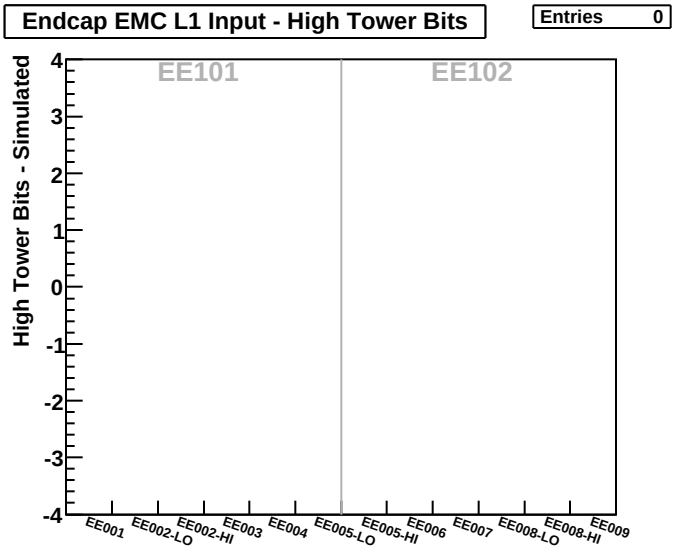

Endcap EMC L1 Input - Low Eta Sum
Entries 2413608
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

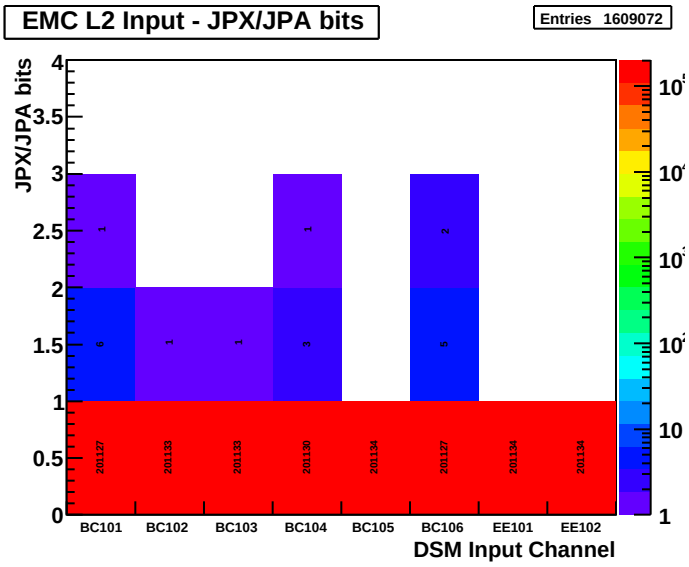

Endcap EMC L1 Input - Low Eta Sum
Entries 142782
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

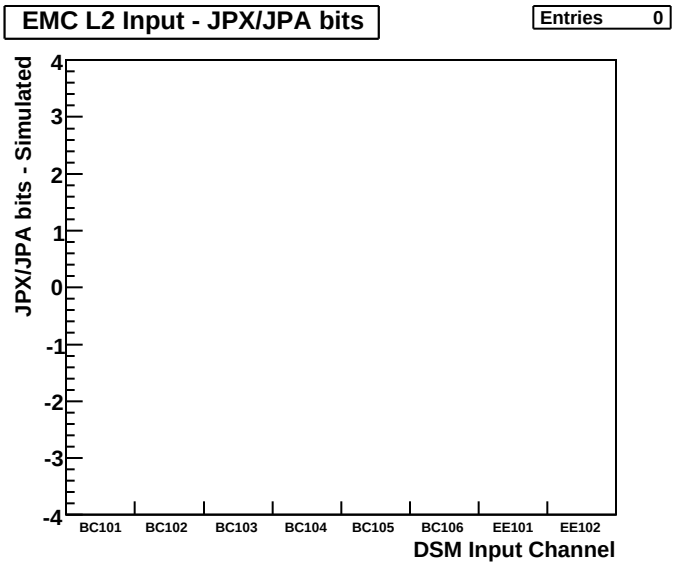

Endcap EMC L1 Input - High Eta Sum
Entries 2413608
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

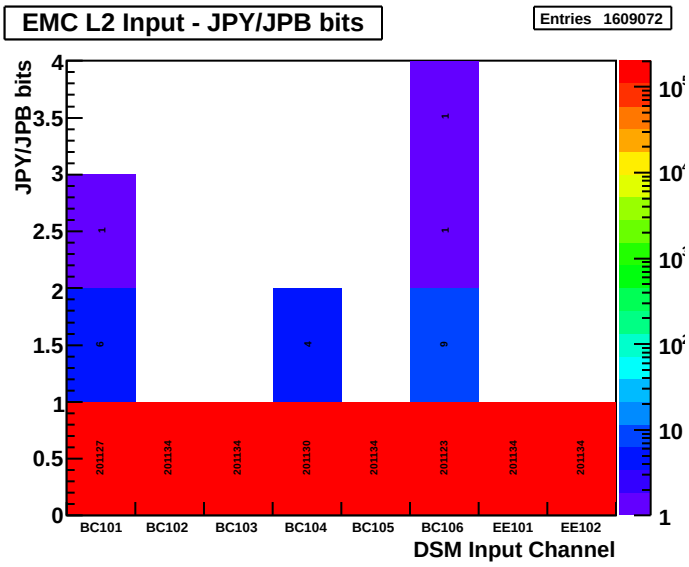

Endcap EMC L1 Input - High Eta Sum
Entries 63595
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

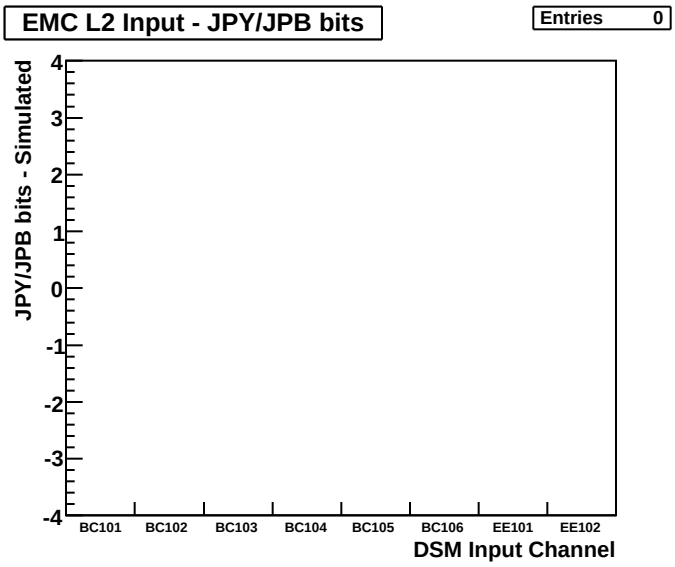

Endcap EMC L1 Input - High Tower Bits
Entries 2413608
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

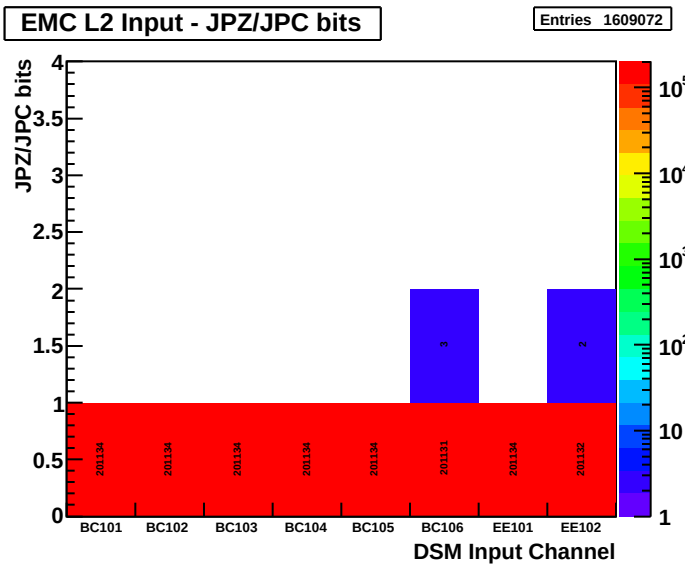

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

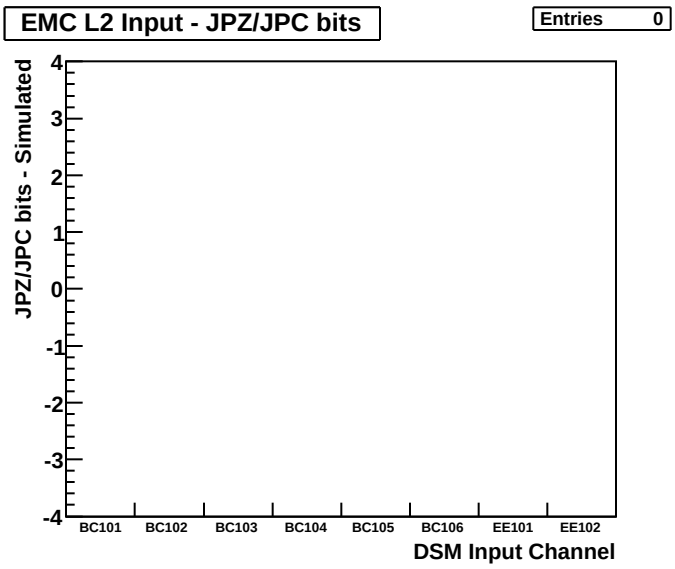

EMC L2 Input - JPX/JPA bits
Entries 1609072
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

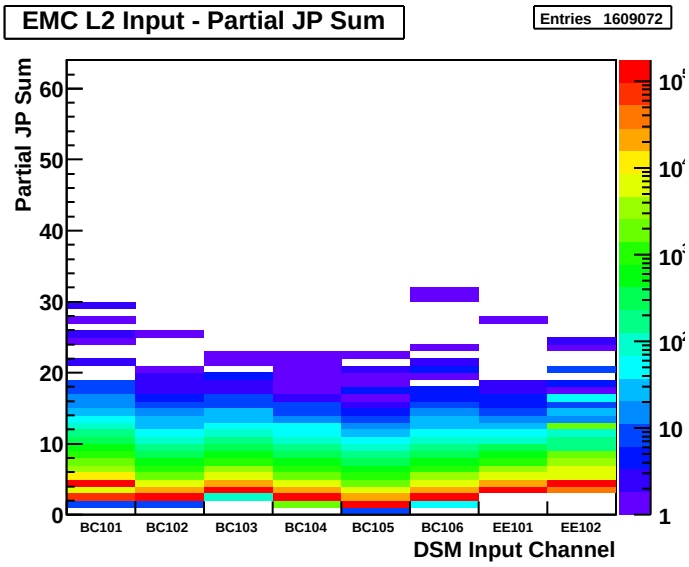

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

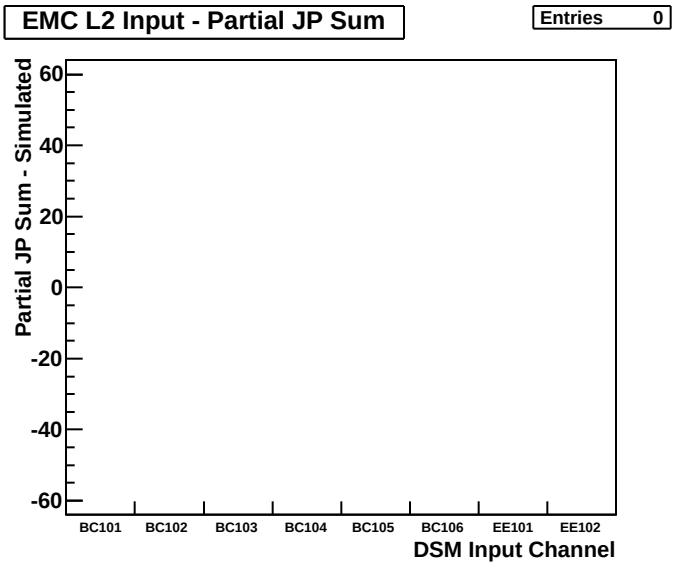

EMC L2 Input - JPY/JPB bits
Entries 1609072
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

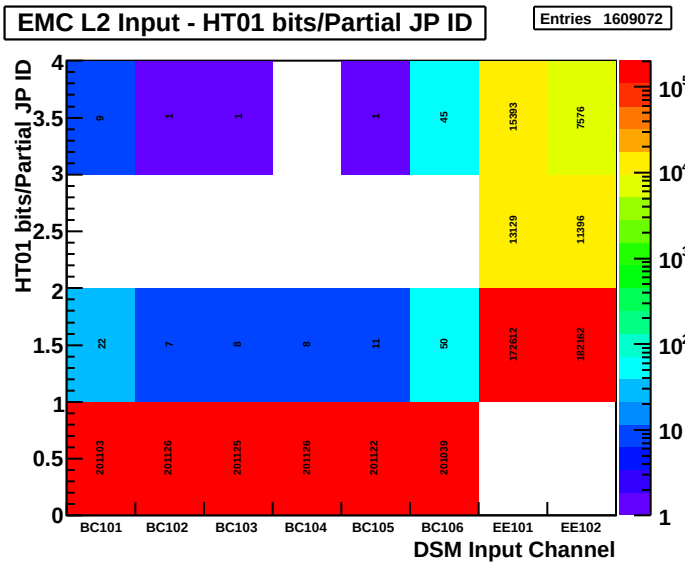

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

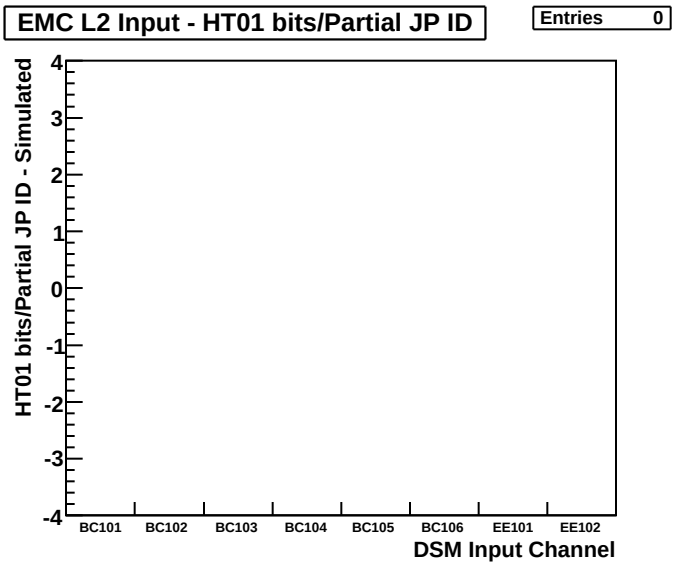

EMC L2 Input - JPZ/JPC bits
Entries 1609072
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

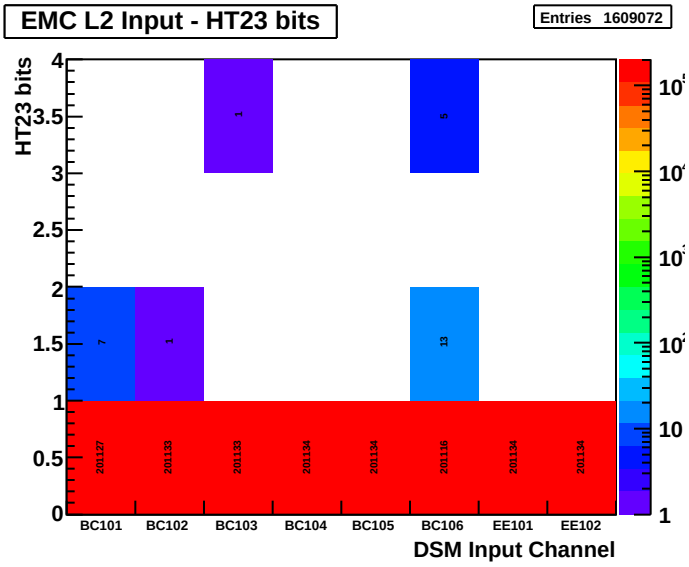

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

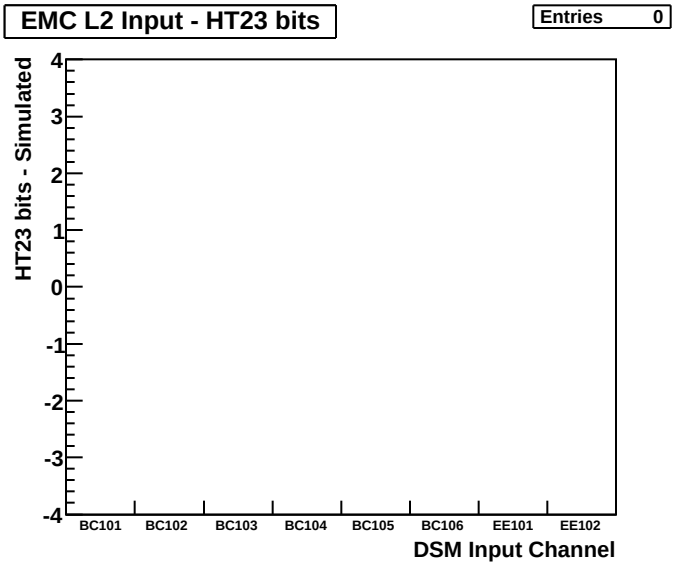

EMC L2 Input - Partial JP Sum
Entries 1609072
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

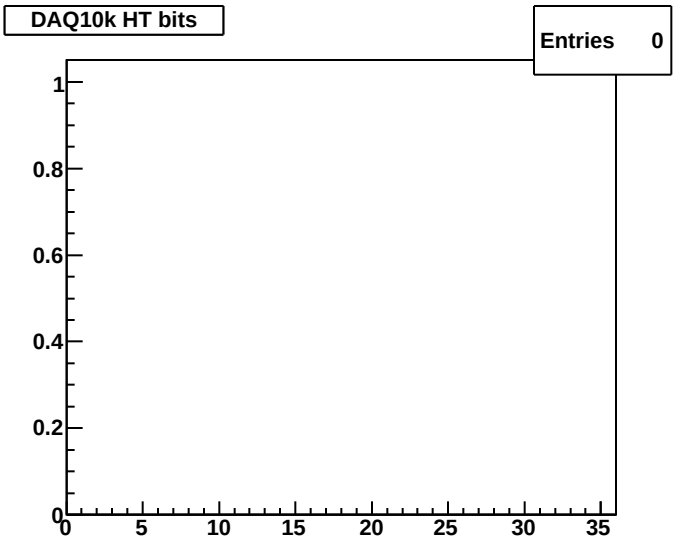

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

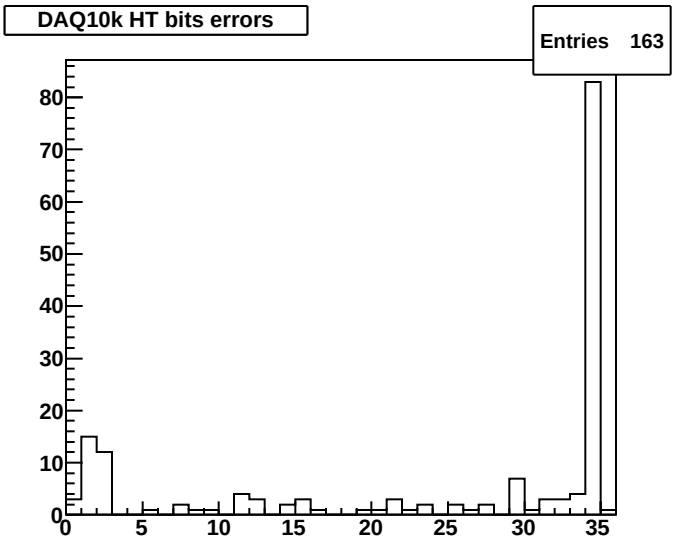

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1609072
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

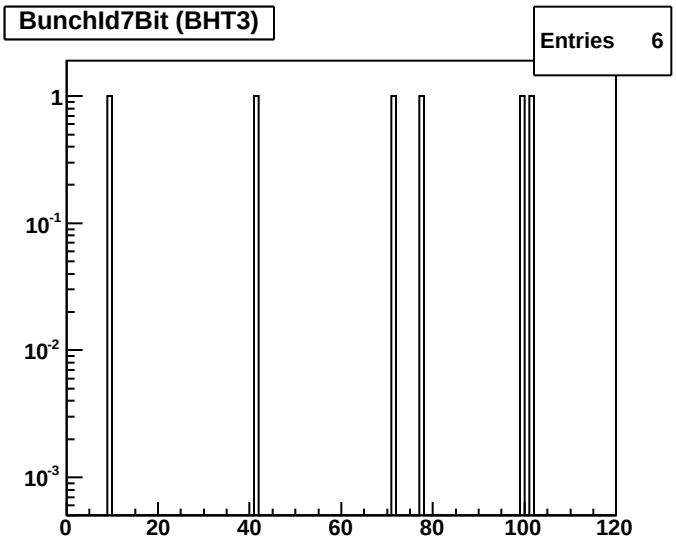

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

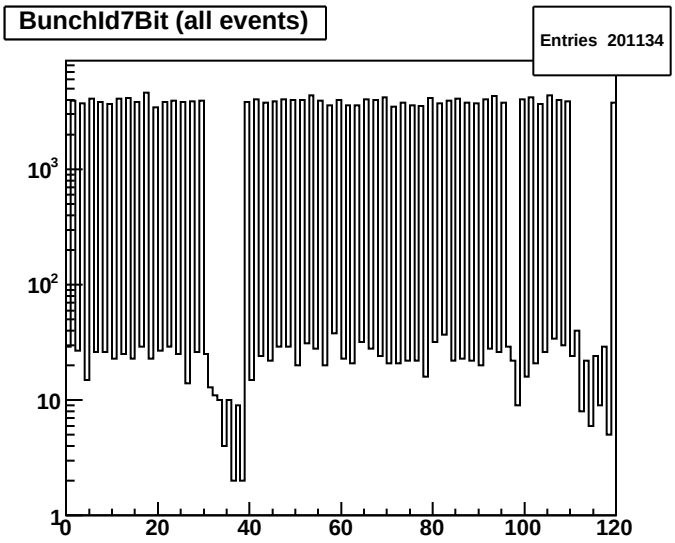

EMC L2 Input - HT23 bits
Entries 1609072
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

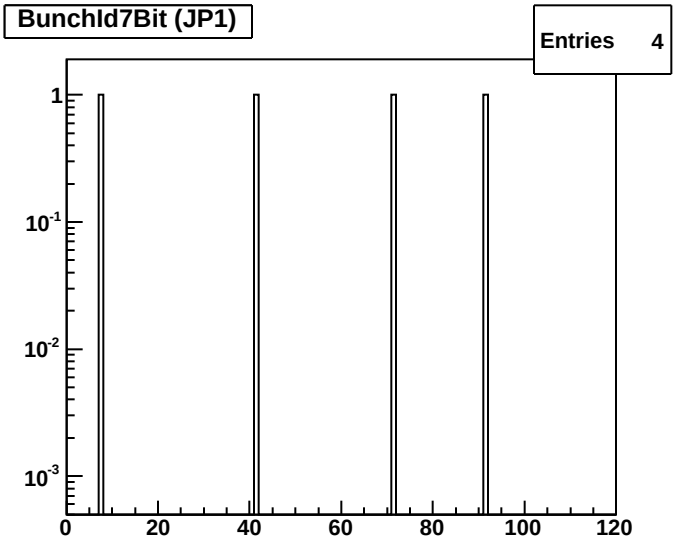

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

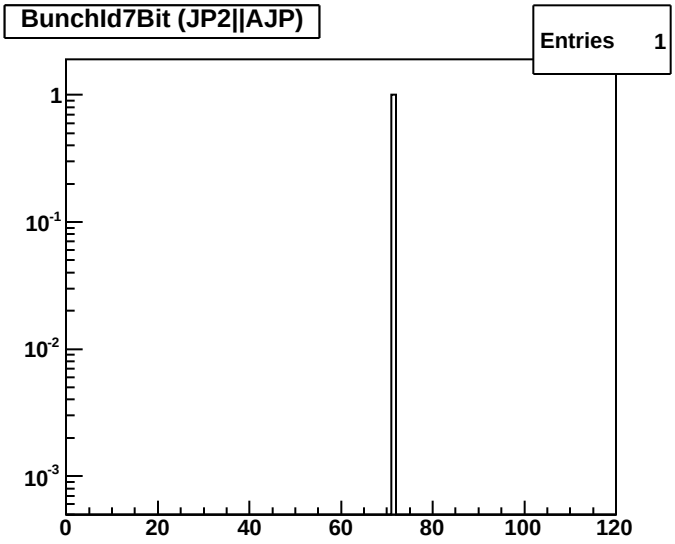

DAQ10k HT bits
Entries 0

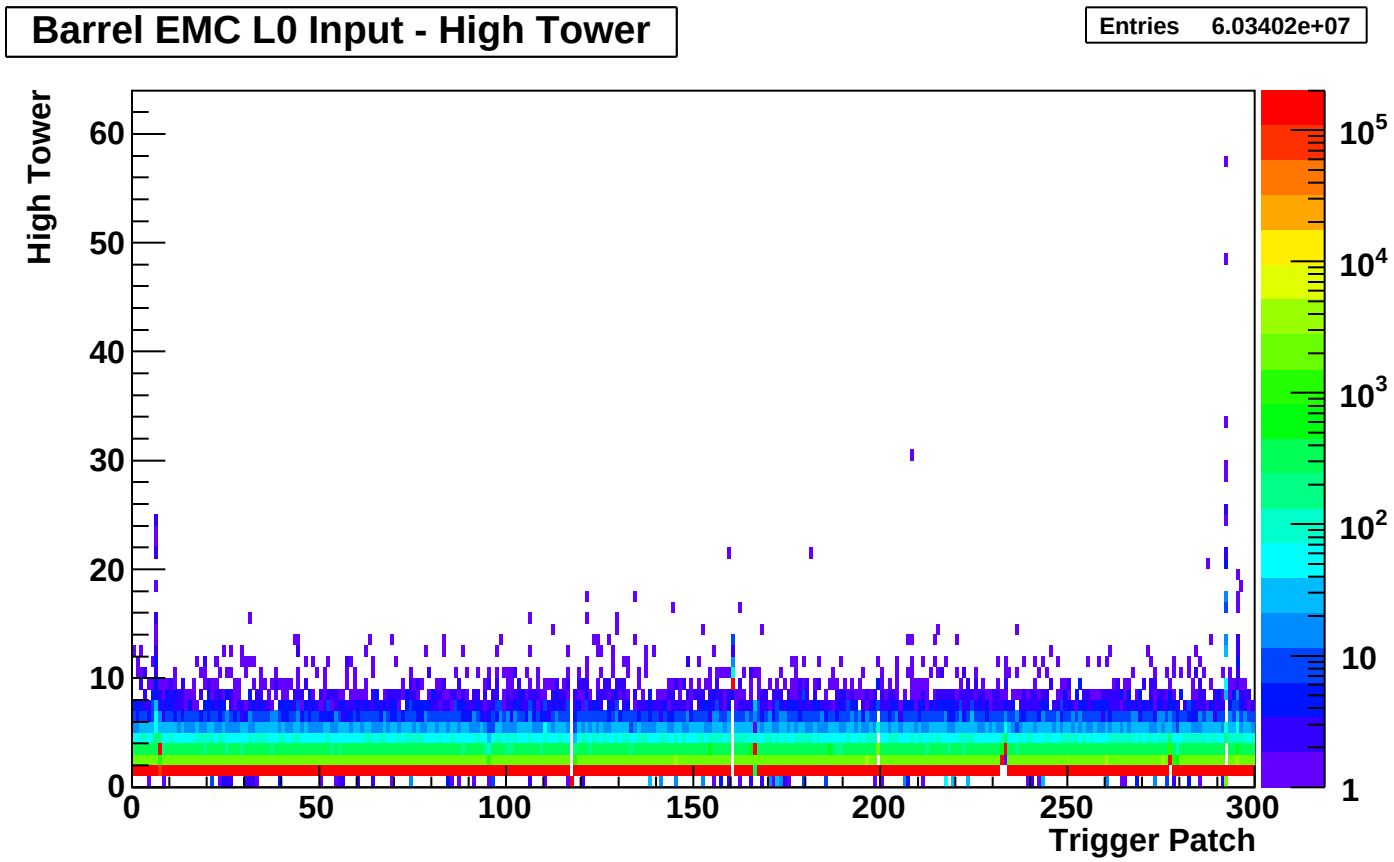

DAQ10k HT bits errors
Entries 163

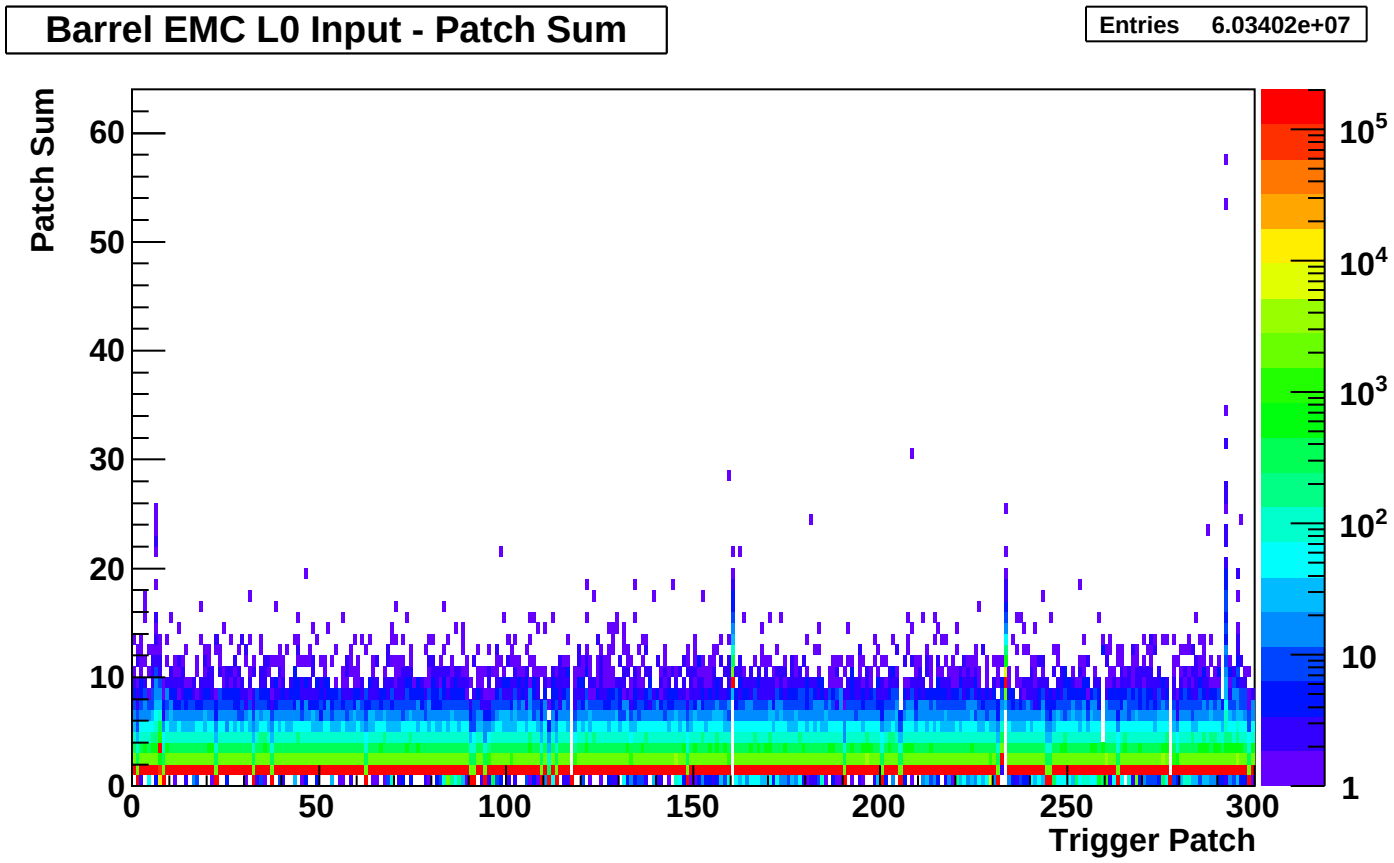

BunchId7Bit (BHT3)
Entries 6

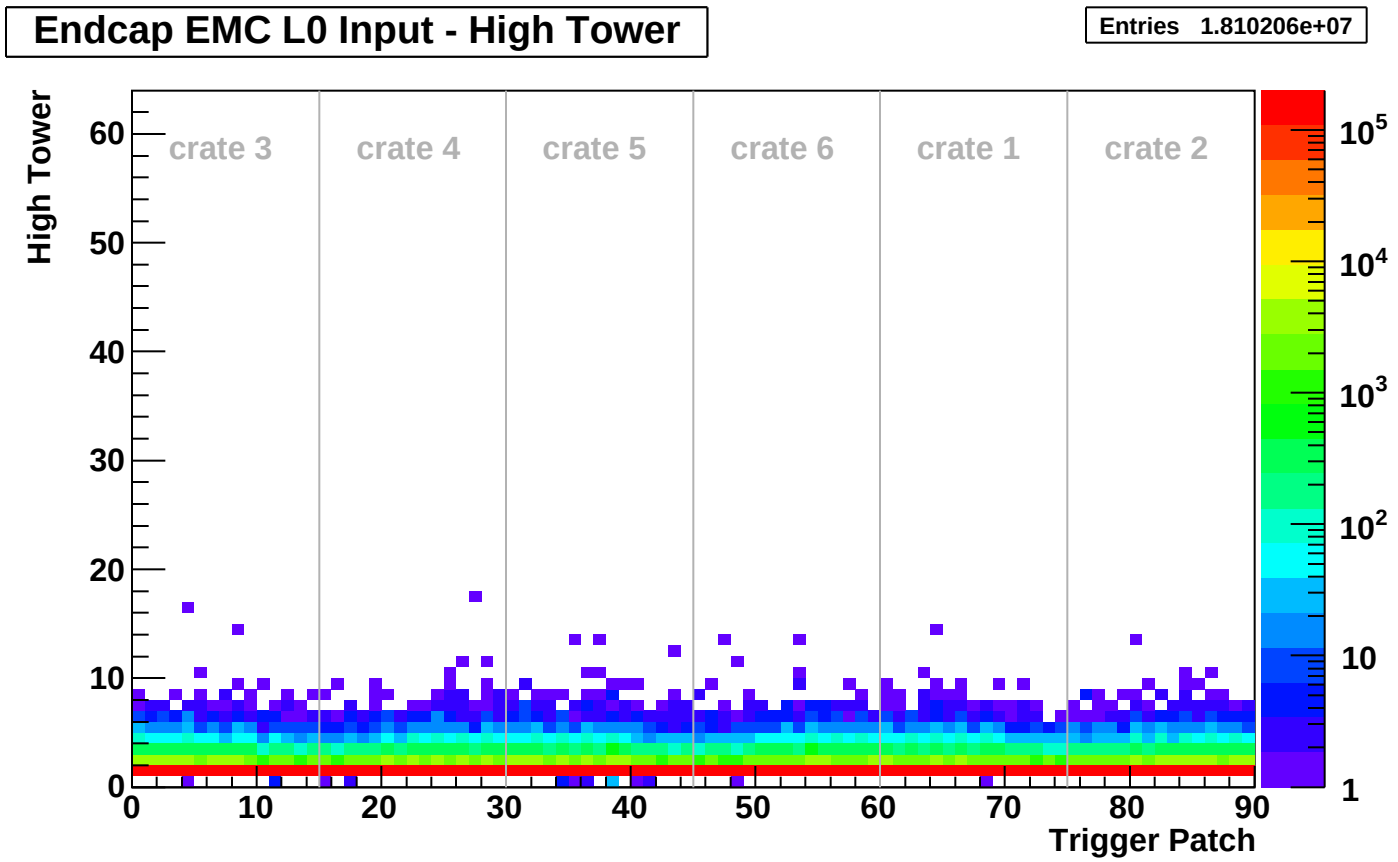

BunchId7Bit (all events)
Entries 201134

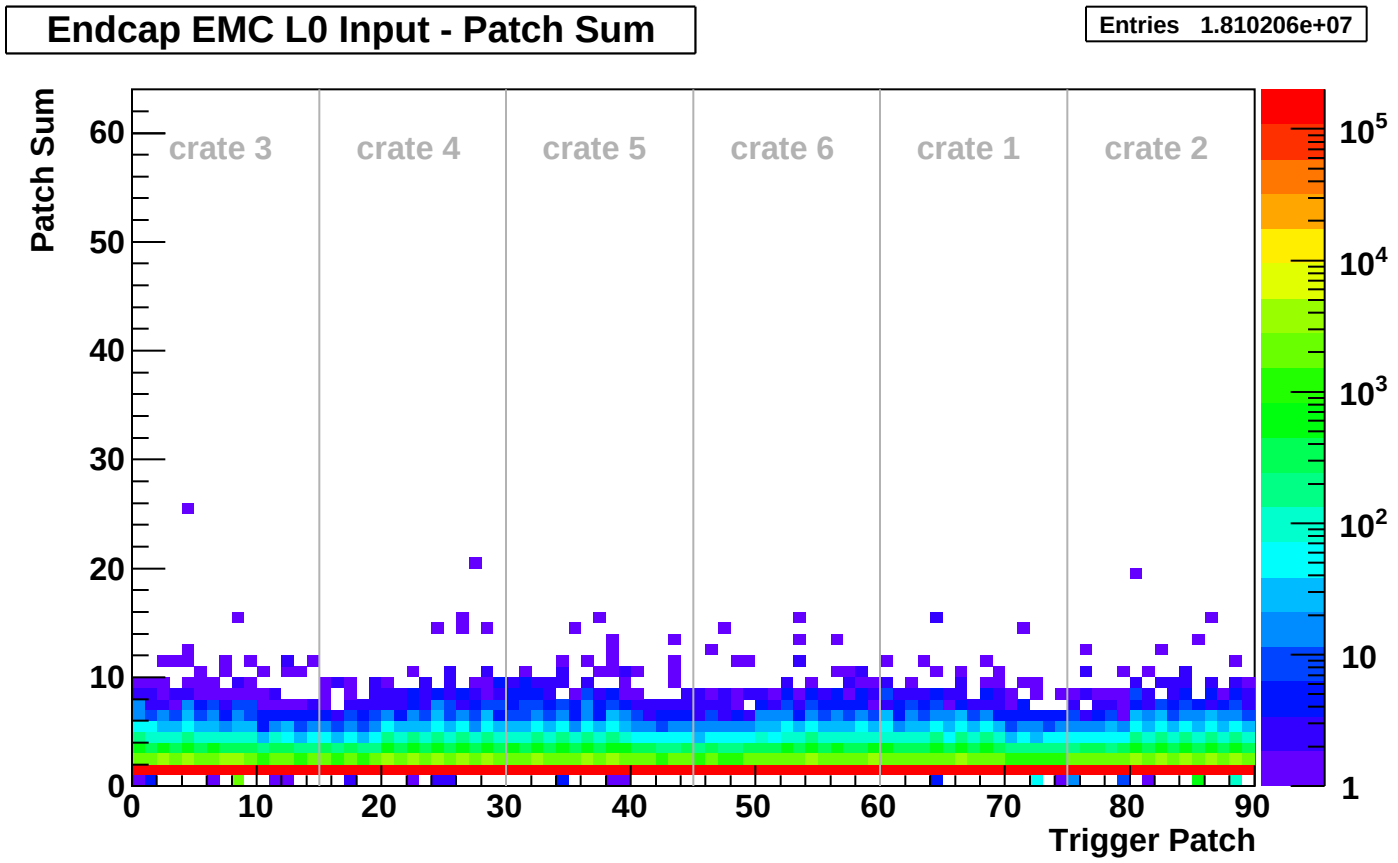

BunchId7Bit (JP1)
Entries 4

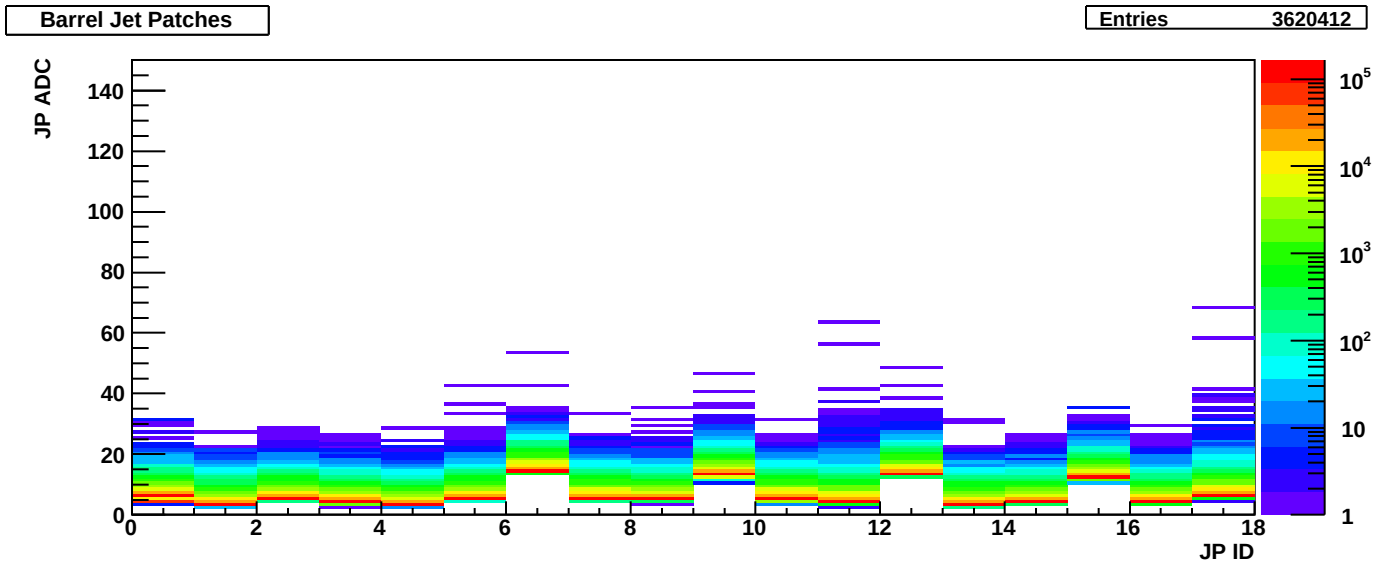

BunchId7Bit (JP2||AJP)
Entries 1

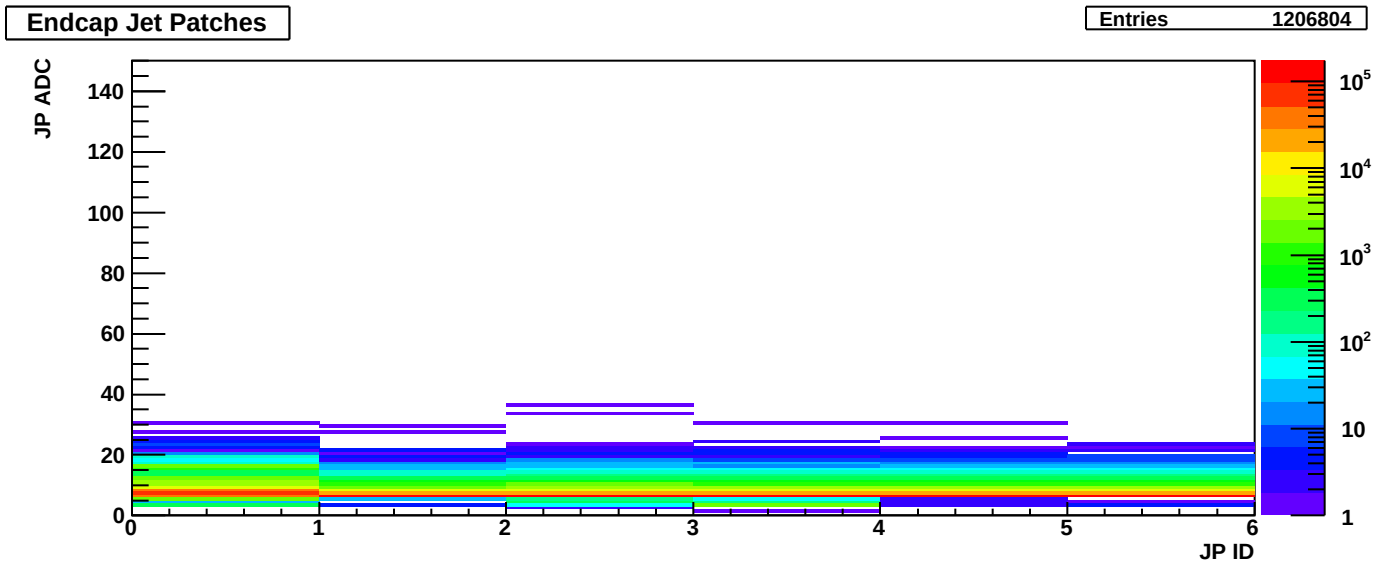

Barrel EMC L0 Input - High Tower
Entries 6.03402e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

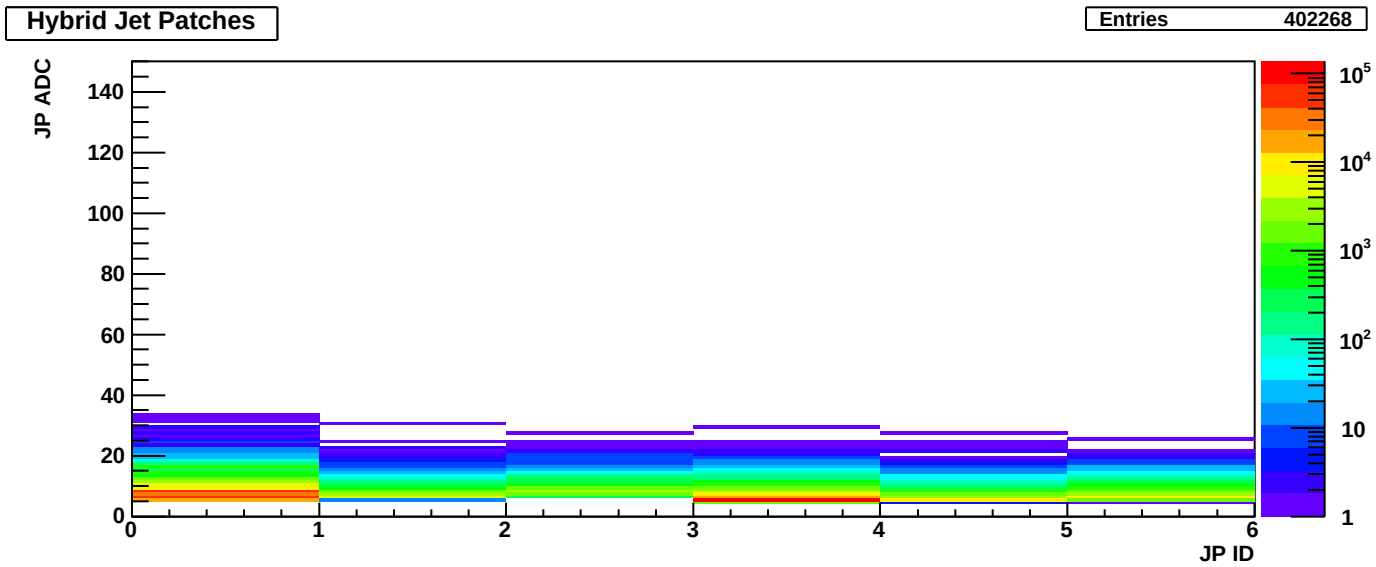

Barrel EMC L0 Input - Patch Sum
Entries 6.03402e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

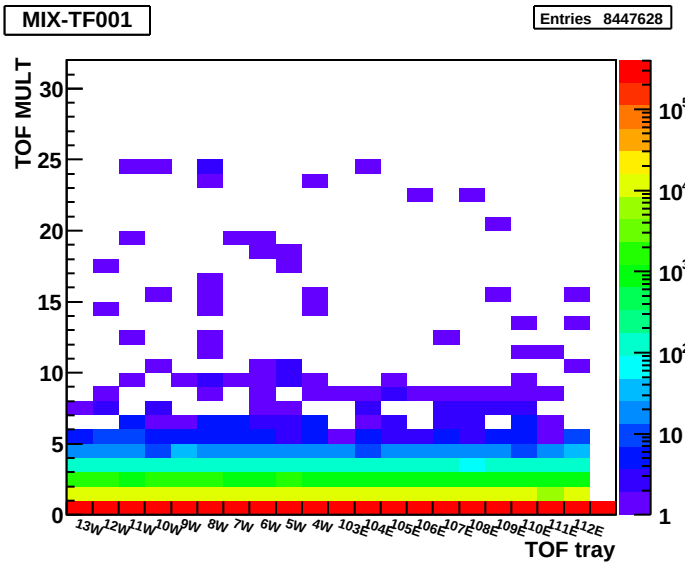

Endcap EMC L0 Input - High Tower
Entries 1.810206e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

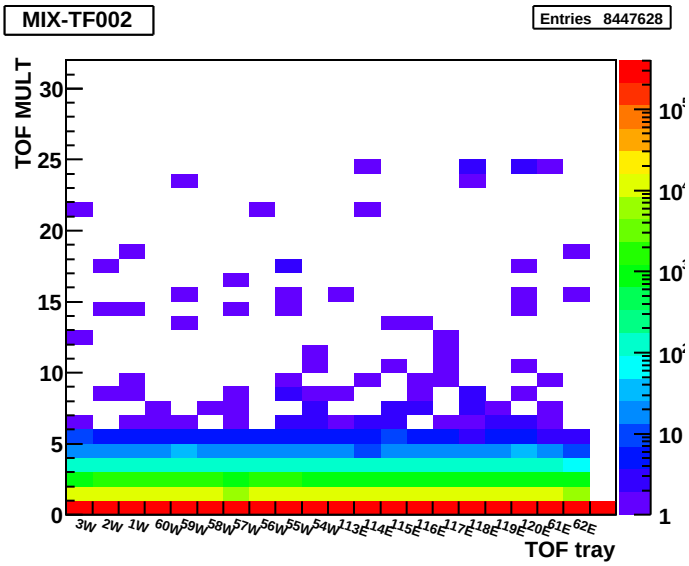

Endcap EMC L0 Input - Patch Sum
Entries 1.810206e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

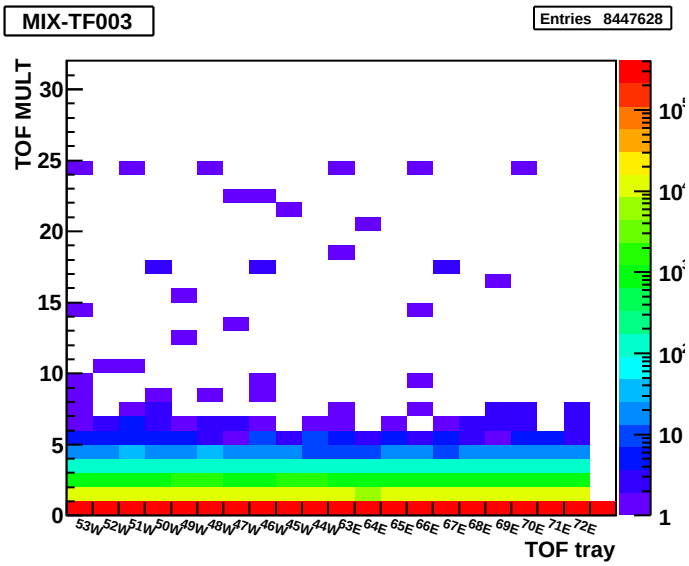

Barrel Jet Patches
Entries 3620412
JP ADC
JP ID

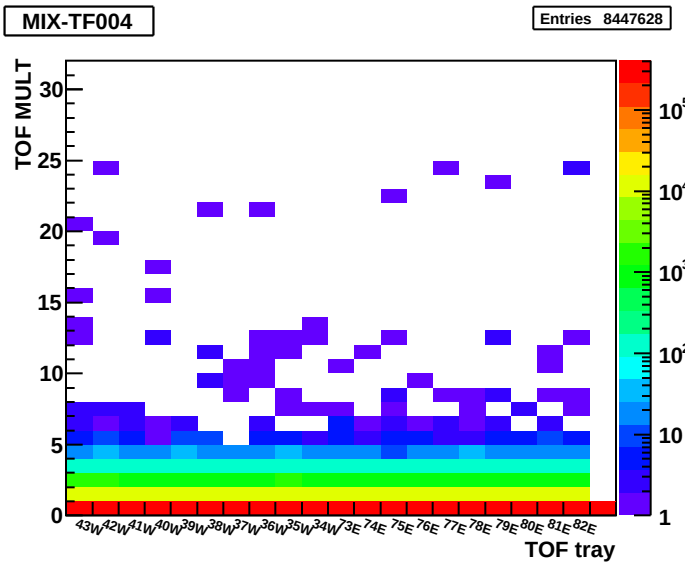

Endcap Jet Patches
Entries 1206804
JP ADC
JP ID

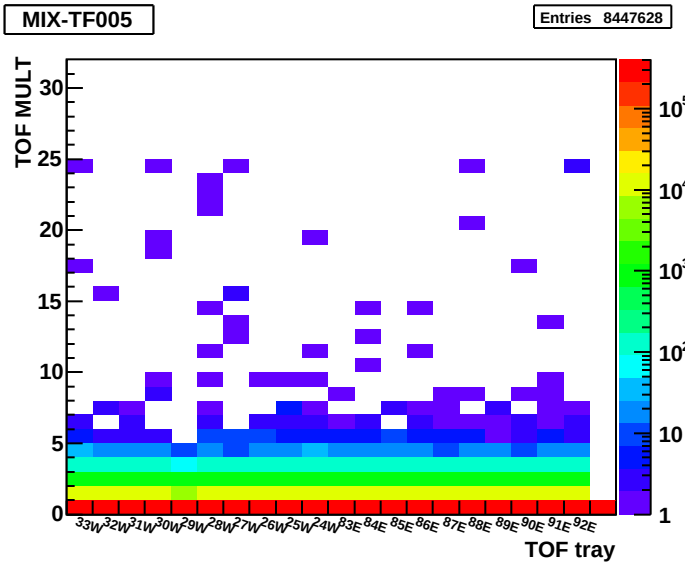

Hybrid Jet Patches
Entries 402268
JP ADC
JP ID

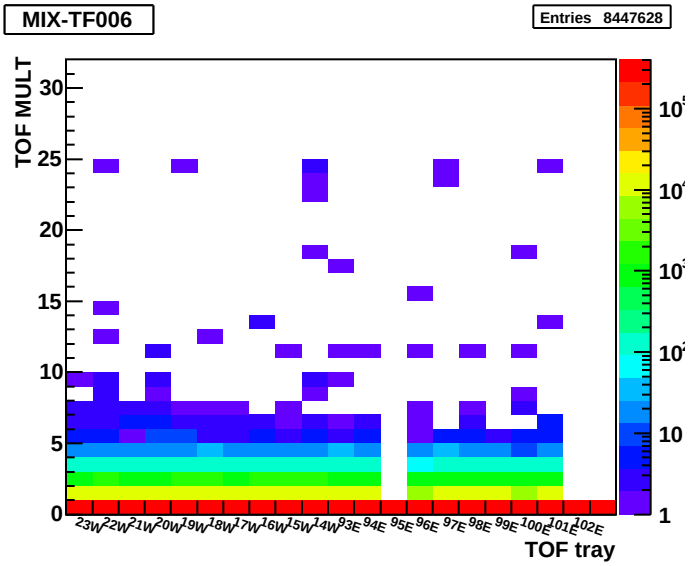

MIX-TF001
Entries 8447628
TOF MULT
TOF tray

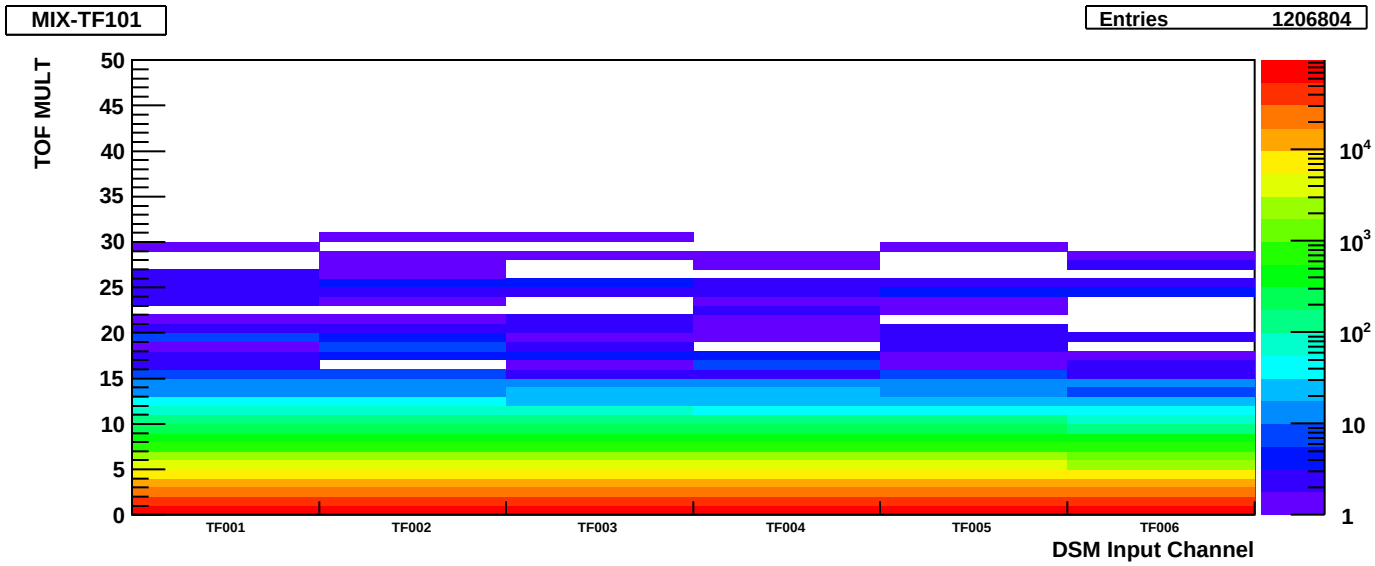

MIX-TF002
Entries 8447628
TOF MULT
TOF tray

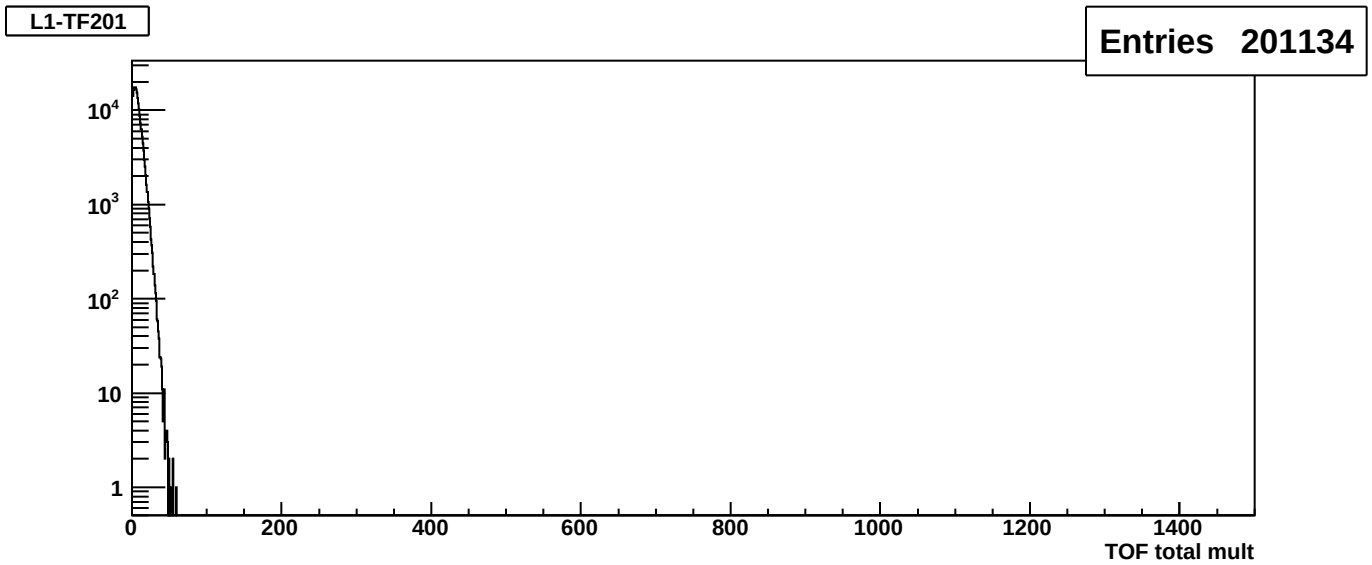

MIX-TF003
Entries 8447628
TOF MULT
TOF tray

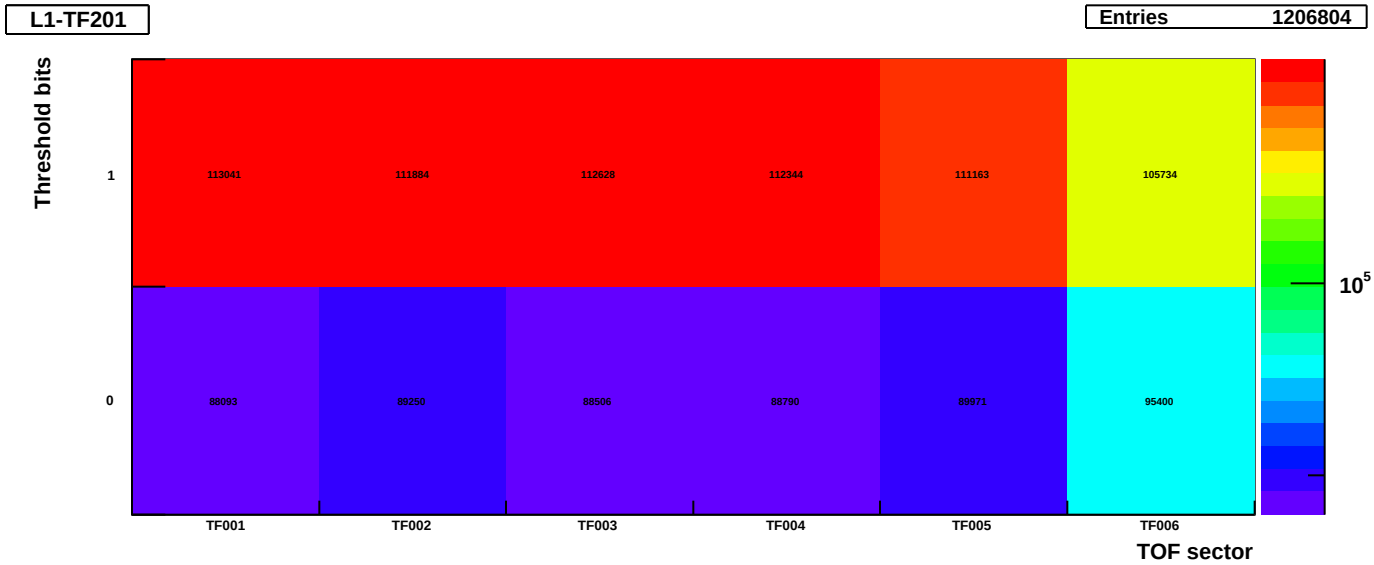

MIX-TF004
Entries 8447628
TOF MULT
TOF tray

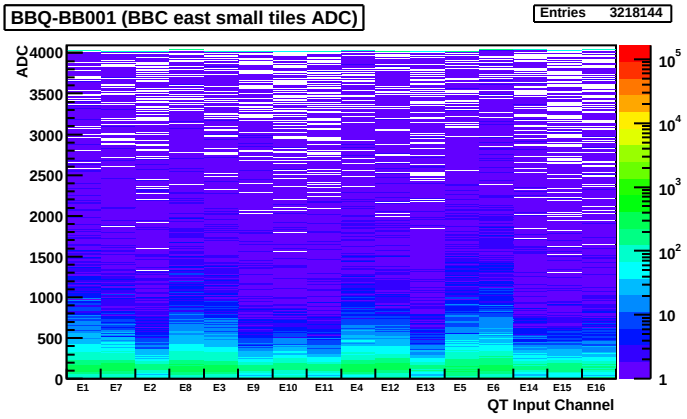

MIX-TF005
Entries 8447628
TOF MULT
TOF tray

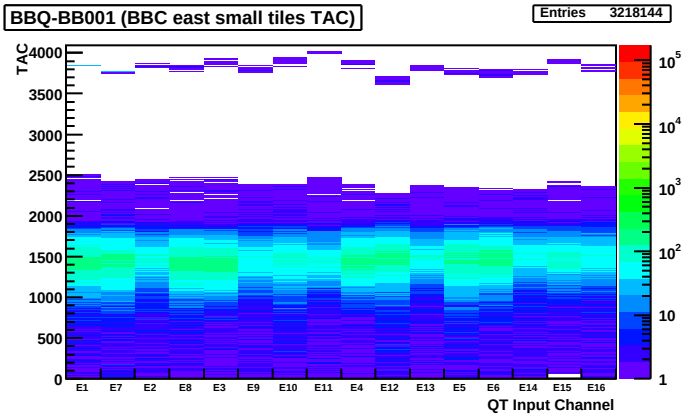

MIX-TF006
Entries 8447628
TOF MULT
TOF tray

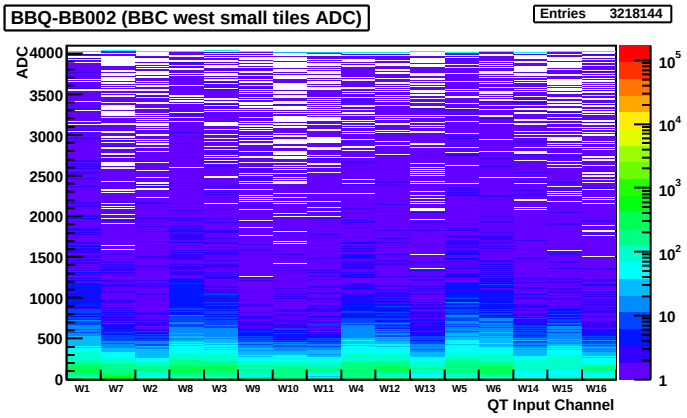

MIX-TF101
Entries 1206804
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

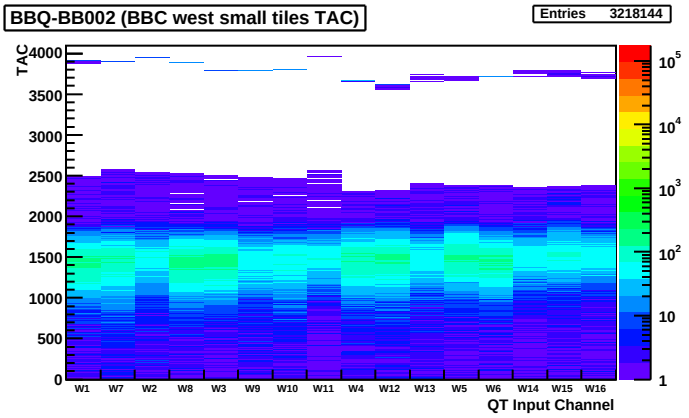

L1-TF201
Entries 201134
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

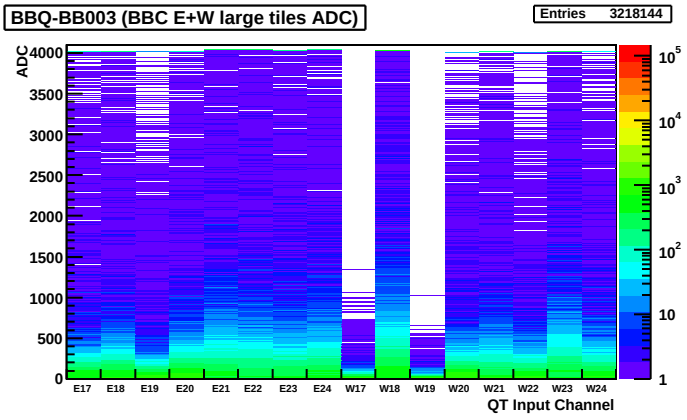

L1-TF201
Entries 1206804
Threshold bits
1
0
113041
111884
112628
112344
111163
105734
88093
89250
88506
88790
89971
95400
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

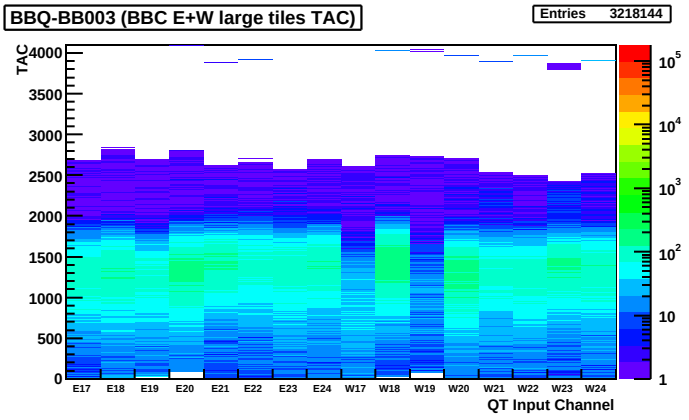

BBQ-BB001 (BBC east small tiles ADC)
Entries 3218144
ADC
QT Input Channel

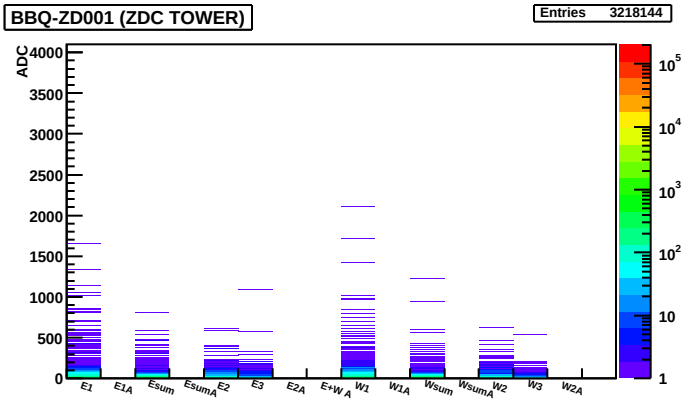

BBQ-BB001 (BBC east small tiles TAC)
Entries 3218144
TAC
QT Input Channel

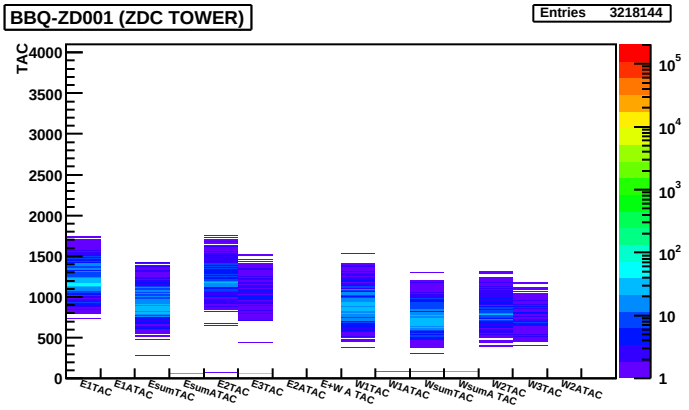

BBQ-BB002 (BBC west small tiles ADC)
Entries 3218144
ADC
QT Input Channel

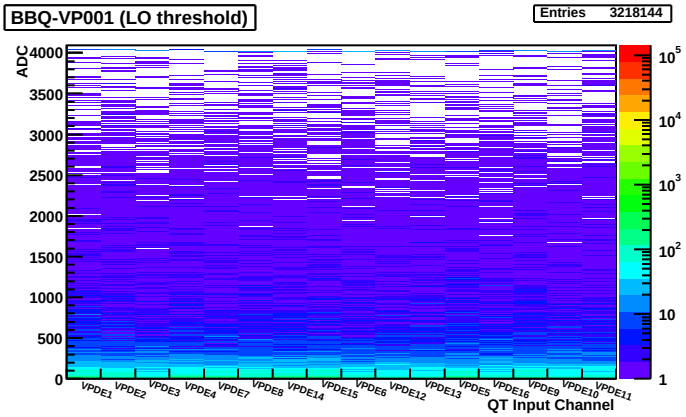

BBQ-BB002 (BBC west small tiles TAC)
Entries 3218144
TAC
QT Input Channel

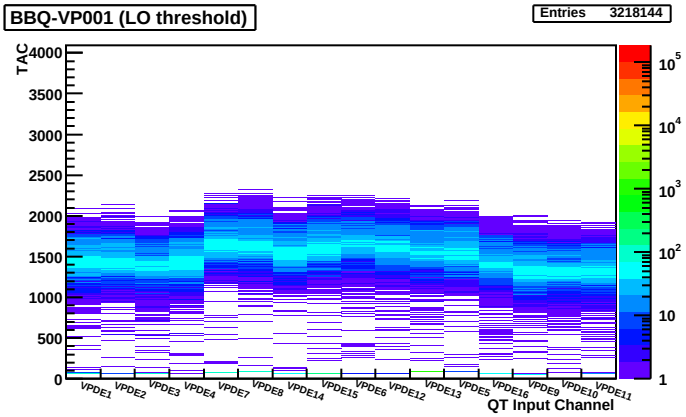

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3218144
ADC
QT Input Channel

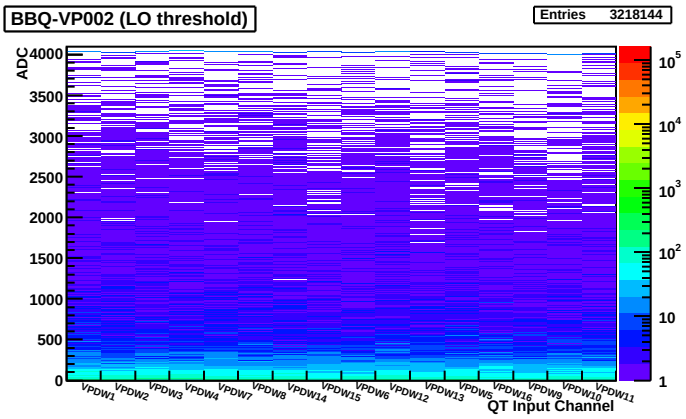

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3218144
TAC
QT Input Channel

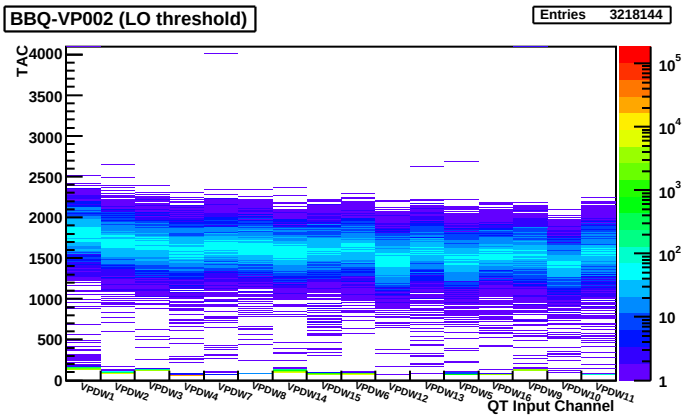

BBQ-ZD001 (ZDC TOWER)
Entries 3218144
ADC

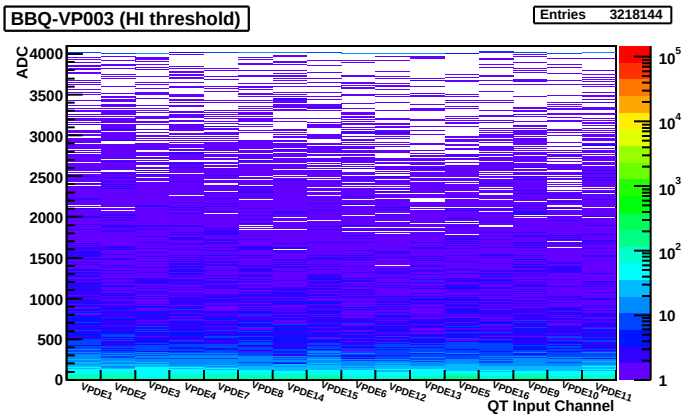

BBQ-ZD001 (ZDC TOWER)
Entries 3218144
TAC

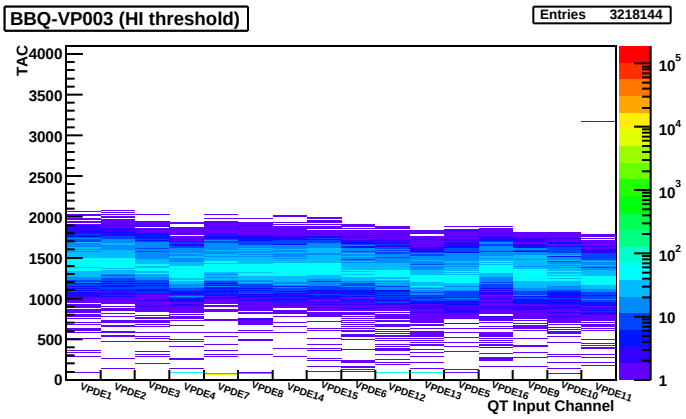

BBQ-VP001 (LO threshold)
Entries 3218144
ADC
QT Input Channel

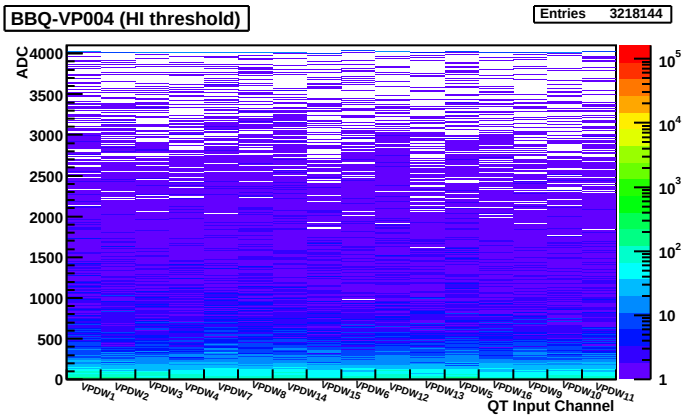

BBQ-VP001 (LO threshold)
Entries 3218144
TAC
QT Input Channel

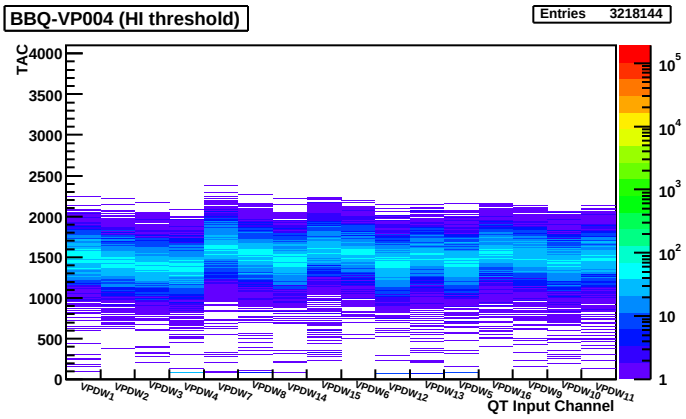

BBQ-VP002 (LO threshold)
Entries 3218144
ADC
QT Input Channel

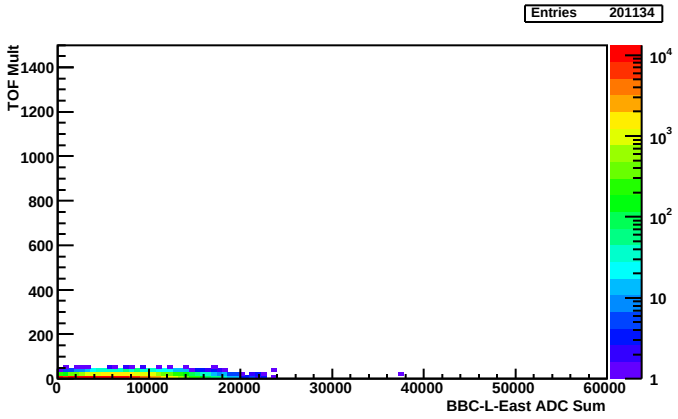

BBQ-VP002 (LO threshold)
Entries 3218144
TAC
QT Input Channel

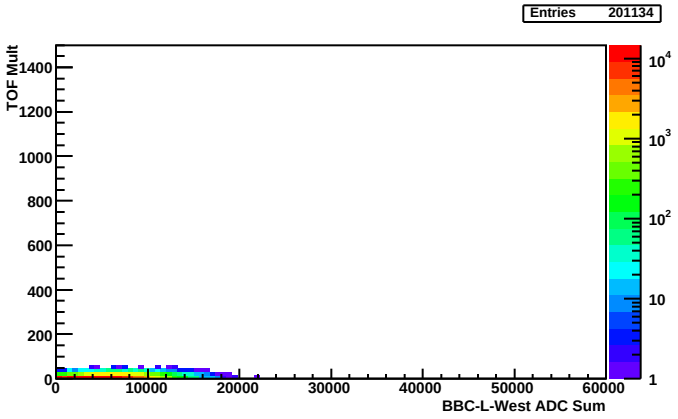

BBQ-VP003 (HI threshold)
Entries 3218144
ADC
QT Input Channel

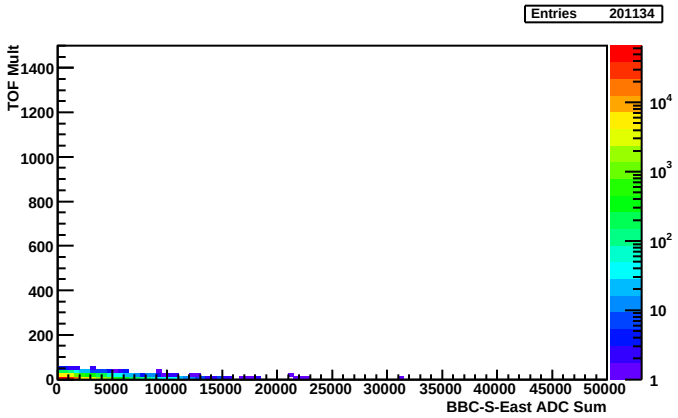

BBQ-VP003 (HI threshold)
Entries 3218144
TAC
QT Input Channel

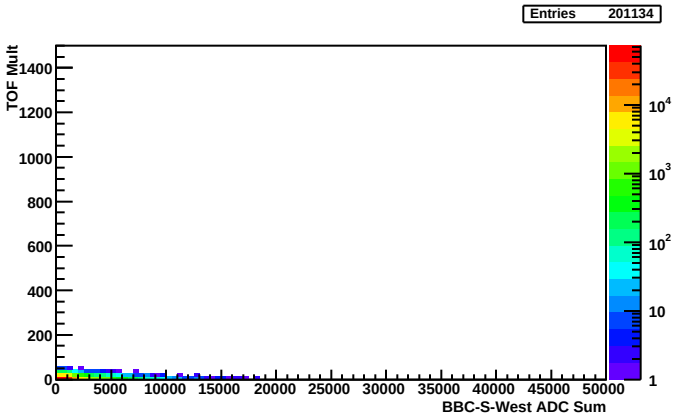

BBQ-VP004 (HI threshold)
Entries 3218144
ADC
QT Input Channel

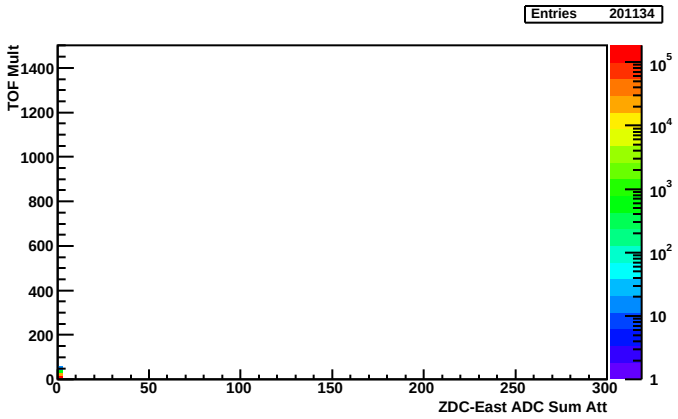

BBQ-VP004 (HI threshold)
Entries 3218144
TAC
QT Input Channel

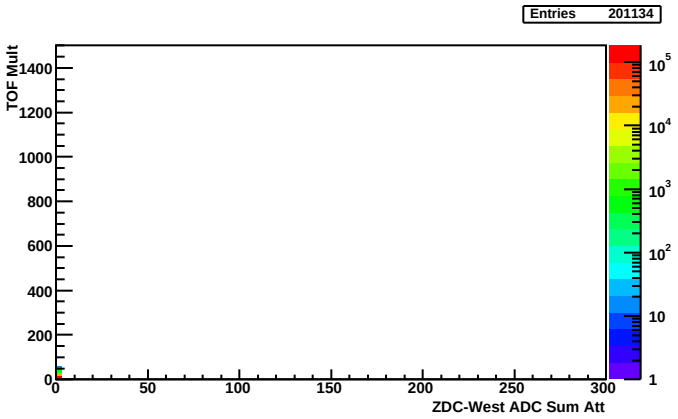

Entries 201134
TOF Mult
BBC-L-East ADC Sum

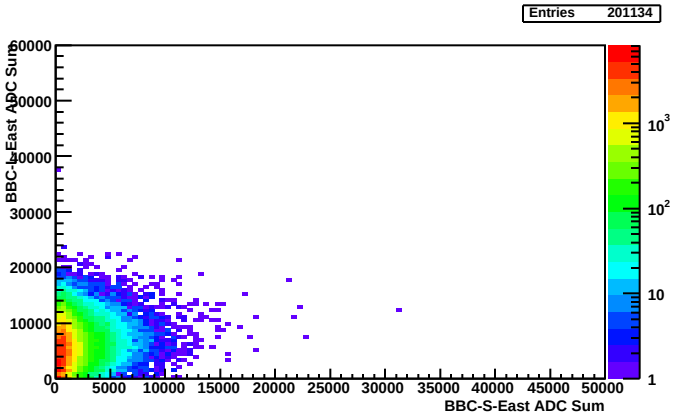

Entries 201134
TOF Mult
BBC-L-West ADC Sum

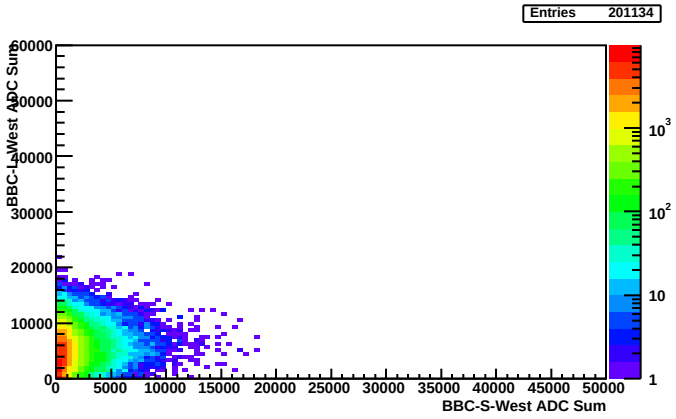

Entries 201134
TOF Mult
BBC-S-East ADC Sum

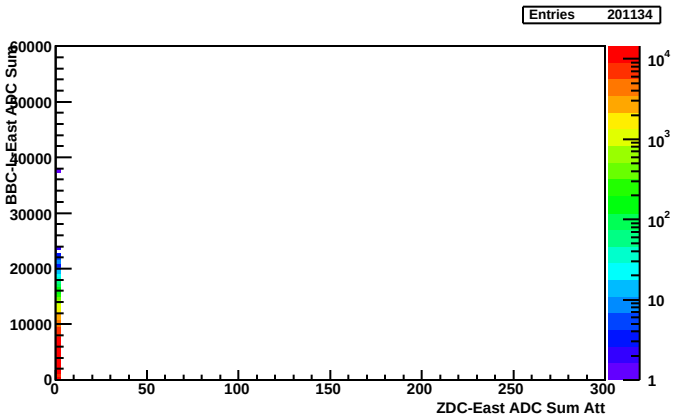

Entries 201134
TOF Mult
BBC-S-West ADC Sum

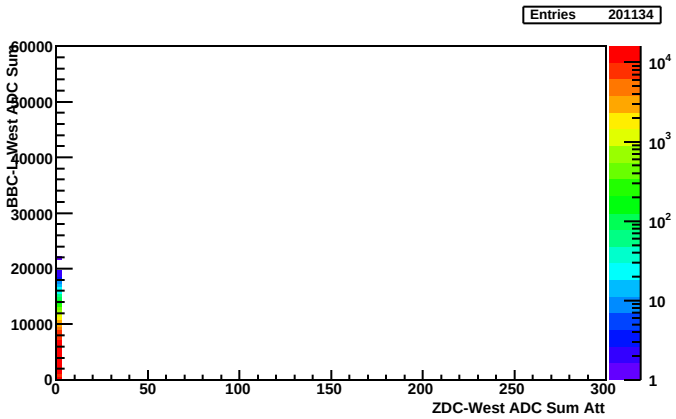

Entries 201134
TOF Mult
ZDC-East ADC Sum Att

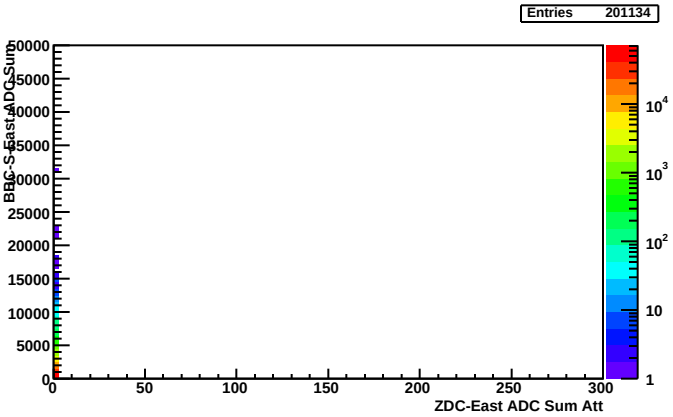

Entries 201134
TOF Mult
ZDC-West ADC Sum Att

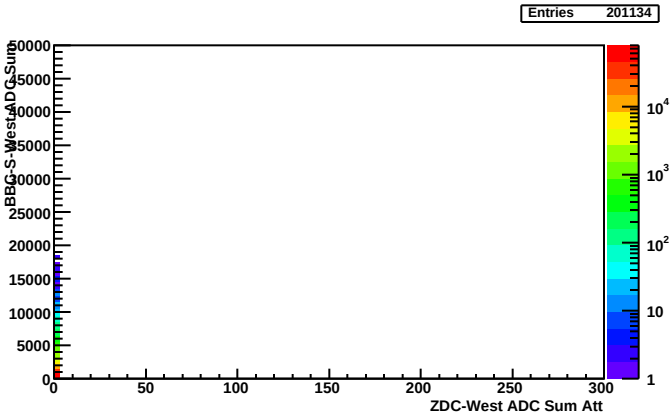

Entries 201134
BBC-L-East ADC Sum
BBC-S-East ADC Sum

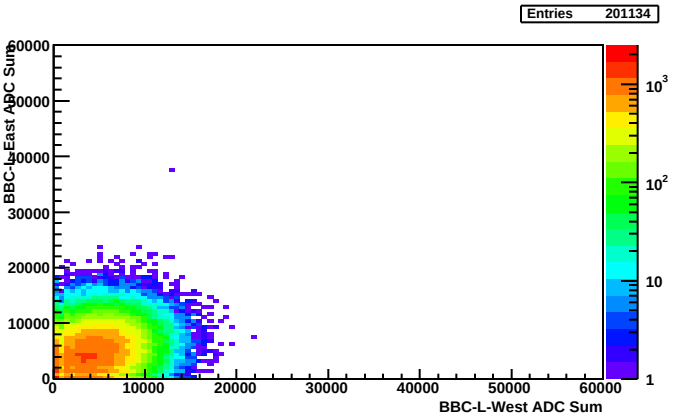

Entries 201134
BBC-L-West ADC Sum
BBC-S-West ADC Sum

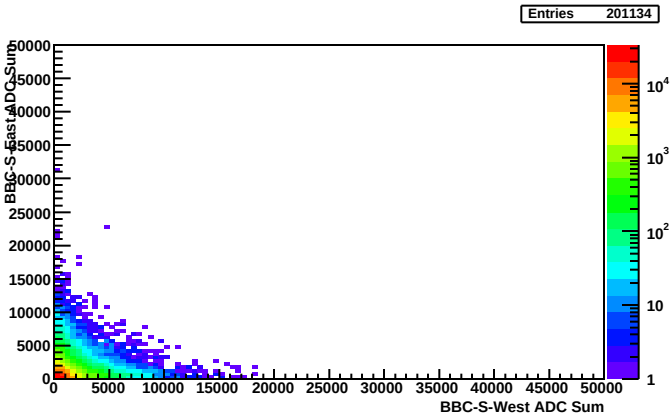

Entries 201134
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

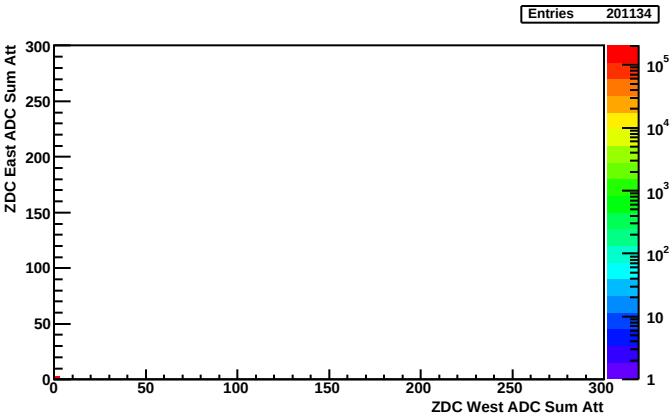

Entries 201134
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

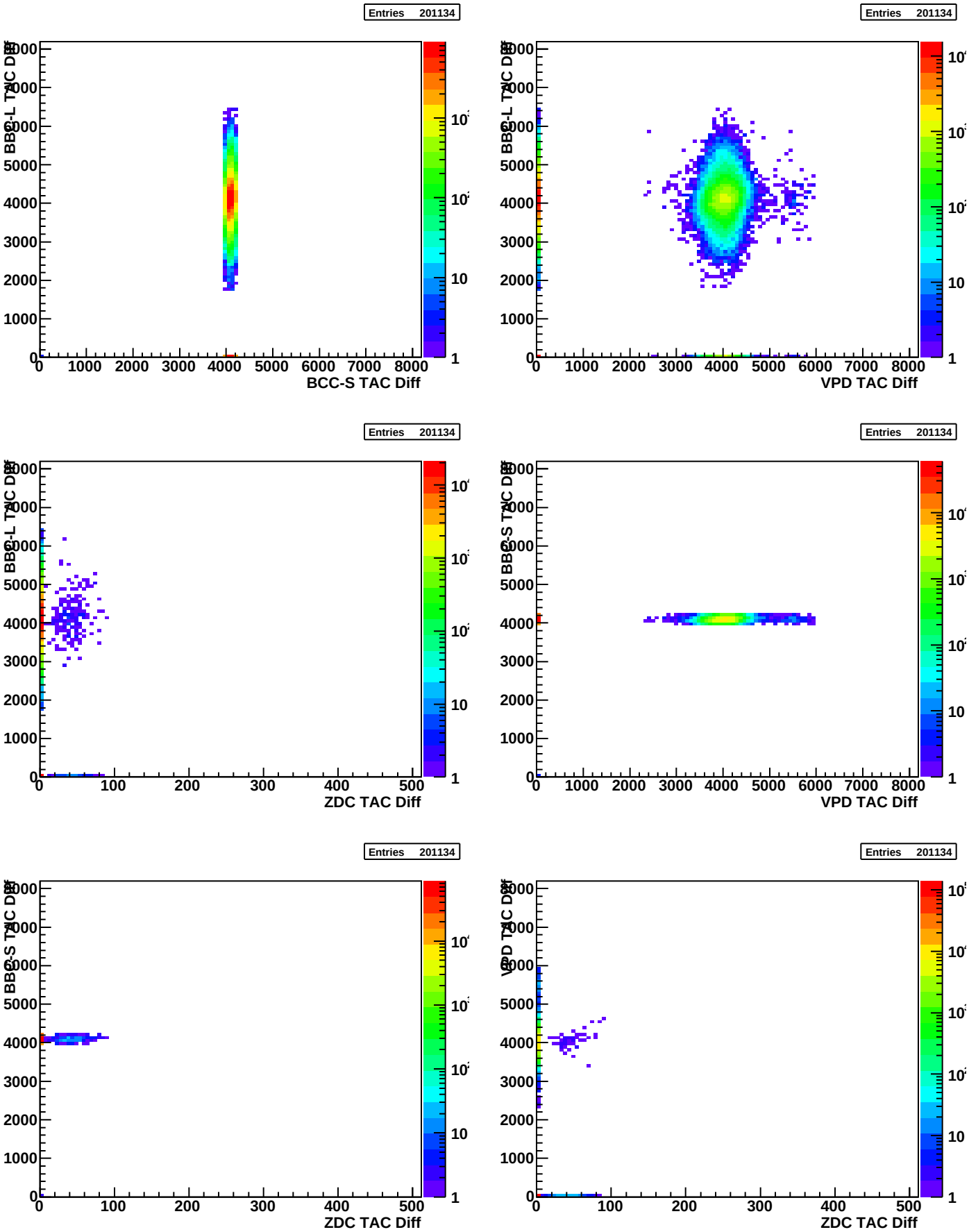

Entries 201134
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

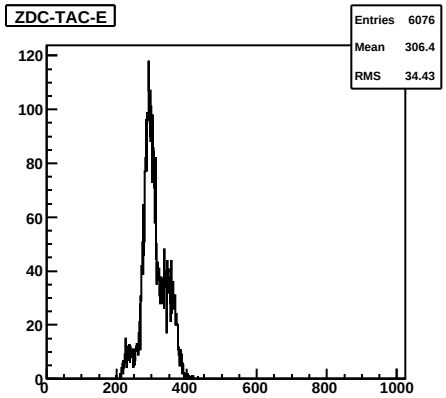

Entries 201134
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

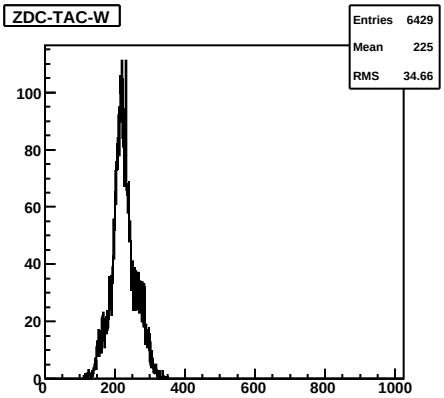

Entries 201134
BBC-L-East ADC Sum
BBC-L-West ADC Sum

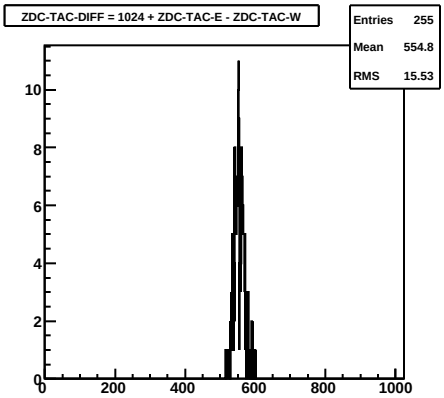

Entries 201134
BBC-S-East ADC Sum
BBC-S-West ADC Sum

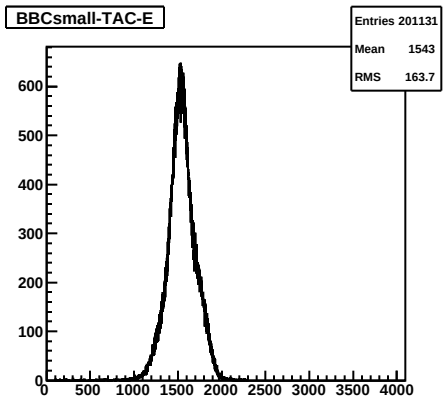

Entries 201134
ZDC East ADC Sum Att
ZDC West ADC Sum Att

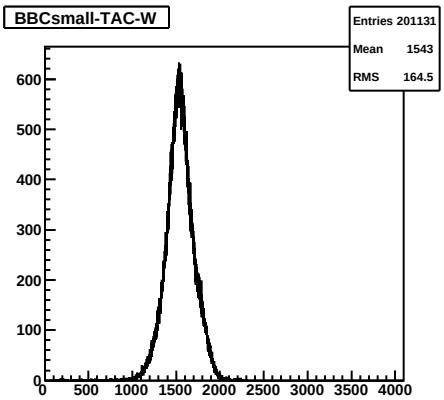

Entries 201134
BBC-L TAC Diff
BCC-S TAC Diff
Entries 201134
BBC-L TAC Diff
VPD TAC Diff
Entries 201134
BBC-L TAC Diff
ZDC TAC Diff
Entries 201134
BBC-S TAC Diff
VPD TAC Diff
Entries 201134
BBC-S TAC Diff
ZDC TAC Diff
Entries 201134
VPD TAC Diff
ZDC TAC Diff

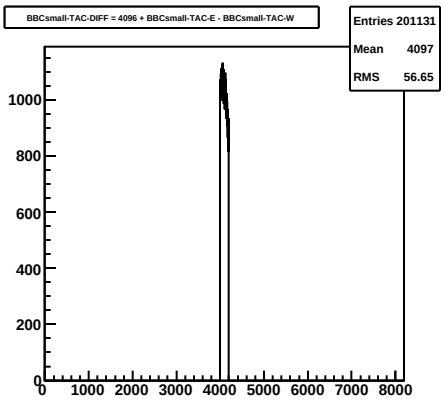

ZDC-TAC-E
Entries 6076
Mean 306.4
RMS 34.43

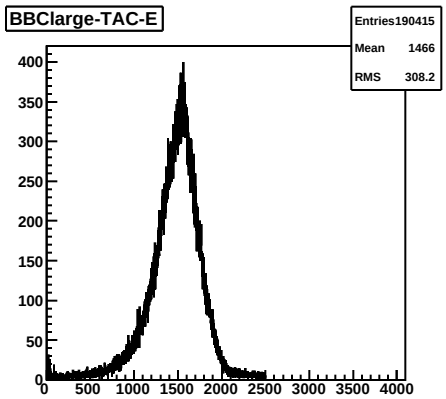

ZDC-TAC-W
Entries 6429
Mean 225
RMS 34.66

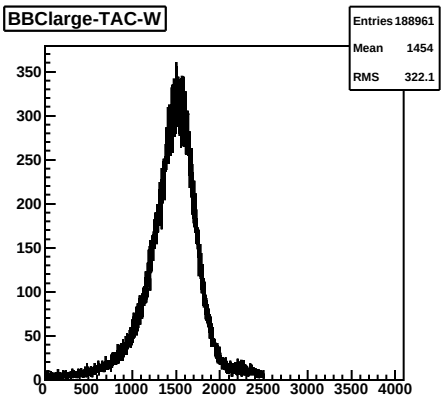

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 255
Mean 554.8
RMS 15.53

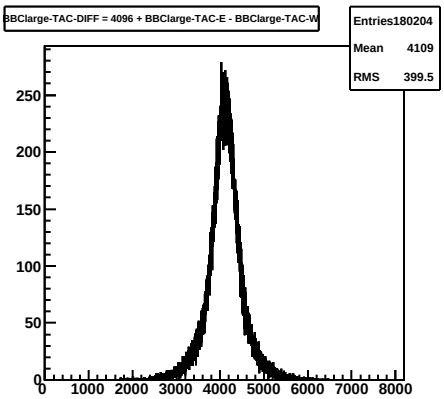

BBCsmall-TAC-E
Entries 201131
Mean 1543
RMS 163.7

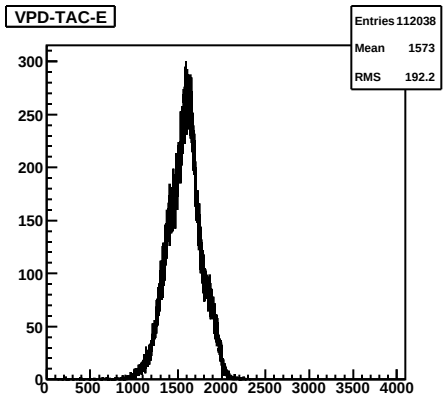

BBCsmall-TAC-W
Entries 201131
Mean 1543
RMS 164.5

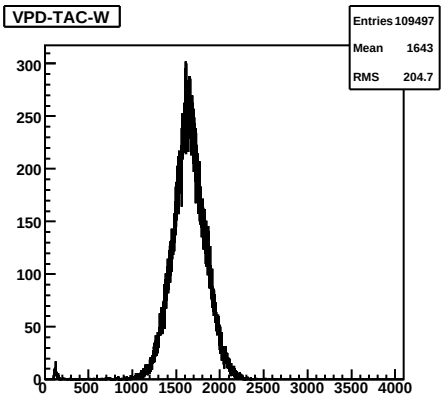

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 201131
Mean 4097
RMS 56.65

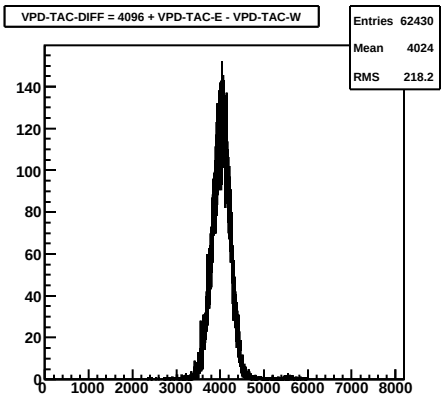

BBClarge-TAC-E
Entries 190415
Mean 1466
RMS 308.2

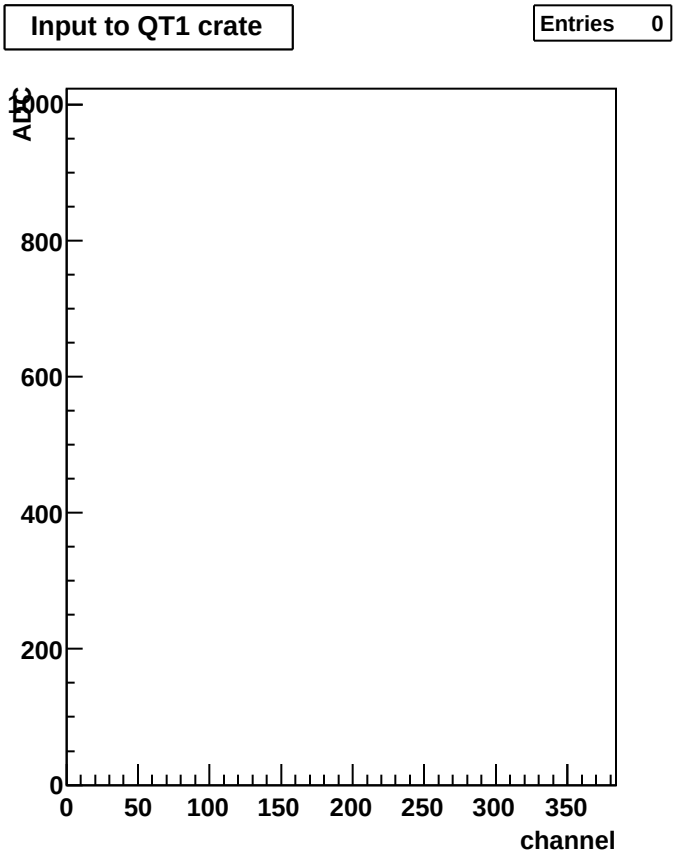

BBClarge-TAC-W
Entries 188961
Mean 1454
RMS 322.1

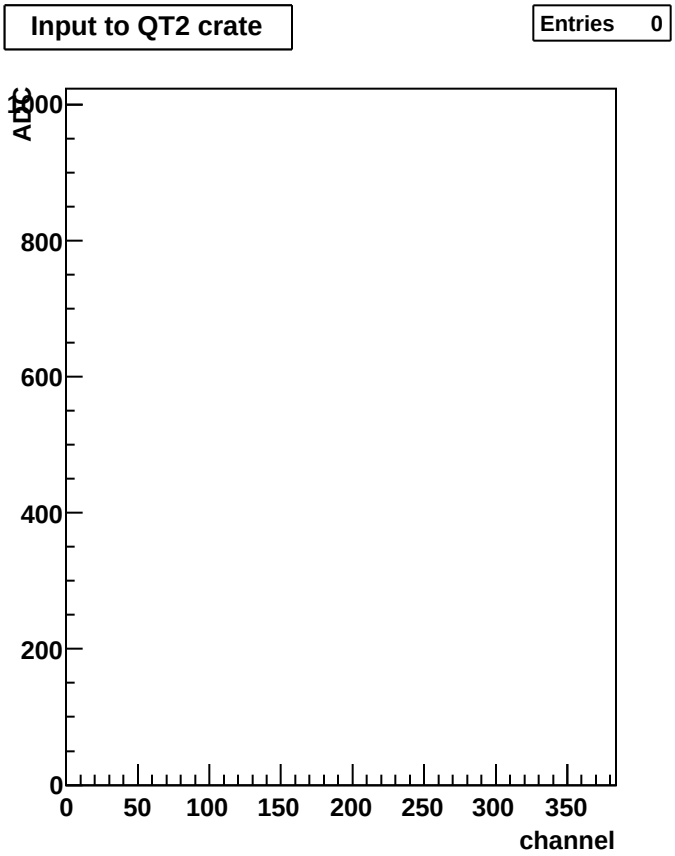

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 180204
Mean 4109
RMS 399.5

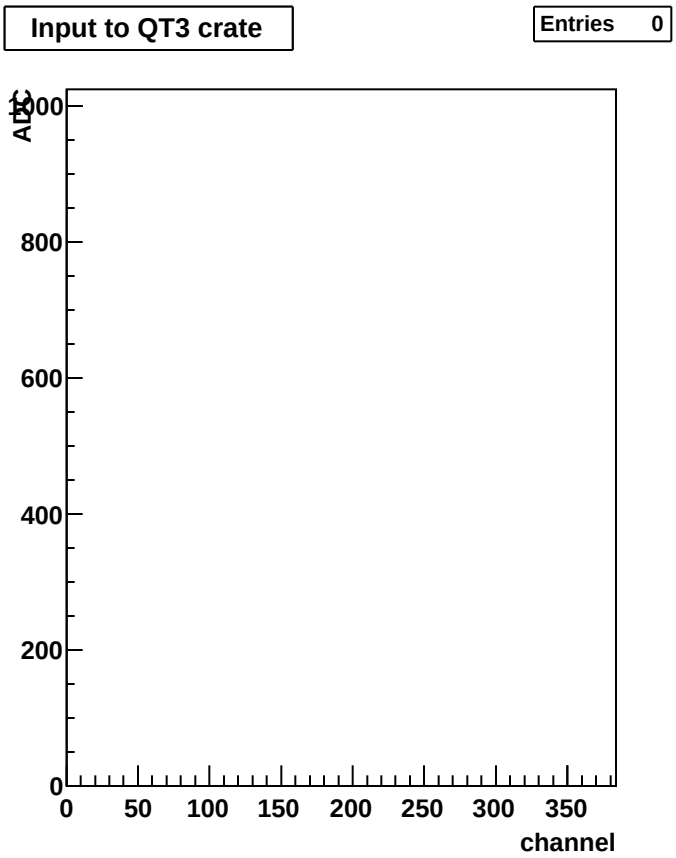

VPD-TAC-E
Entries 112038
Mean 1573
RMS 192.2

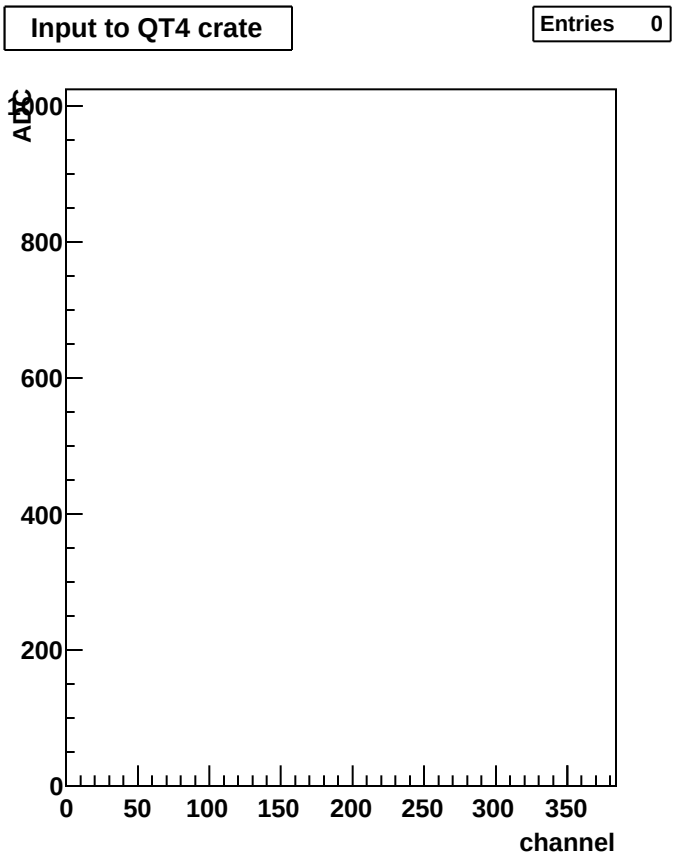

VPD-TAC-W
Entries 109497
Mean 1643
RMS 204.7

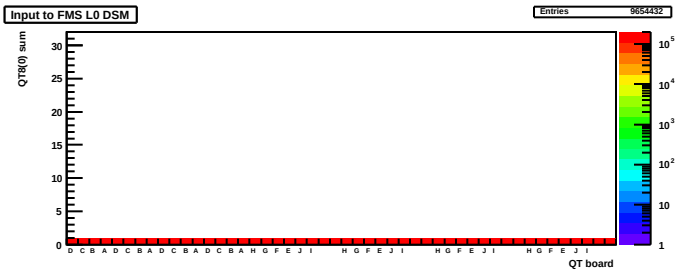

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 62430
Mean 4024
RMS 218.2

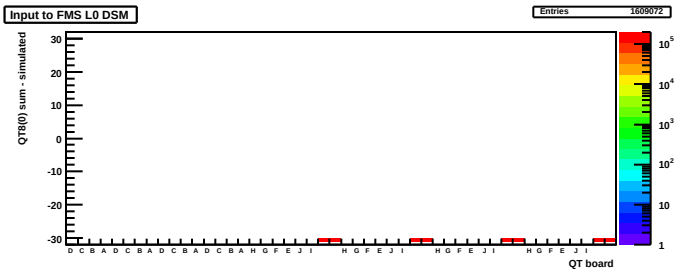

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

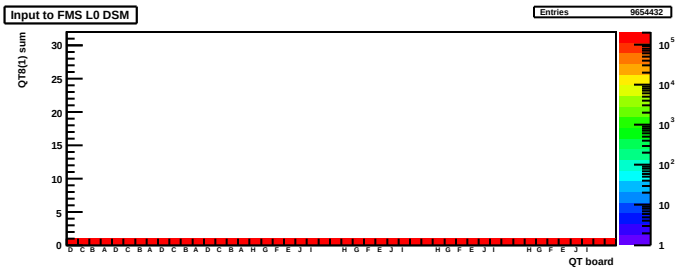

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

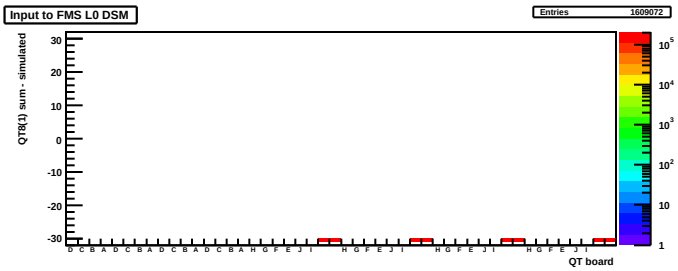

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

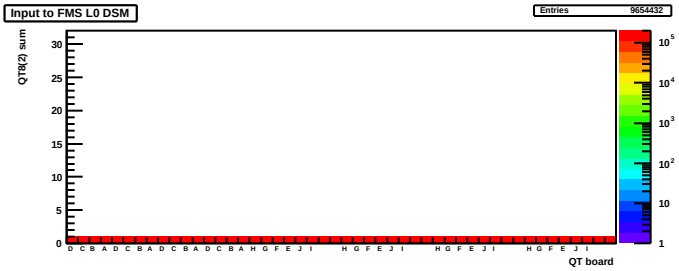

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

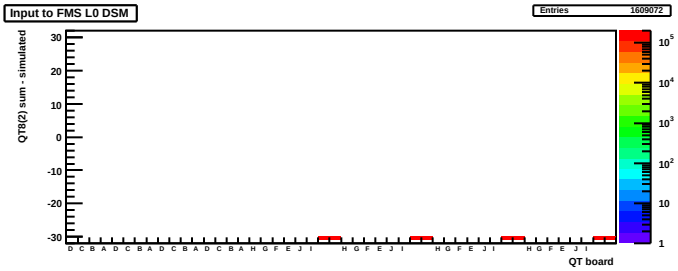

Input to FMS L0 DSM
Entries 9654432
QT8(0) sum
QT board

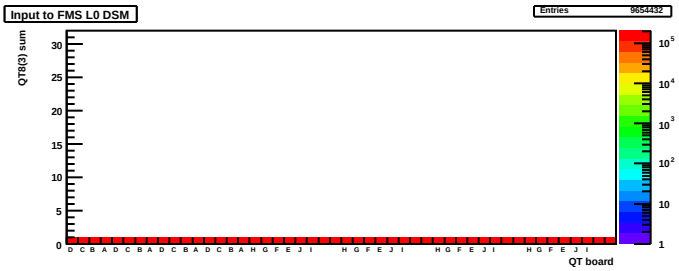

Input to FMS L0 DSM
Entries 1609072
QT8(0) sum - simulated
QT board

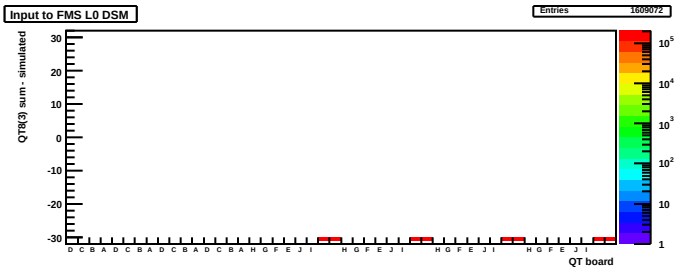

Input to FMS L0 DSM
Entries 9654432
QT8(1) sum
QT board

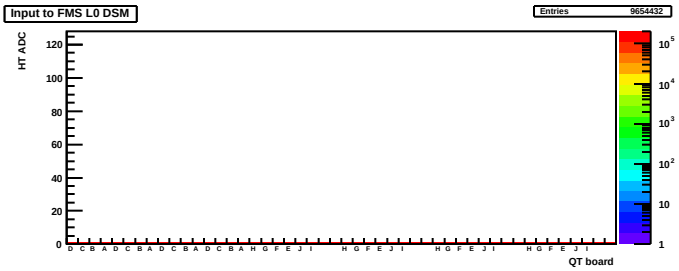

Input to FMS L0 DSM
Entries 1609072
QT8(1) sum - simulated
QT board

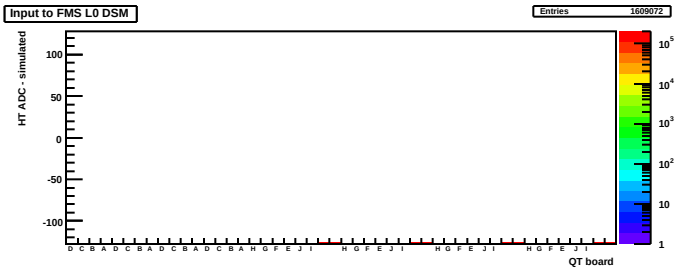

Input to FMS L0 DSM
Entries 9654432
QT8(2) sum
QT board

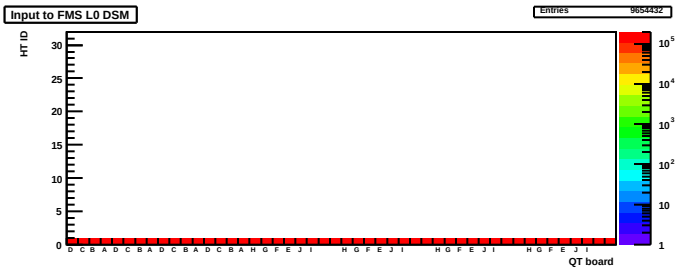

Input to FMS L0 DSM
Entries 1609072
QT8(2) sum - simulated
QT board

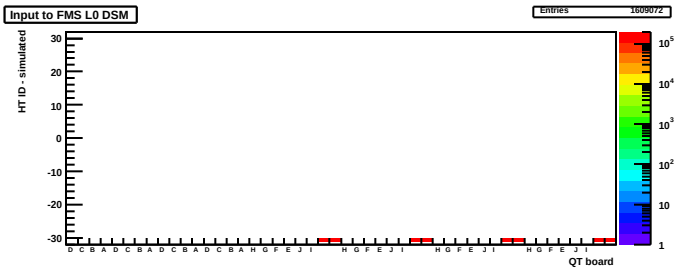

Input to FMS L0 DSM
Entries 9654432
QT8(3) sum
QT board

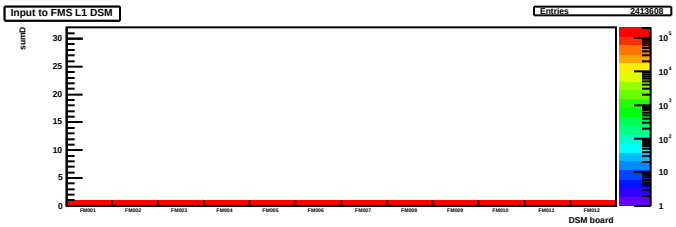

Input to FMS L0 DSM
Entries 1609072
QT8(3) sum - simulated
QT board

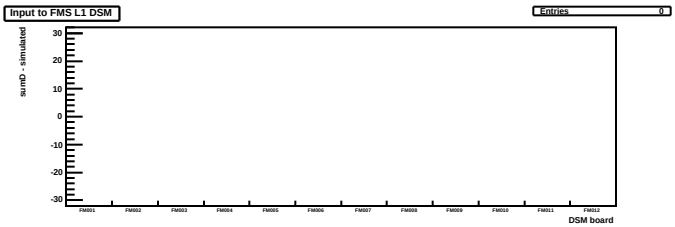

Input to FMS L0 DSM
Entries 9654432
HT ADC
QT board

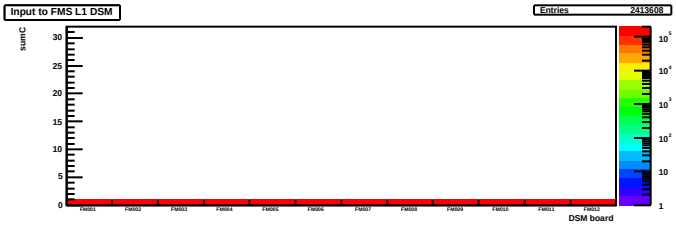

Input to FMS L0 DSM
Entries 1609072
HT ADC - simulated
QT board

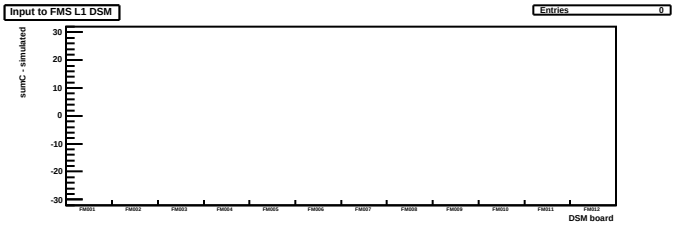

Input to FMS L0 DSM
Entries 9654432
HT ID
QT board

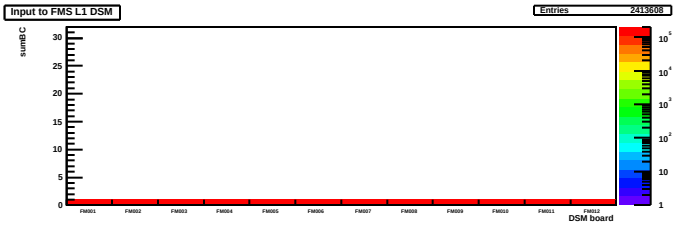

Input to FMS L0 DSM
Entries 1609072
HT ID - simulated
QT board

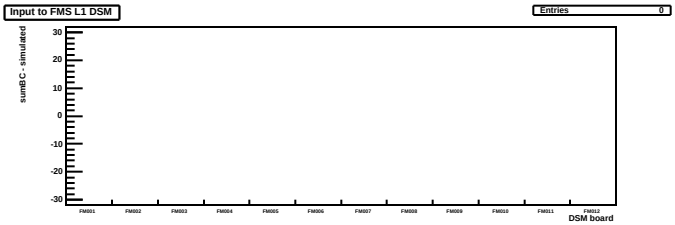

Input to FMS L1 DSM
Entries 2413608
sumD
DSM board

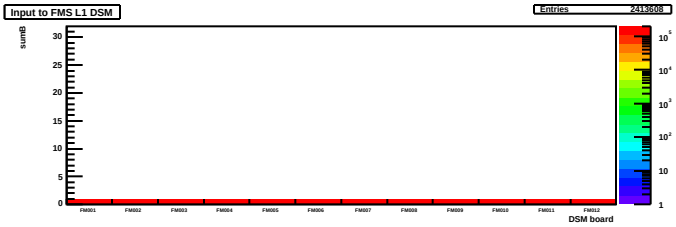

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

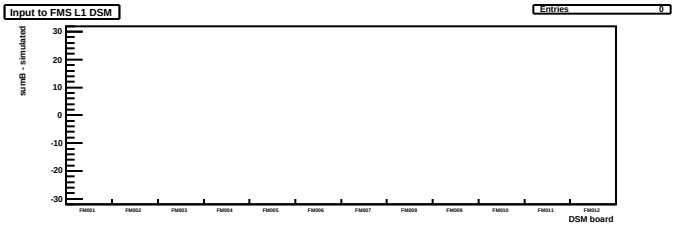

Input to FMS L1 DSM
Entries 2413608
sumC
DSM board

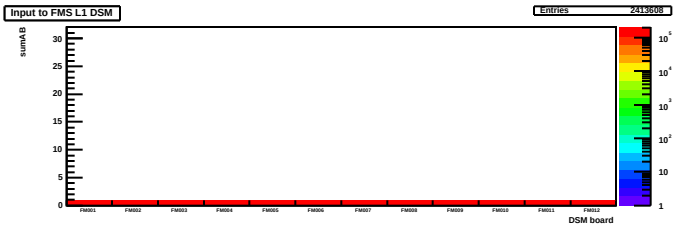

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

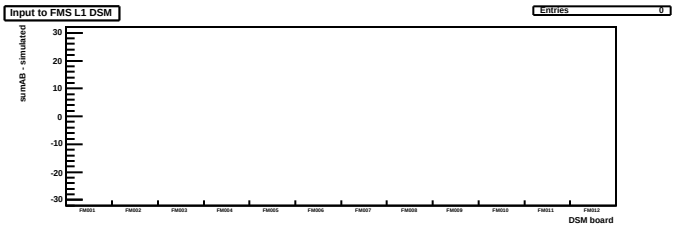

Input to FMS L1 DSM
Entries 2413608
sumBC
DSM board

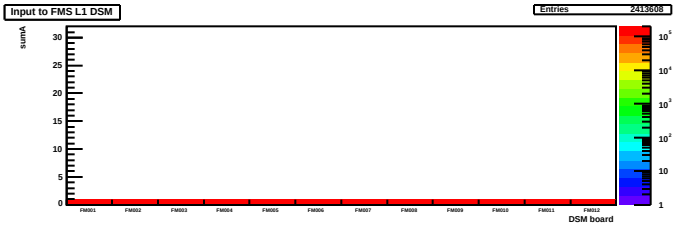

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

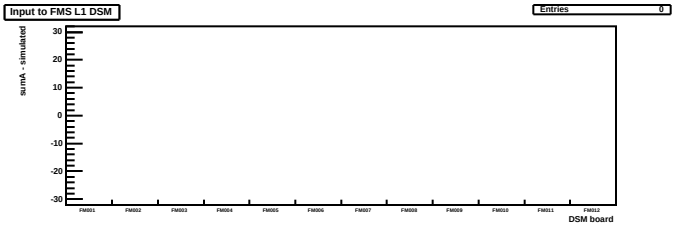

Input to FMS L1 DSM
Entries 2413608
sumB
DSM board

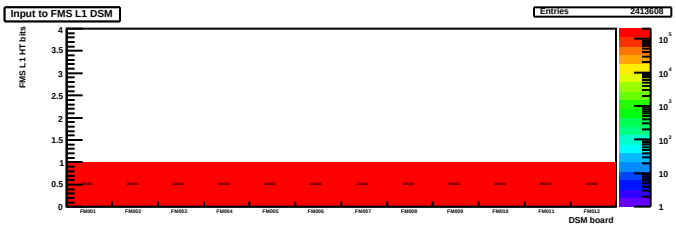

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

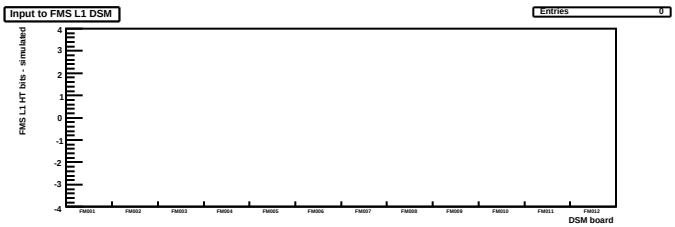

Input to FMS L1 DSM
Entries 2413608
sumAB
DSM board

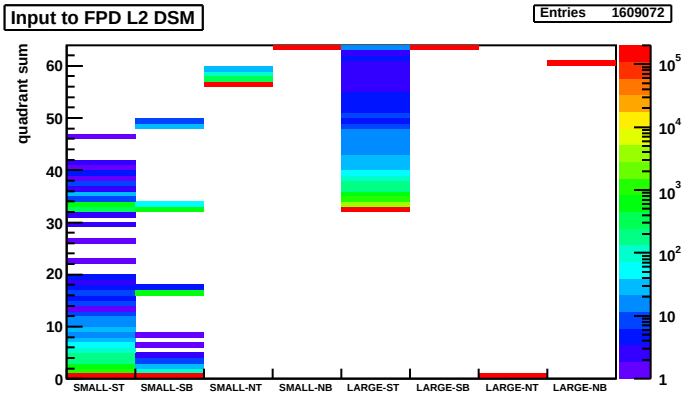

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

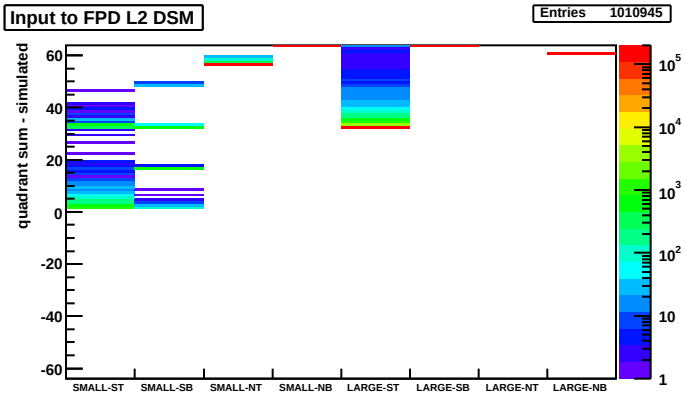

Input to FMS L1 DSM
Entries 2413608
sumA
DSM board

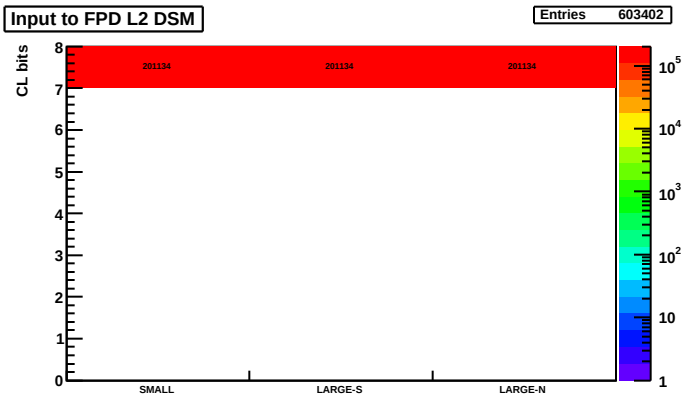

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

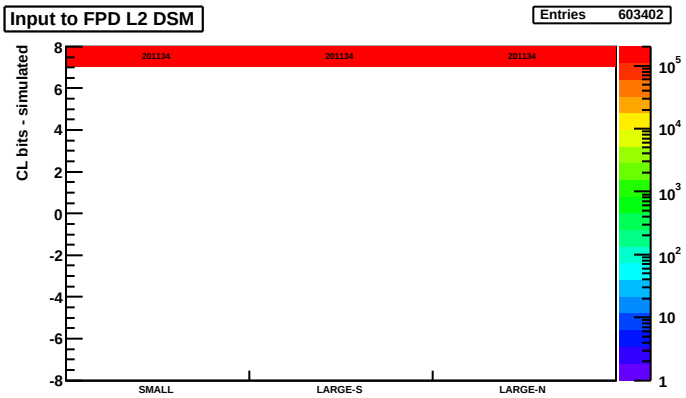

Input to FMS L1 DSM
Entries 2413608
FMS L1 HT bits
DSM board

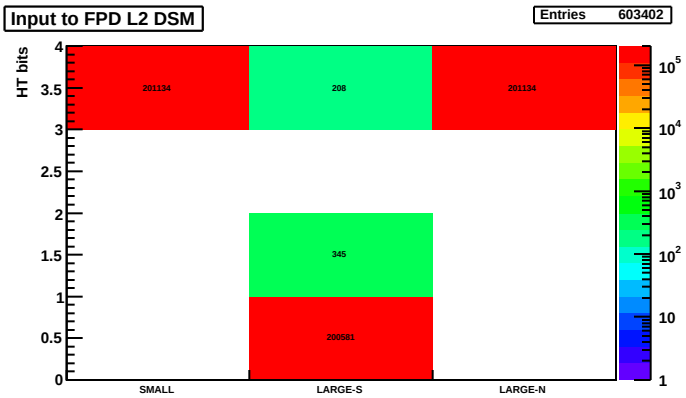

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

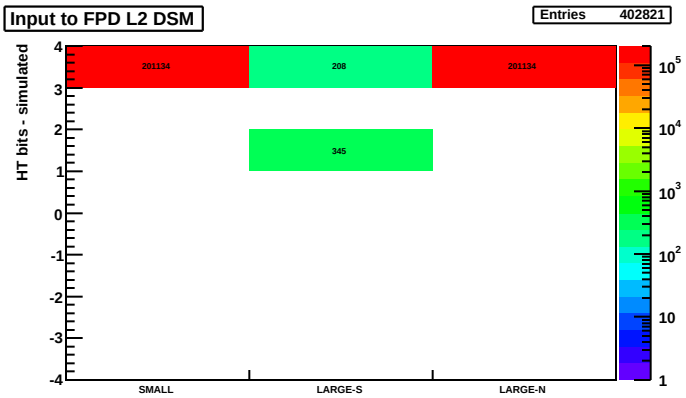

Input to FPD L2 DSM
Entries 1609072
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

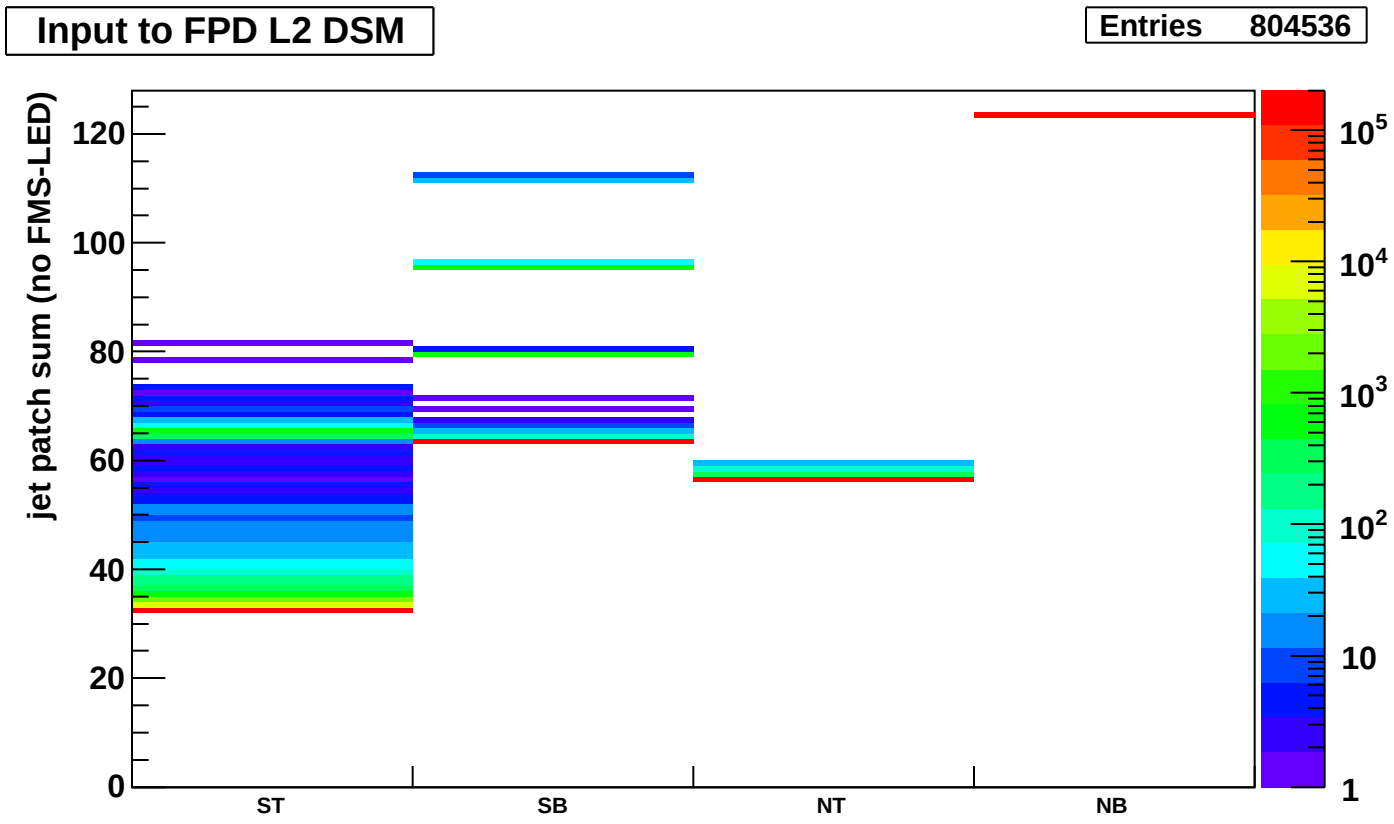

Input to FPD L2 DSM
Entries 1010945
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

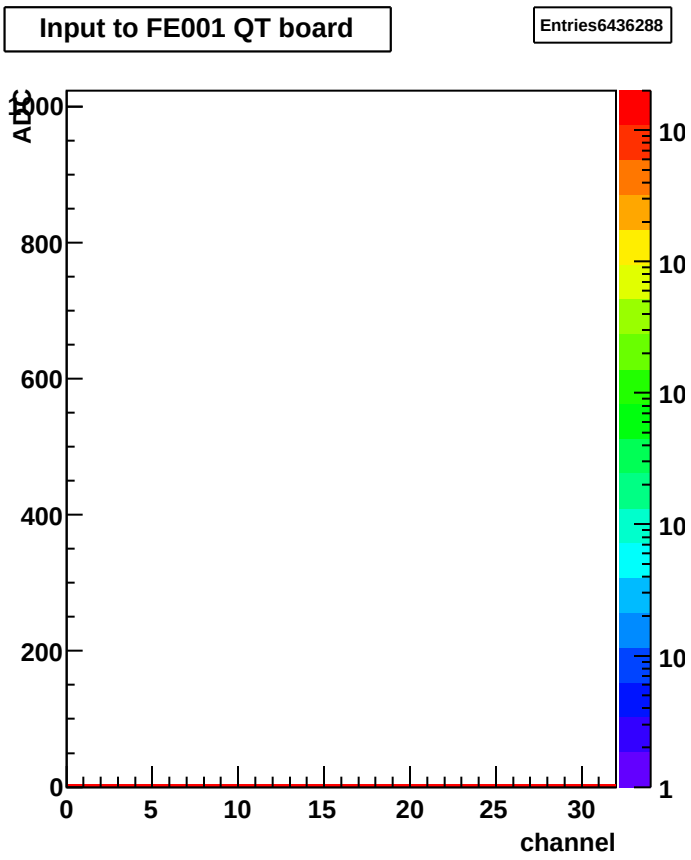

Input to FPD L2 DSM
Entries 603402
CL bits
201134
201134
201134
SMALL
LARGE-S
LARGE-N

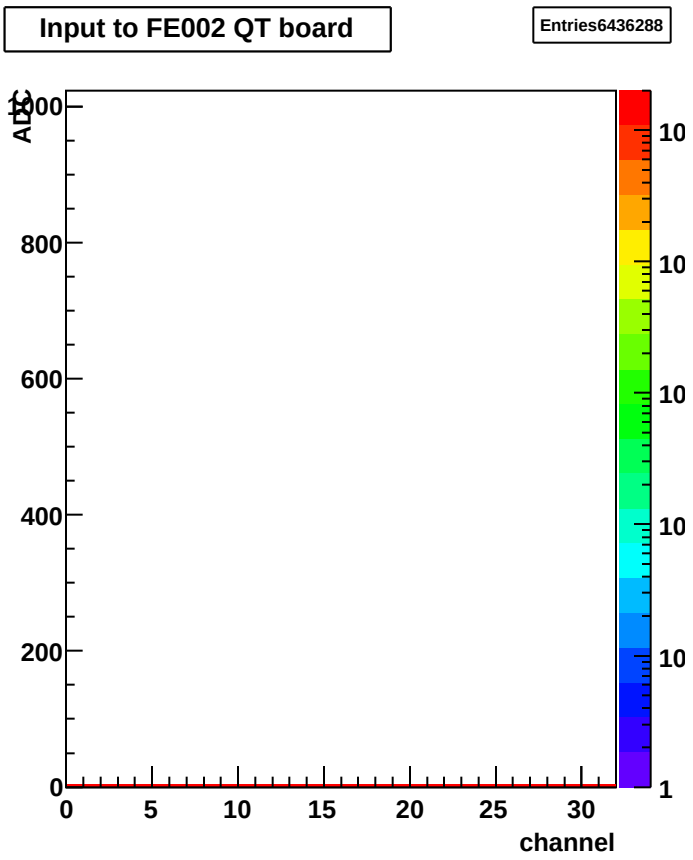

Input to FPD L2 DSM
Entries 603402
CL bits - simulated
201134
201134
201134
SMALL
LARGE-S
LARGE-N

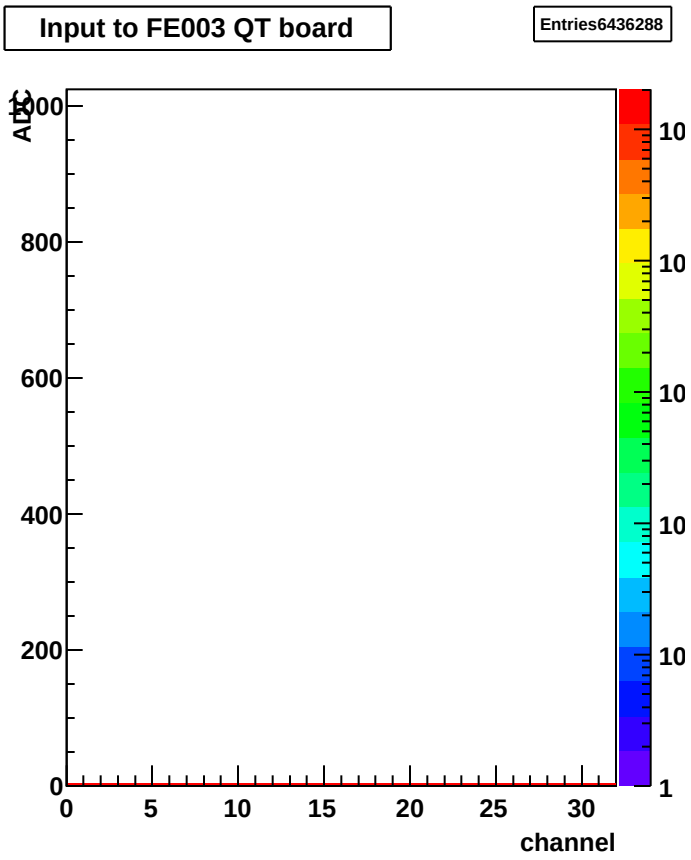

Input to FPD L2 DSM
Entries 603402
HT bits
201134
208
201134
345
200581
SMALL
LARGE-S
LARGE-N

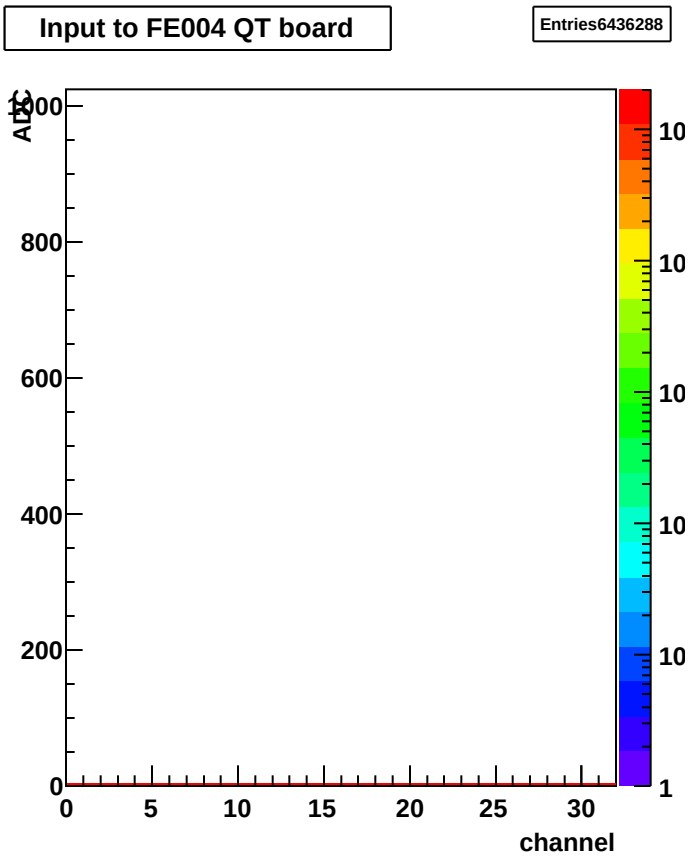

Input to FPD L2 DSM
Entries 402821
HT bits - simulated
201134
208
201134
345
SMALL
LARGE-S
LARGE-N

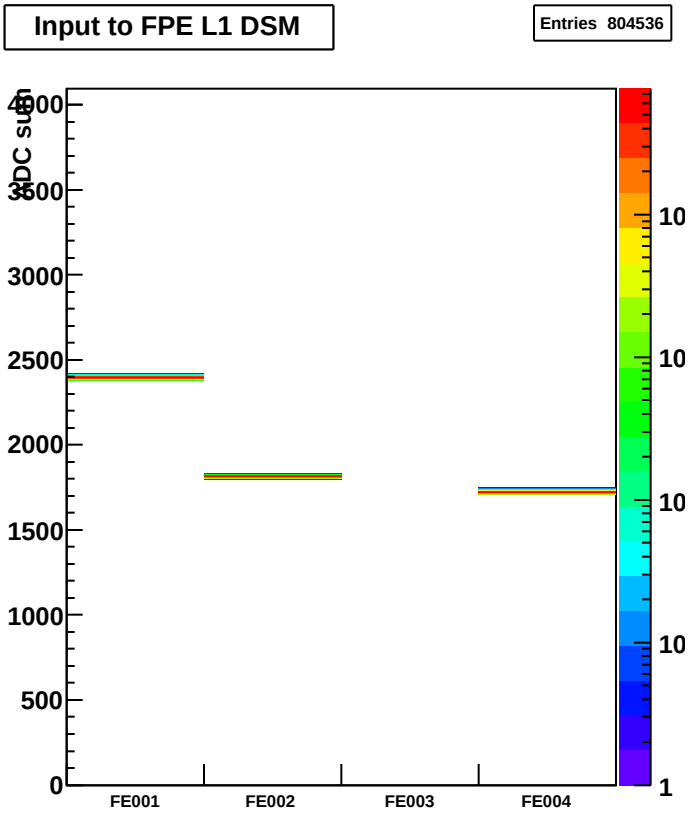

Input to FPD L2 DSM
Entries 804536
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

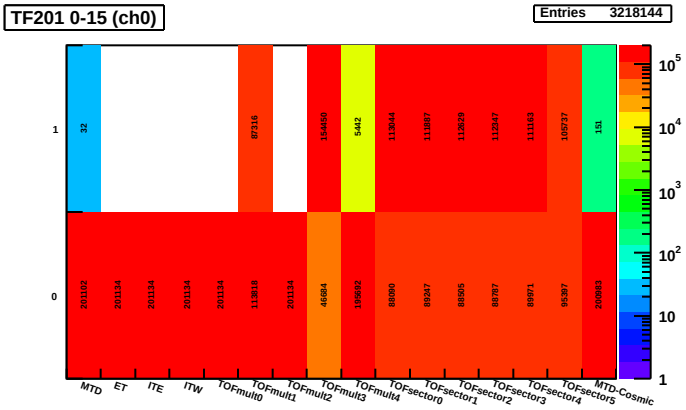

Input to FE001 QT board
Entries6436288
ADC
channel

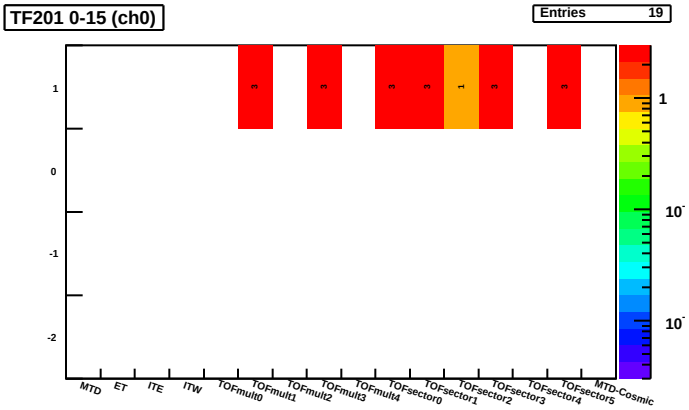

Input to FE002 QT board
Entries6436288
ADC
channel

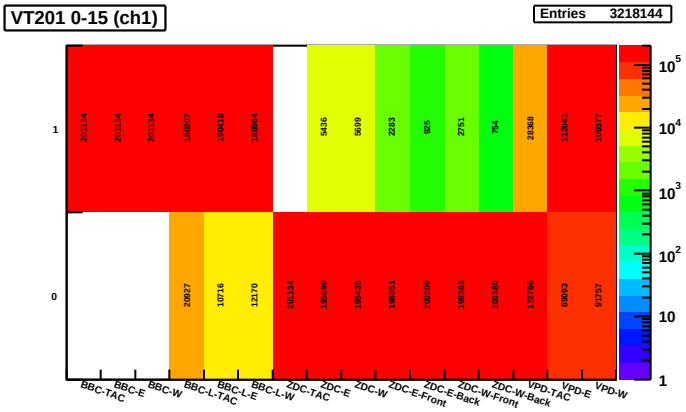

Input to FE003 QT board
Entries6436288
ADC
channel

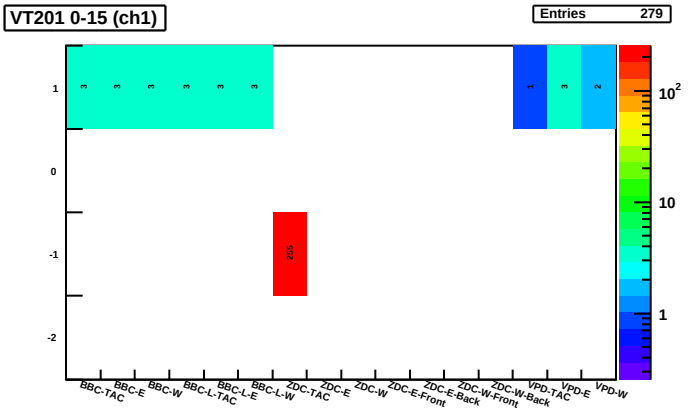

Input to FE004 QT board
Entries6436288
ADC
channel

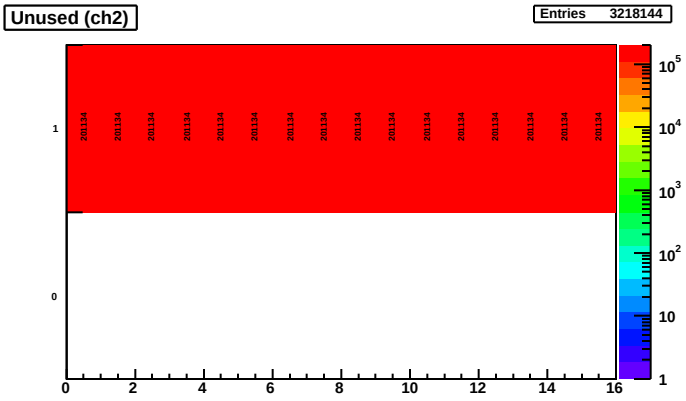

Input to FPE L1 DSM
Entries 804536
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

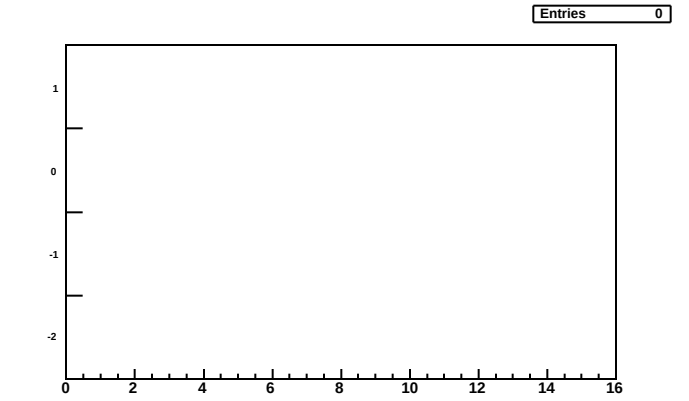

TF201 0-15 (ch0)
Entries 3218144
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

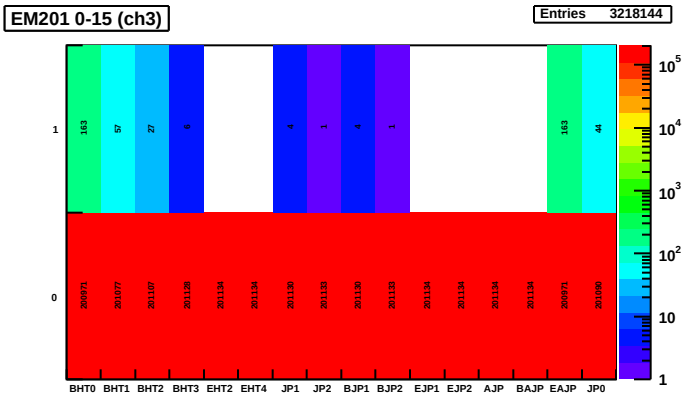

TF201 0-15 (ch0)
Entries 19
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

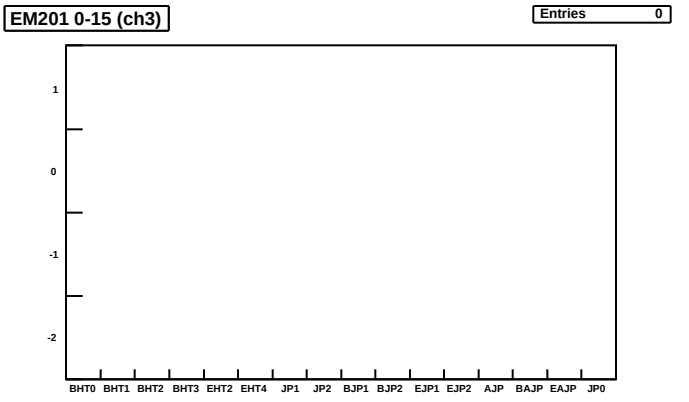

VT201 0-15 (ch1)
Entries 3218144
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 279
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

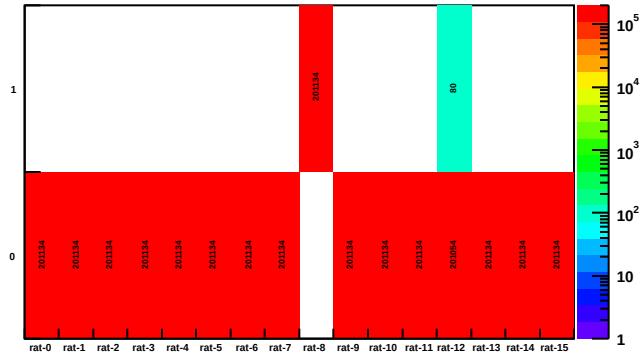

Unused (ch2)
Entries 3218144

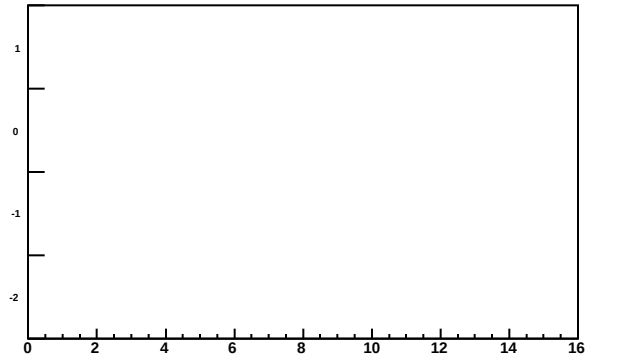

Entries 0


EM201 0-15 (ch3)
Entries 3218144
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

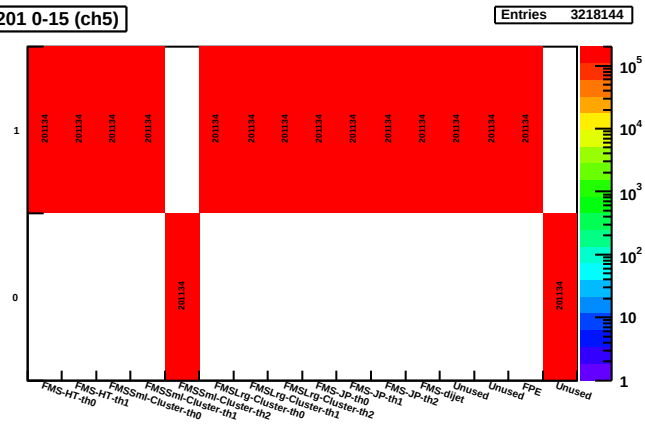

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

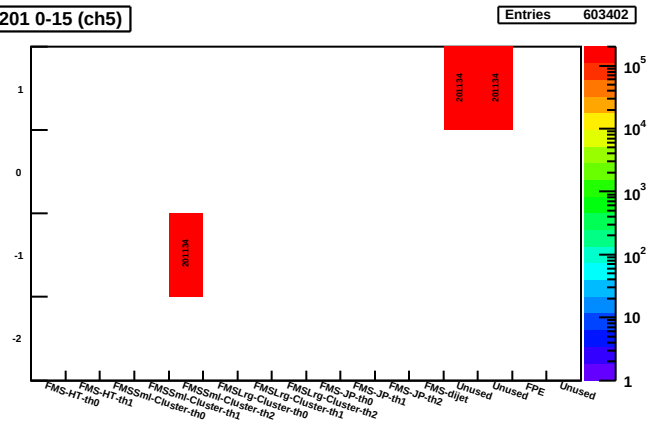




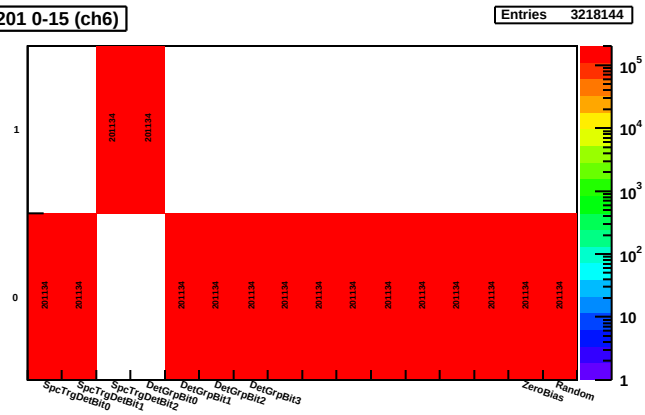
FP201 0-15 (ch5)

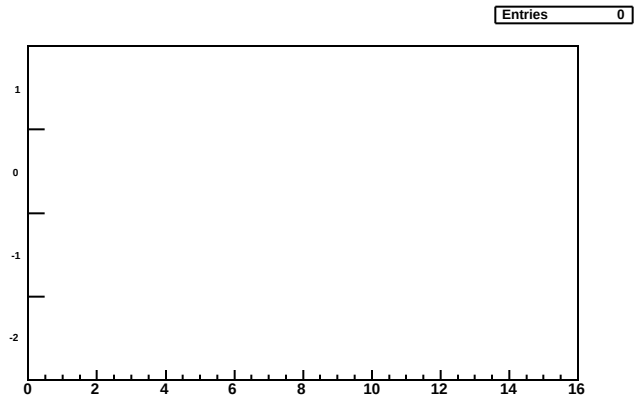
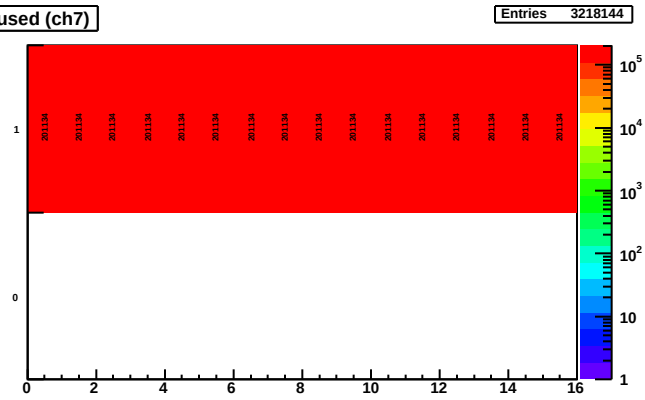
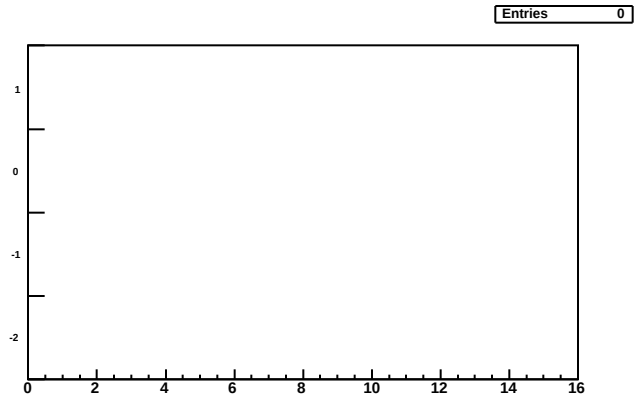


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)