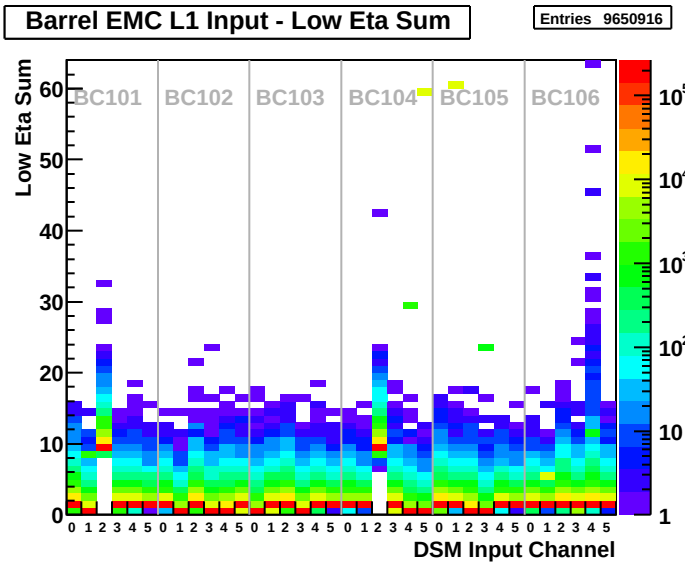
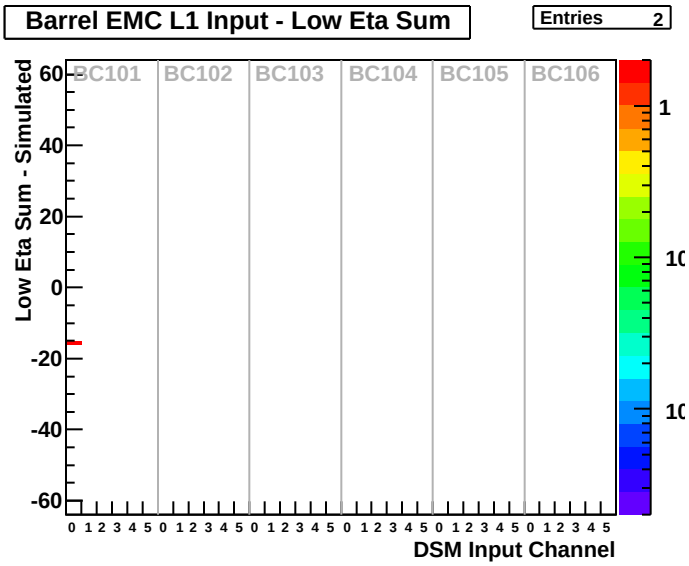
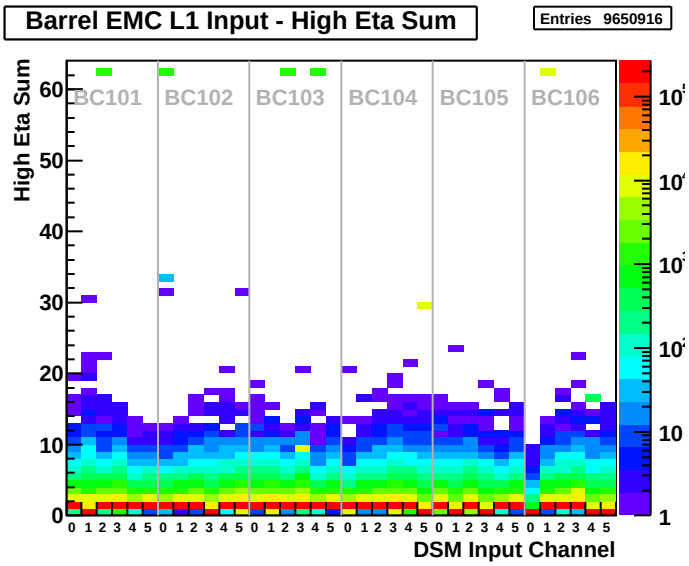
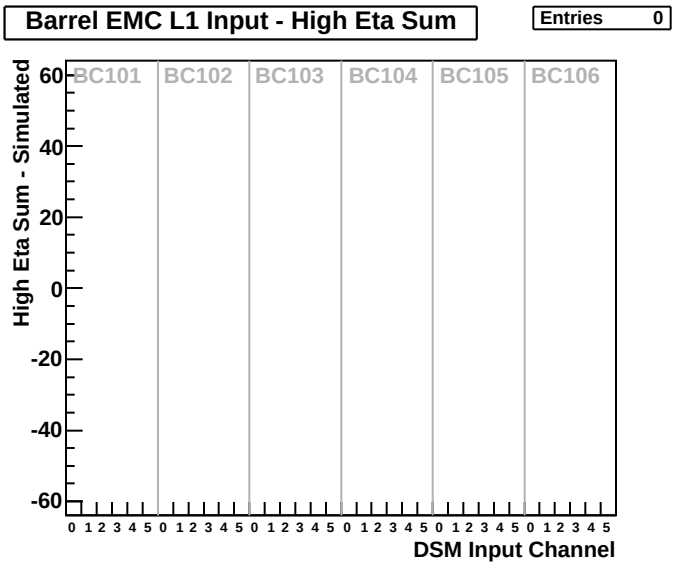
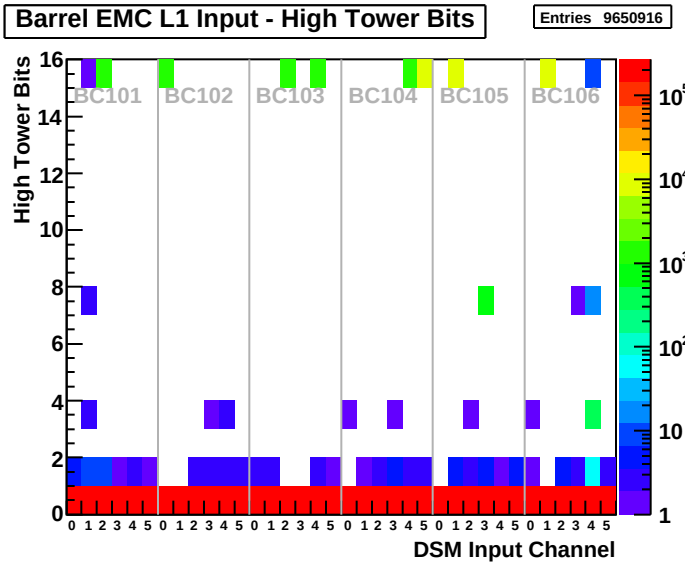
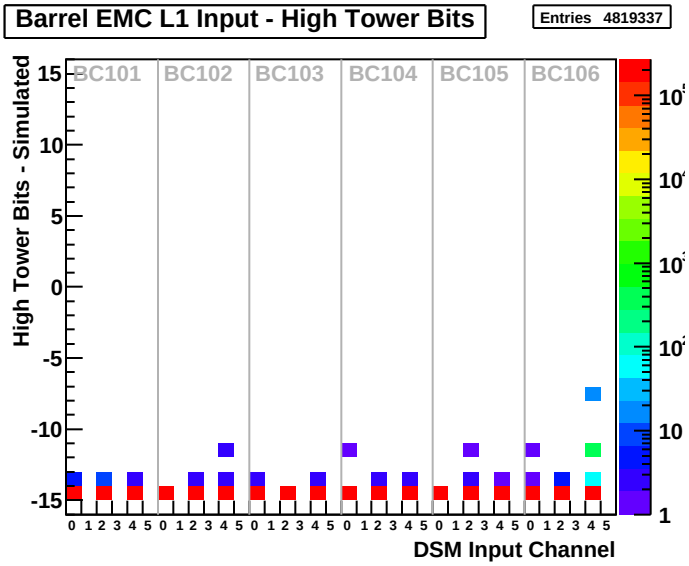

Barrel EMC L1 Input - Low Eta Sum
Entries 9650916
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

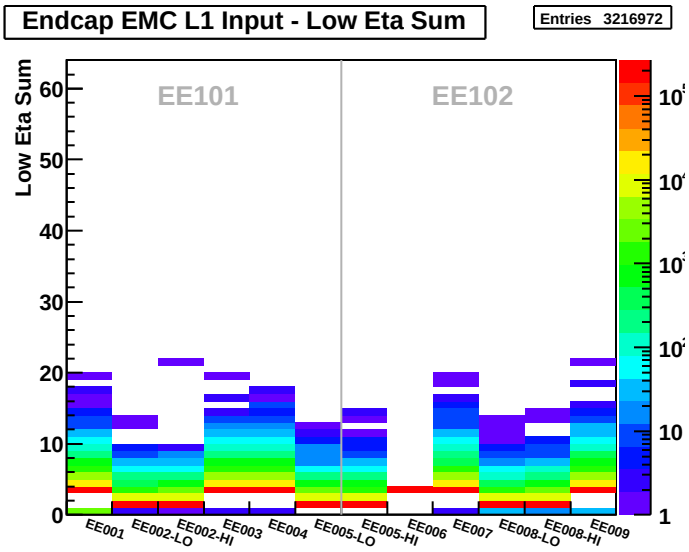

Barrel EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

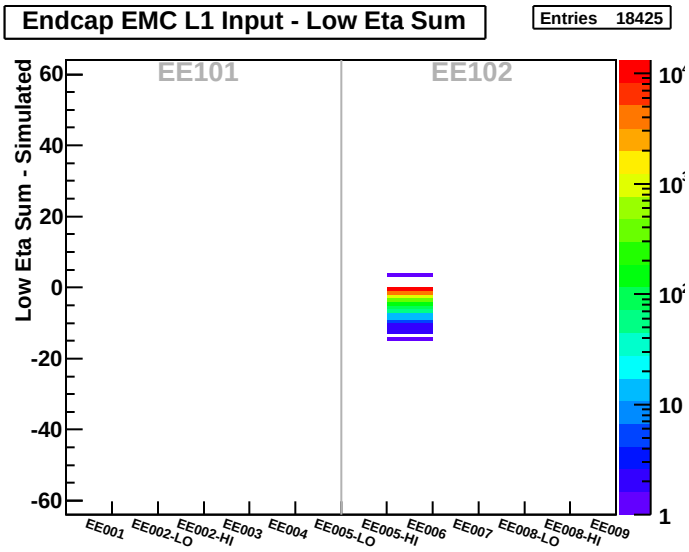

Barrel EMC L1 Input - High Eta Sum
Entries 9650916
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

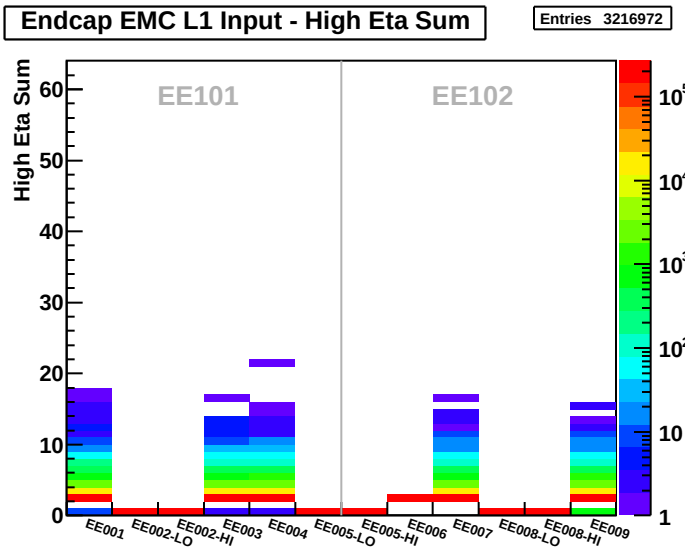

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

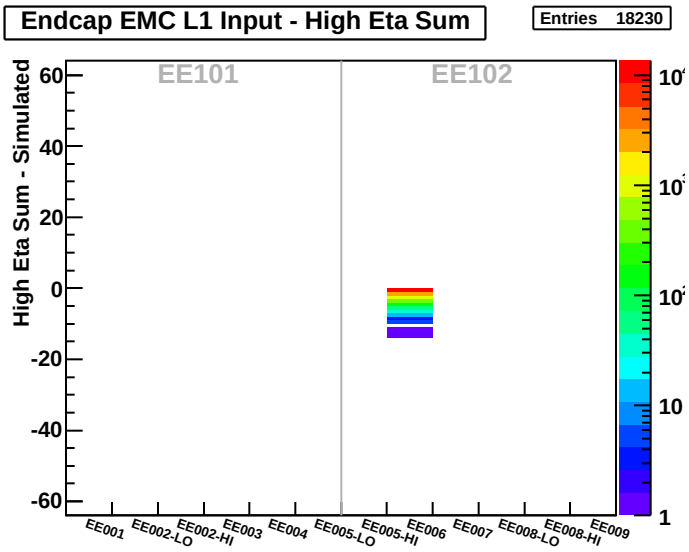

Barrel EMC L1 Input - High Tower Bits
Entries 9650916
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

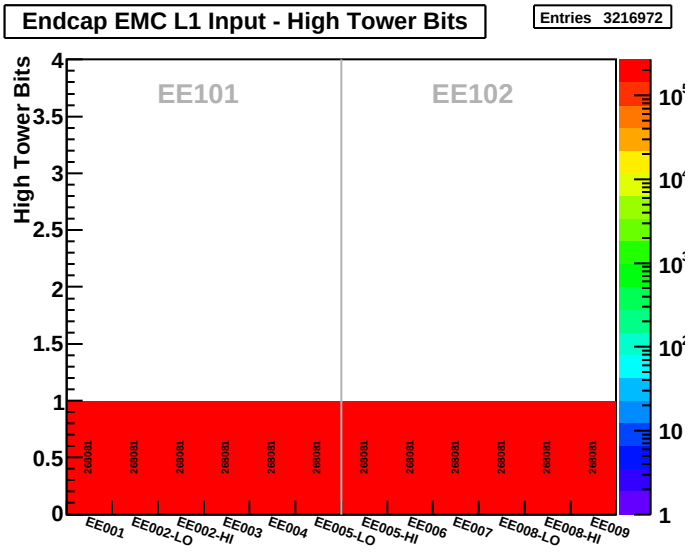

Barrel EMC L1 Input - High Tower Bits
Entries 4819337
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

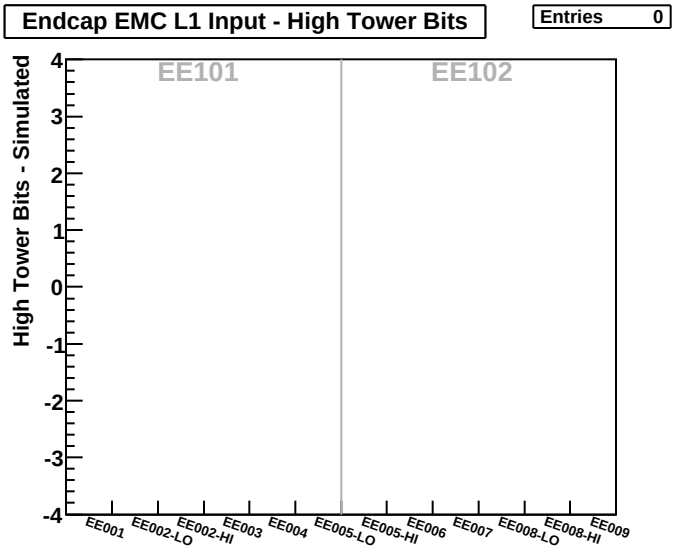

Endcap EMC L1 Input - Low Eta Sum
Entries 3216972
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

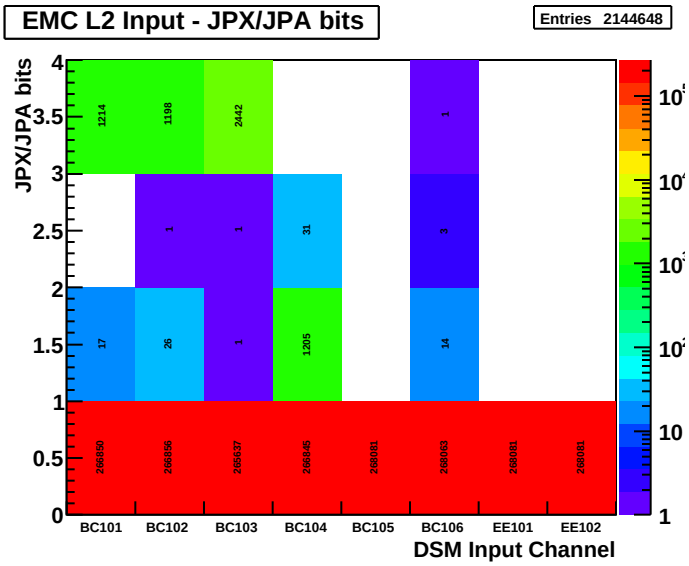

Endcap EMC L1 Input - Low Eta Sum
Entries 18425
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

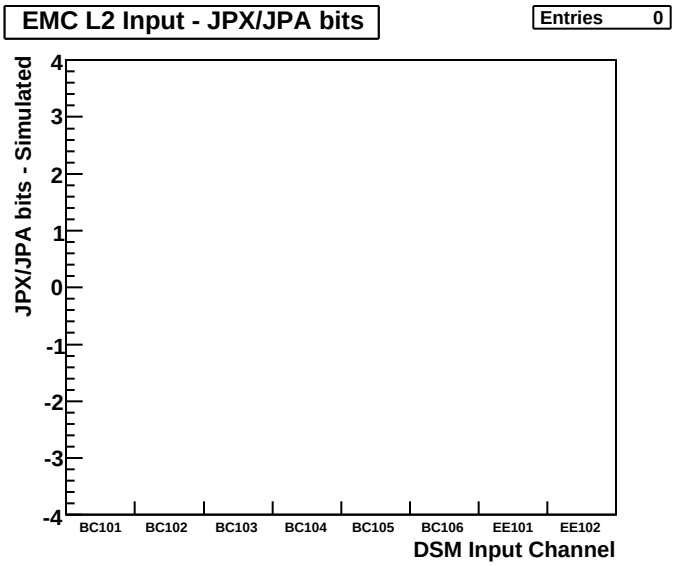

Endcap EMC L1 Input - High Eta Sum
Entries 3216972
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

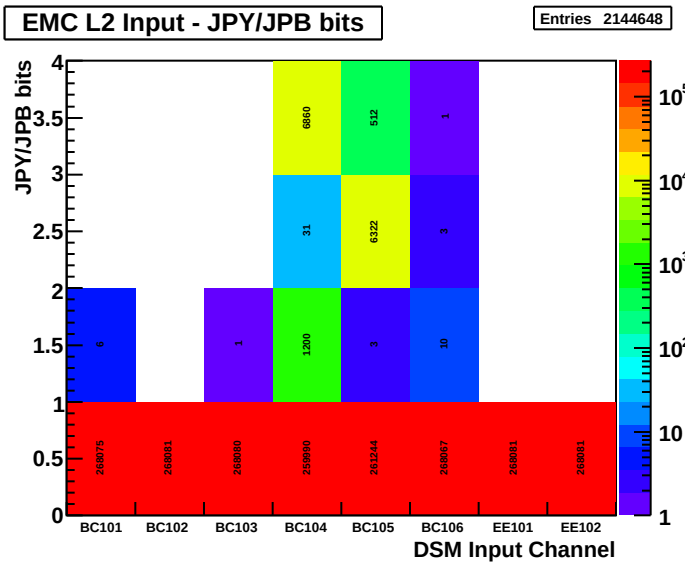

Endcap EMC L1 Input - High Eta Sum
Entries 18230
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

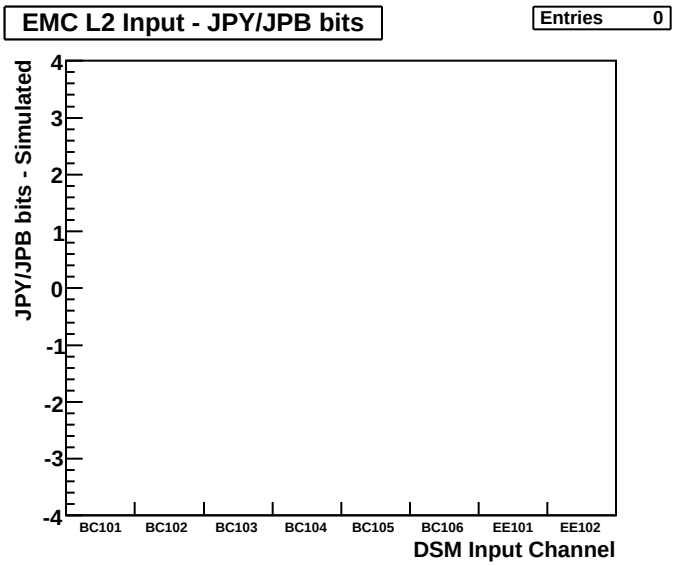

Endcap EMC L1 Input - High Tower Bits
Entries 3216972
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

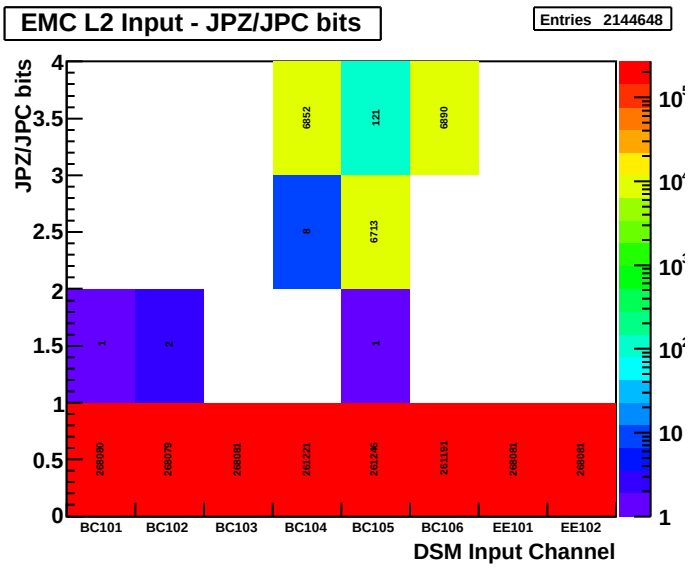

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

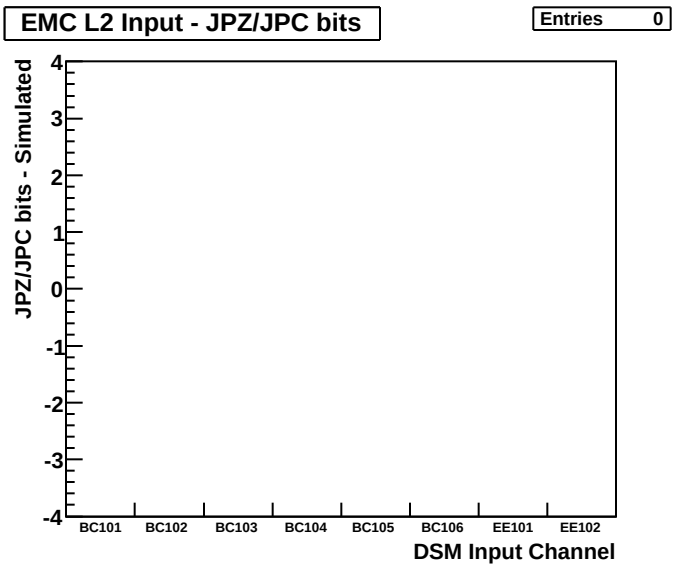

EMC L2 Input - JPX/JPA bits
Entries 2144648
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

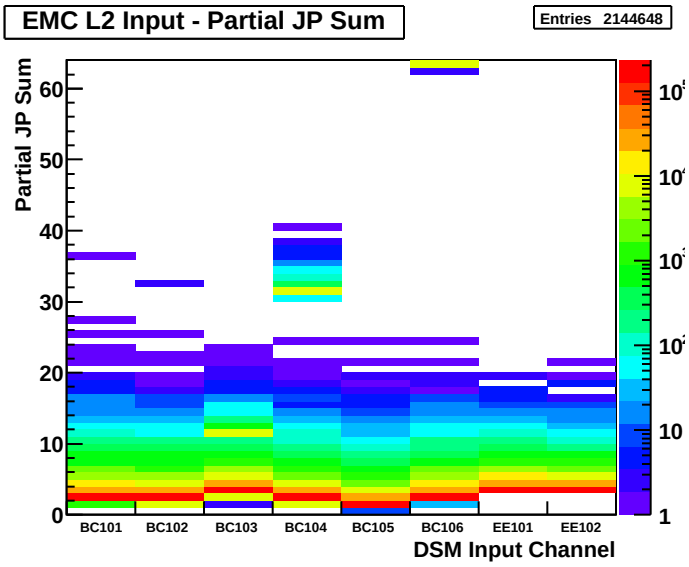

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

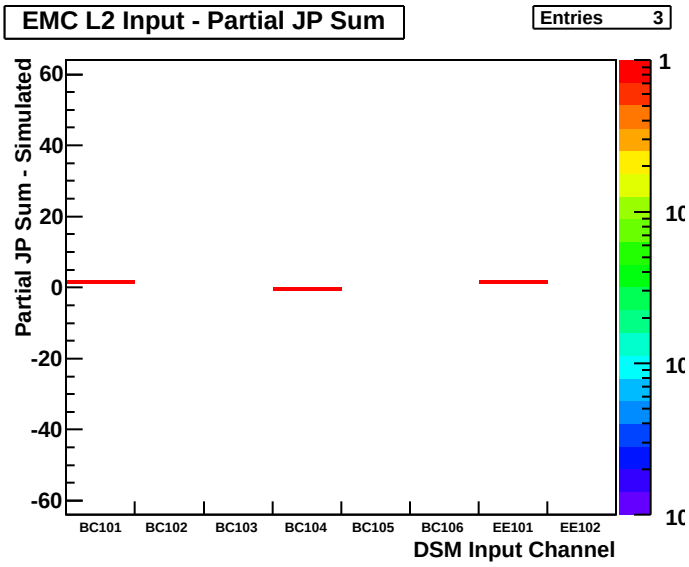

EMC L2 Input - JPY/JPB bits
Entries 2144648
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

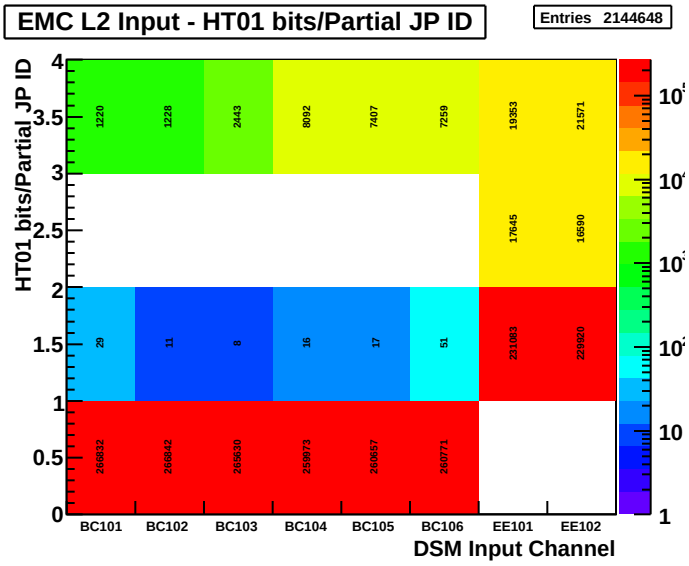

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

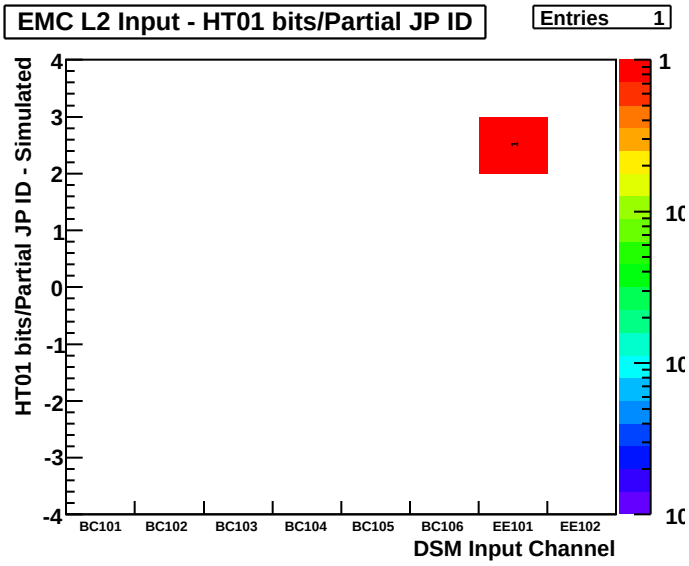

EMC L2 Input - JPZ/JPC bits
Entries 2144648
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

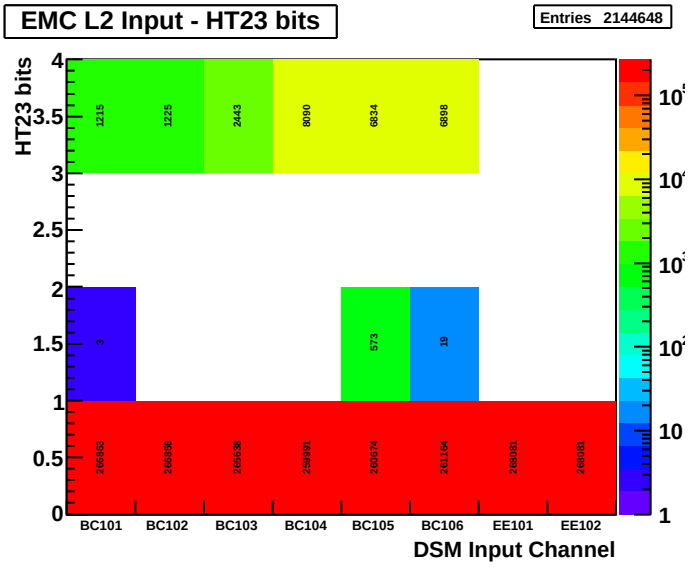

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

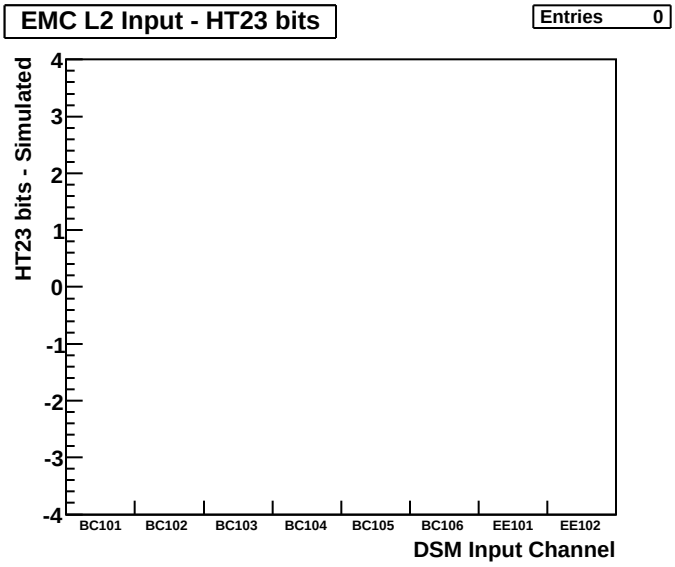

EMC L2 Input - Partial JP Sum
Entries 2144648
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

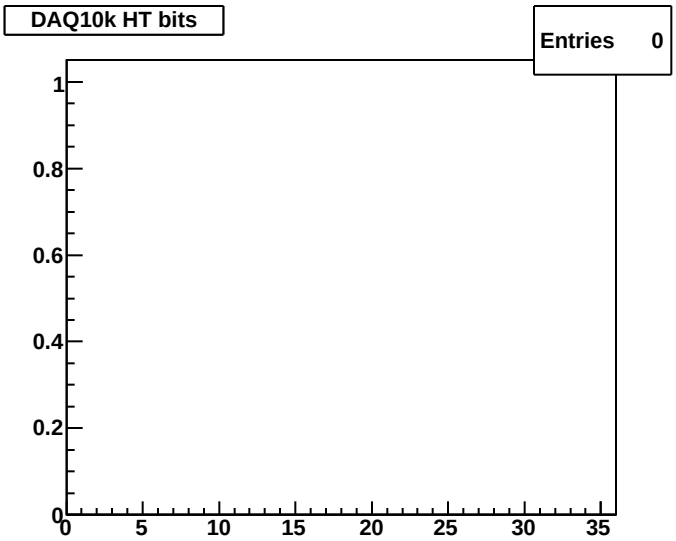

EMC L2 Input - Partial JP Sum
Entries 3
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

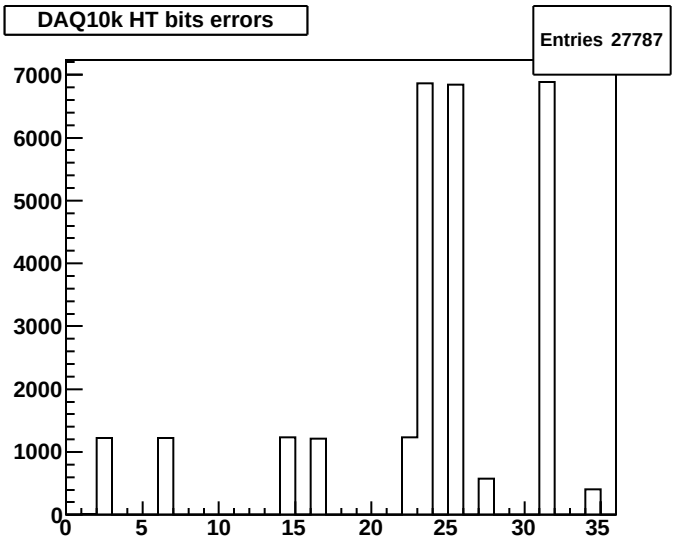

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2144648
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

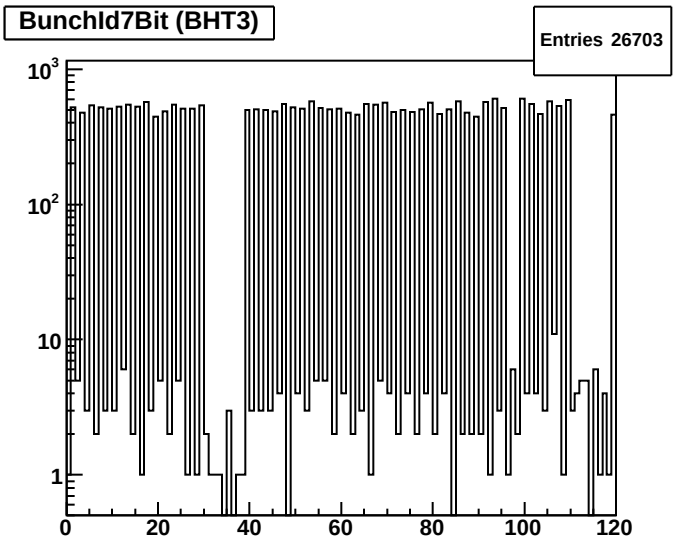

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

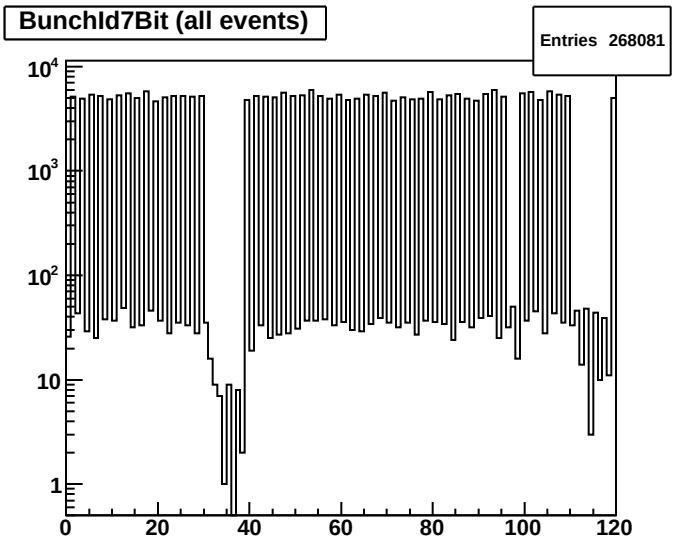

EMC L2 Input - HT23 bits
Entries 2144648
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

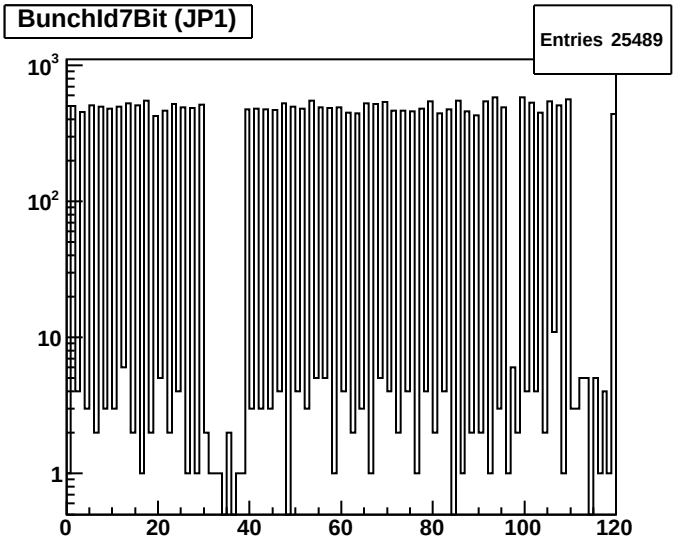

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

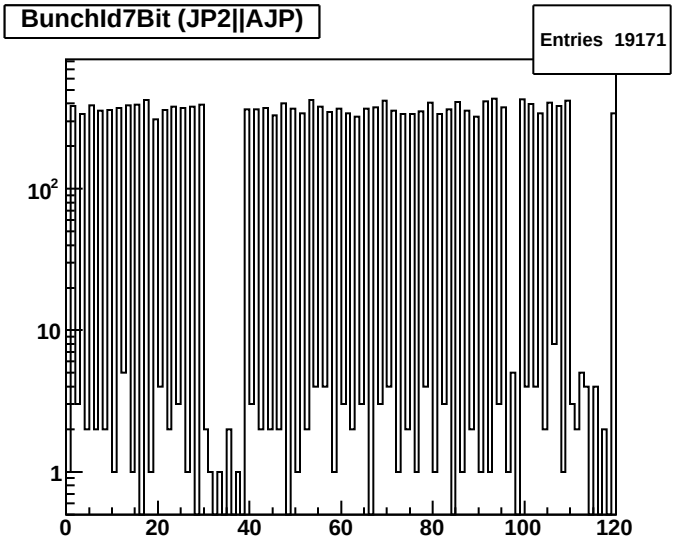

DAQ10k HT bits
Entries 0

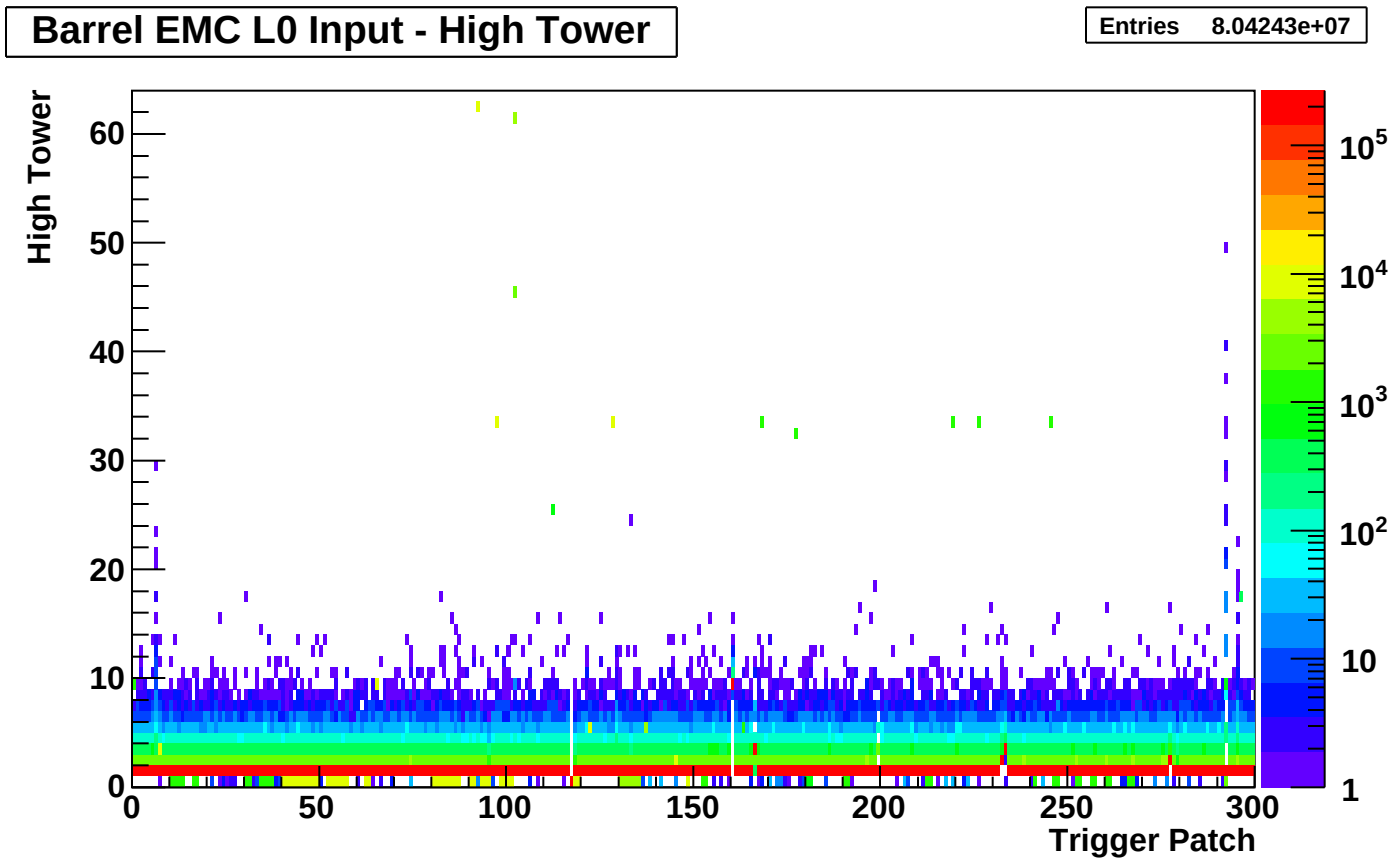

DAQ10k HT bits errors
Entries 27787

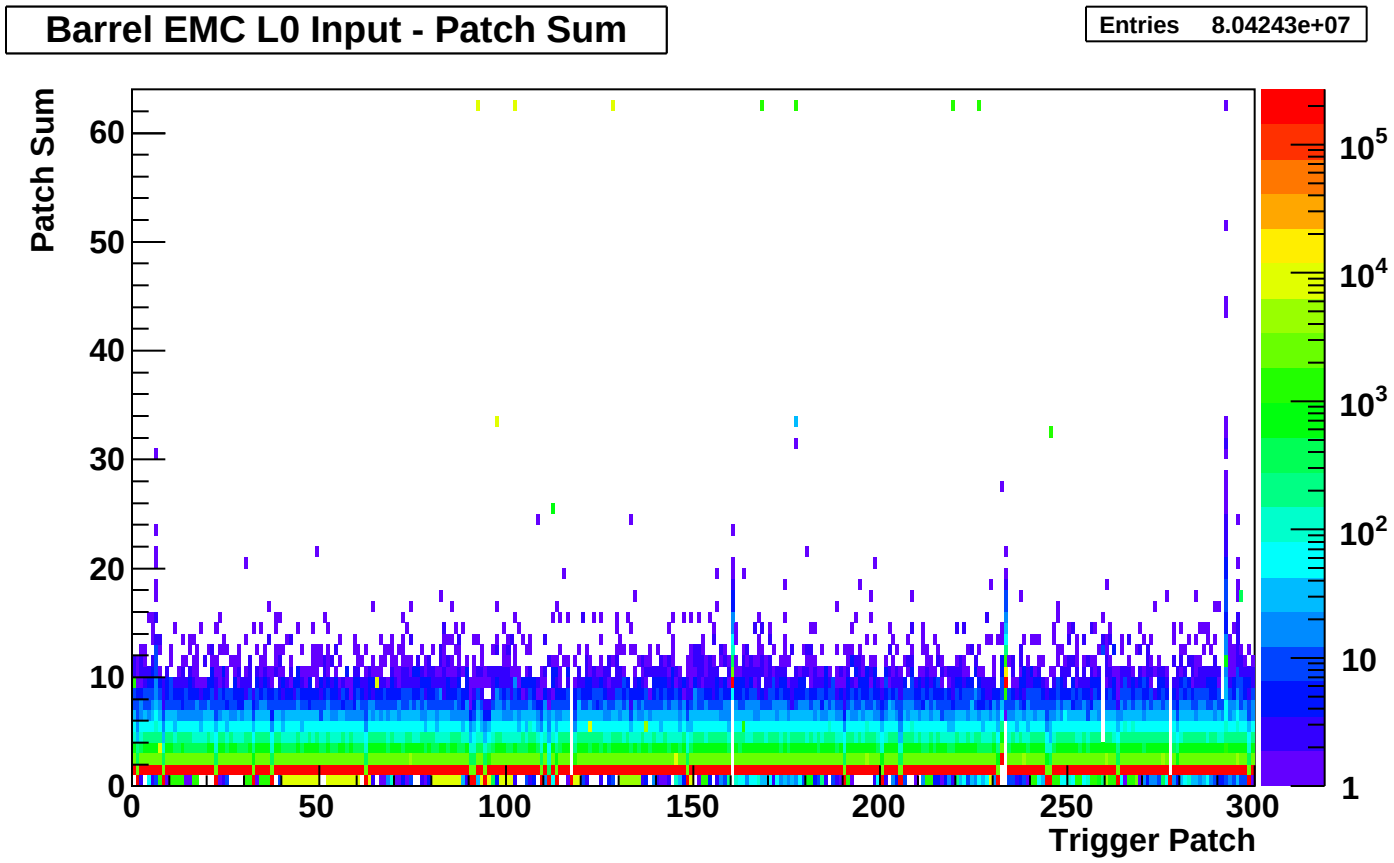

BunchId7Bit (BHT3)
Entries 26703

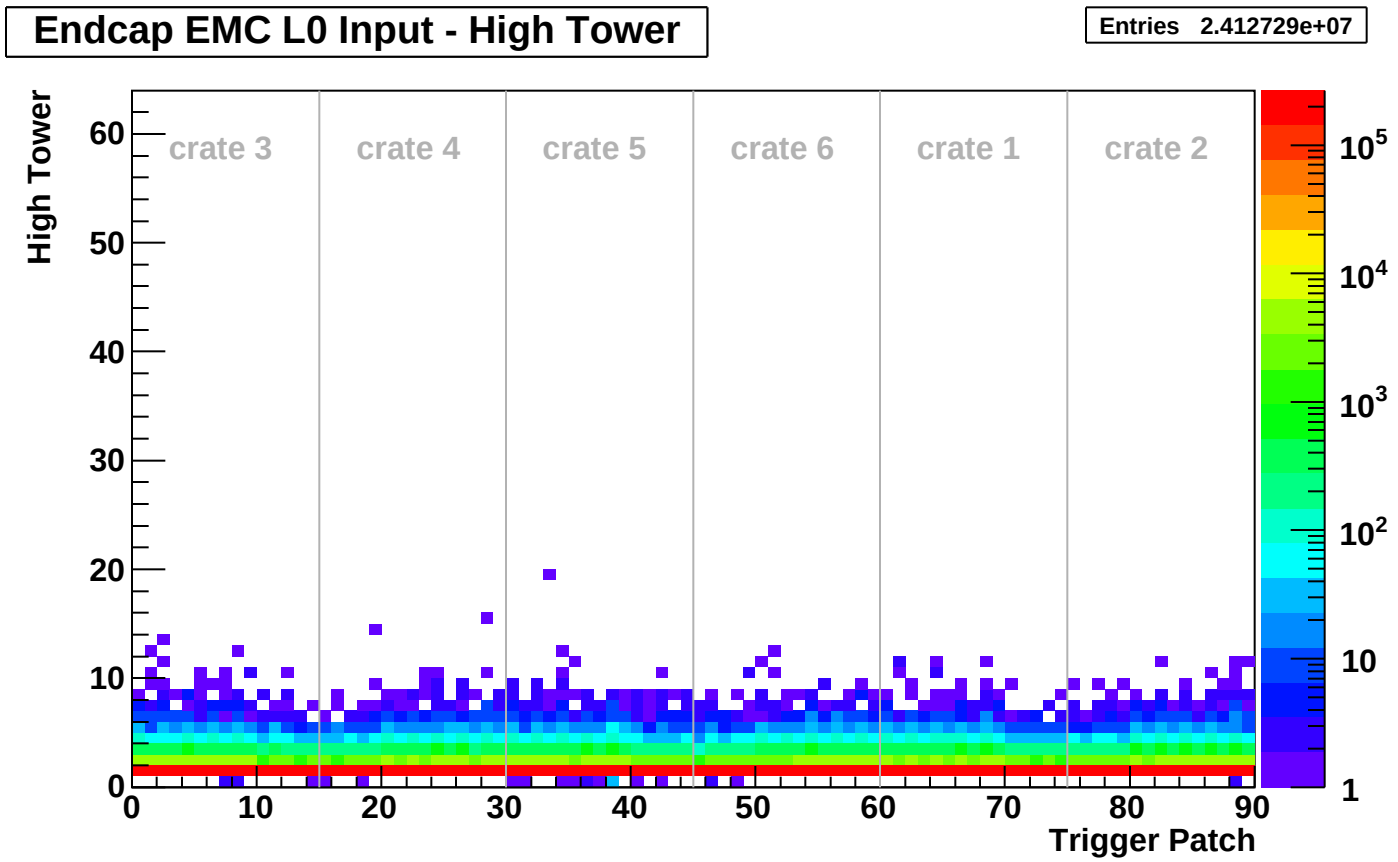

BunchId7Bit (all events)
Entries 268081

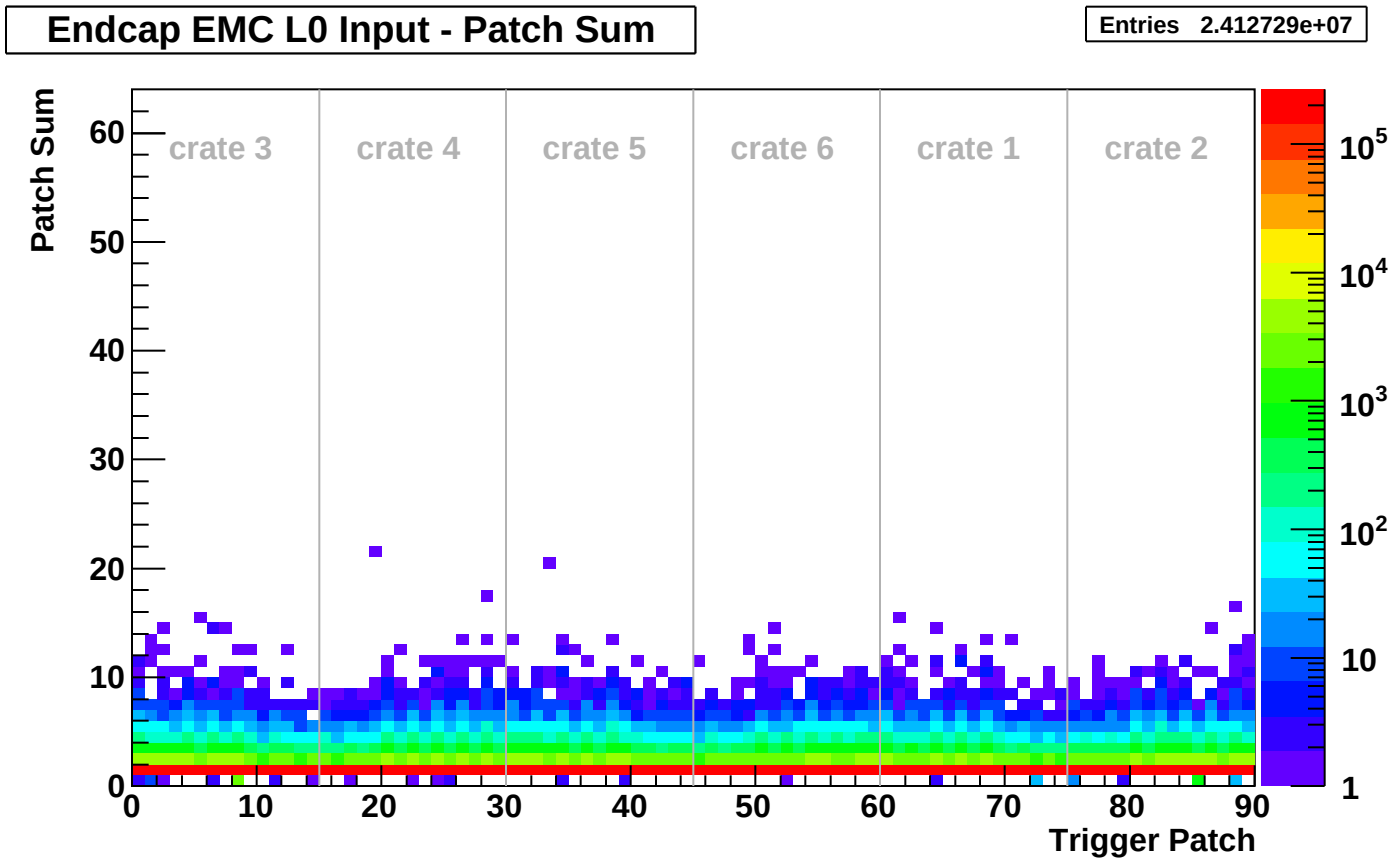

BunchId7Bit (JP1)
Entries 25489

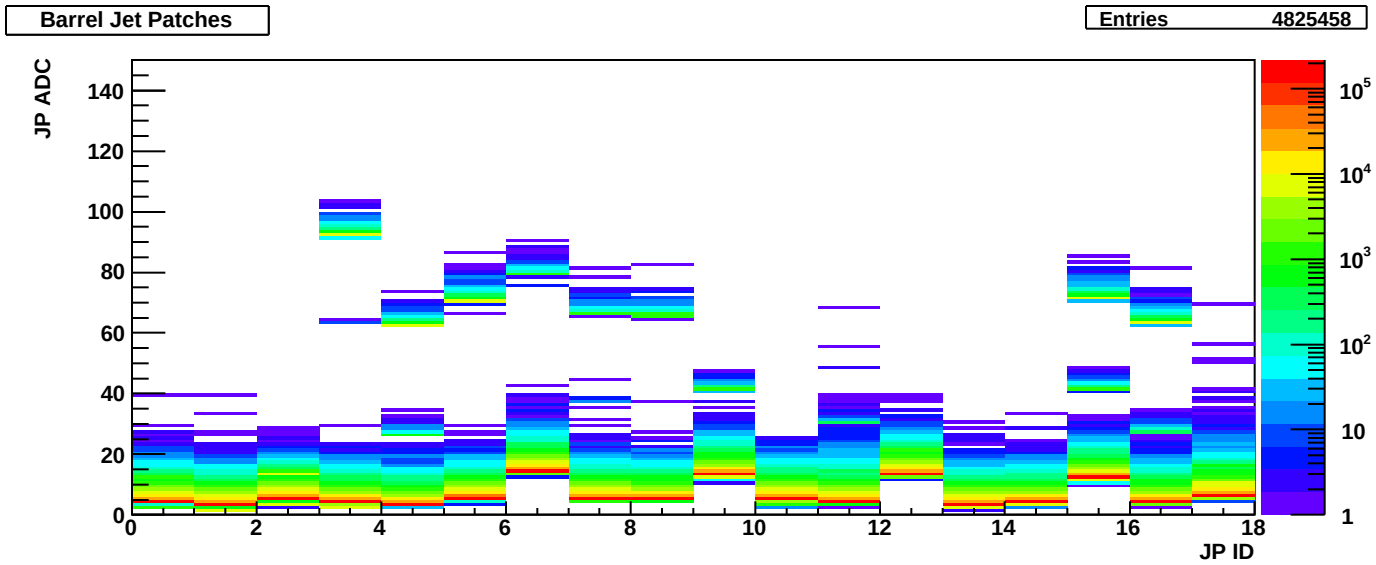

BunchId7Bit (JP2||AJP)
Entries 19171

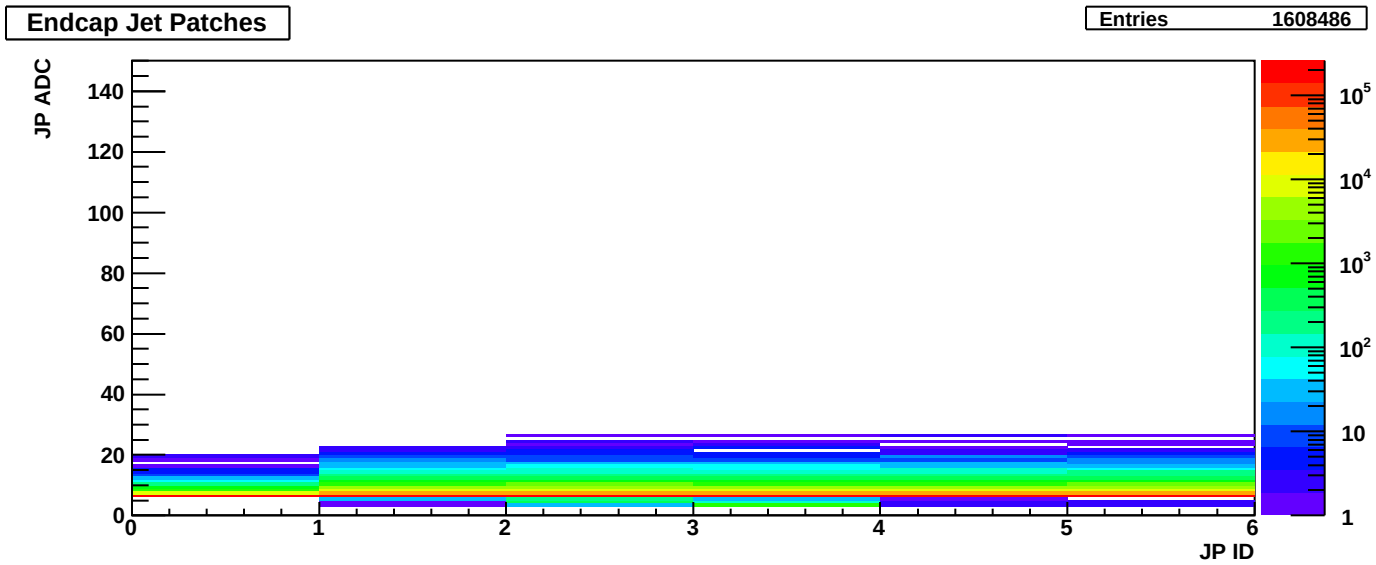

Barrel EMC L0 Input - High Tower
Entries 8.04243e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

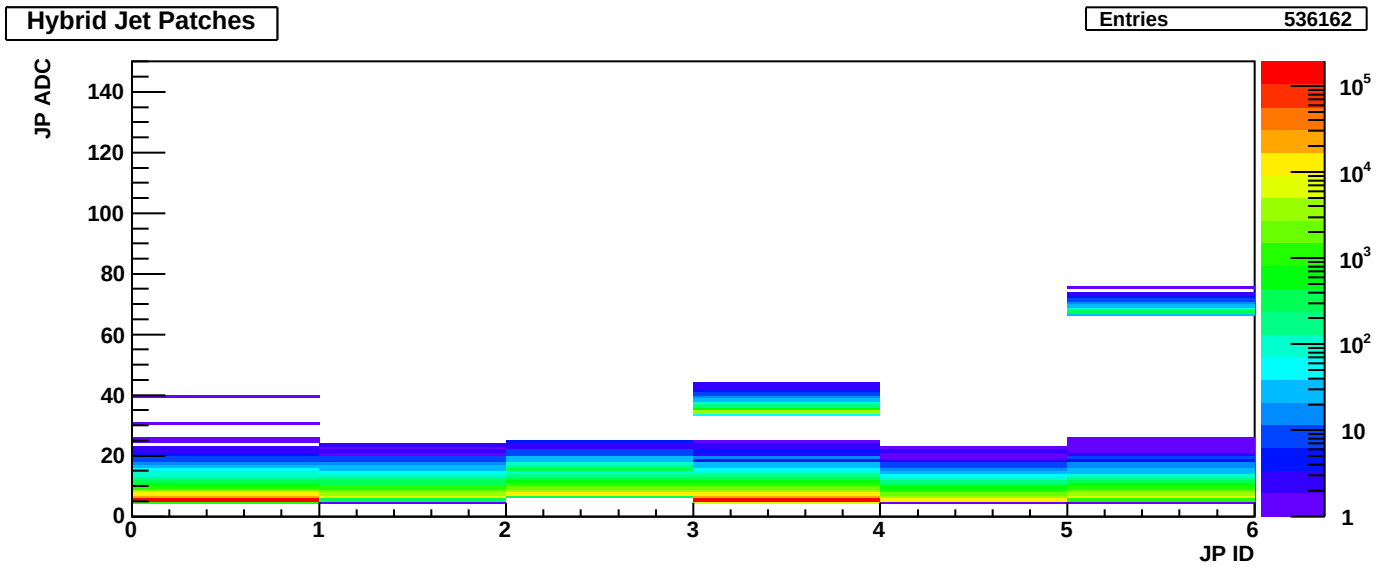

Barrel EMC L0 Input - Patch Sum
Entries 8.04243e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

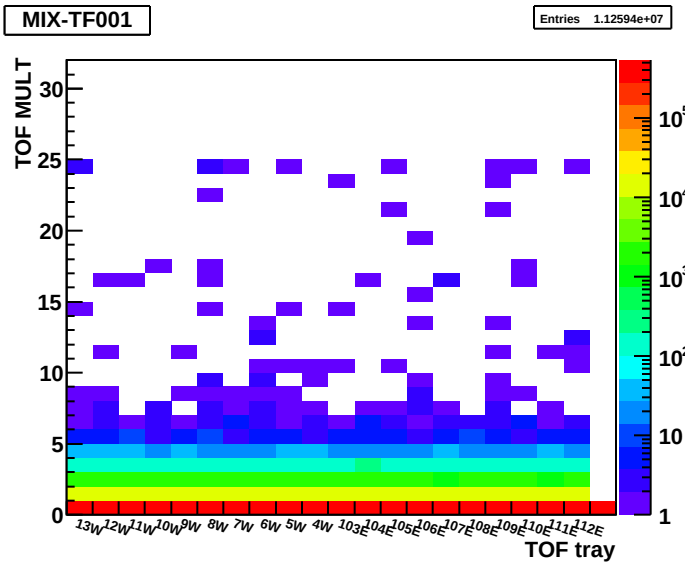

Endcap EMC L0 Input - High Tower
Entries 2.412729e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

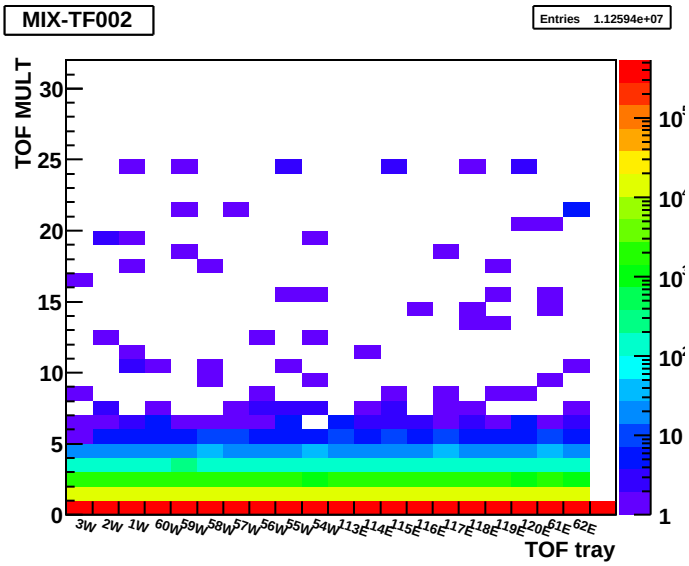

Endcap EMC L0 Input - Patch Sum
Entries 2.412729e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

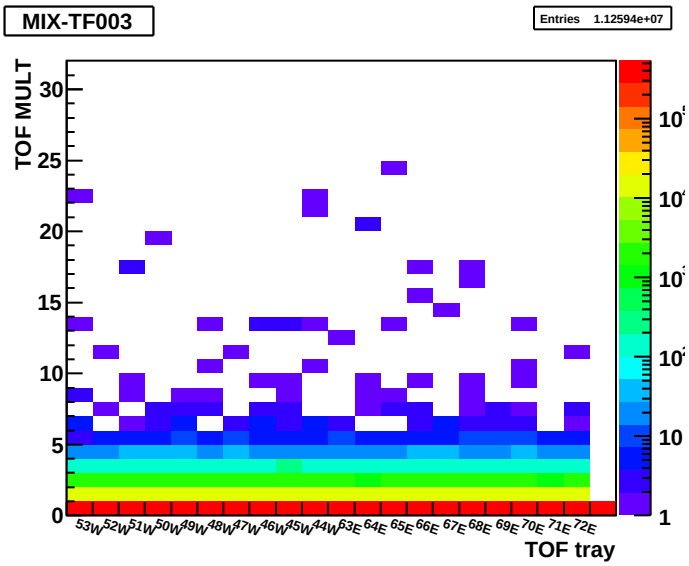

Barrel Jet Patches
Entries 4825458
JP ADC
JP ID

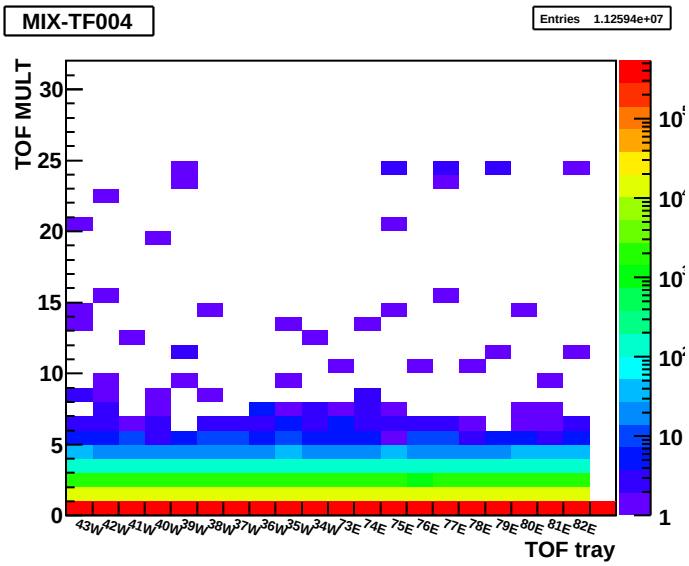

Endcap Jet Patches
Entries 1608486
JP ADC
JP ID

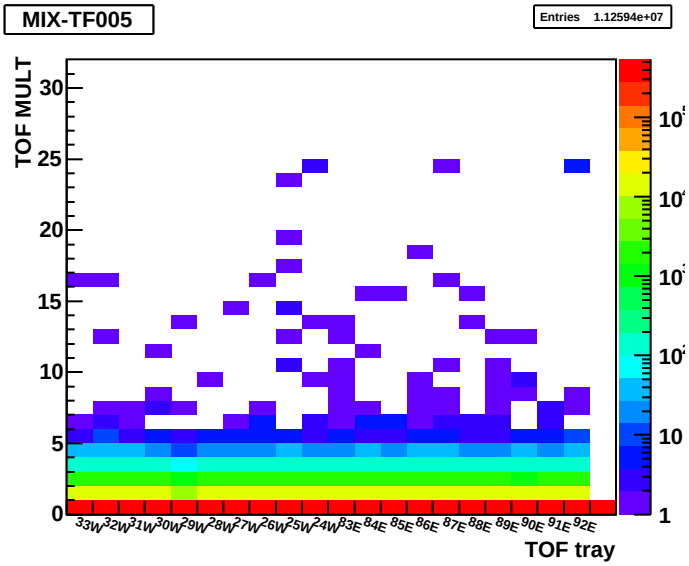

Hybrid Jet Patches
Entries 536162
JP ADC
JP ID

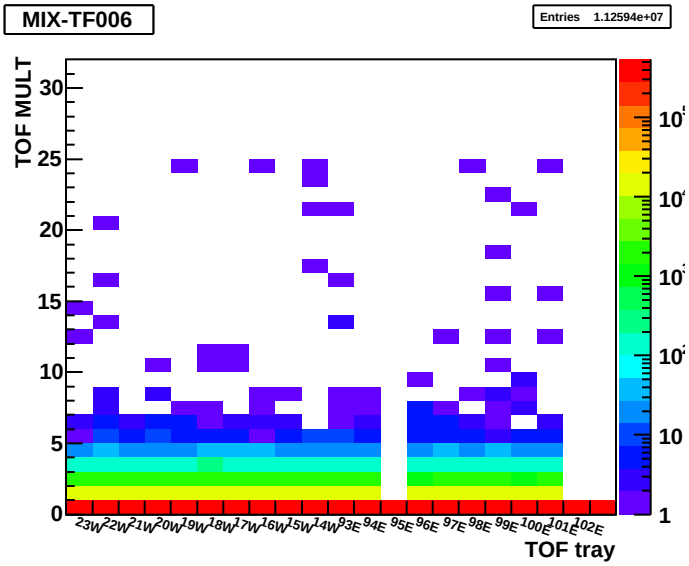

MIX-TF001
Entries 1.12594e+07
TOF MULT
TOF tray

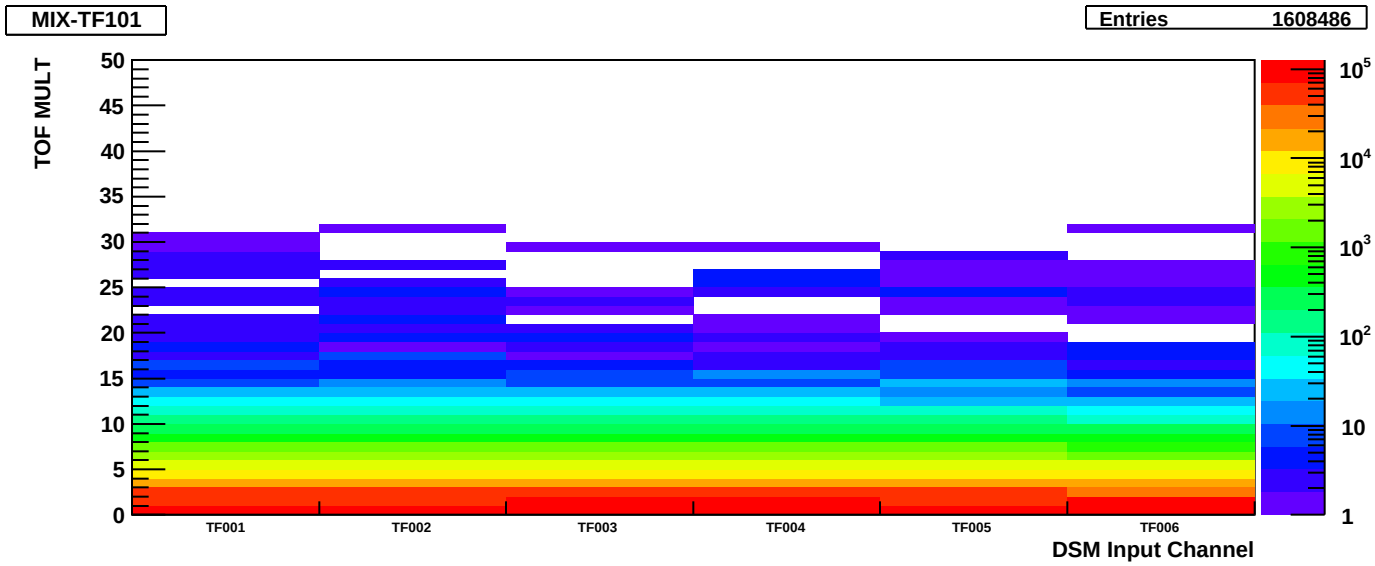

MIX-TF002
Entries 1.12594e+07
TOF MULT
TOF tray

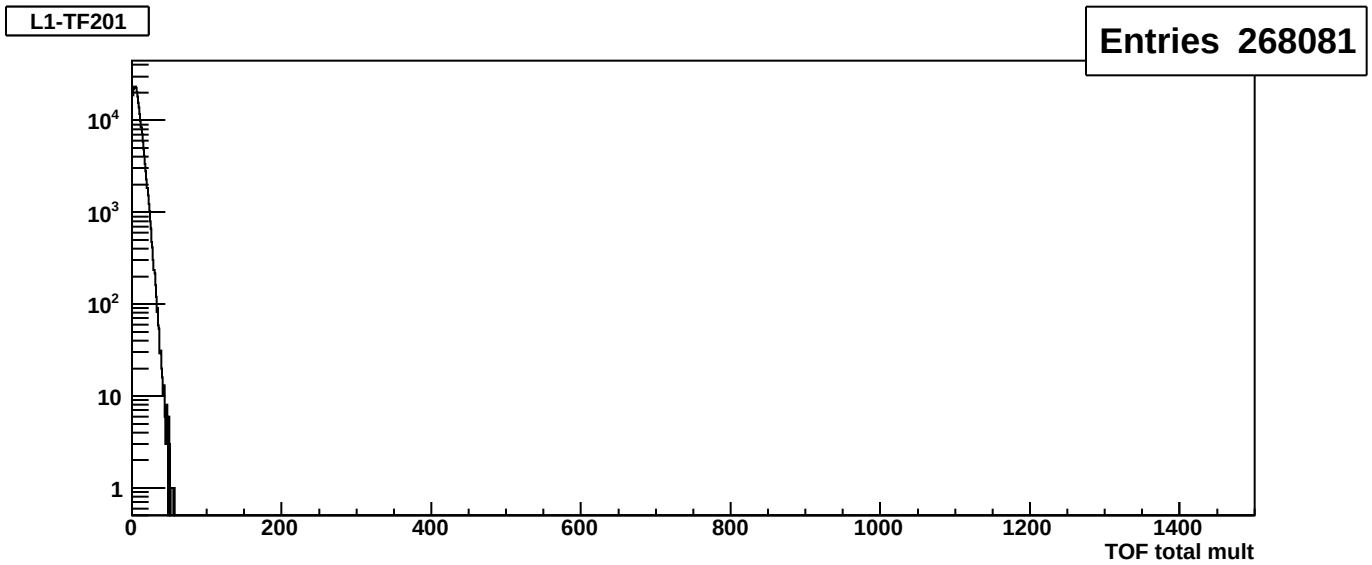

MIX-TF003
Entries 1.12594e+07
TOF MULT
TOF tray

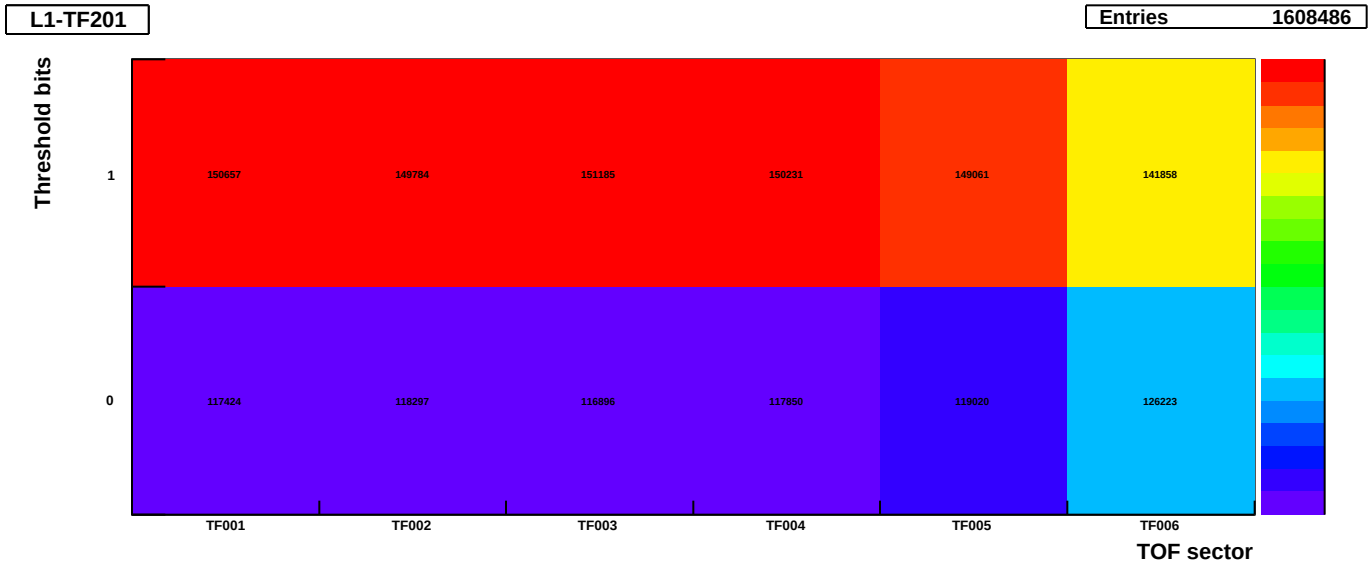

MIX-TF004
Entries 1.12594e+07
TOF MULT
TOF tray

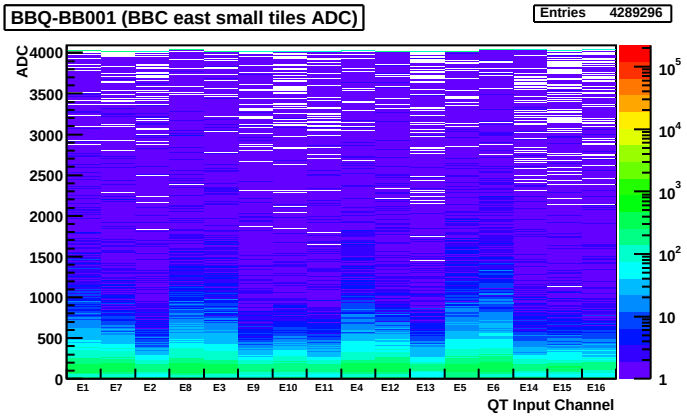

MIX-TF005
Entries 1.12594e+07
TOF MULT
TOF tray

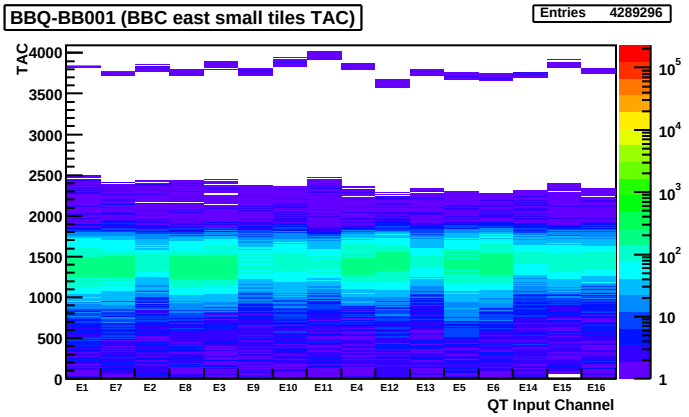

MIX-TF006
Entries 1.12594e+07
TOF MULT
TOF tray

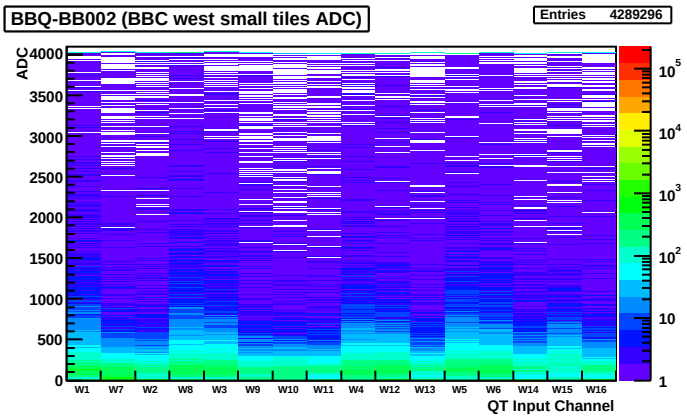

MIX-TF101
Entries 1608486
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

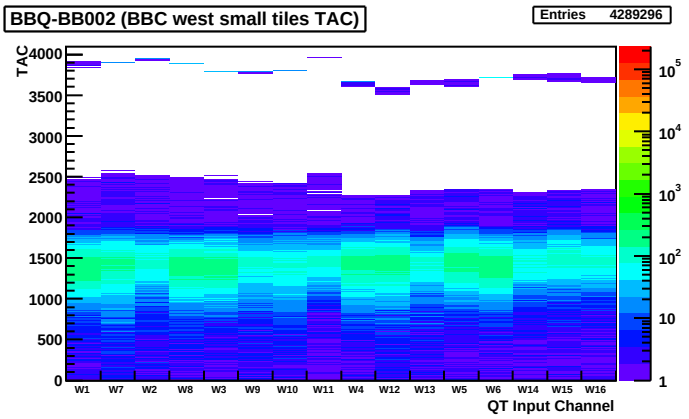

L1-TF201
Entries 268081
TOF total mult

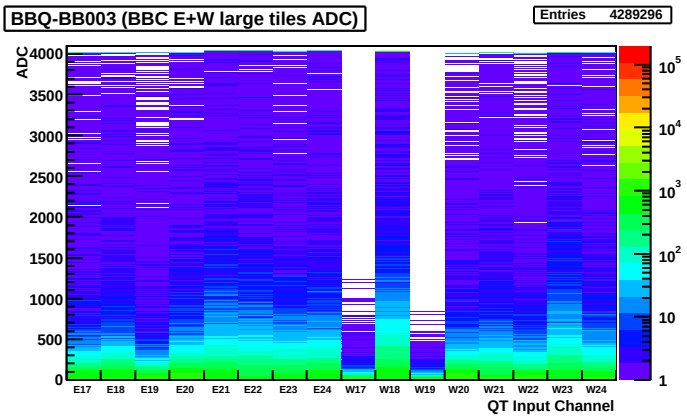

L1-TF201
Entries 1608486
Threshold bits
150657
149784
151185
150231
149061
141858
117424
118297
116896
117850
119020
126223
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

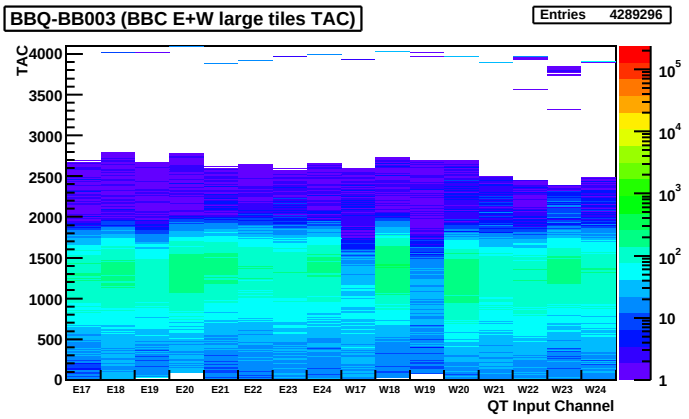

BBQ-BB001 (BBC east small tiles ADC)
Entries 4289296
ADC
QT Input Channel

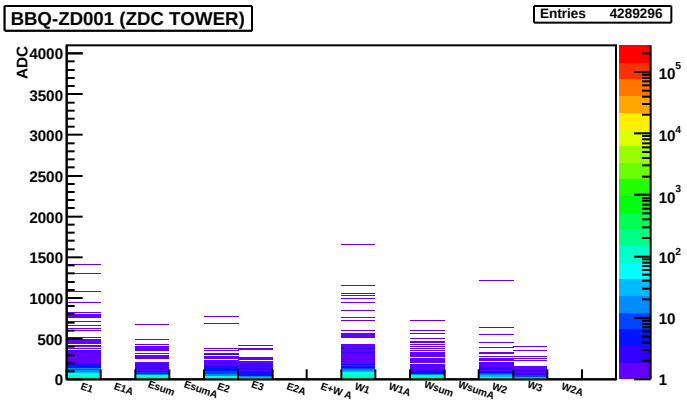

BBQ-BB001 (BBC east small tiles TAC)
Entries 4289296
TAC
QT Input Channel

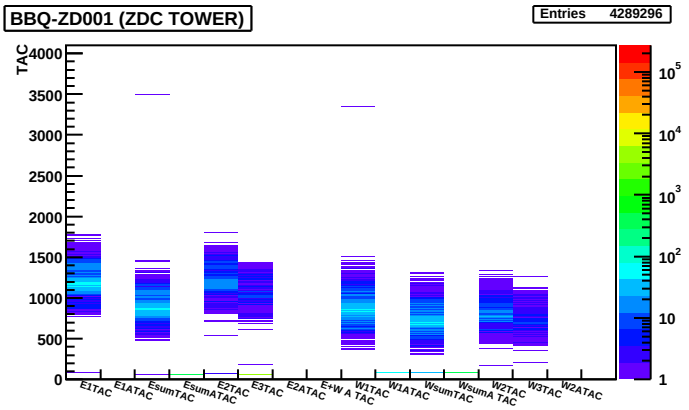

BBQ-BB002 (BBC west small tiles ADC)
Entries 4289296
ADC
QT Input Channel

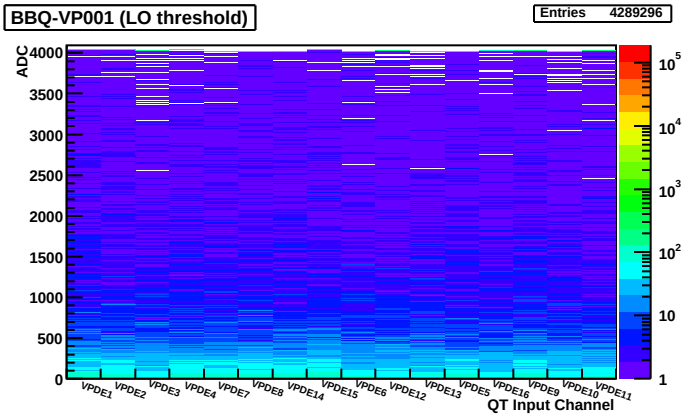

BBQ-BB002 (BBC west small tiles TAC)
Entries 4289296
TAC
QT Input Channel

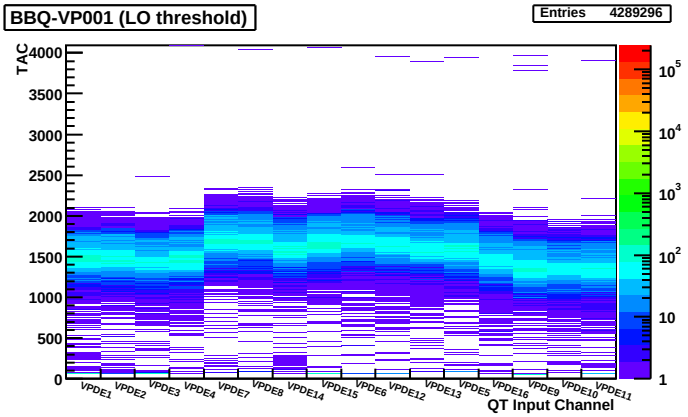

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4289296
ADC
QT Input Channel

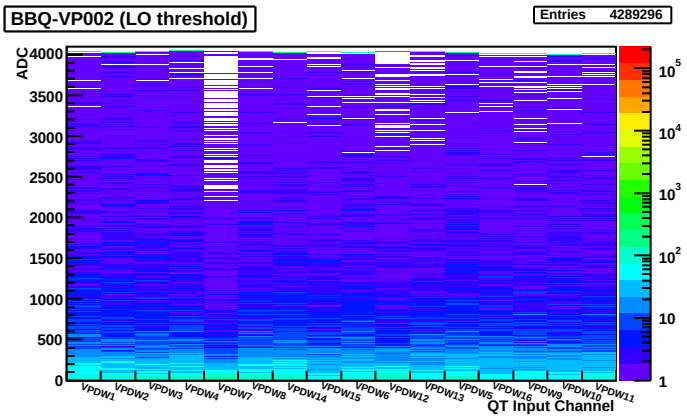

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4289296
TAC
QT Input Channel

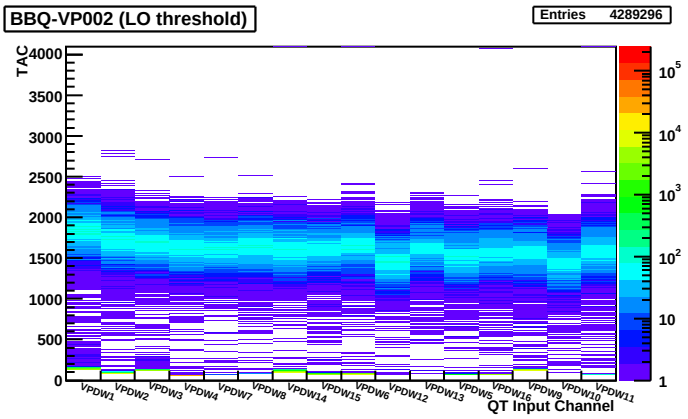

BBQ-ZD001 (ZDC TOWER)
Entries 4289296
ADC

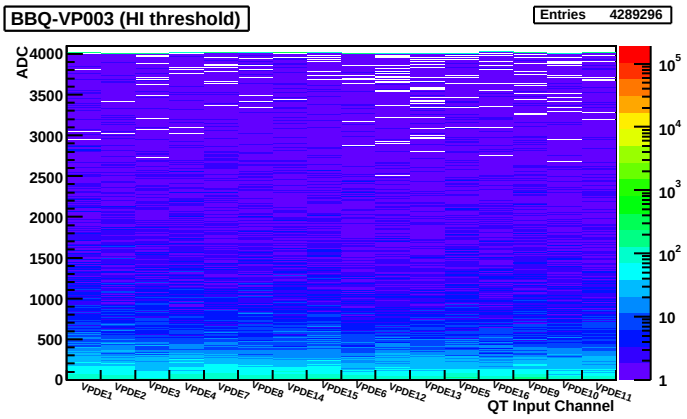

BBQ-ZD001 (ZDC TOWER)
Entries 4289296
TAC

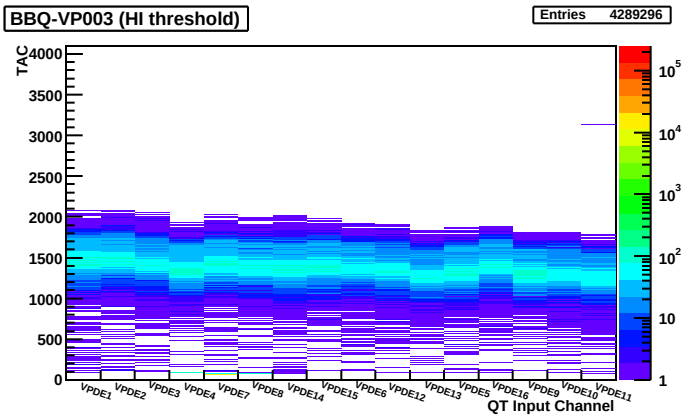

BBQ-VP001 (LO threshold)
Entries 4289296
ADC
QT Input Channel

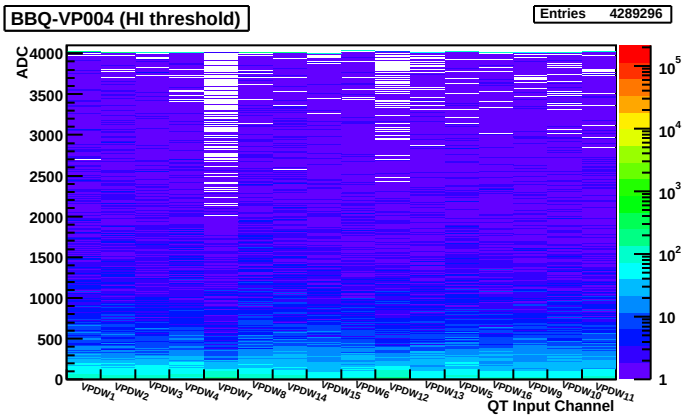

BBQ-VP001 (LO threshold)
Entries 4289296
TAC
QT Input Channel

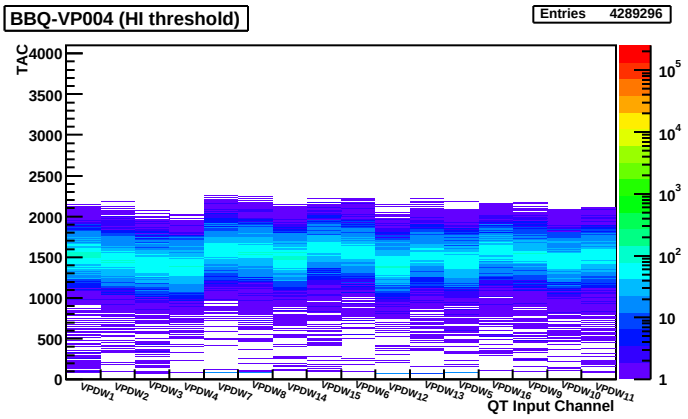

BBQ-VP002 (LO threshold)
Entries 4289296
ADC
QT Input Channel

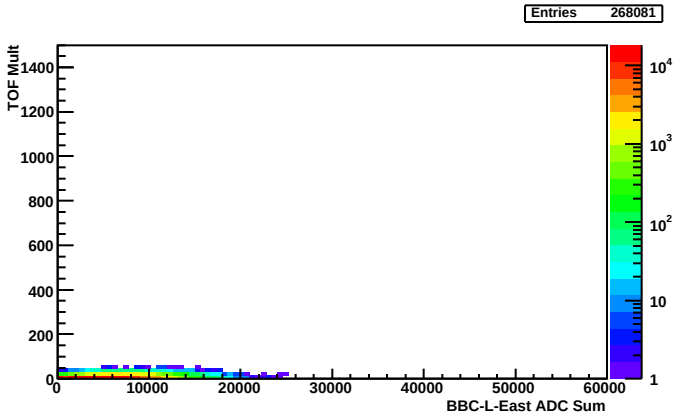

BBQ-VP002 (LO threshold)
Entries 4289296
TAC
QT Input Channel

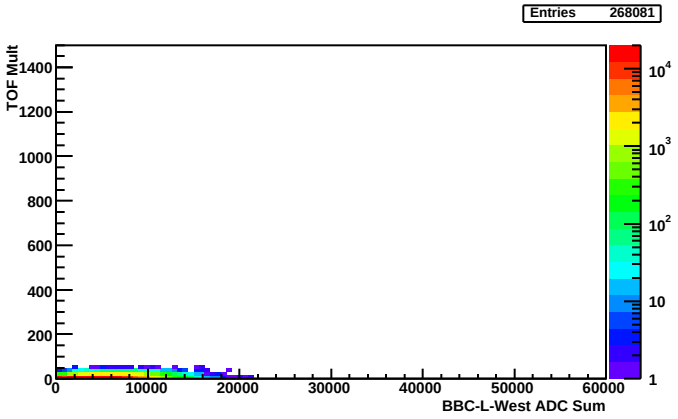

BBQ-VP003 (HI threshold)
Entries 4289296
ADC
QT Input Channel

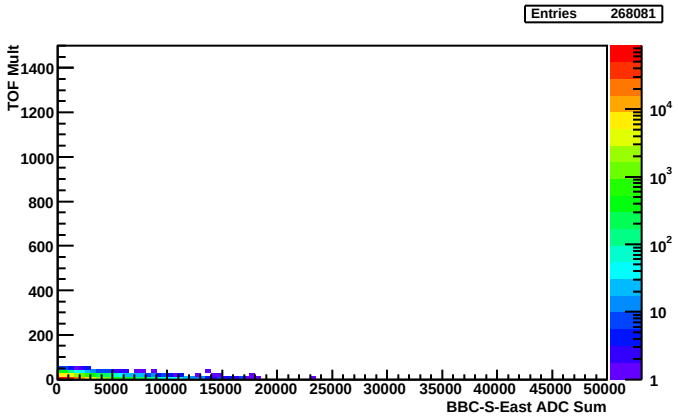

BBQ-VP003 (HI threshold)
Entries 4289296
TAC
QT Input Channel

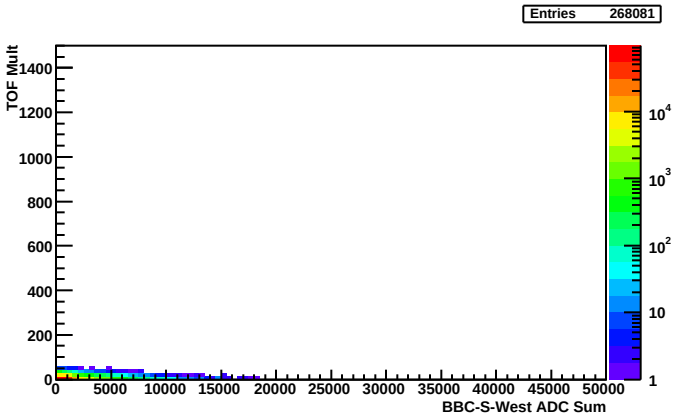

BBQ-VP004 (HI threshold)
Entries 4289296
ADC
QT Input Channel

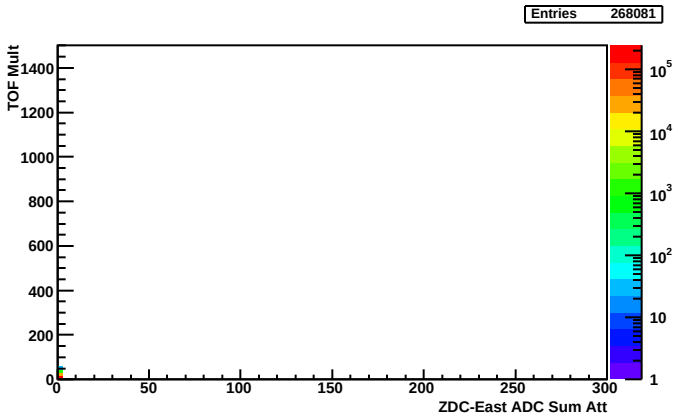

BBQ-VP004 (HI threshold)
Entries 4289296
TAC
QT Input Channel

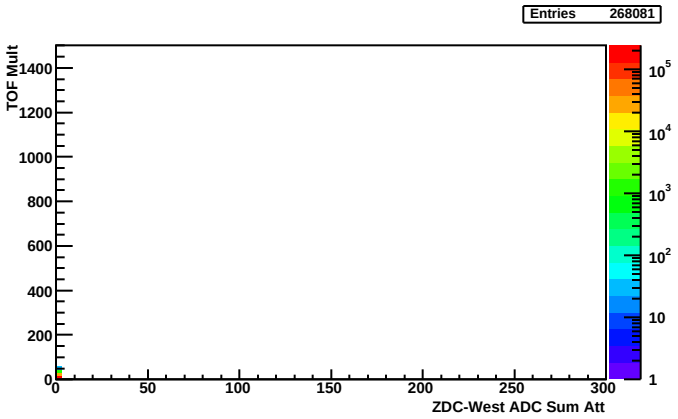

Entries 268081
TOF Mult
BBC-L-East ADC Sum

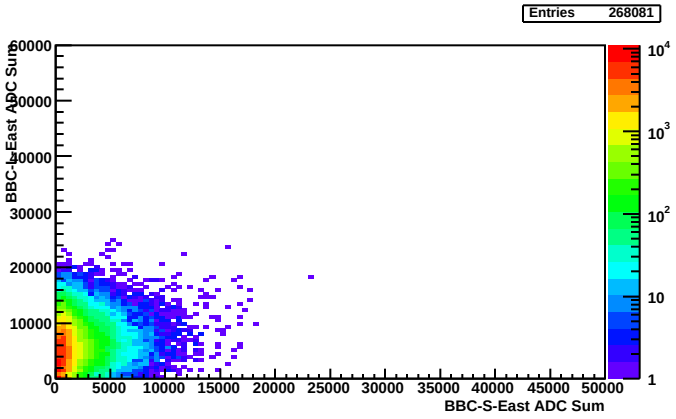

Entries 268081
TOF Mult
BBC-L-West ADC Sum

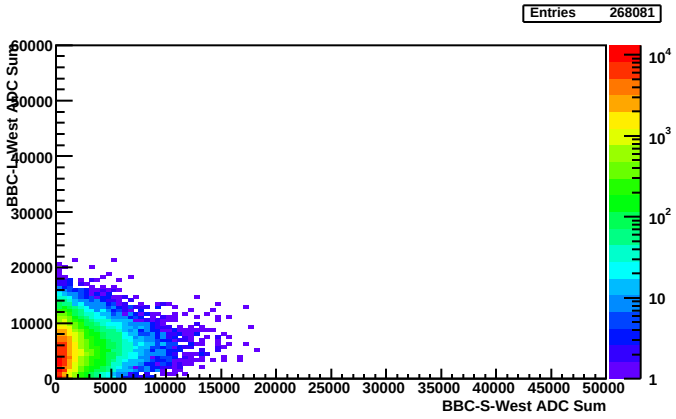

Entries 268081
TOF Mult
BBC-S-East ADC Sum

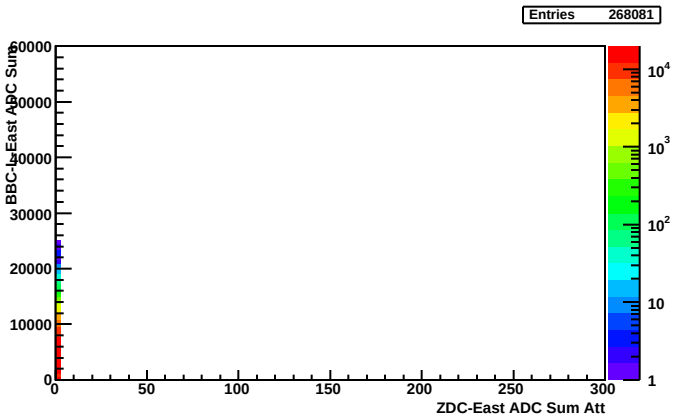

Entries 268081
TOF Mult
BBC-S-West ADC Sum

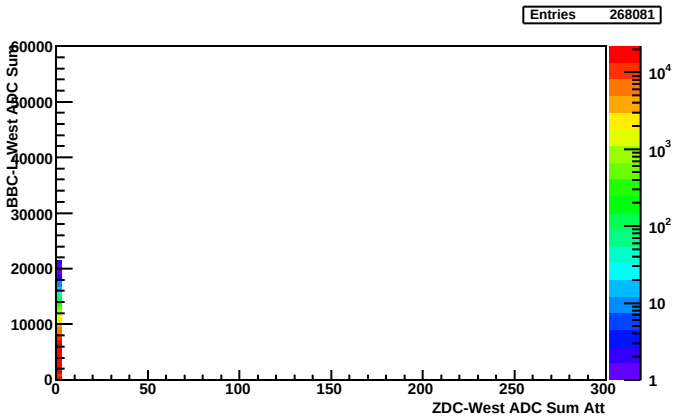

Entries 268081
TOF Mult
ZDC-East ADC Sum Att

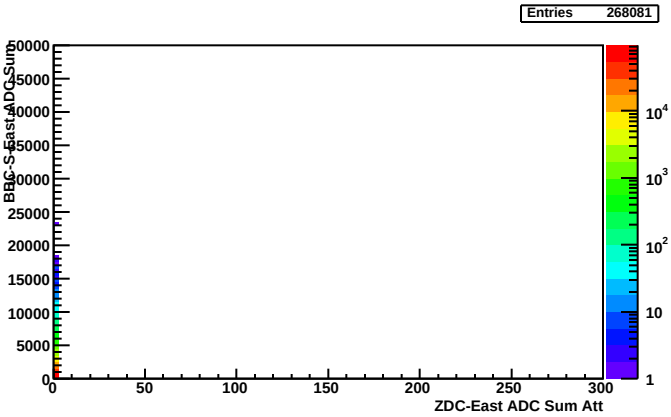

Entries 268081
TOF Mult
ZDC-West ADC Sum Att

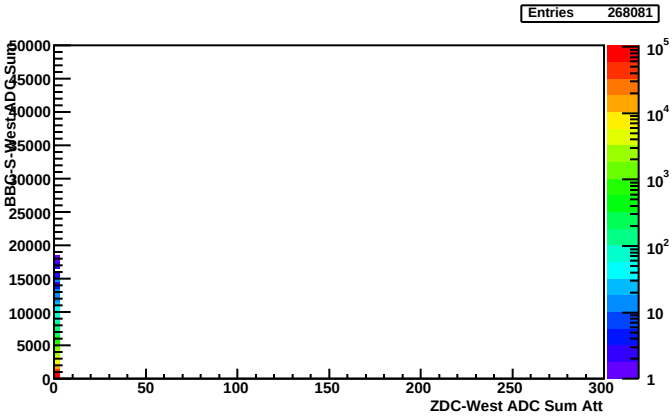

Entries 268081
BBC-L-East ADC Sum
BBC-S-East ADC Sum

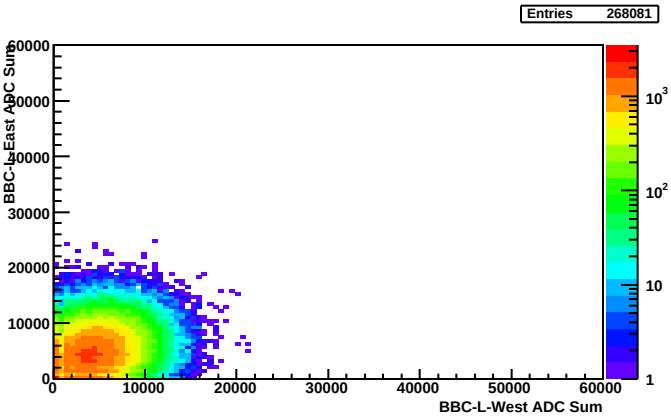

Entries 268081
BBC-L-West ADC Sum
BBC-S-West ADC Sum

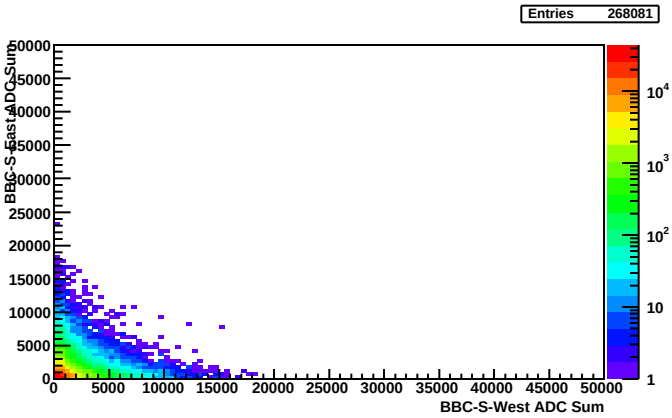

Entries 268081
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

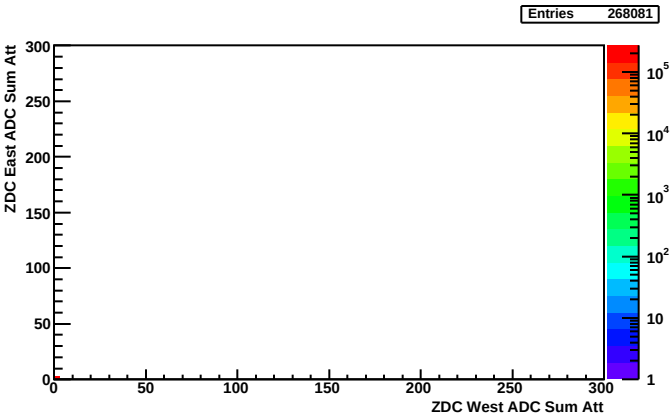

Entries 268081
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

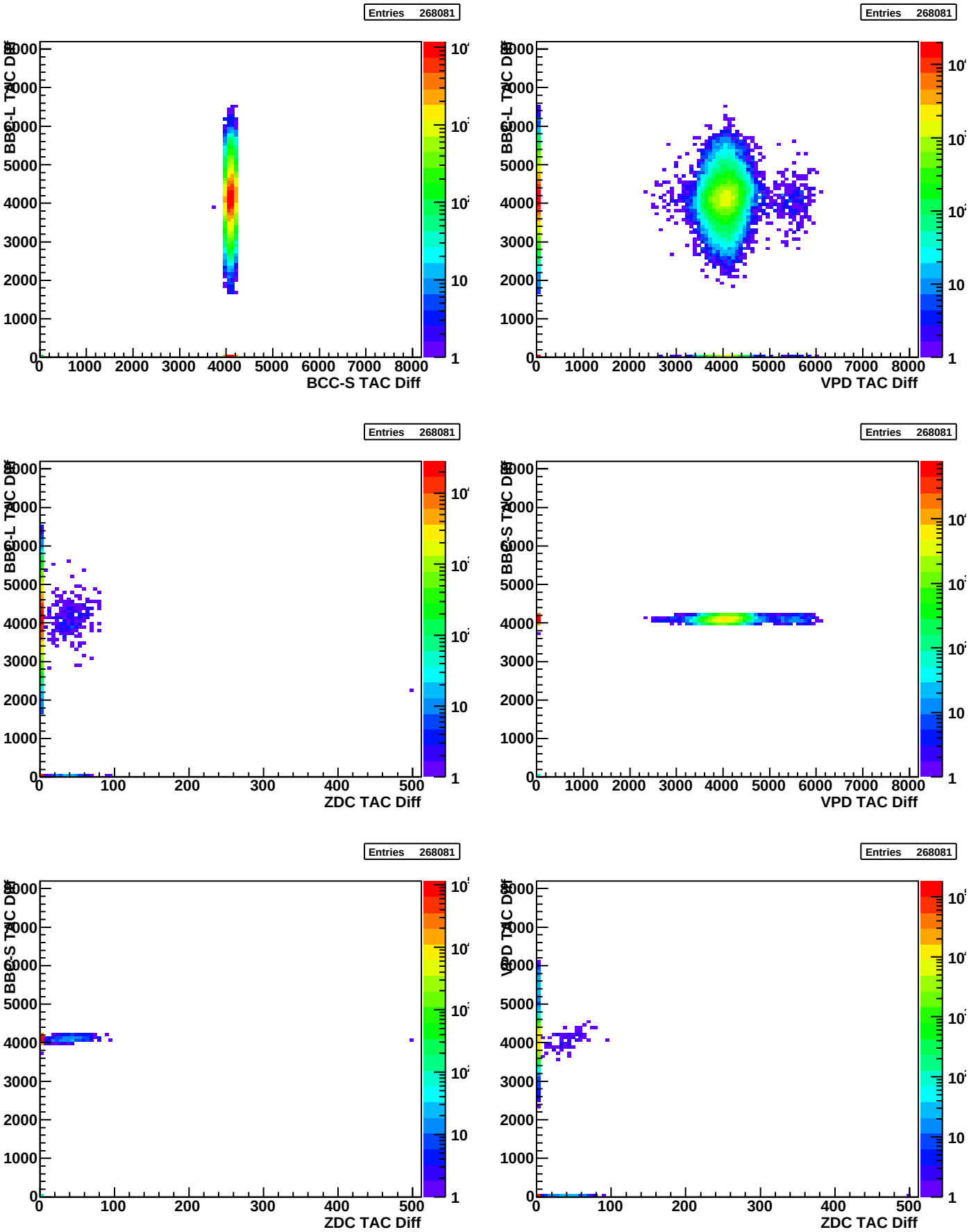

Entries 268081
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

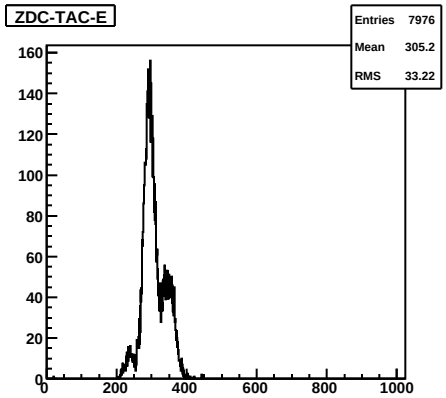

Entries 268081
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

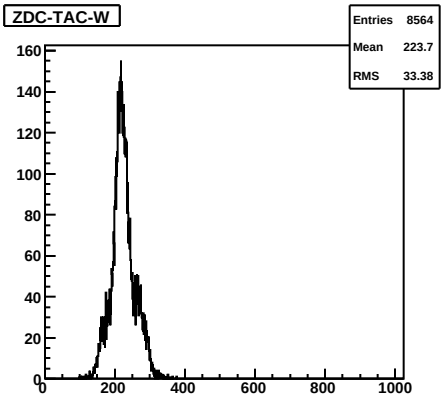

Entries 268081
BBC-L-East ADC Sum
BBC-L-West ADC Sum

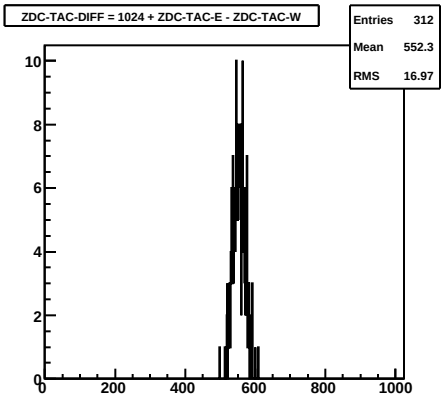

Entries 268081
BBC-S-East ADC Sum
BBC-S-West ADC Sum

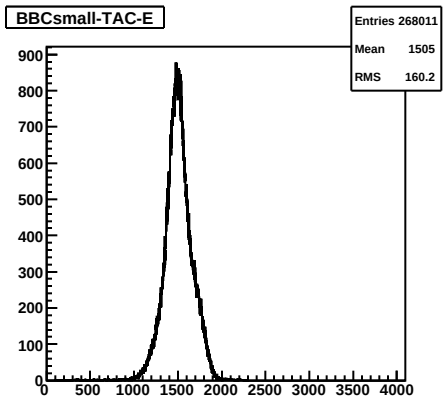

Entries 268081
ZDC East ADC Sum Att
ZDC West ADC Sum Att

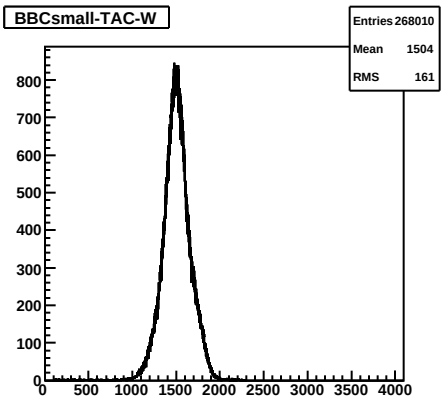

Entries 268081
BBC-L TAC Diff
BCC-S TAC Diff
Entries 268081
BBC-L TAC Diff
VPD TAC Diff
Entries 268081
BBC-L TAC Diff
ZDC TAC Diff
Entries 268081
BBC-S TAC Diff
VPD TAC Diff
Entries 268081
BBC-S TAC Diff
ZDC TAC Diff
Entries 268081
VPD TAC Diff
ZDC TAC Diff

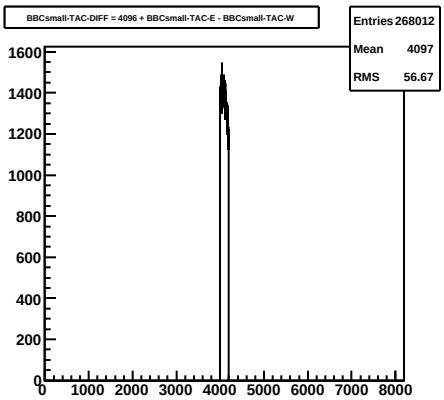

ZDC-TAC-E
Entries 7976
Mean 305.2
RMS 33.22

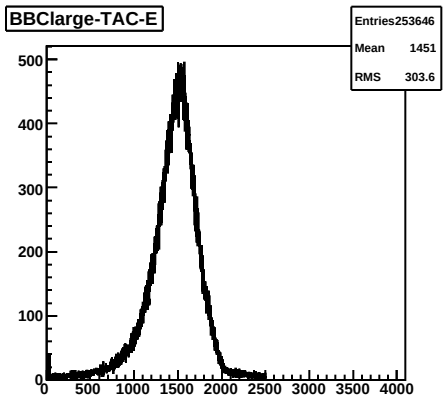

ZDC-TAC-W
Entries 8564
Mean 223.7
RMS 33.38

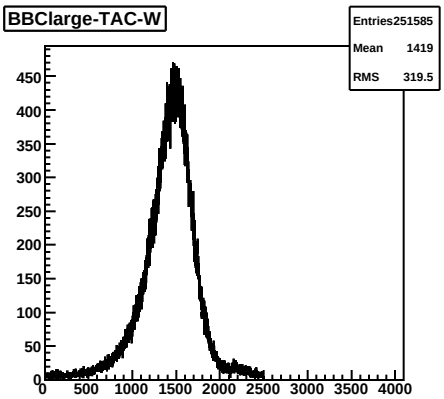

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 312
Mean 552.3
RMS 16.97

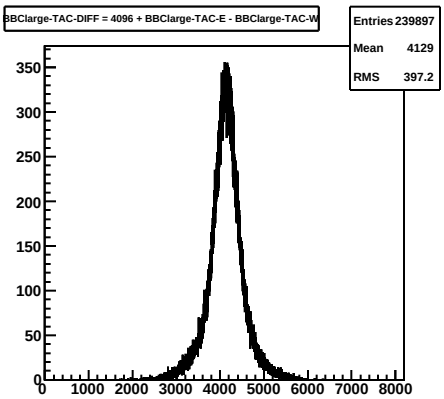

BBCsmall-TAC-E
Entries 268011
Mean 1505
RMS 160.2

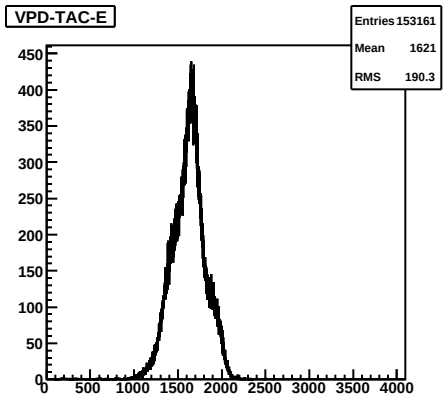

BBCsmall-TAC-W
Entries 268010
Mean 1504
RMS 161

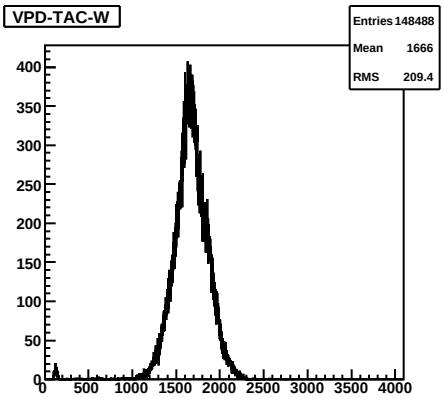

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 268012
Mean 4097
RMS 56.67

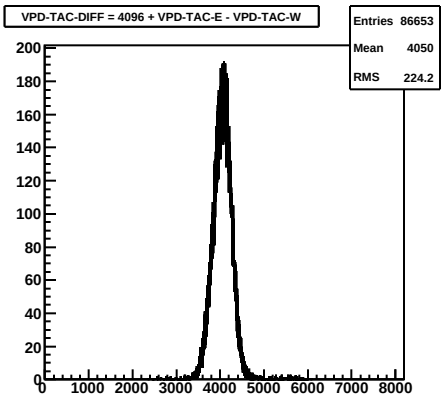

BBClarge-TAC-E
Entries 253646
Mean 1451
RMS 303.6

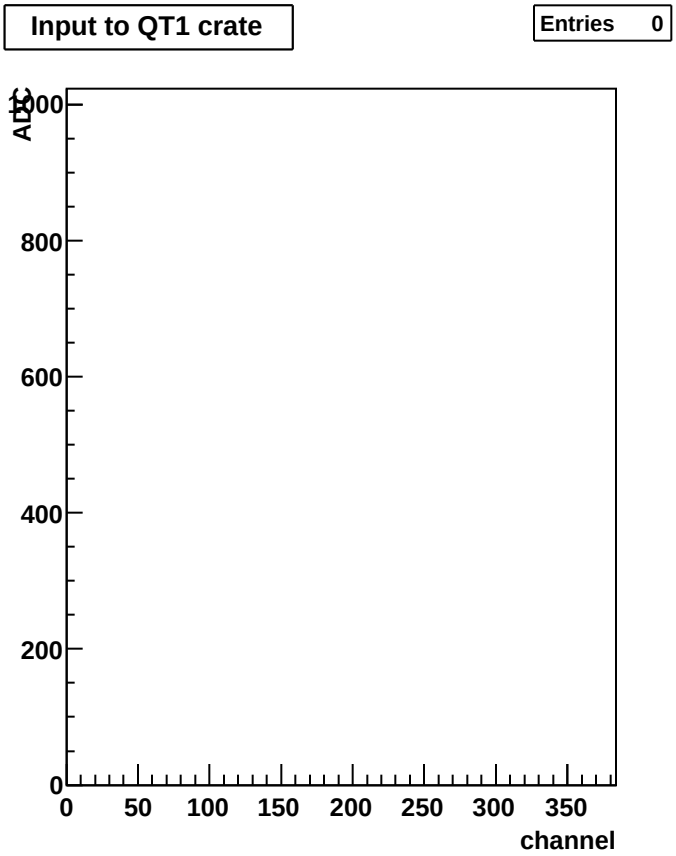

BBClarge-TAC-W
Entries 251585
Mean 1419
RMS 319.5

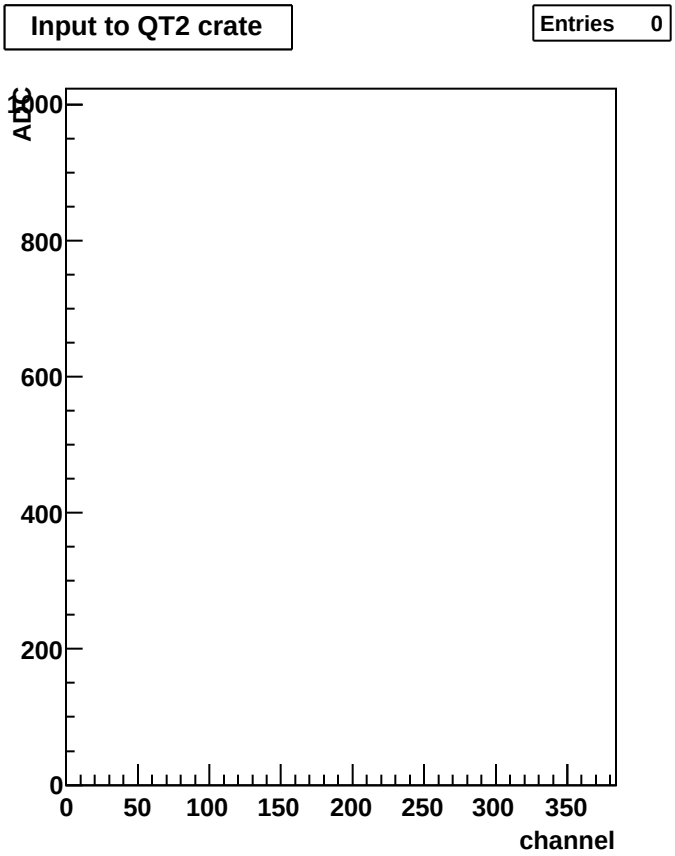

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 239897
Mean 4129
RMS 397.2

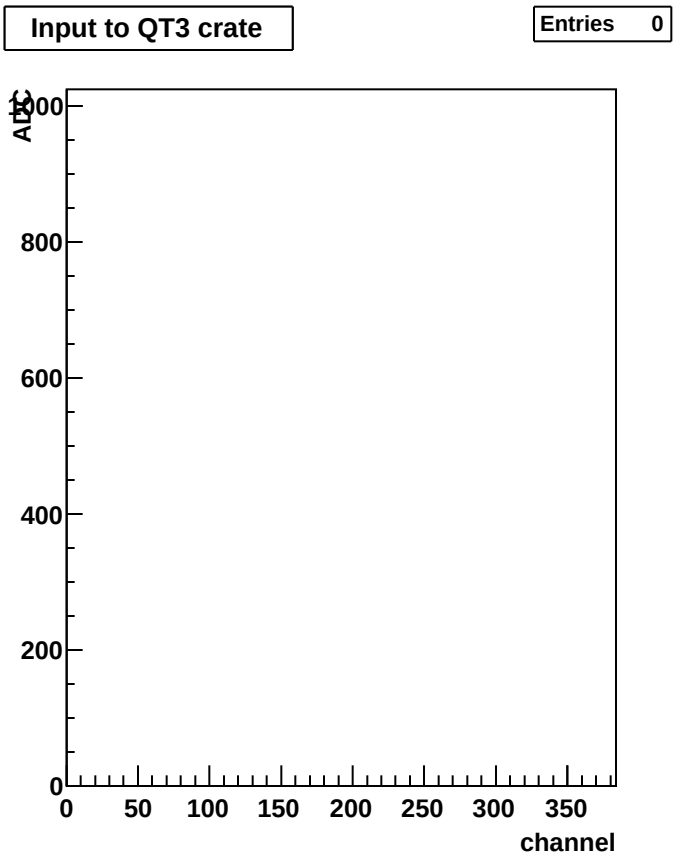

VPD-TAC-E
Entries 153161
Mean 1621
RMS 190.3

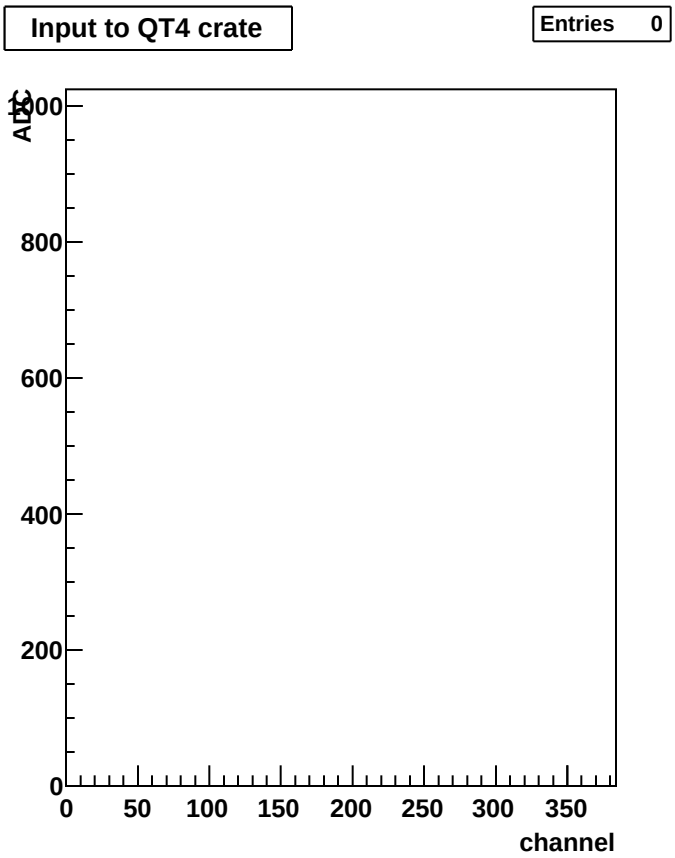

VPD-TAC-W
Entries 148488
Mean 1666
RMS 209.4

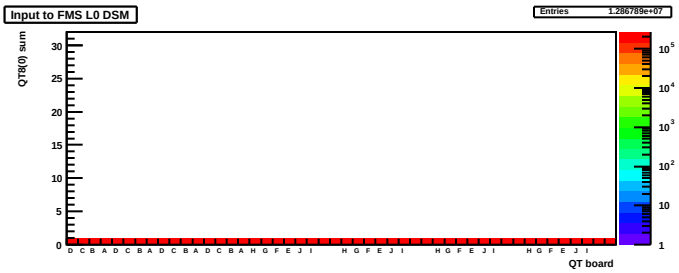

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 86653
Mean 4050
RMS 224.2

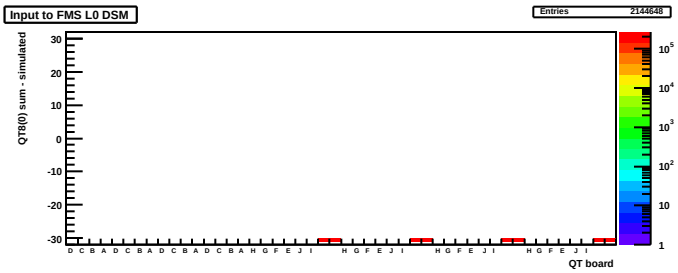

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

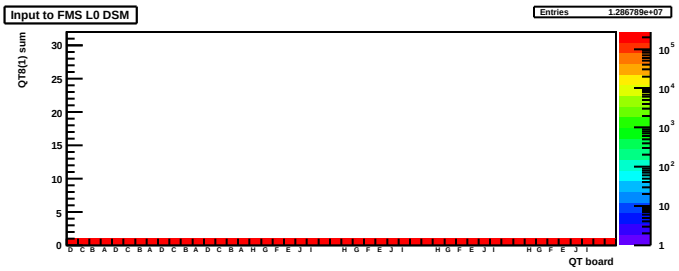

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

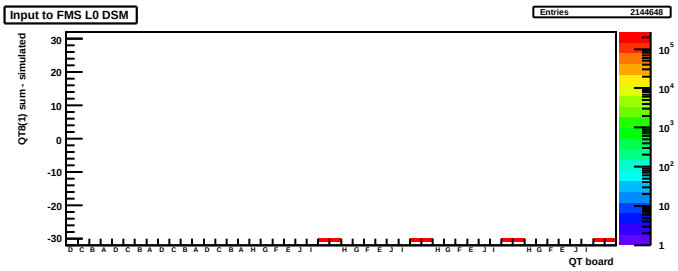

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

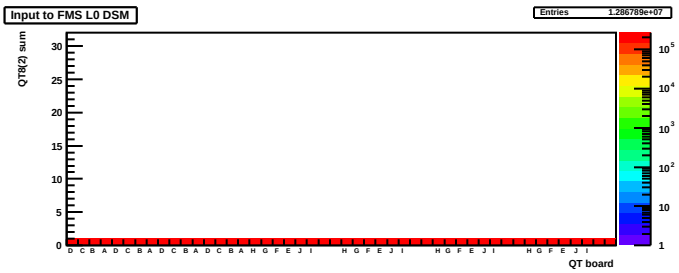

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

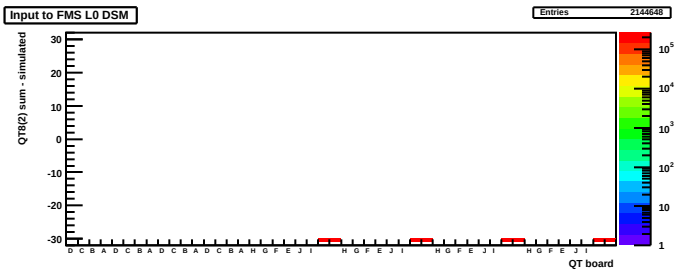

Input to FMS L0 DSM
Entries 1.286789e+07
QT8(0) sum
QT board

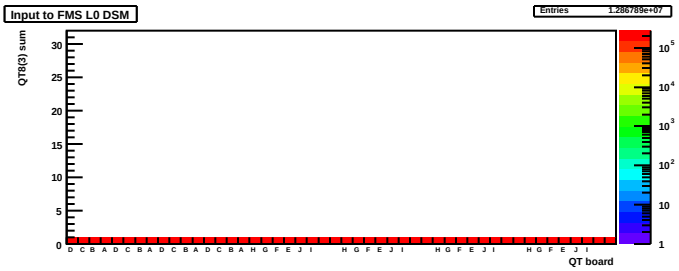

Input to FMS L0 DSM
Entries 2144648
QT8(0) sum - simulated
QT board

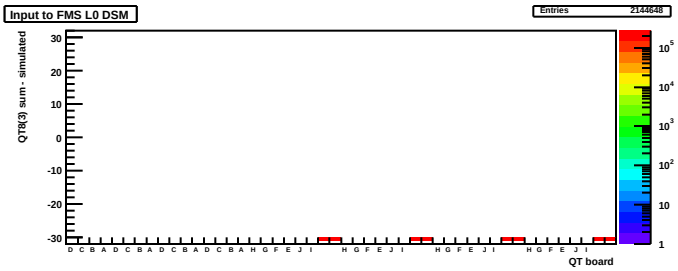

Input to FMS L0 DSM
Entries 1.286789e+07
QT8(1) sum
QT board

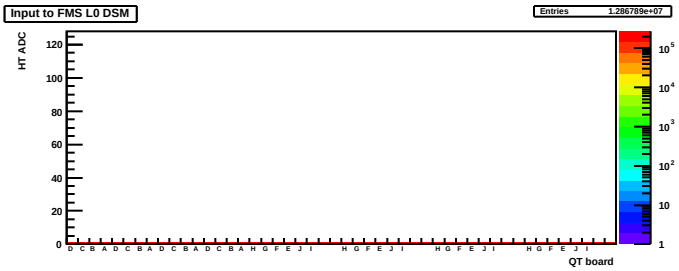

Input to FMS L0 DSM
Entries 2144648
QT8(1) sum - simulated
QT board

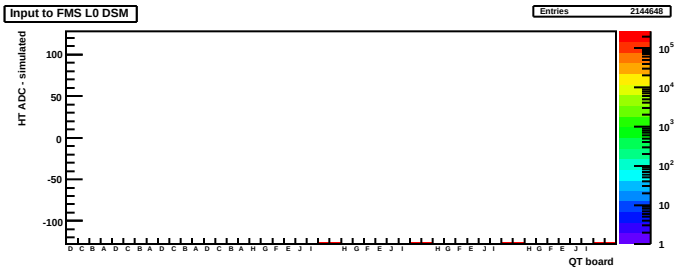

Input to FMS L0 DSM
Entries 1.286789e+07
QT8(2) sum
QT board

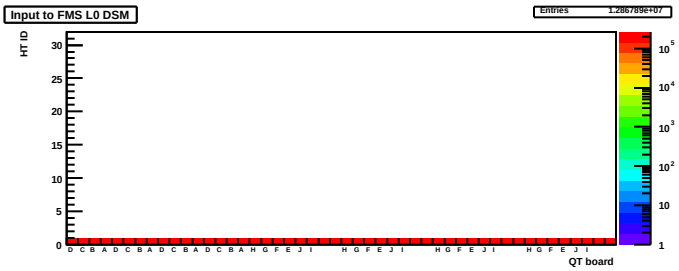

Input to FMS L0 DSM
Entries 2144648
QT8(2) sum - simulated
QT board

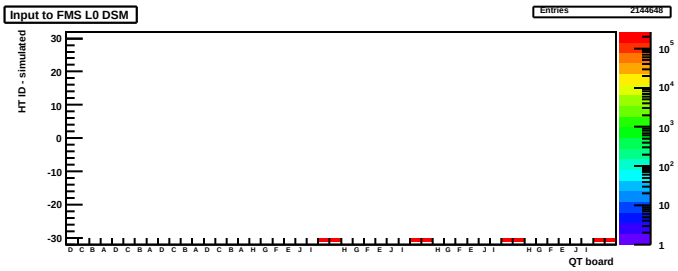

Input to FMS L0 DSM
Entries 1.286789e+07
QT8(3) sum
QT board

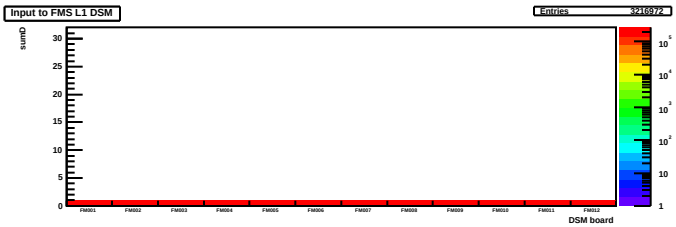

Input to FMS L0 DSM
Entries 2144648
QT8(3) sum - simulated
QT board

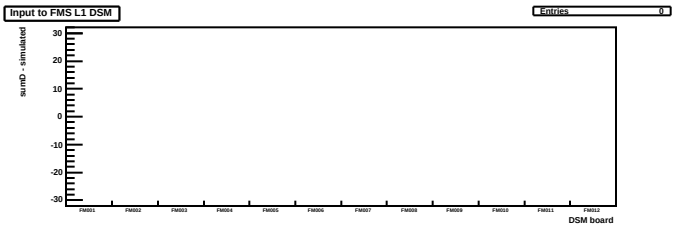

Input to FMS L0 DSM
Entries 1.286789e+07
HT ADC
QT board

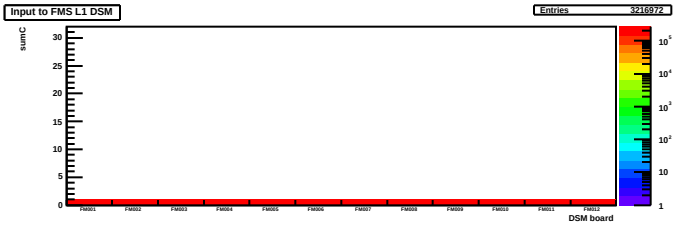

Input to FMS L0 DSM
Entries 2144648
HT ADC - simulated
QT board

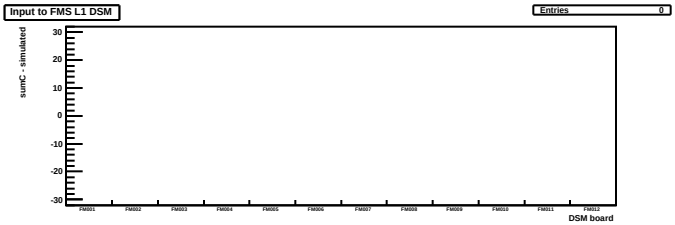

Input to FMS L0 DSM
Entries 1.286789e+07
HT ID
QT board

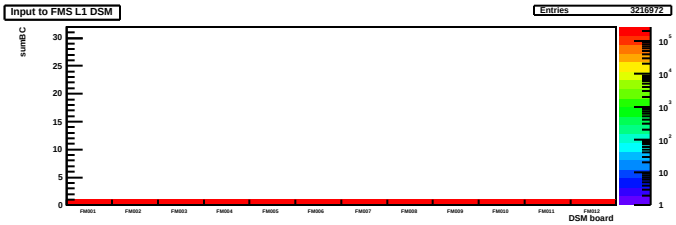

Input to FMS L0 DSM
Entries 2144648
HT ID - simulated
QT board

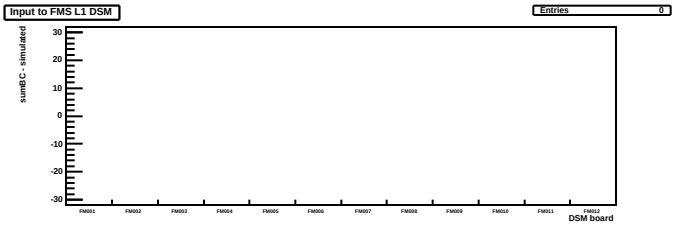

Input to FMS L1 DSM
Entries 3216972
sumD
DSM board

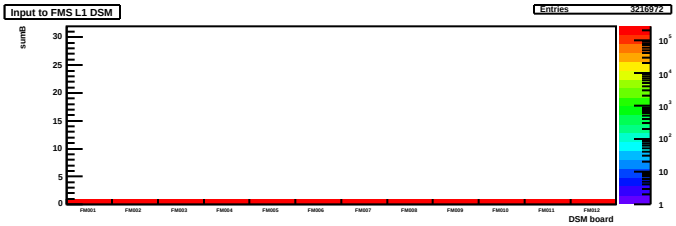

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

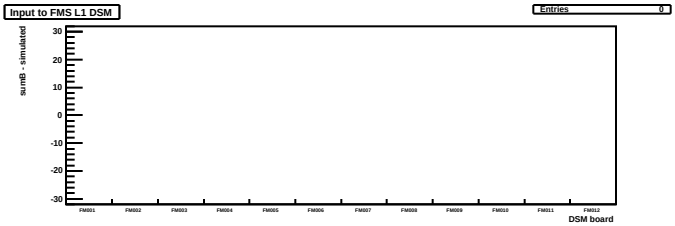

Input to FMS L1 DSM
Entries 3216972
sumC
DSM board

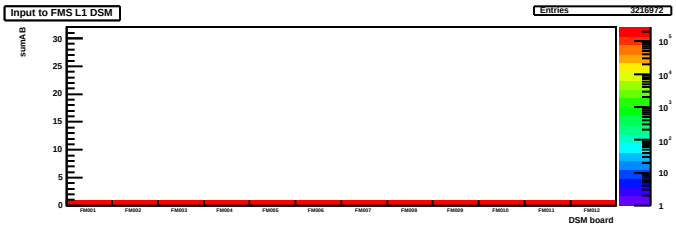

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

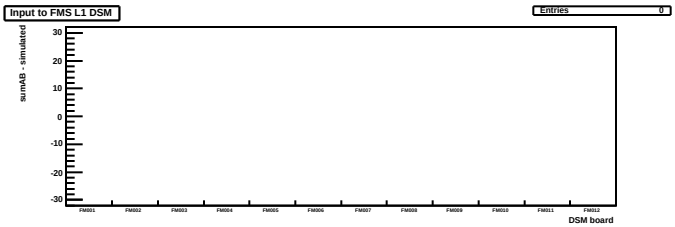

Input to FMS L1 DSM
Entries 3216972
sumBC
DSM board

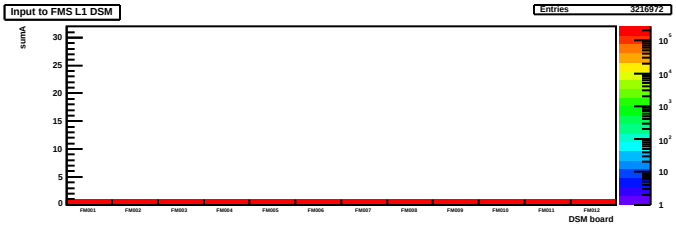

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

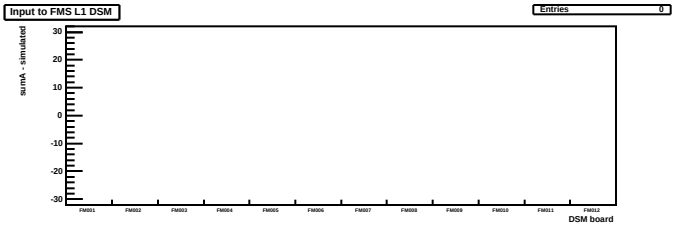

Input to FMS L1 DSM
Entries 3216972
sumB
DSM board

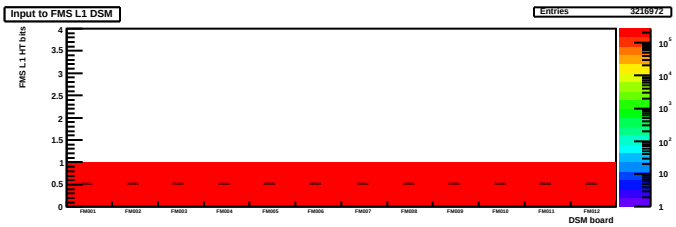

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

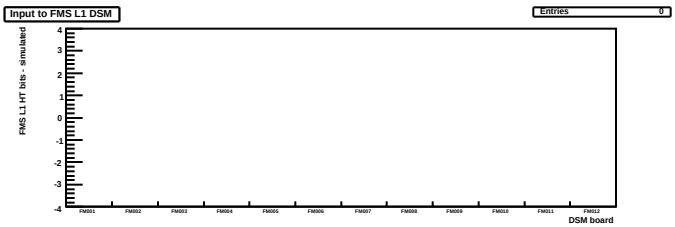

Input to FMS L1 DSM
Entries 3216972
sumAB
DSM board

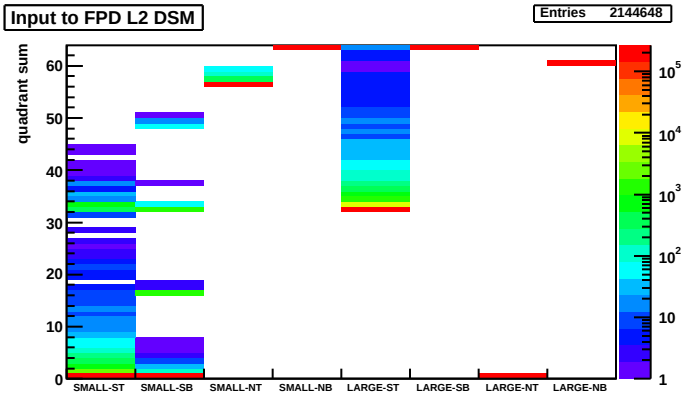

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

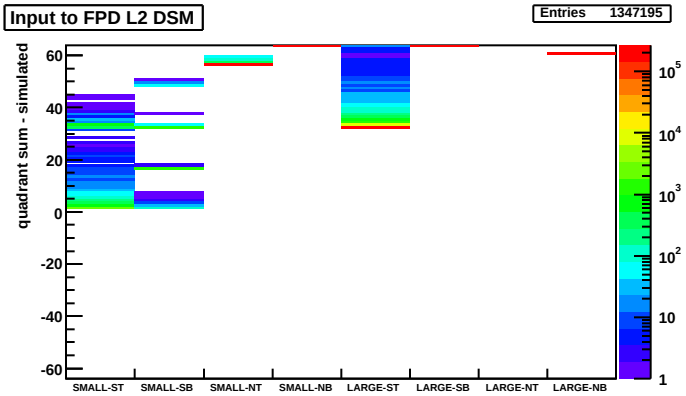

Input to FMS L1 DSM
Entries 3216972
sumA
DSM board

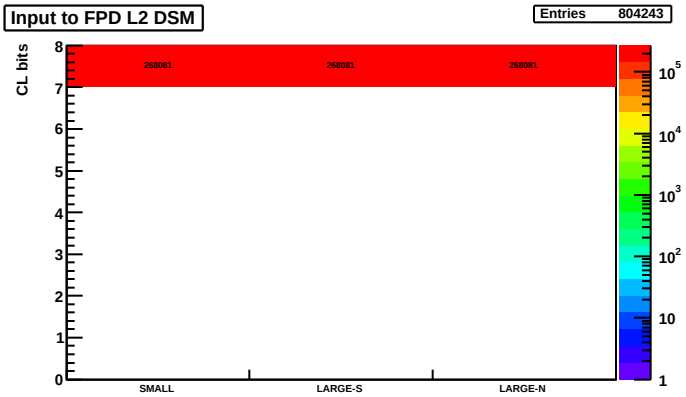

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

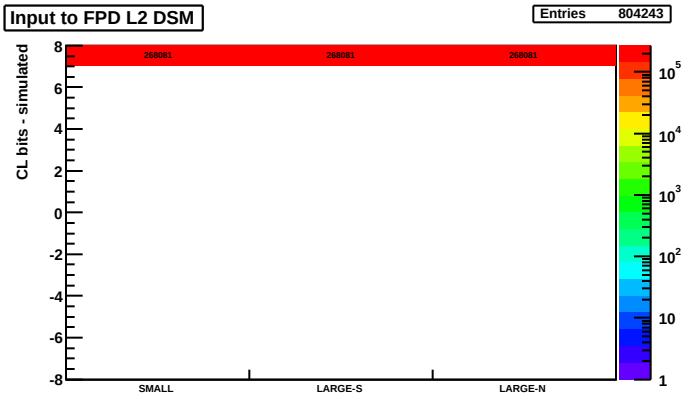

Input to FMS L1 DSM
Entries 3216972
FMS L1 HT bits
DSM board

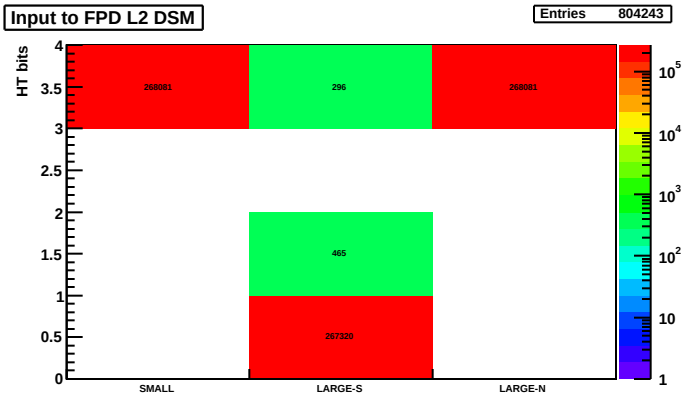

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

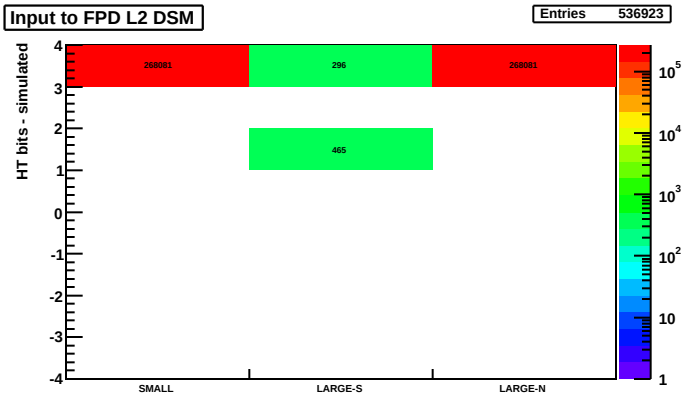

Input to FPD L2 DSM
Entries 2144648
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

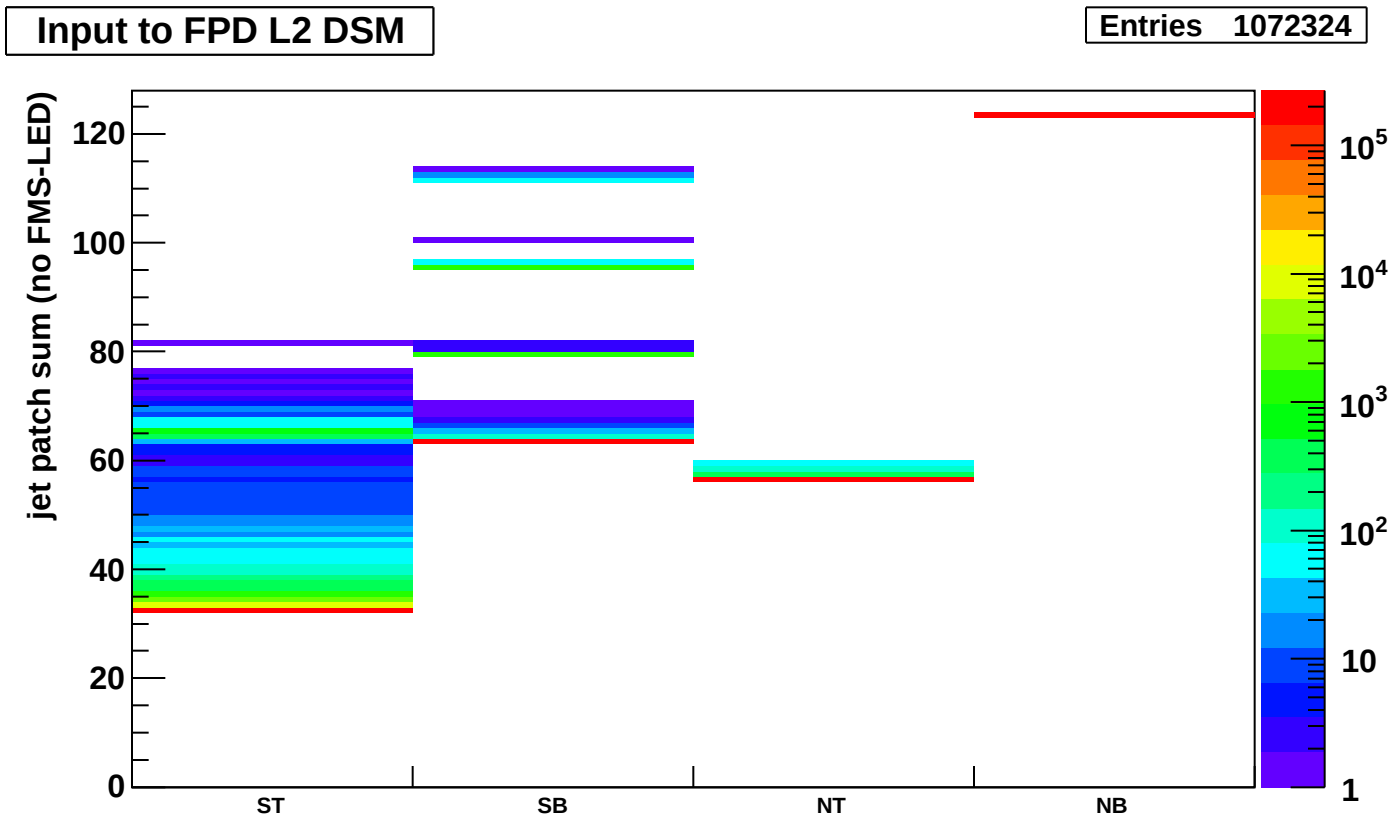

Input to FPD L2 DSM
Entries 1347195
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

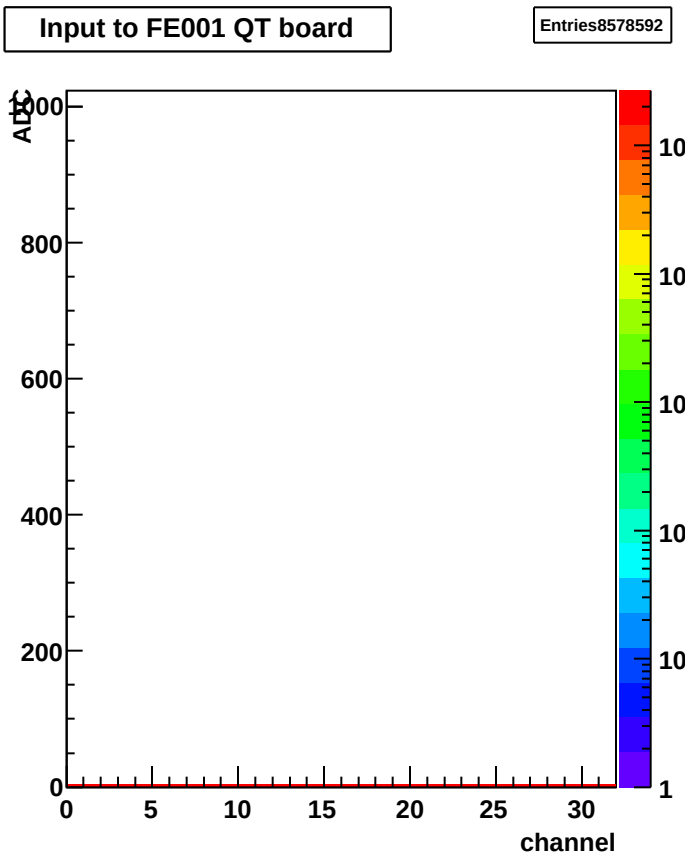

Input to FPD L2 DSM
Entries 804243
CL bits
268081
268081
268081
SMALL
LARGE-S
LARGE-N

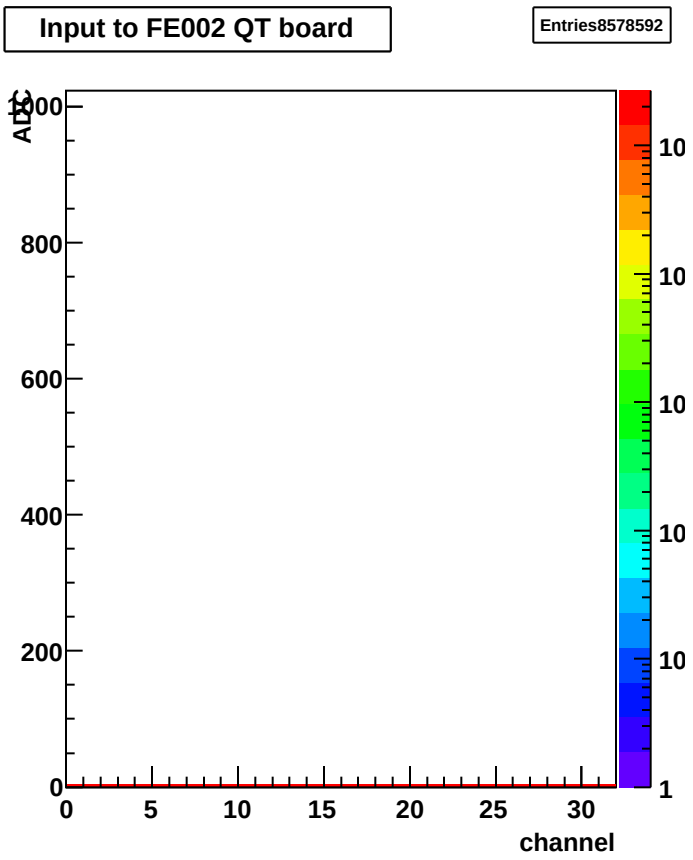

Input to FPD L2 DSM
Entries 804243
CL bits - simulated
268081
268081
268081
SMALL
LARGE-S
LARGE-N

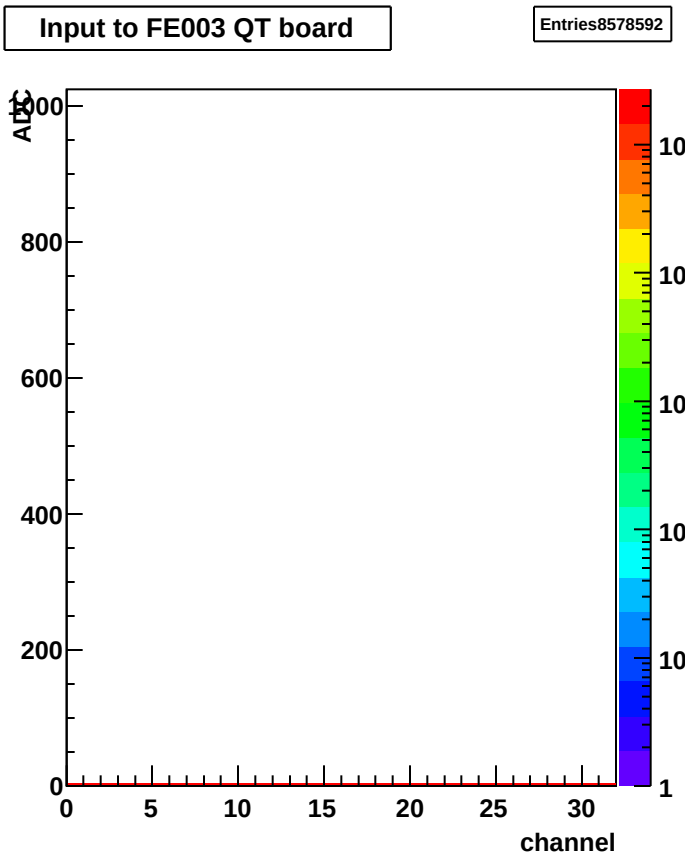

Input to FPD L2 DSM
Entries 804243
HT bits
268081
296
268081
465
267320
SMALL
LARGE-S
LARGE-N

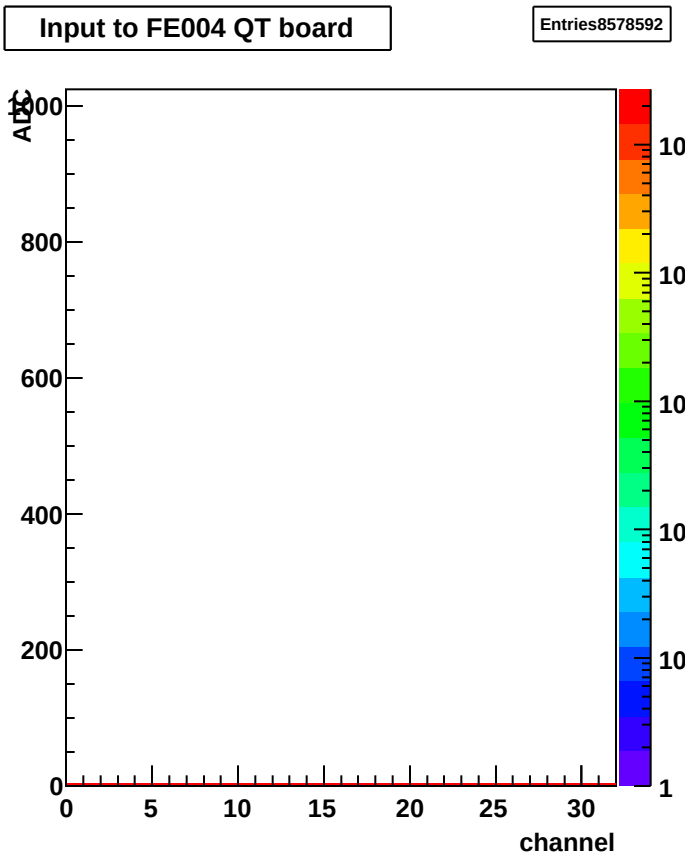

Input to FPD L2 DSM
Entries 536923
HT bits - simulated
268081
296
268081
465
SMALL
LARGE-S
LARGE-N

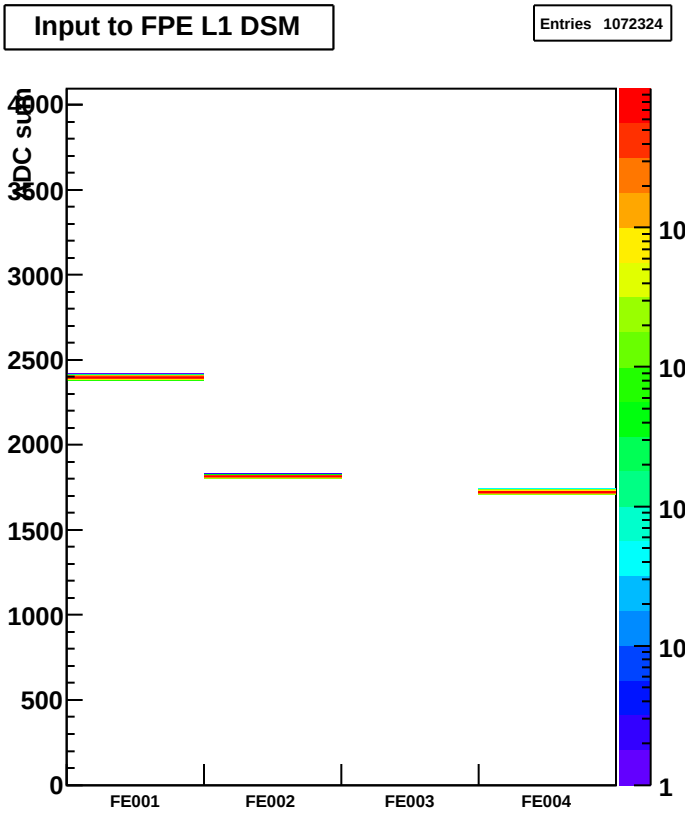

Input to FPD L2 DSM
Entries 1072324
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

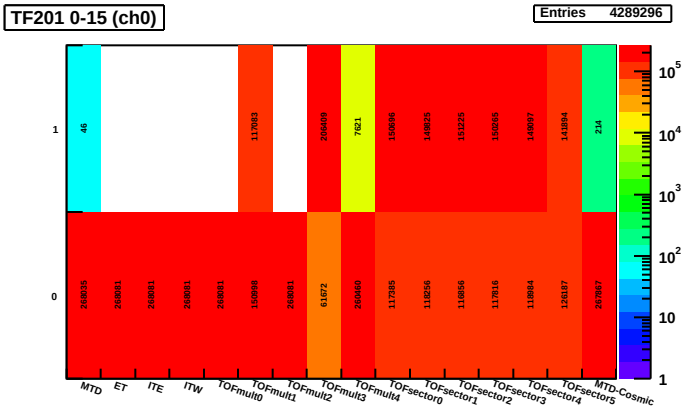

Input to FE001 QT board
Entries8578592
ADC
channel

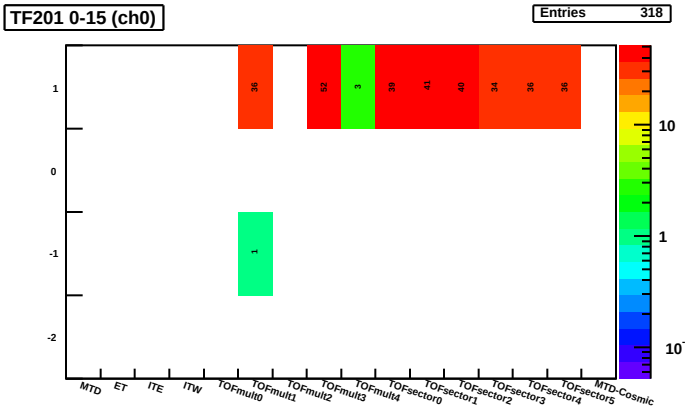

Input to FE002 QT board
Entries8578592
ADC
channel

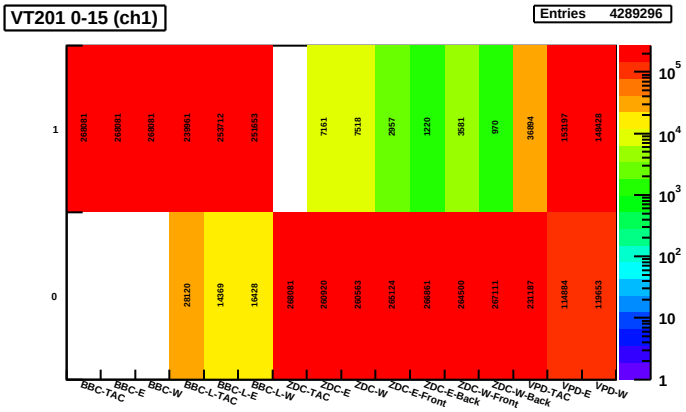

Input to FE003 QT board
Entries8578592
ADC
channel

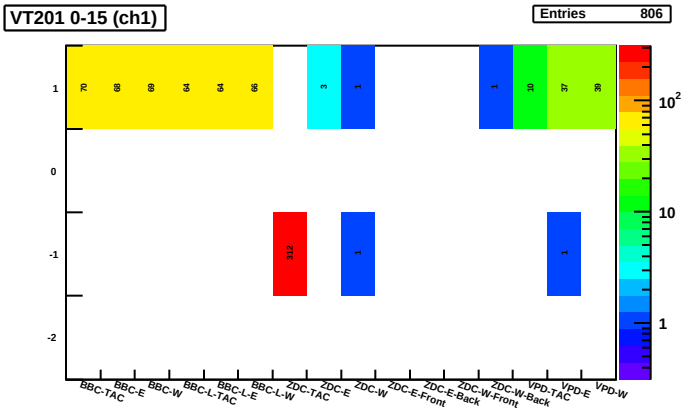

Input to FE004 QT board
Entries8578592
ADC
channel

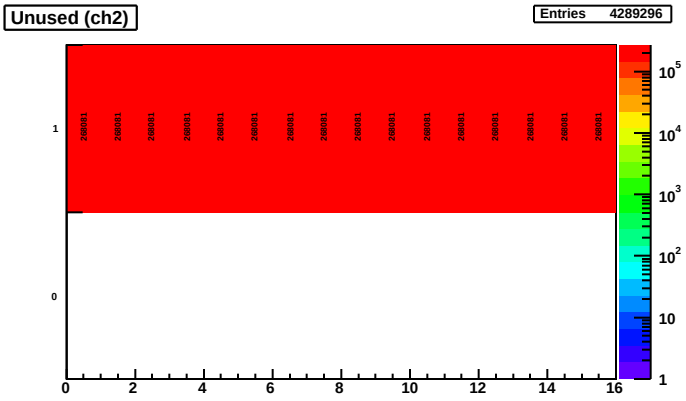

Input to FPE L1 DSM
Entries 1072324
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

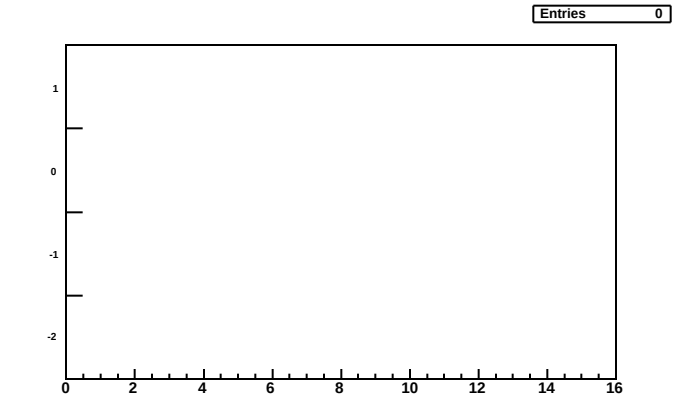

TF201 0-15 (ch0)
Entries 4289296
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

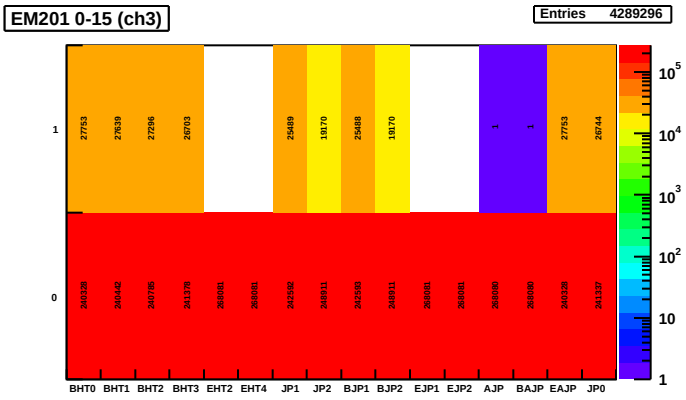

TF201 0-15 (ch0)
Entries 318
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

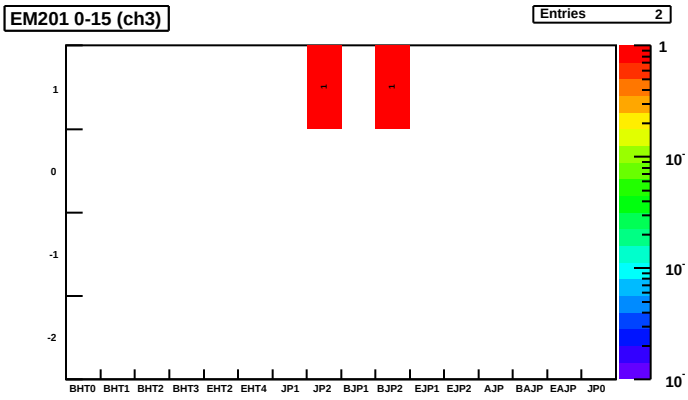

VT201 0-15 (ch1)
Entries 4289296
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 806
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

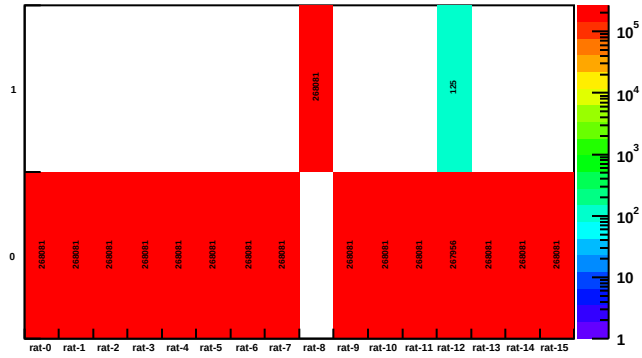

Unused (ch2)
Entries 4289296

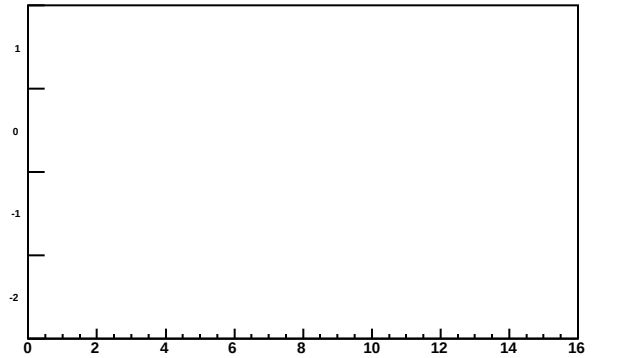

Entries 0


EM201 0-15 (ch3)
Entries 4289296
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

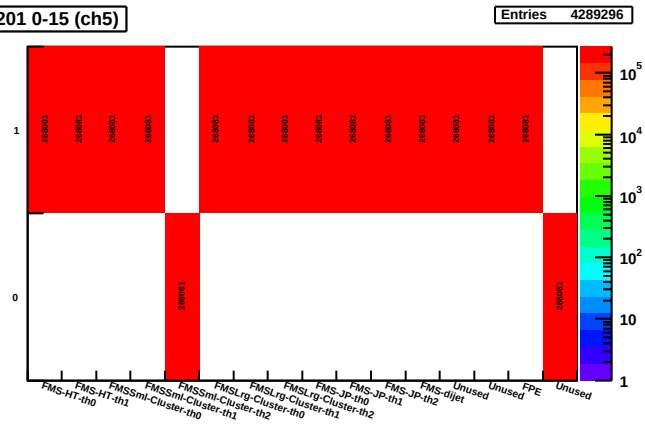

EM201 0-15 (ch3)
Entries 2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

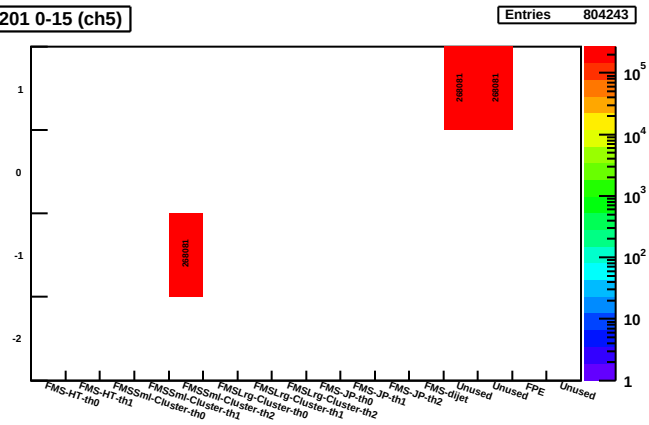




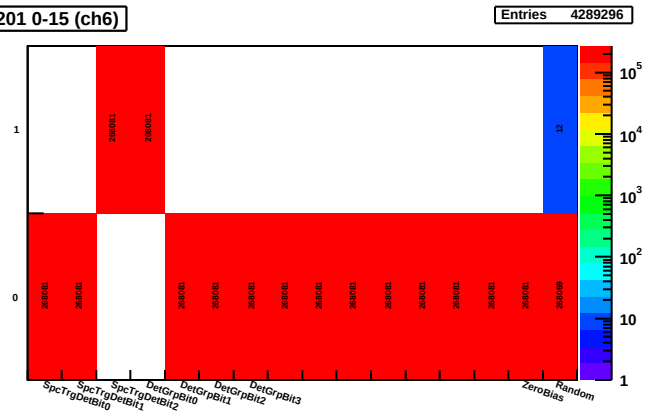
FP201 0-15 (ch5)

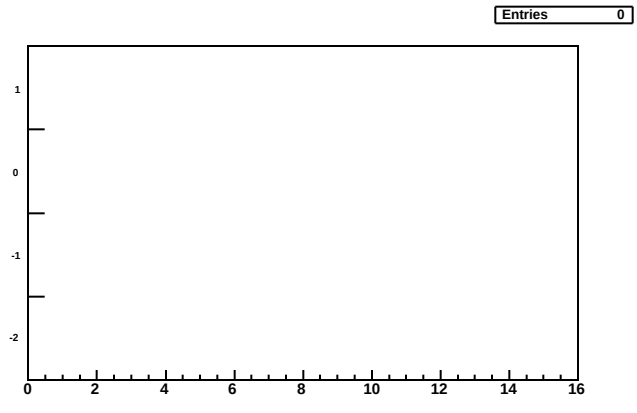
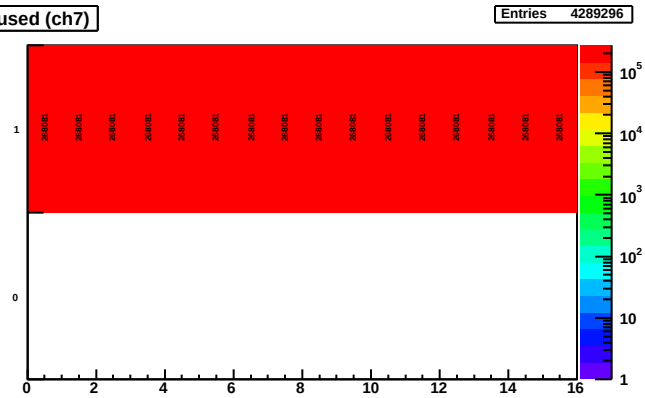
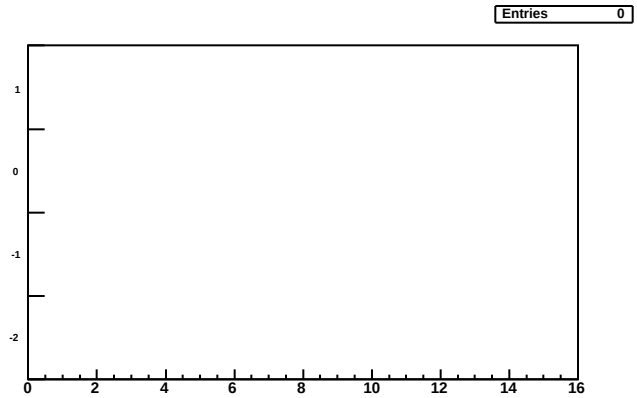


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)