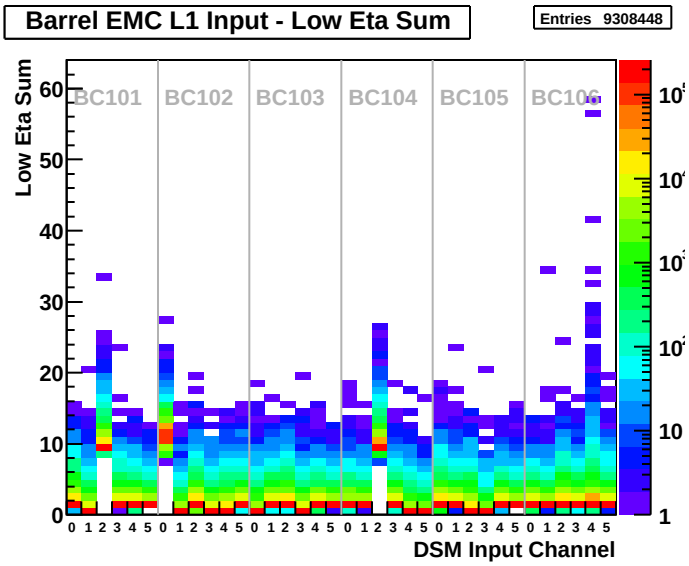
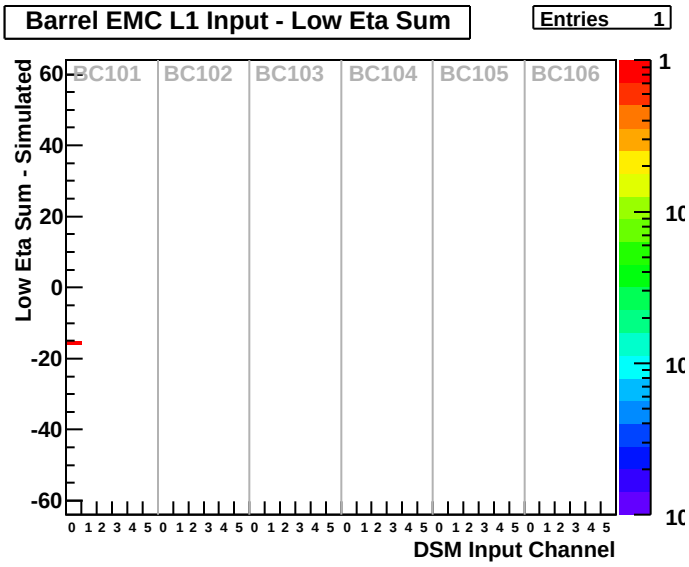
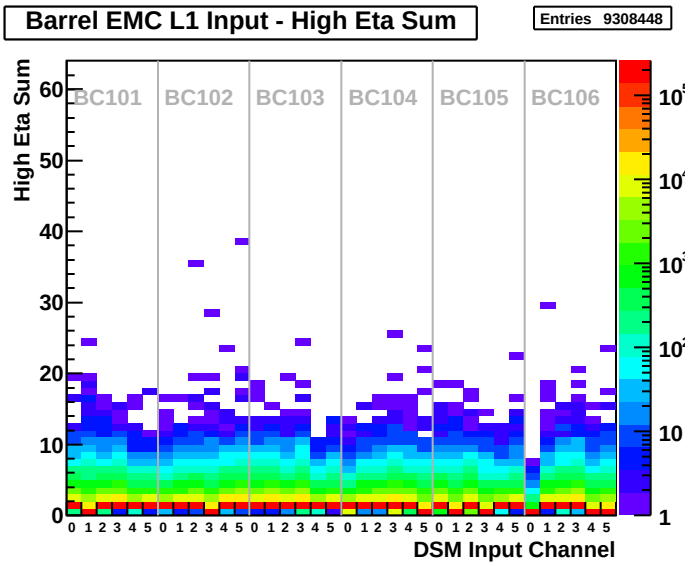
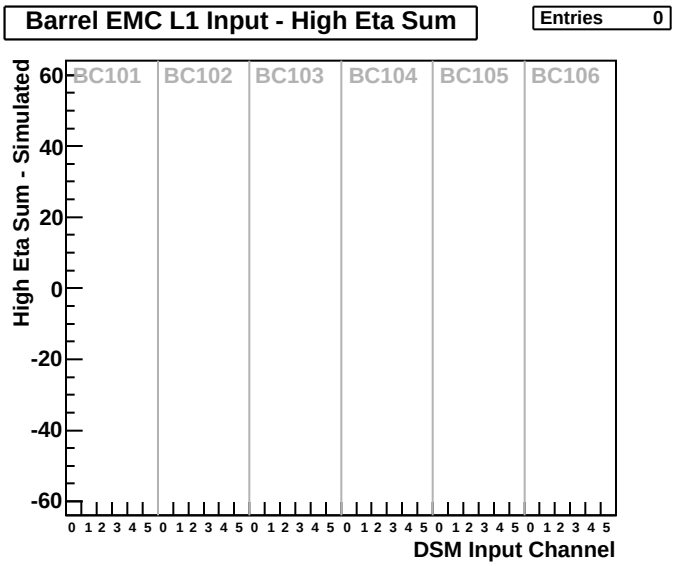
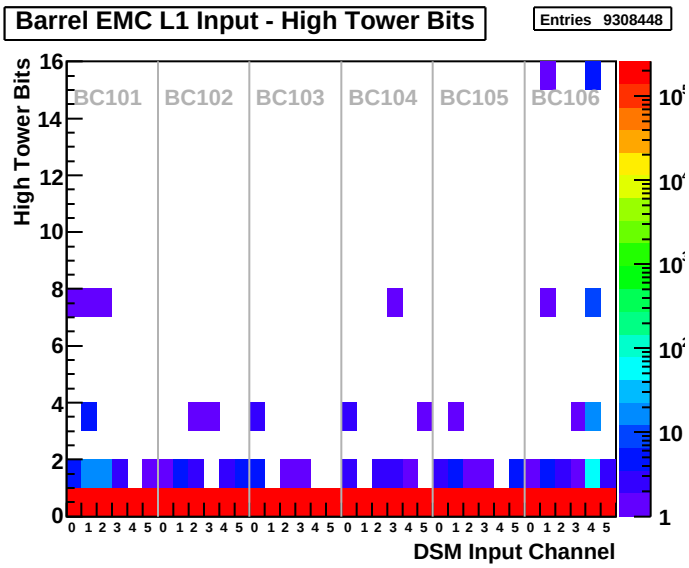
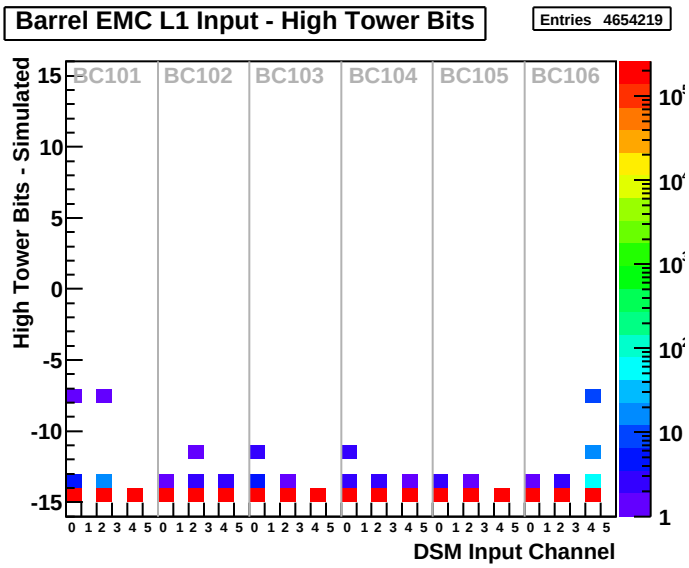

Barrel EMC L1 Input - Low Eta Sum
Entries 9308448
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

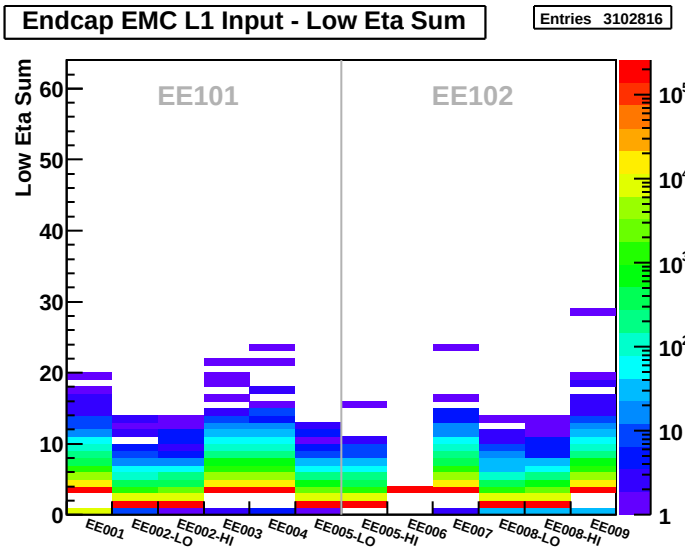

Barrel EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

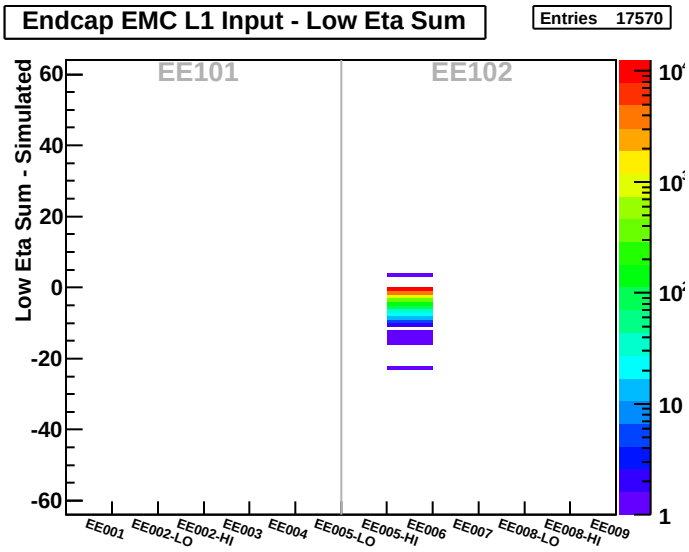

Barrel EMC L1 Input - High Eta Sum
Entries 9308448
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

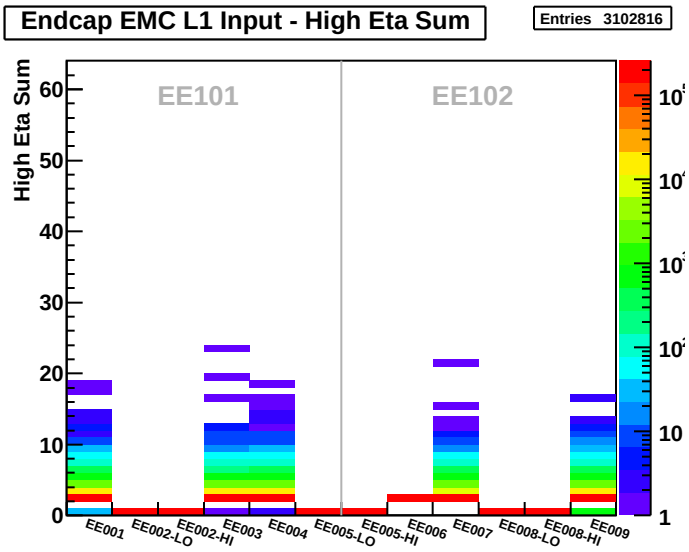

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

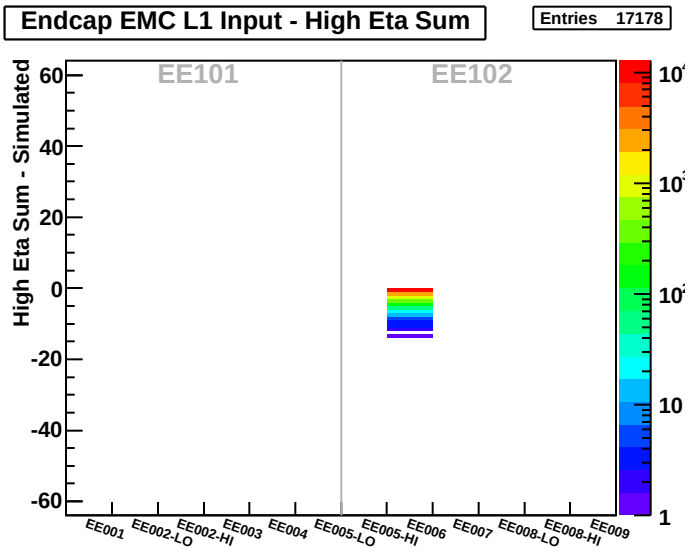

Barrel EMC L1 Input - High Tower Bits
Entries 9308448
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

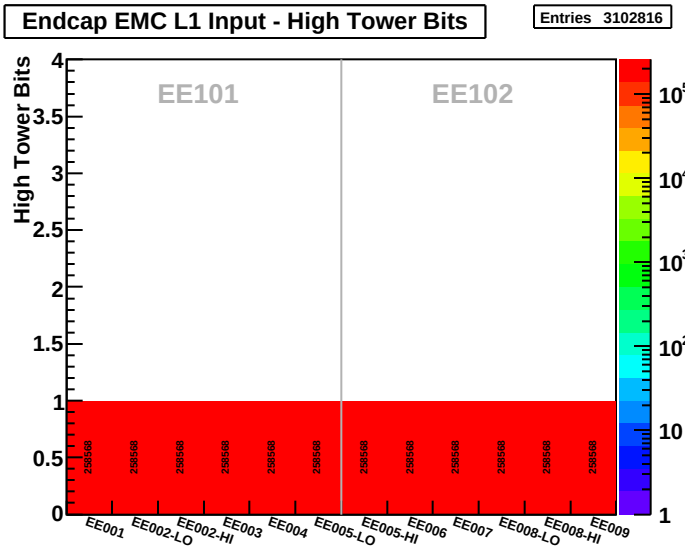

Barrel EMC L1 Input - High Tower Bits
Entries 4654219
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

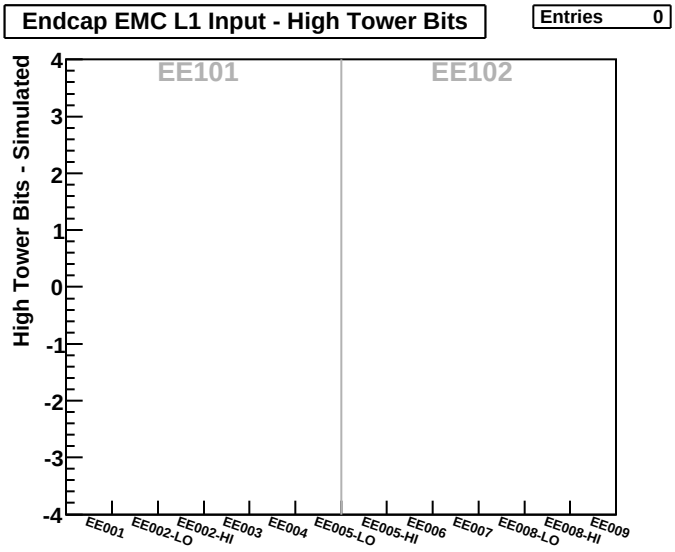

Endcap EMC L1 Input - Low Eta Sum
Entries 3102816
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

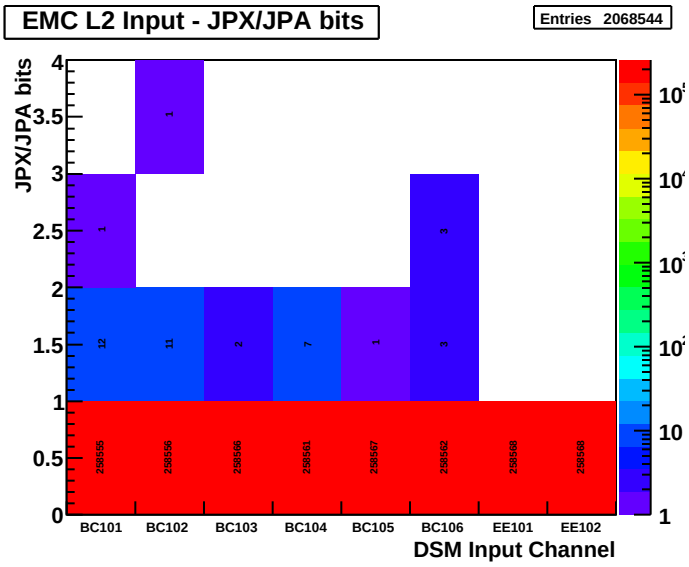

Endcap EMC L1 Input - Low Eta Sum
Entries 17570
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

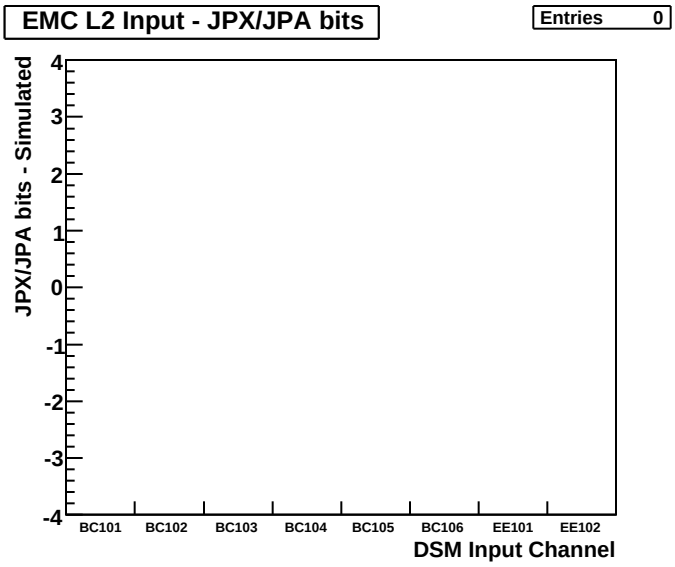

Endcap EMC L1 Input - High Eta Sum
Entries 3102816
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

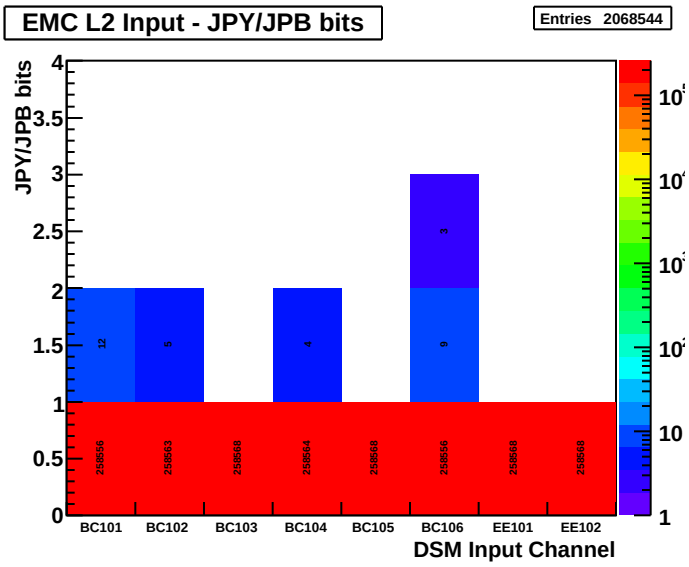

Endcap EMC L1 Input - High Eta Sum
Entries 17178
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

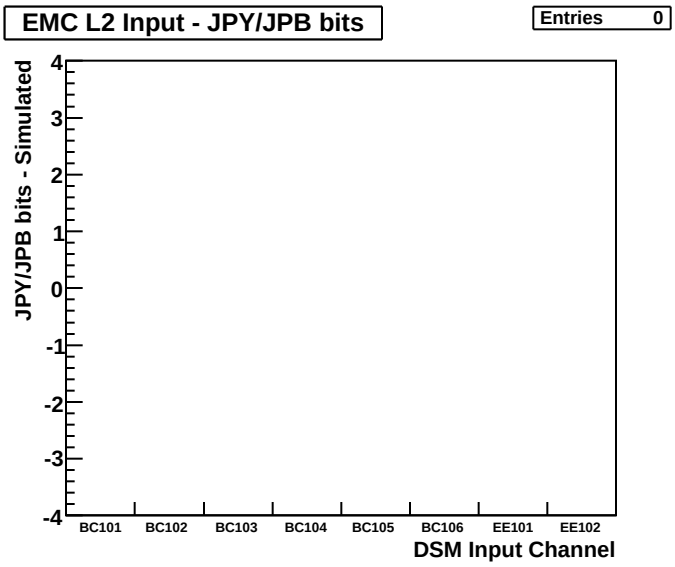

Endcap EMC L1 Input - High Tower Bits
Entries 3102816
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

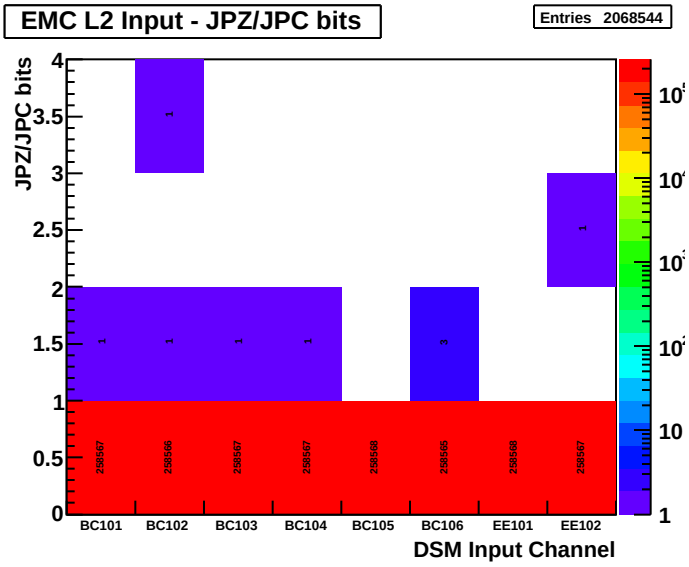

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

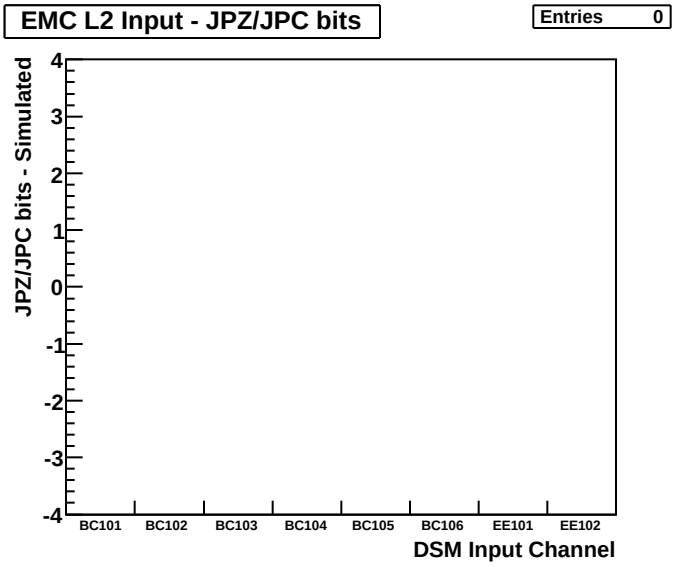

EMC L2 Input - JPX/JPA bits
Entries 2068544
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

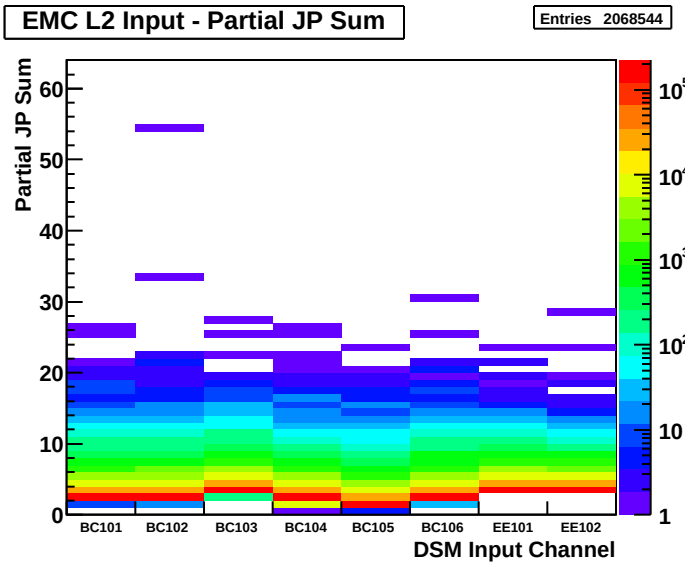

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

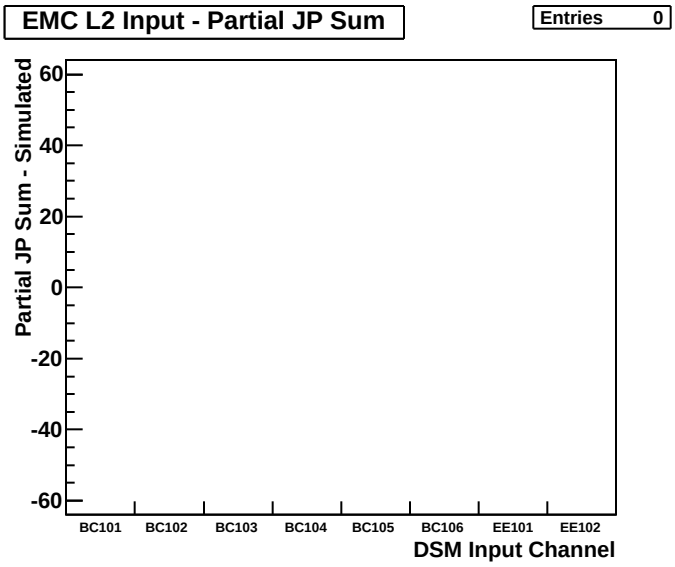

EMC L2 Input - JPY/JPB bits
Entries 2068544
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

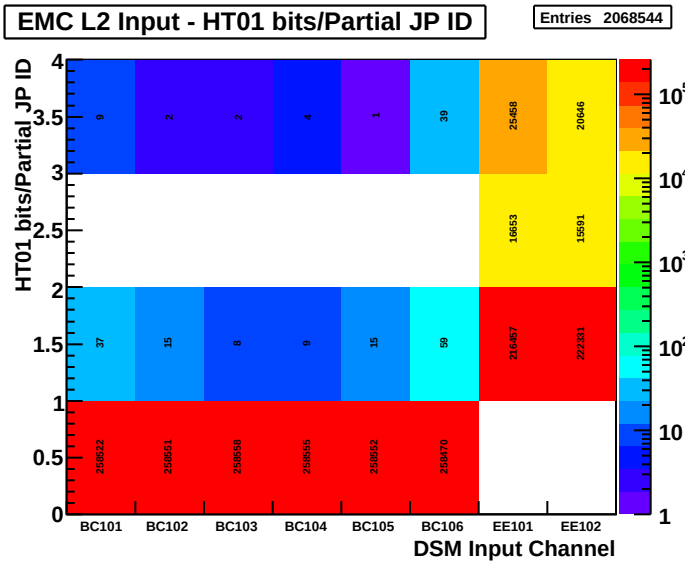

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

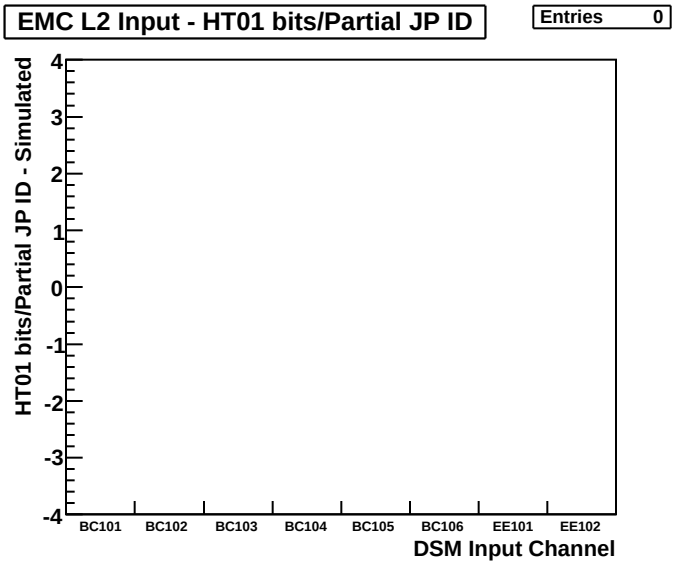

EMC L2 Input - JPZ/JPC bits
Entries 2068544
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

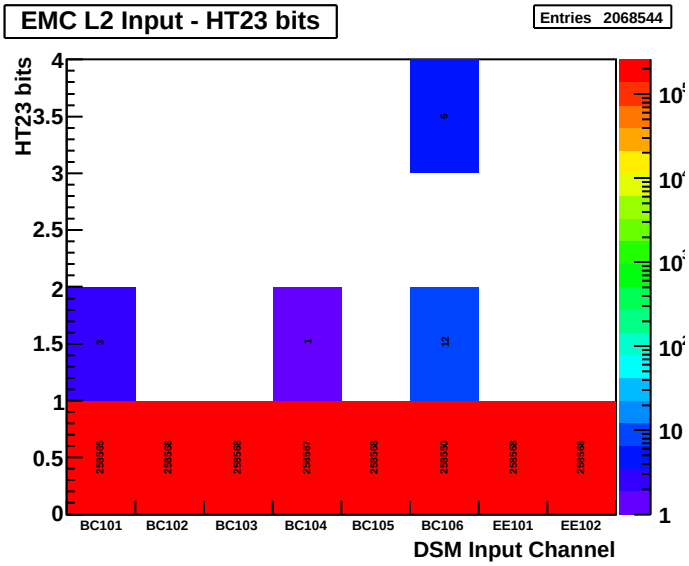

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

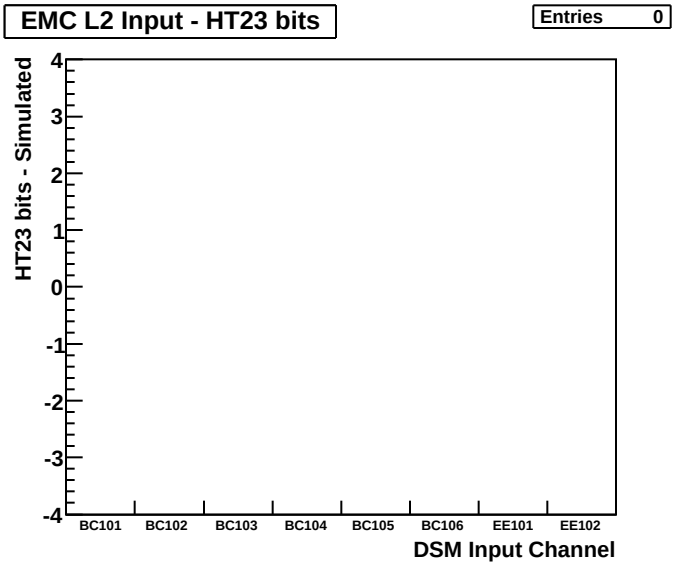

EMC L2 Input - Partial JP Sum
Entries 2068544
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

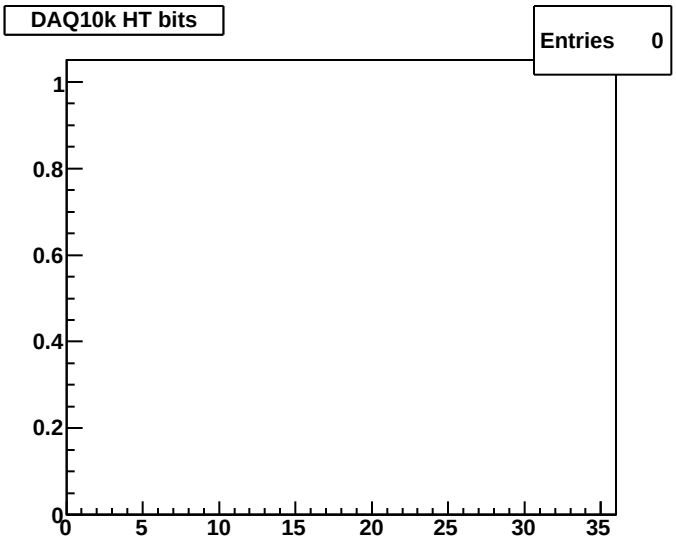

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

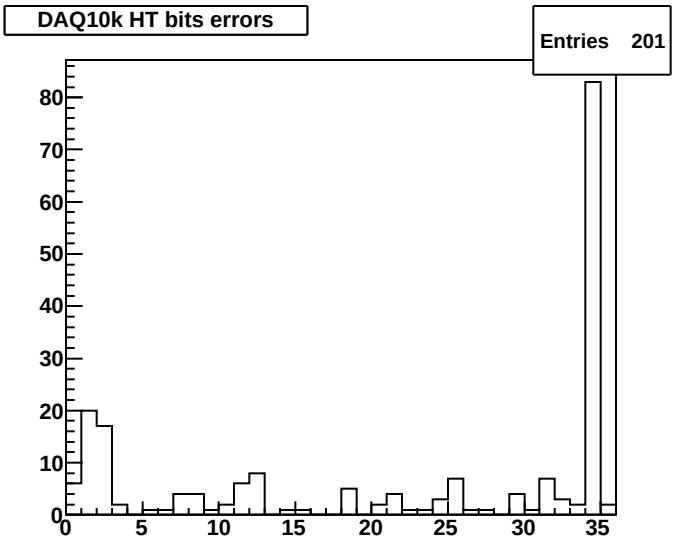

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2068544
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

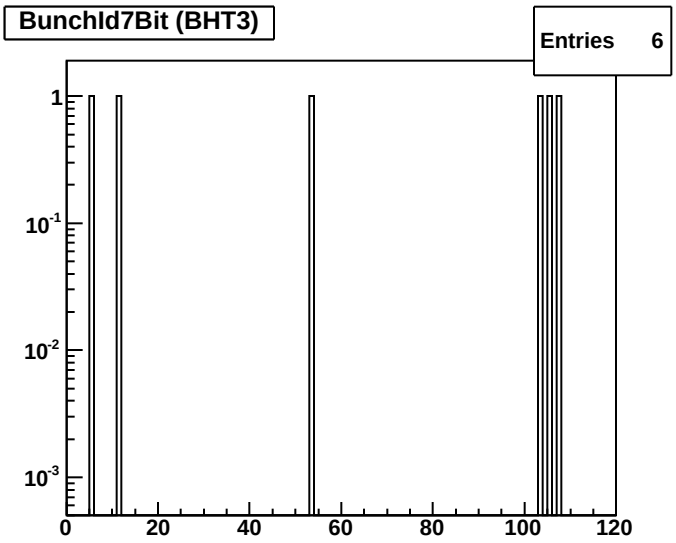

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

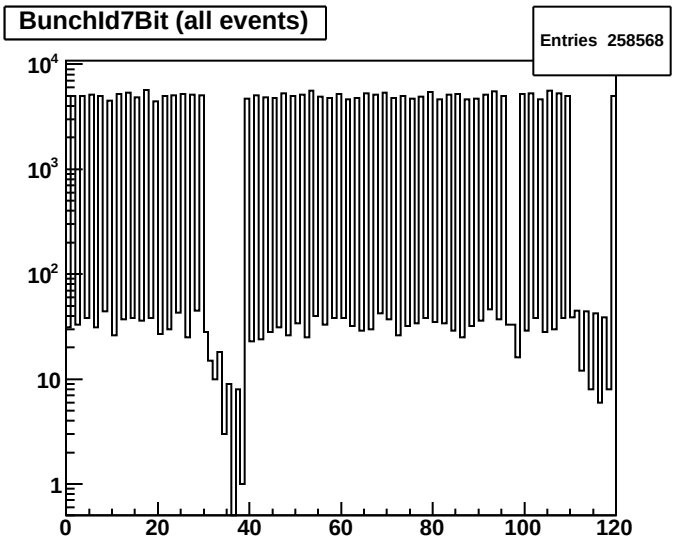

EMC L2 Input - HT23 bits
Entries 2068544
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

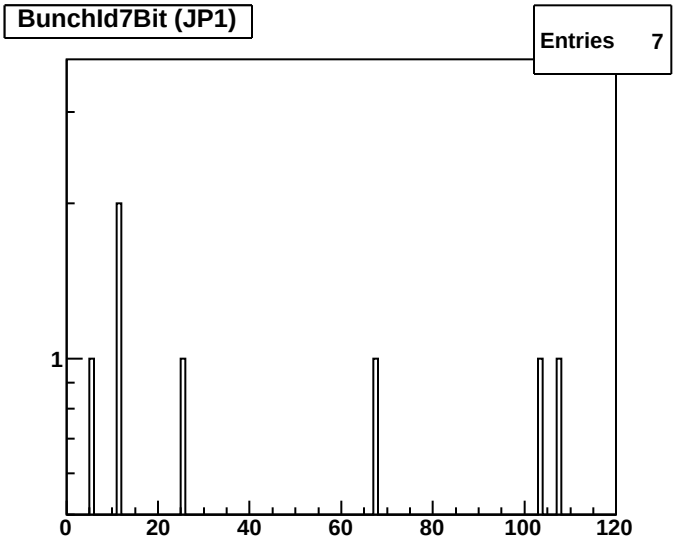

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

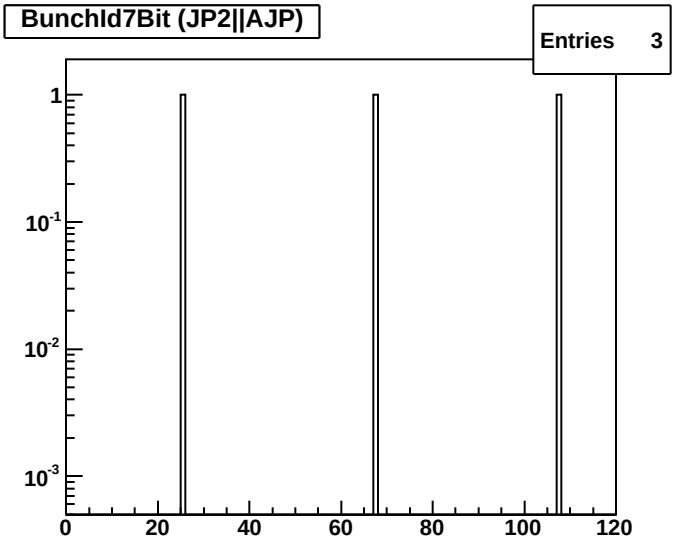

DAQ10k HT bits
Entries 0

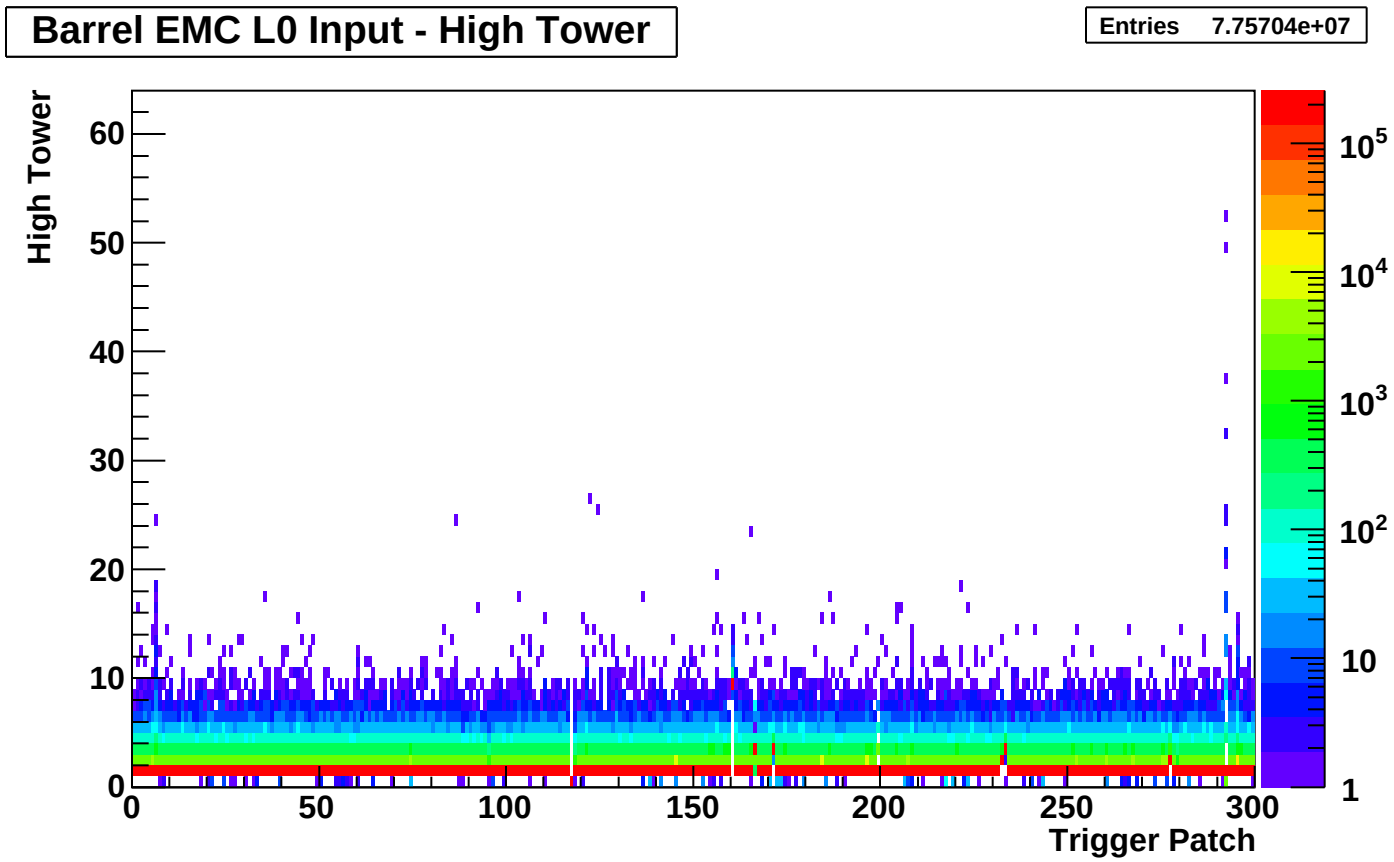

DAQ10k HT bits errors
Entries 201

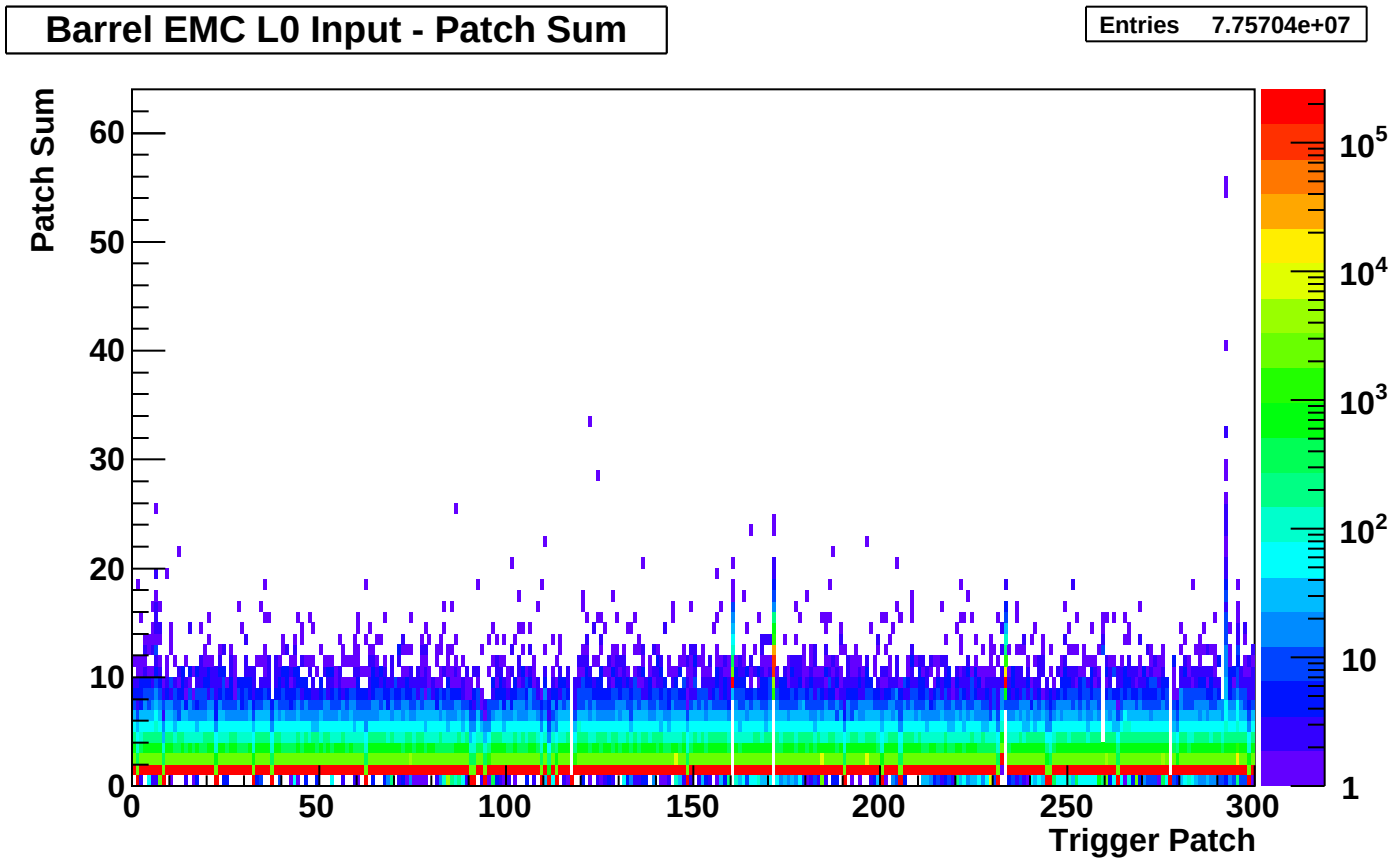

BunchId7Bit (BHT3)
Entries 6

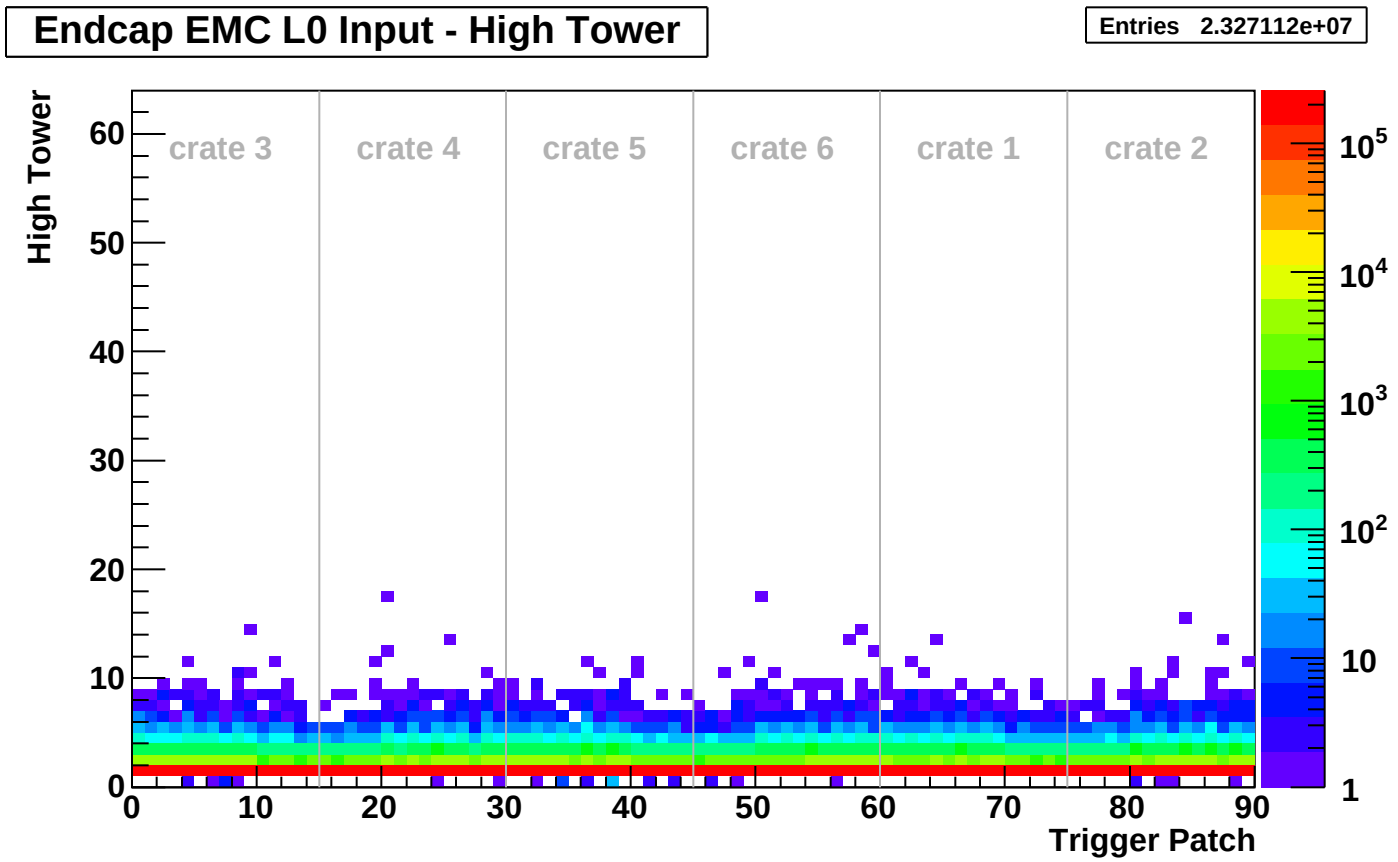

BunchId7Bit (all events)
Entries 258568

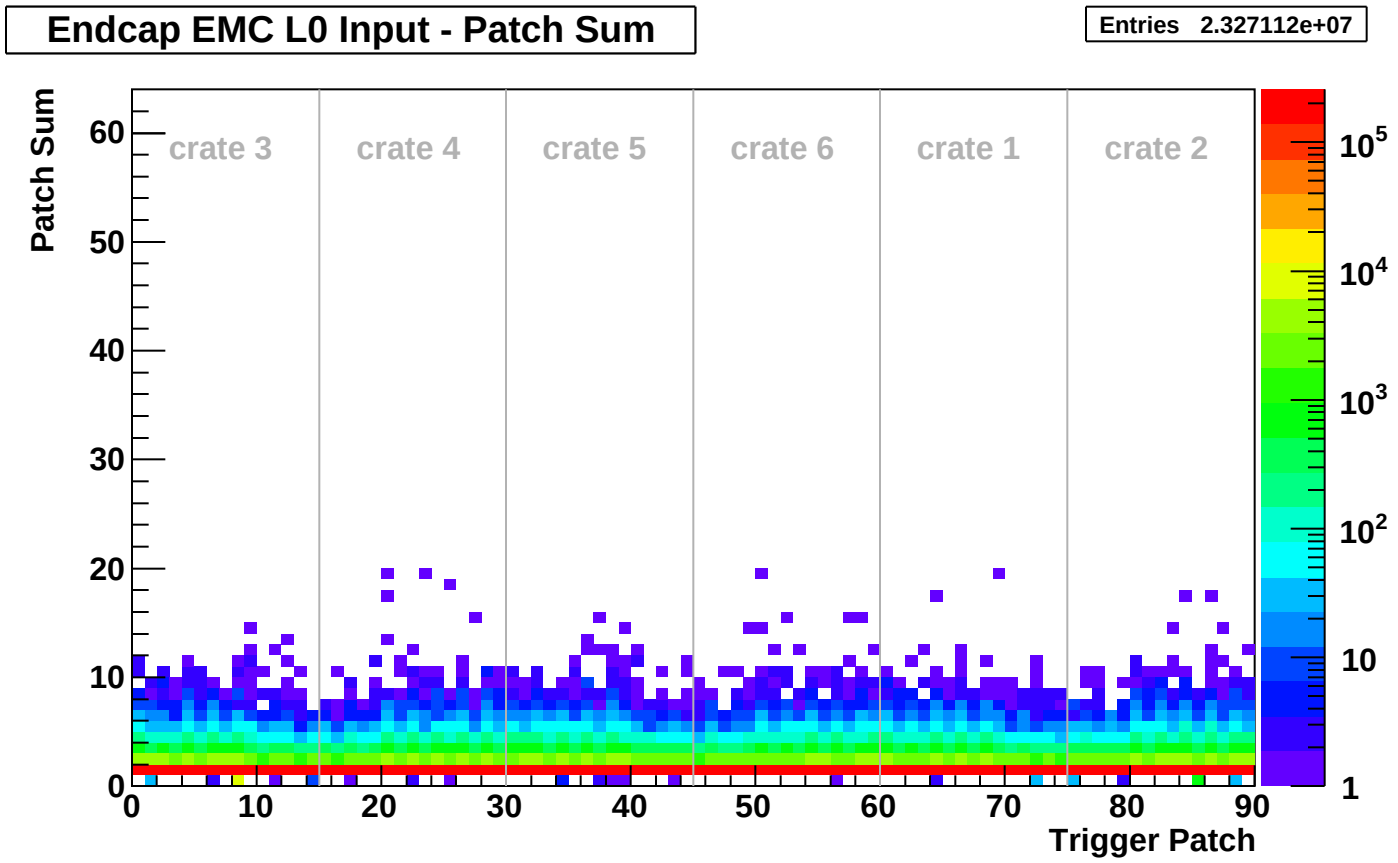

BunchId7Bit (JP1)
Entries 7

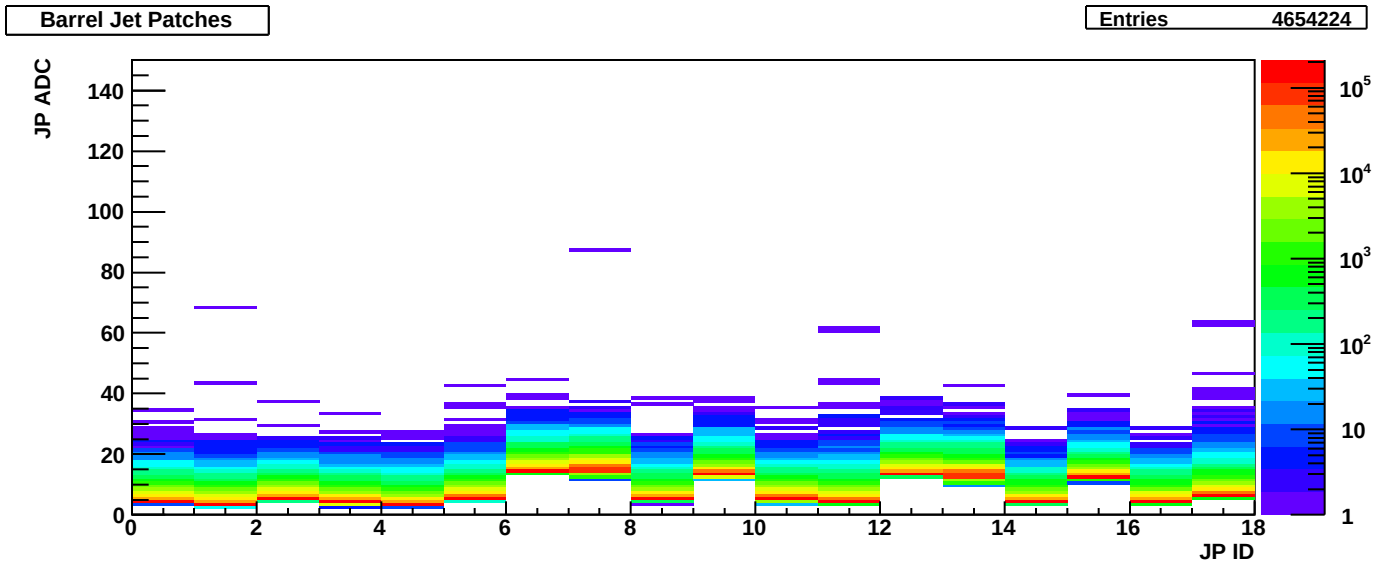

BunchId7Bit (JP2||AJP)
Entries 3

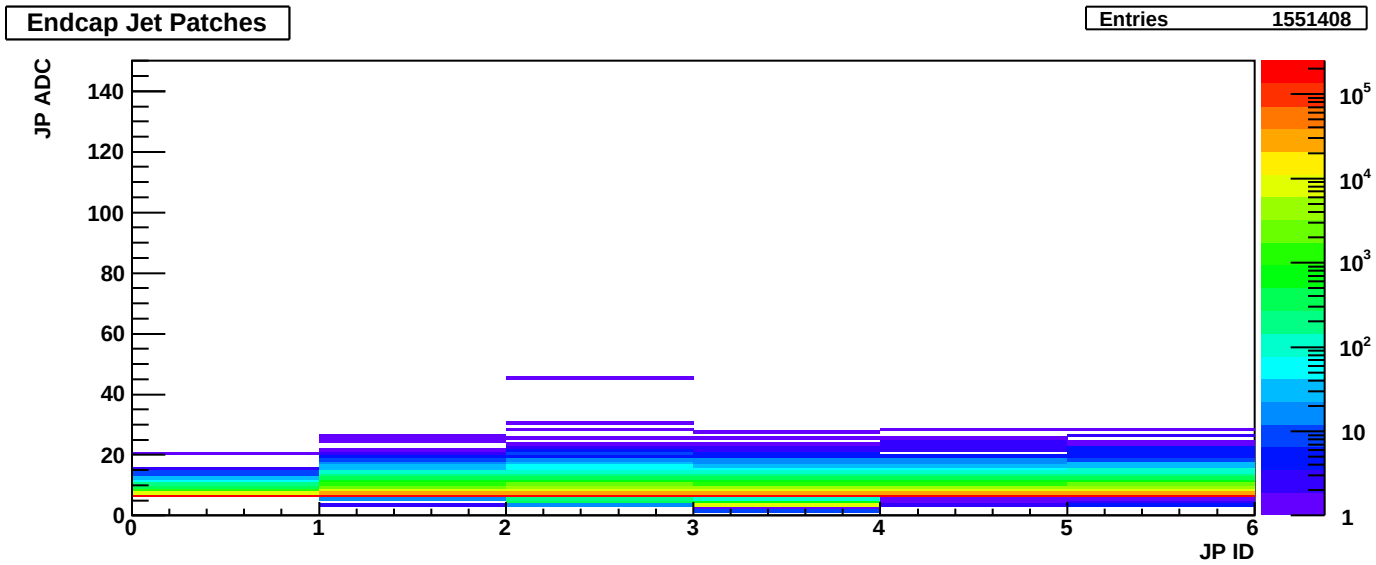

Barrel EMC L0 Input - High Tower
Entries 7.75704e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

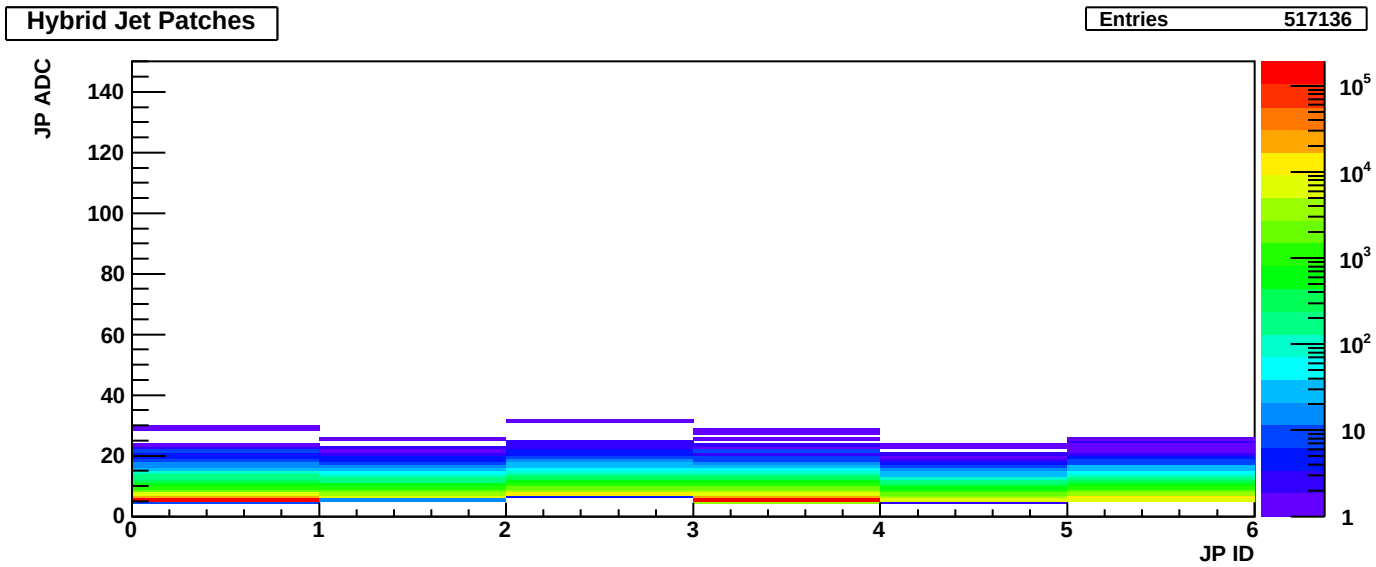

Barrel EMC L0 Input - Patch Sum
Entries 7.75704e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

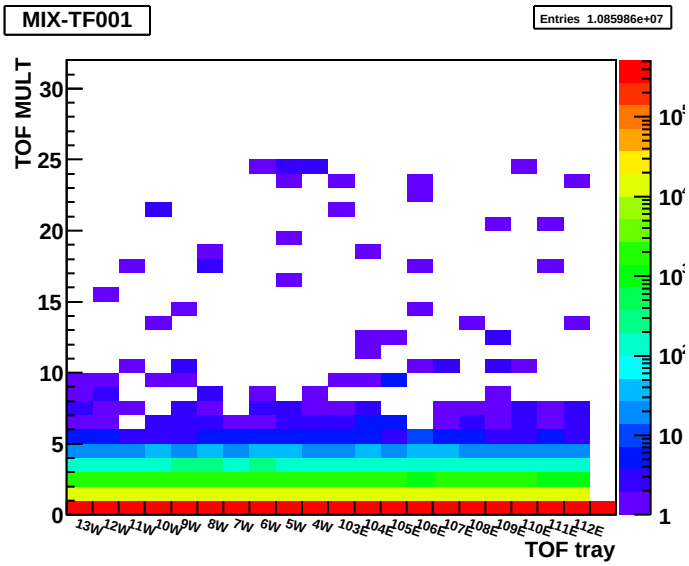

Endcap EMC L0 Input - High Tower
Entries 2.327112e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

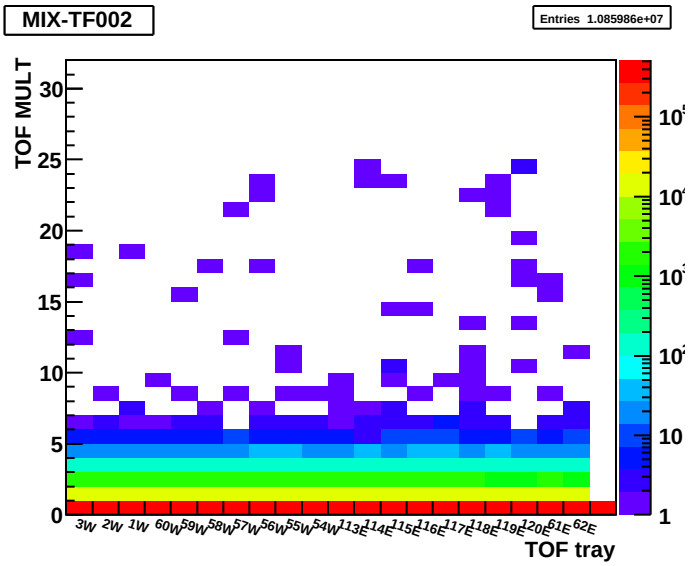

Endcap EMC L0 Input - Patch Sum
Entries 2.327112e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

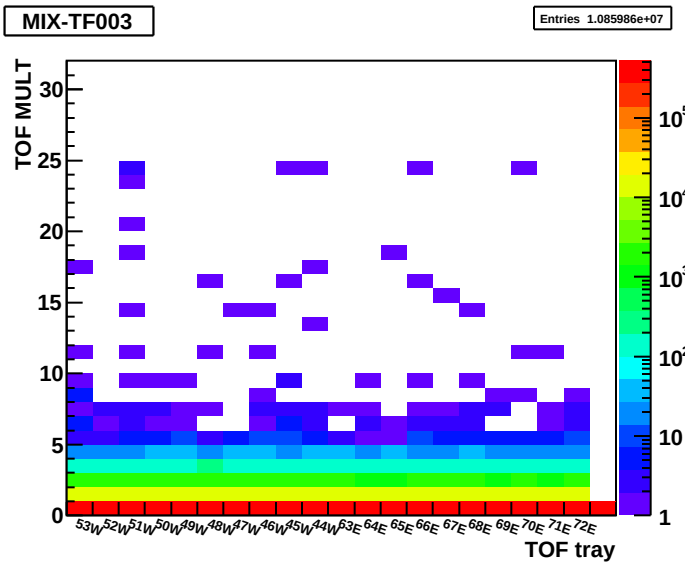

Barrel Jet Patches
Entries 4654224
JP ADC
JP ID

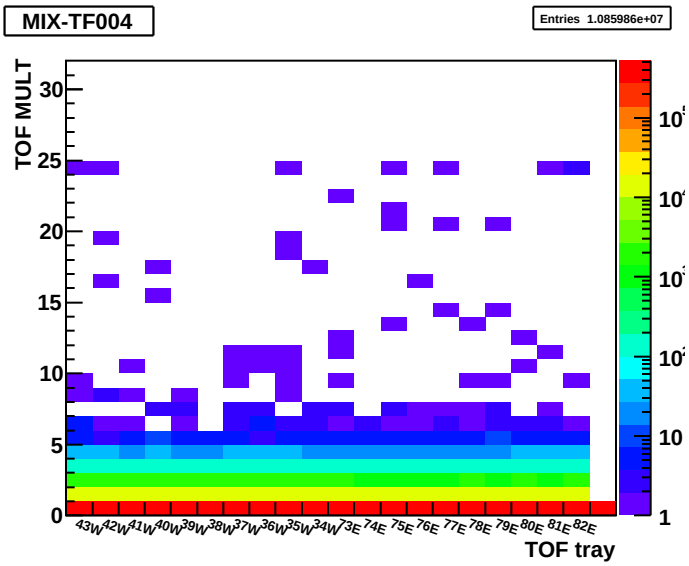

Endcap Jet Patches
Entries 1551408
JP ADC
JP ID

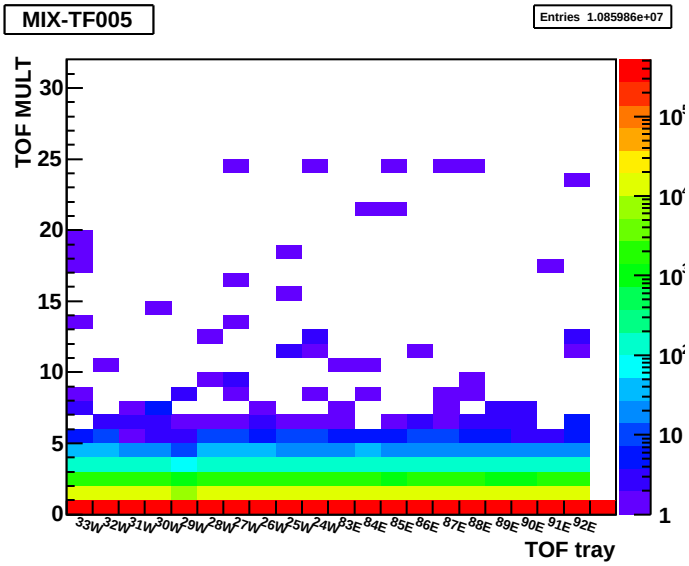

Hybrid Jet Patches
Entries 517136
JP ADC
JP ID

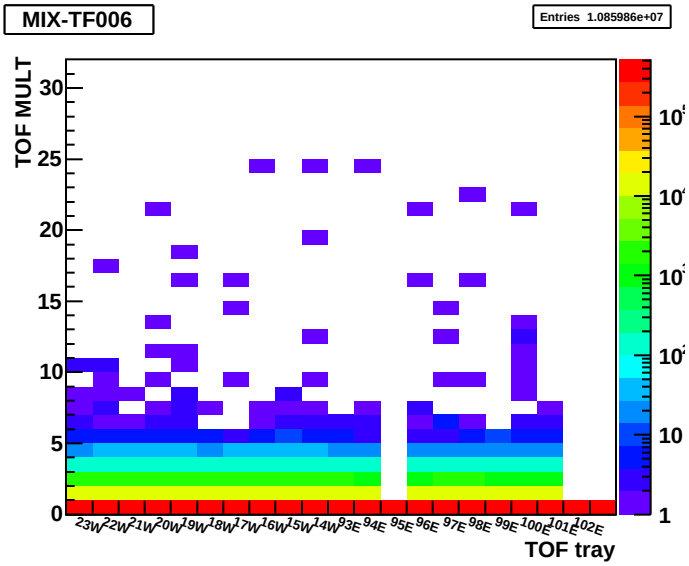

MIX-TF001
Entries 1.085986e+07
TOF MULT
TOF tray

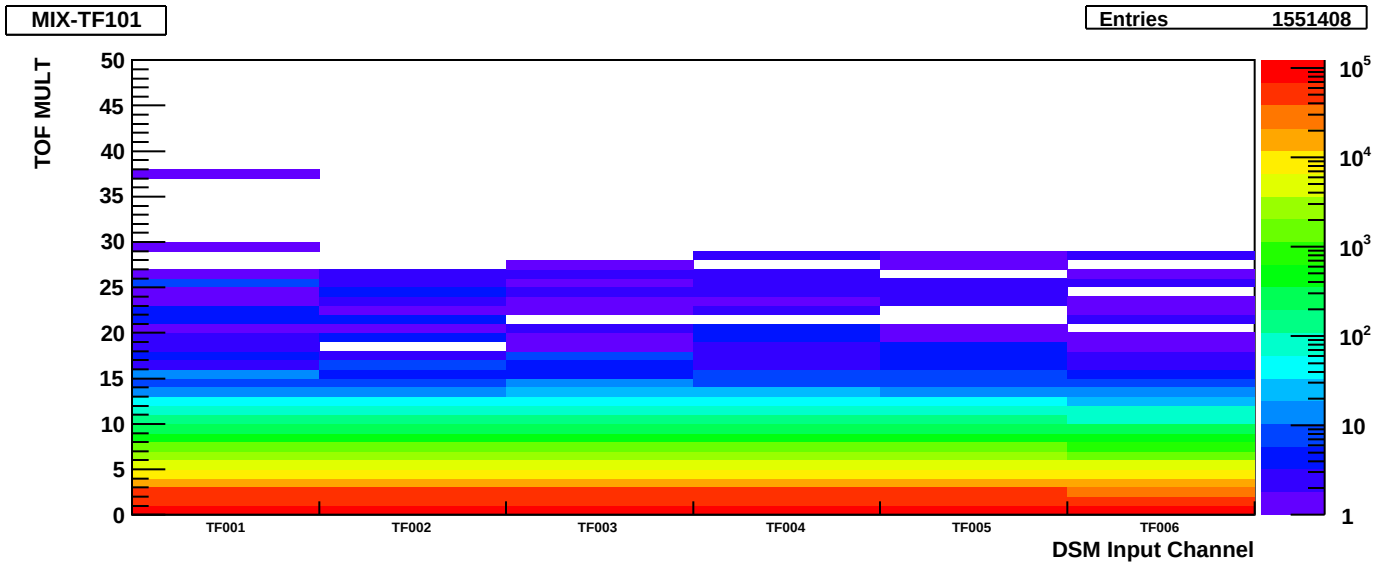

MIX-TF002
Entries 1.085986e+07
TOF MULT
TOF tray

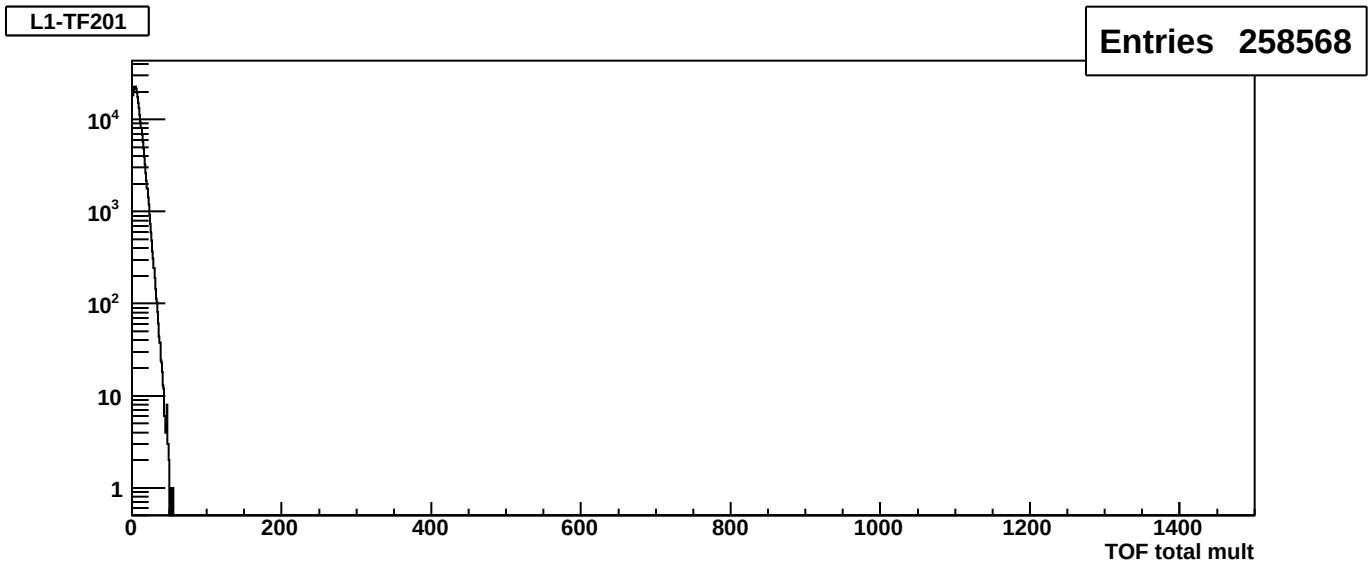

MIX-TF003
Entries 1.085986e+07
TOF MULT
TOF tray

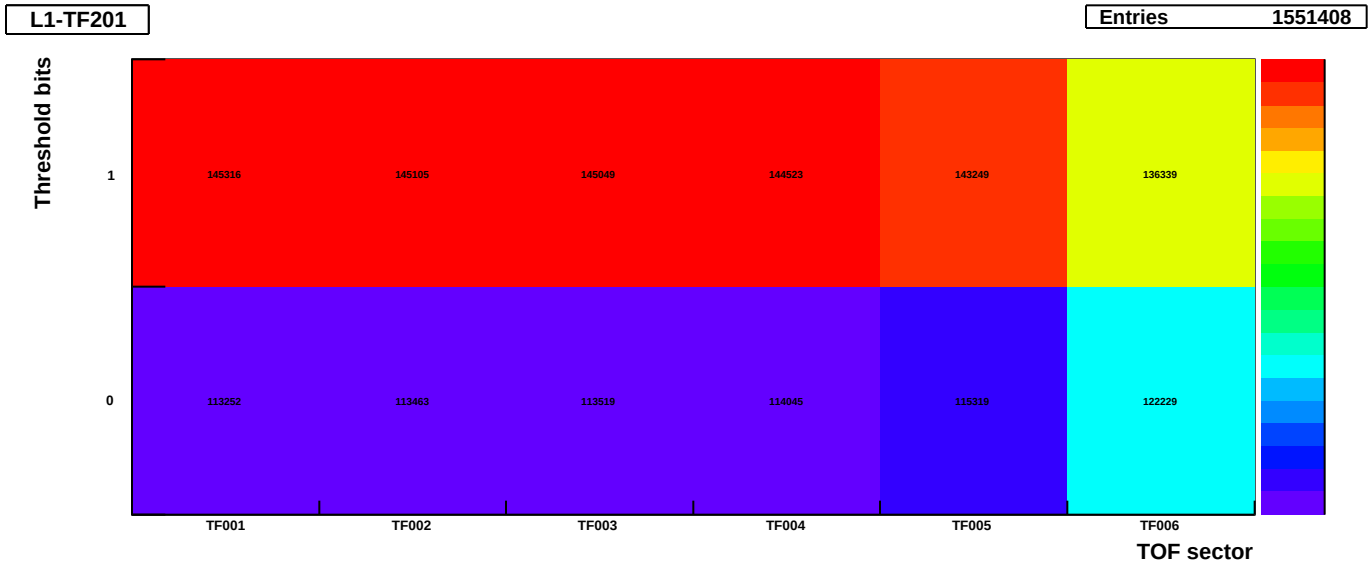

MIX-TF004
Entries 1.085986e+07
TOF MULT
TOF tray

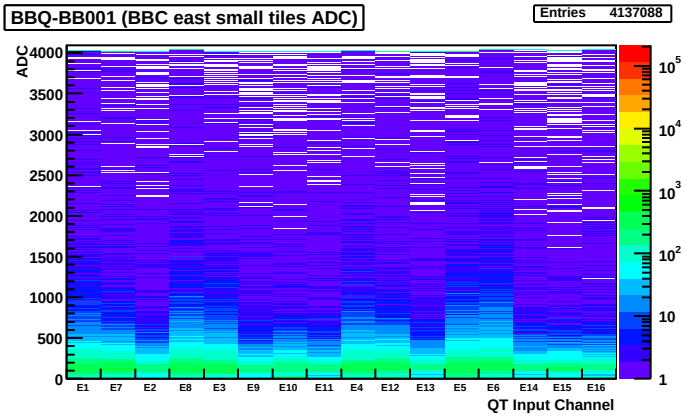

MIX-TF005
Entries 1.085986e+07
TOF MULT
TOF tray

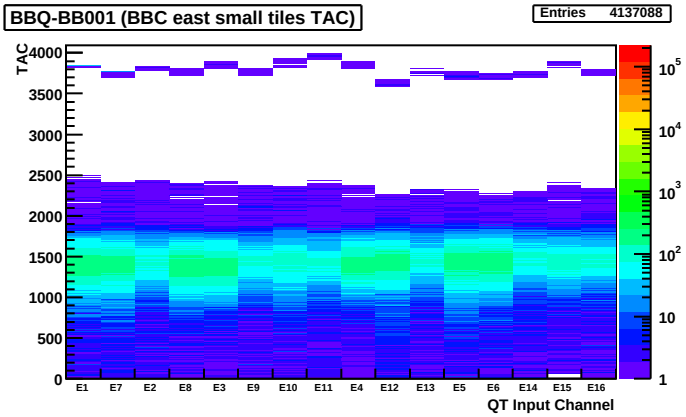

MIX-TF006
Entries 1.085986e+07
TOF MULT
TOF tray

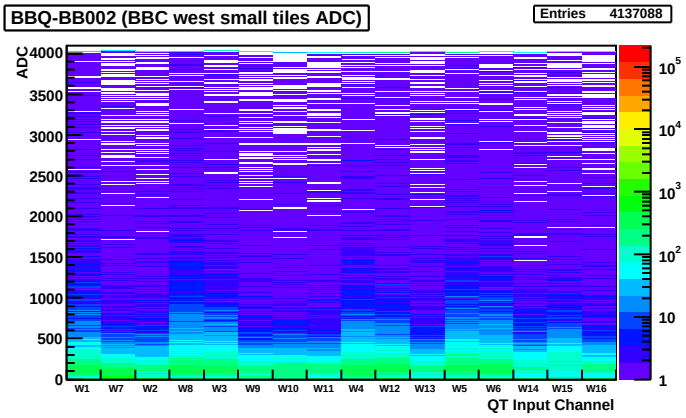

MIX-TF101
Entries 1551408
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

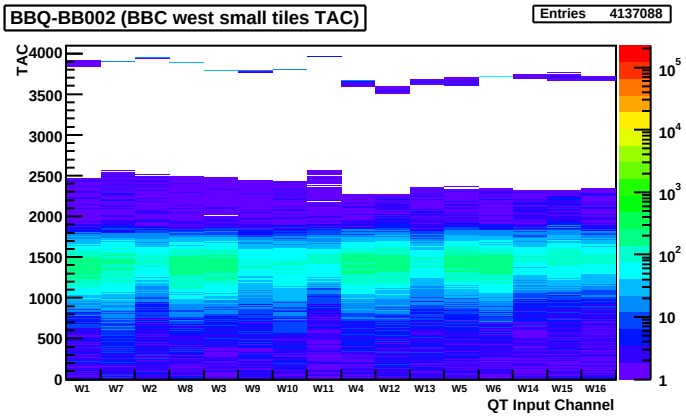

L1-TF201
Entries 258568
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

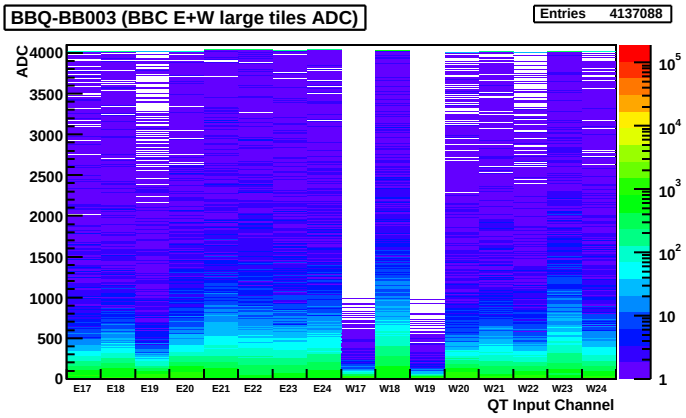

L1-TF201
Entries 1551408
Threshold bits
1
0
145316
145105
145049
144523
143249
136339
113252
113463
113519
114045
115319
122229
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

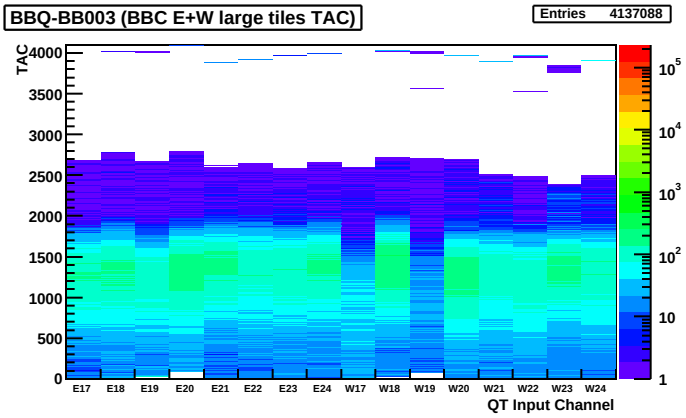

BBQ-BB001 (BBC east small tiles ADC)
Entries 4137088
ADC
QT Input Channel

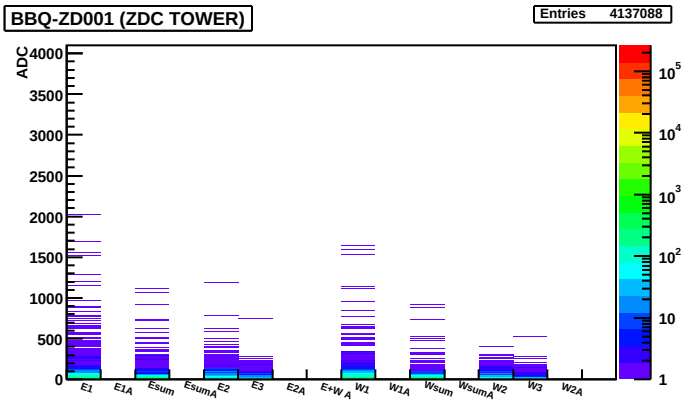

BBQ-BB001 (BBC east small tiles TAC)
Entries 4137088
TAC
QT Input Channel

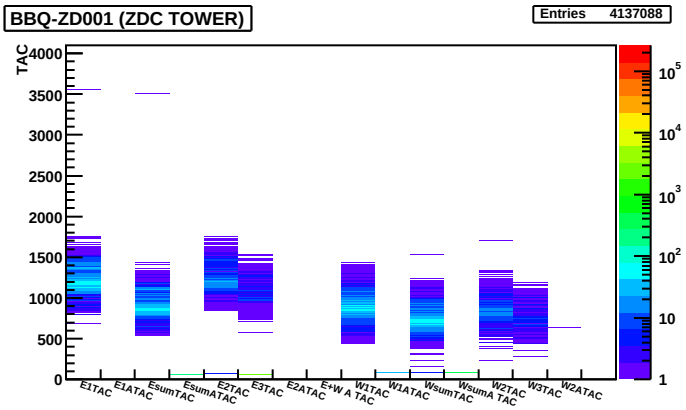

BBQ-BB002 (BBC west small tiles ADC)
Entries 4137088
ADC
QT Input Channel

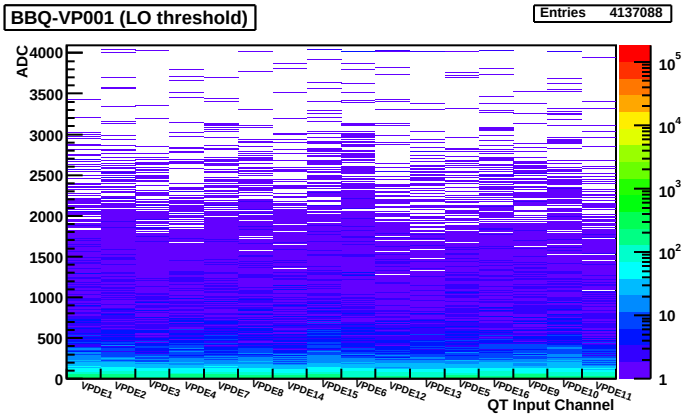

BBQ-BB002 (BBC west small tiles TAC)
Entries 4137088
TAC
QT Input Channel

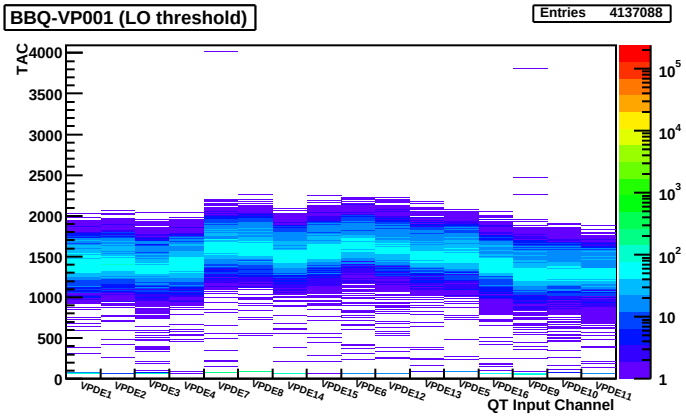

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4137088
ADC
QT Input Channel

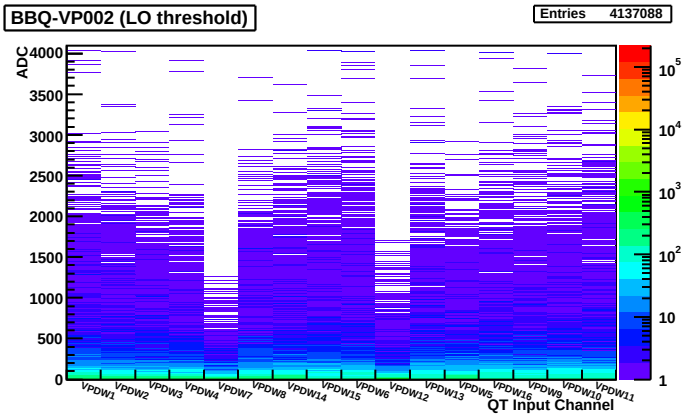

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4137088
TAC
QT Input Channel

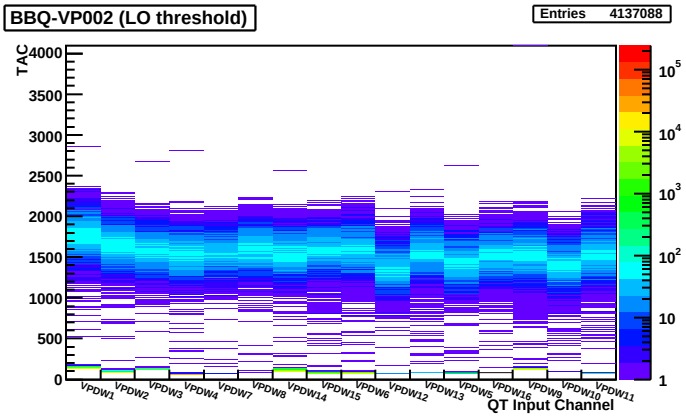

BBQ-ZD001 (ZDC TOWER)
Entries 4137088
ADC

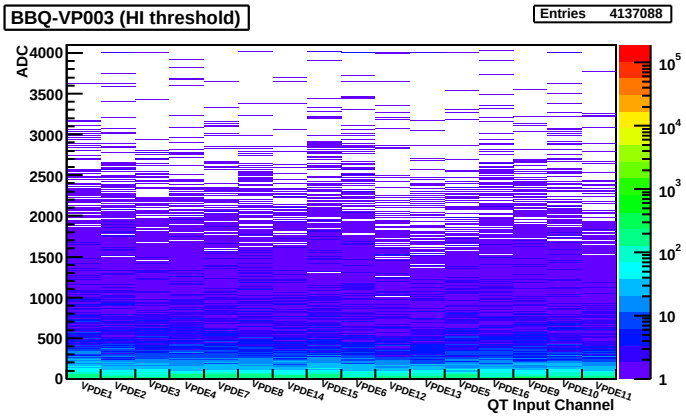

BBQ-ZD001 (ZDC TOWER)
Entries 4137088
TAC

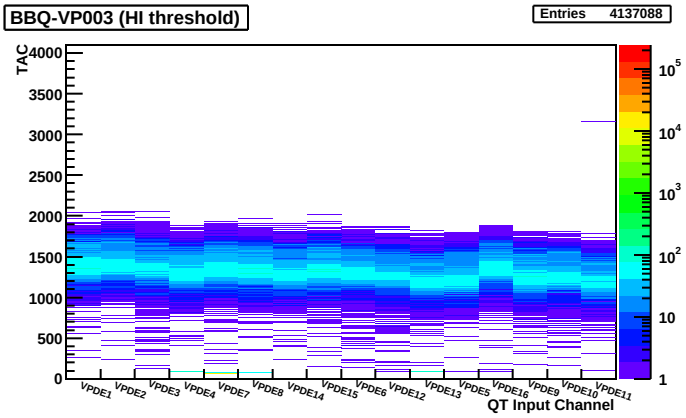

BBQ-VP001 (LO threshold)
Entries 4137088
ADC
QT Input Channel

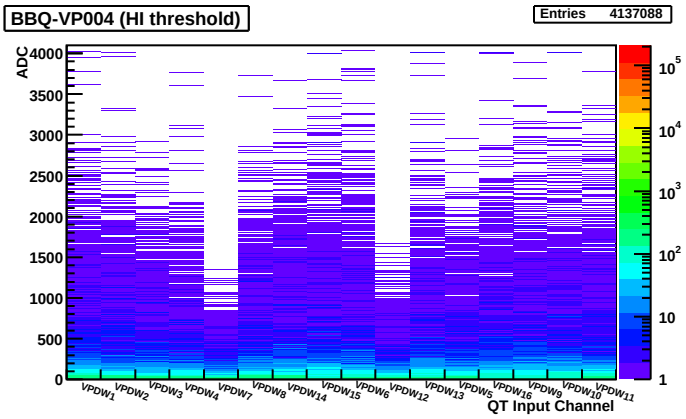

BBQ-VP001 (LO threshold)
Entries 4137088
TAC
QT Input Channel

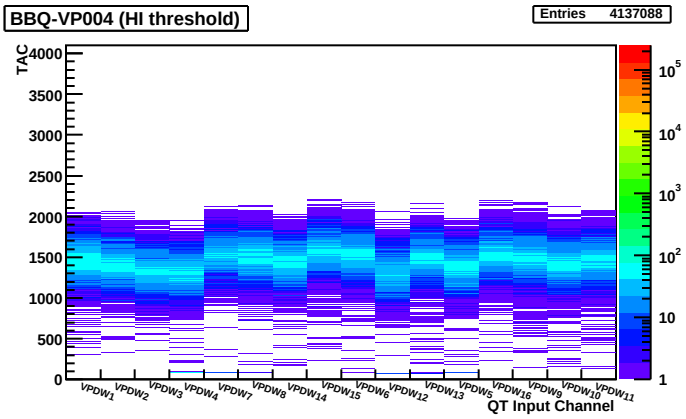

BBQ-VP002 (LO threshold)
Entries 4137088
ADC
QT Input Channel

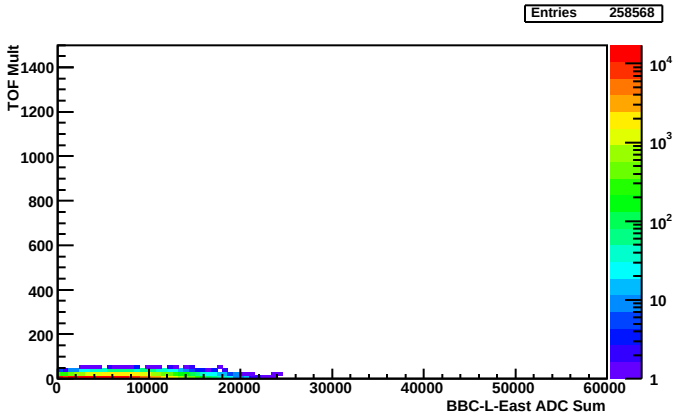

BBQ-VP002 (LO threshold)
Entries 4137088
TAC
QT Input Channel

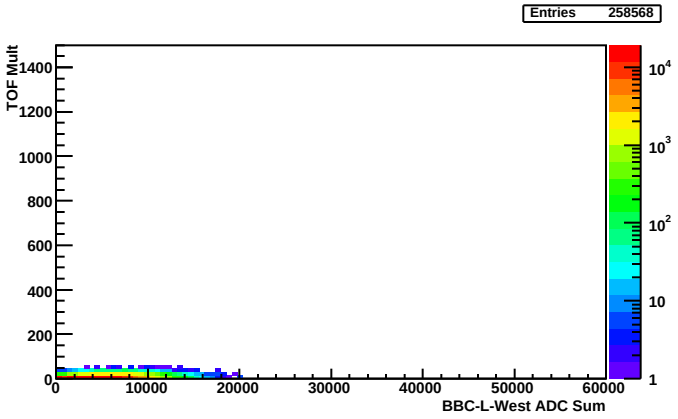

BBQ-VP003 (HI threshold)
Entries 4137088
ADC
QT Input Channel

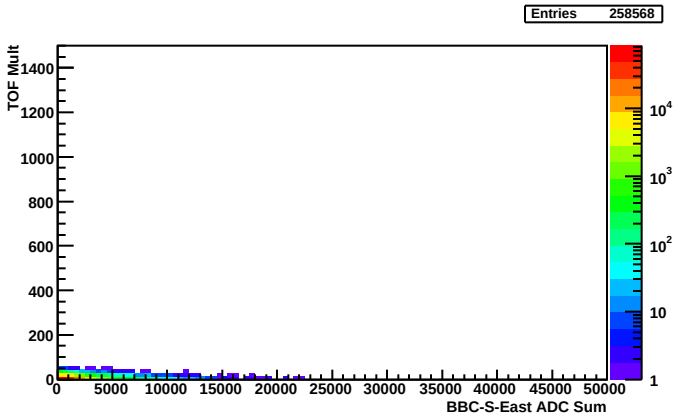

BBQ-VP003 (HI threshold)
Entries 4137088
TAC
QT Input Channel

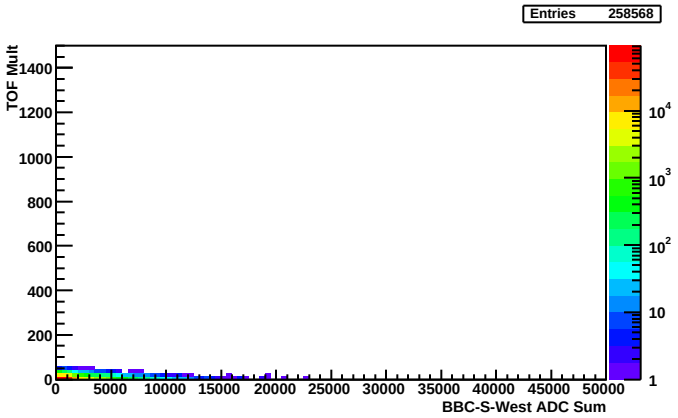

BBQ-VP004 (HI threshold)
Entries 4137088
ADC
QT Input Channel

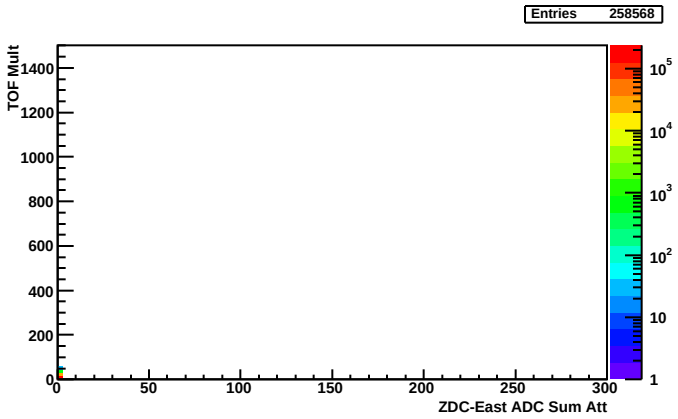

BBQ-VP004 (HI threshold)
Entries 4137088
TAC
QT Input Channel

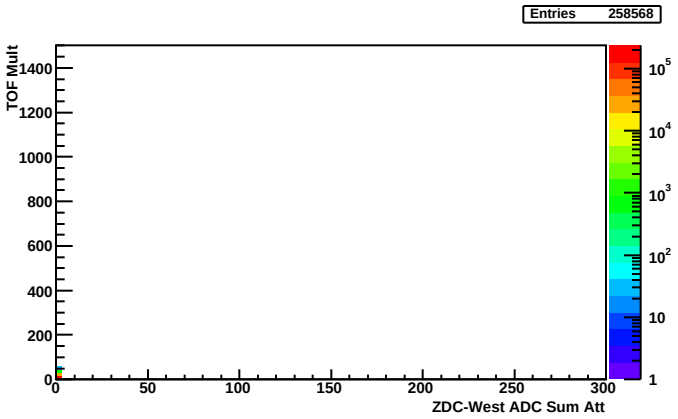

Entries 258568
TOF Mult
BBC-L-East ADC Sum

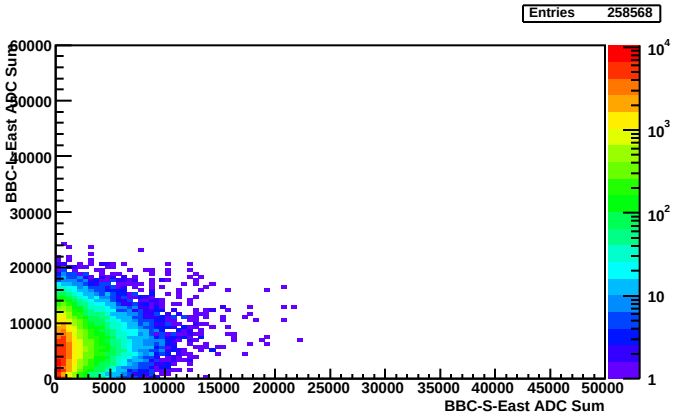

Entries 258568
TOF Mult
BBC-L-West ADC Sum

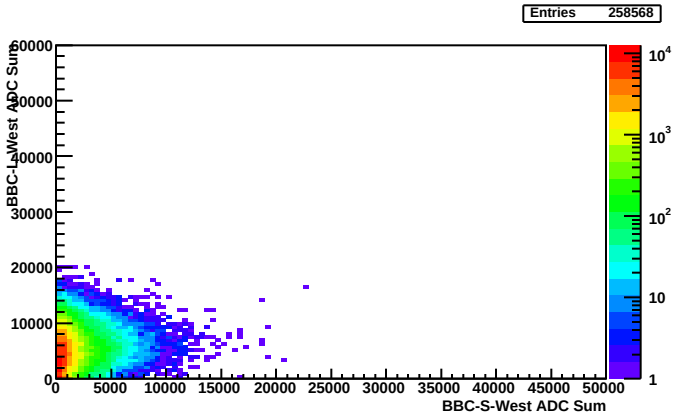

Entries 258568
TOF Mult
BBC-S-East ADC Sum

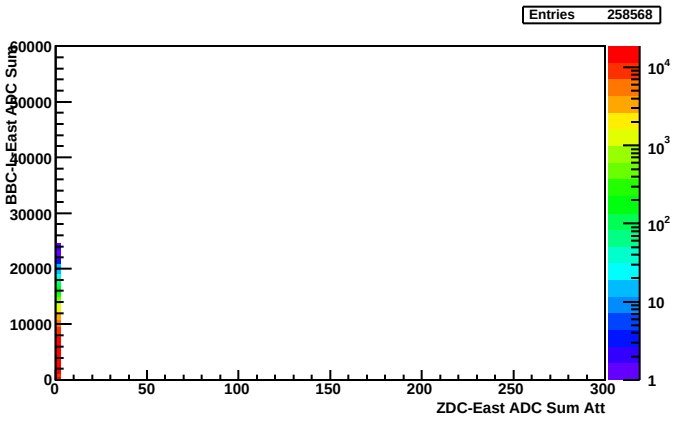

Entries 258568
TOF Mult
BBC-S-West ADC Sum

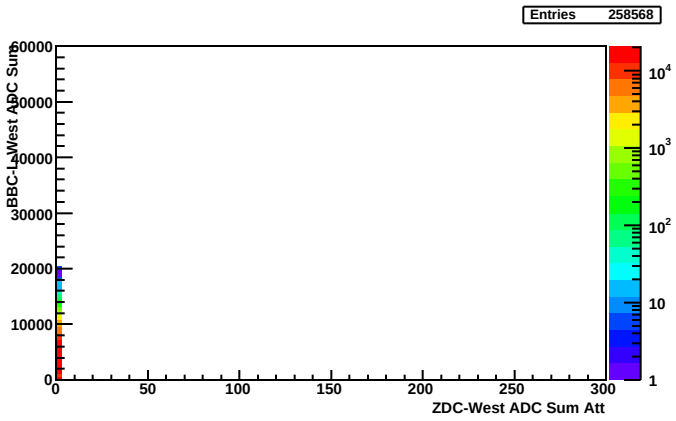

Entries 258568
TOF Mult
ZDC-East ADC Sum Att

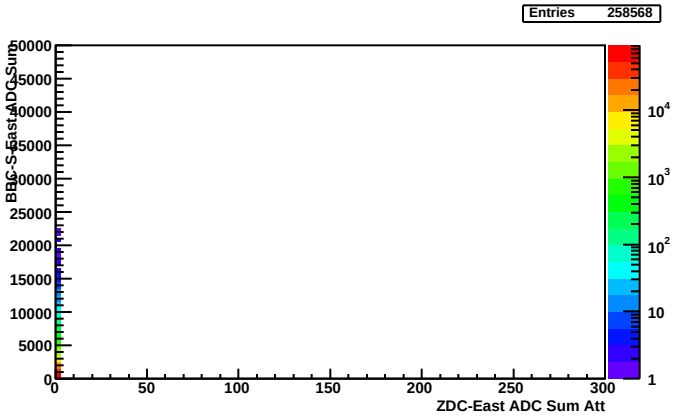

Entries 258568
TOF Mult
ZDC-West ADC Sum Att

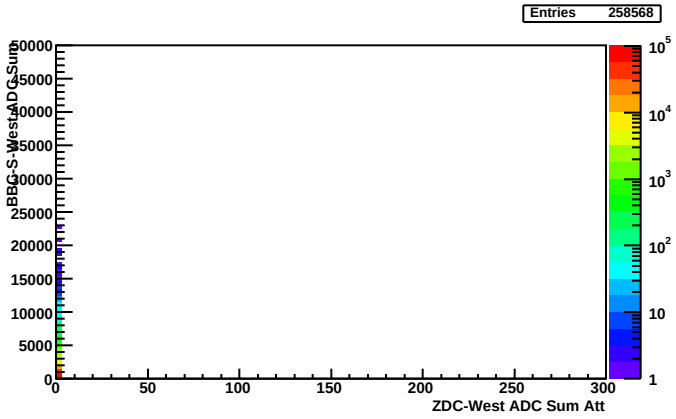

Entries 258568
BBC-L-East ADC Sum
BBC-S-East ADC Sum

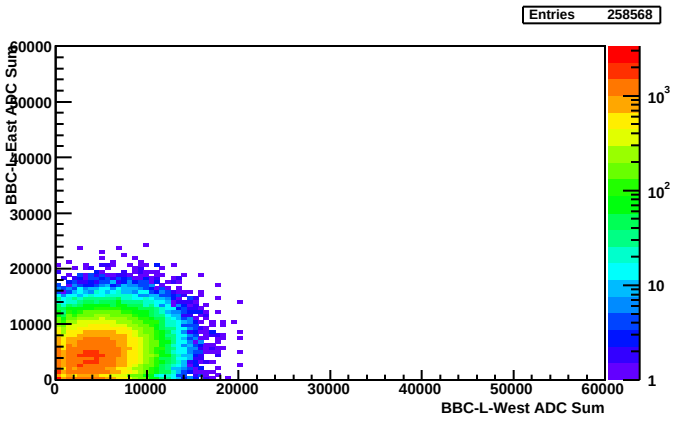

Entries 258568
BBC-L-West ADC Sum
BBC-S-West ADC Sum

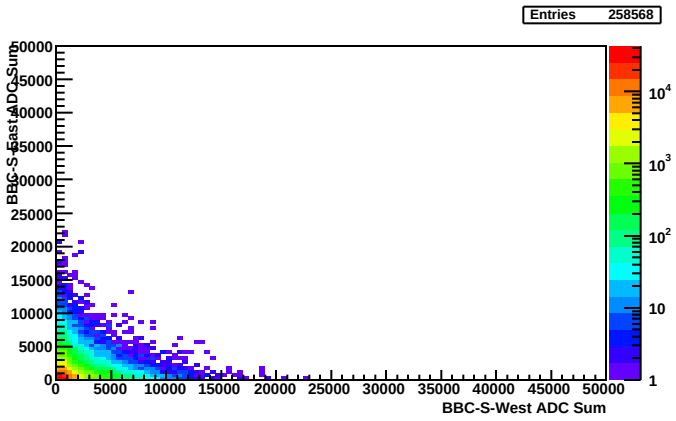

Entries 258568
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

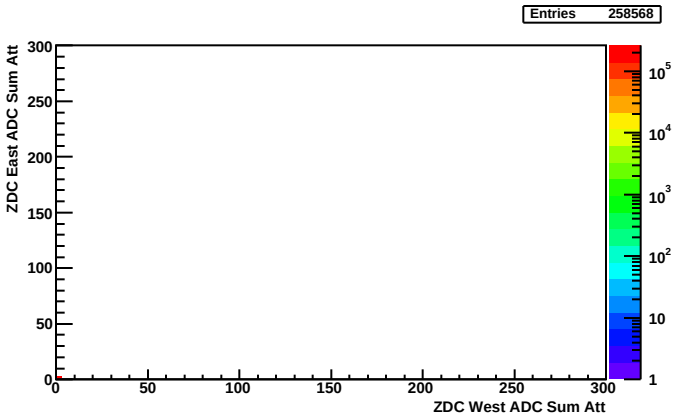

Entries 258568
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

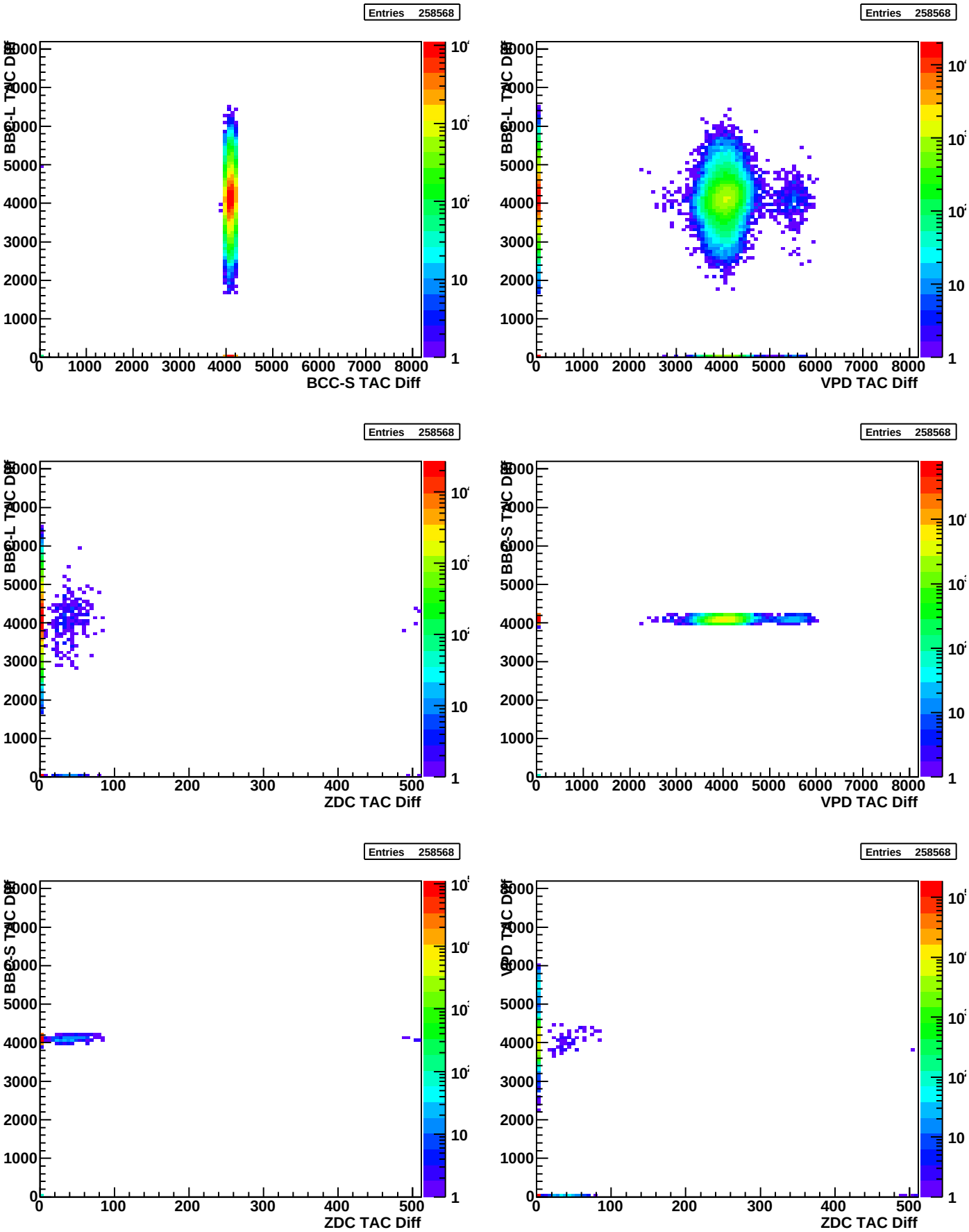

Entries 258568
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

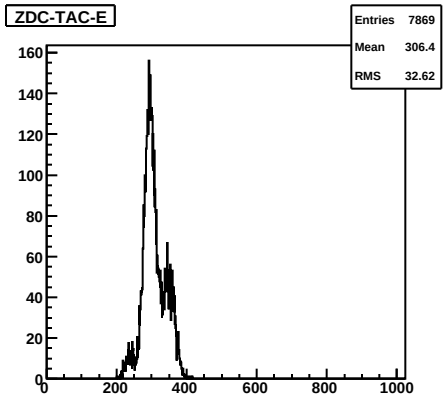

Entries 258568
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

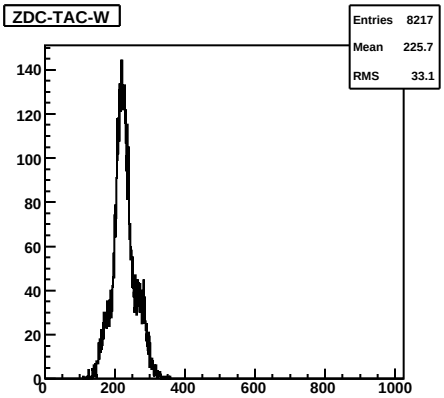

Entries 258568
BBC-L-East ADC Sum
BBC-L-West ADC Sum

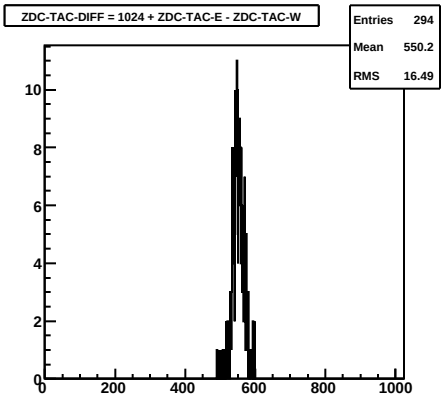

Entries 258568
BBC-S-East ADC Sum
BBC-S-West ADC Sum

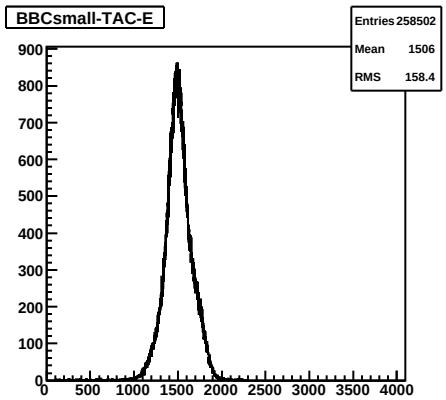

Entries 258568
ZDC East ADC Sum Att
ZDC West ADC Sum Att

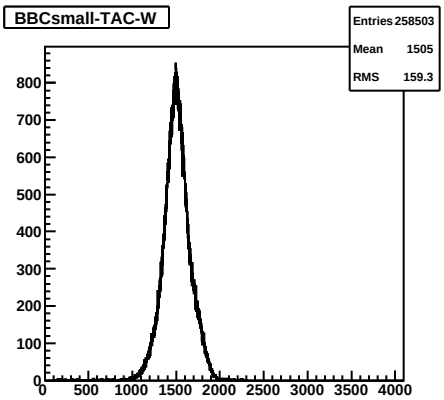

Entries 258568
BBC-L TAC Diff
BCC-S TAC Diff
Entries 258568
BBC-L TAC Diff
VPD TAC Diff
Entries 258568
BBC-L TAC Diff
ZDC TAC Diff
Entries 258568
BBC-S TAC Diff
VPD TAC Diff
Entries 258568
BBC-S TAC Diff
ZDC TAC Diff
Entries 258568
VPD TAC Diff
ZDC TAC Diff

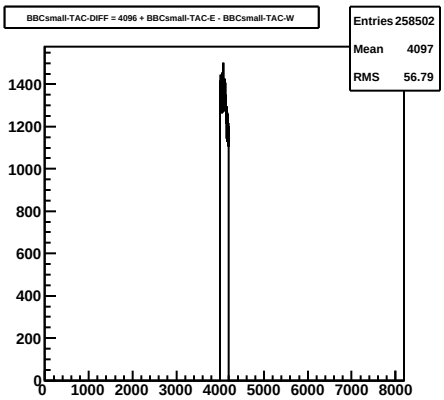

ZDC-TAC-E
Entries 7869
Mean 306.4
RMS 32.62

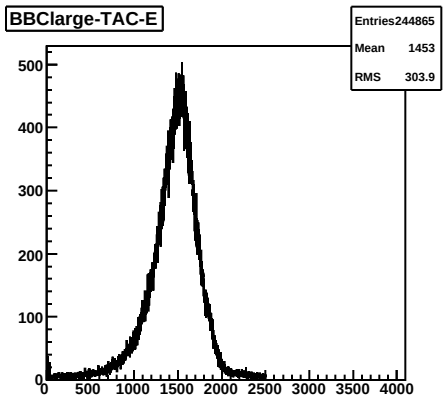

ZDC-TAC-W
Entries 8217
Mean 225.7
RMS 33.1

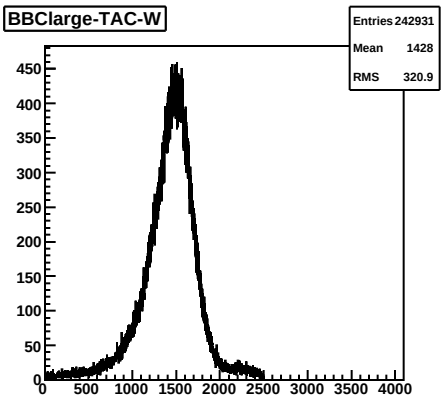

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 294
Mean 550.2
RMS 16.49

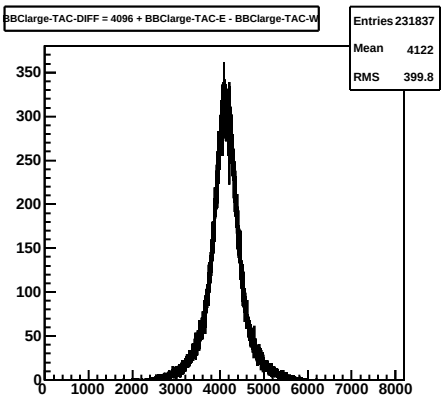

BBCsmall-TAC-E
Entries 258502
Mean 1506
RMS 158.4

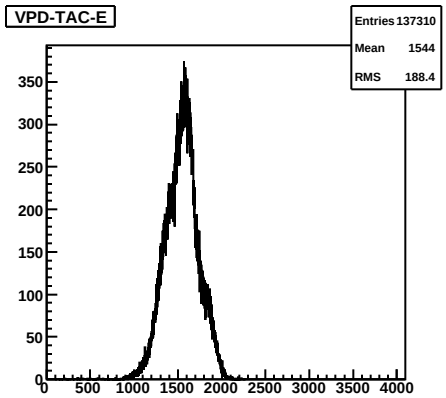

BBCsmall-TAC-W
Entries 258503
Mean 1505
RMS 159.3

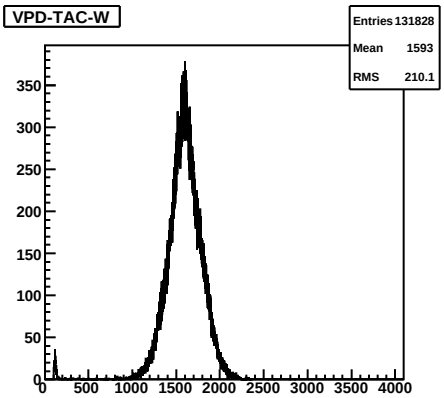

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 258502
Mean 4097
RMS 56.79

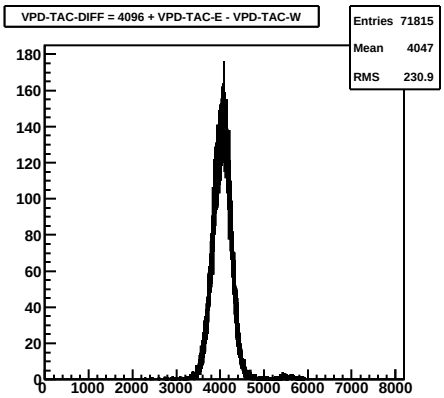

BBClarge-TAC-E
Entries 244865
Mean 1453
RMS 303.9

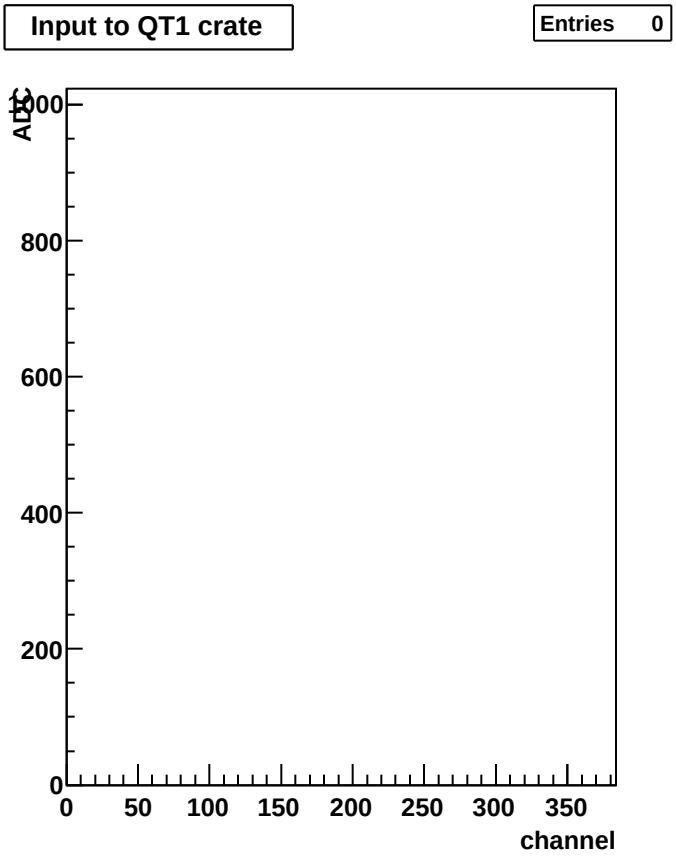

BBClarge-TAC-W
Entries 242931
Mean 1428
RMS 320.9

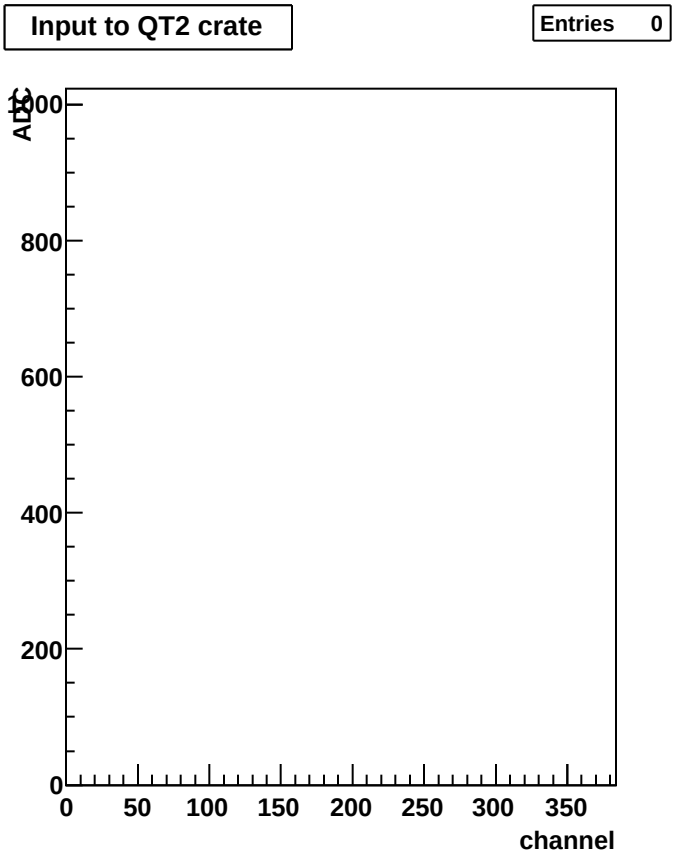

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 231837
Mean 4122
RMS 399.8

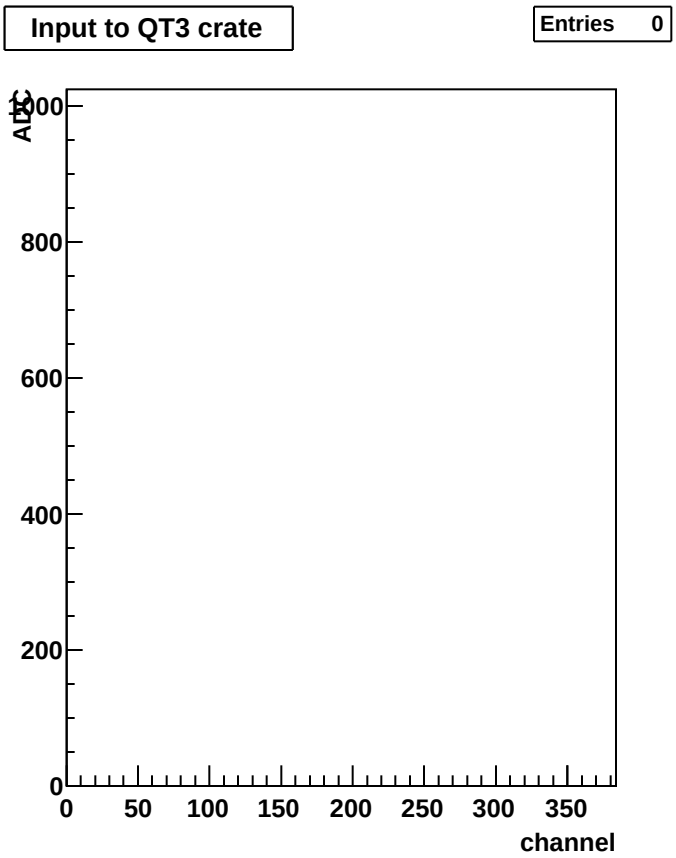

VPD-TAC-E
Entries 137310
Mean 1544
RMS 188.4

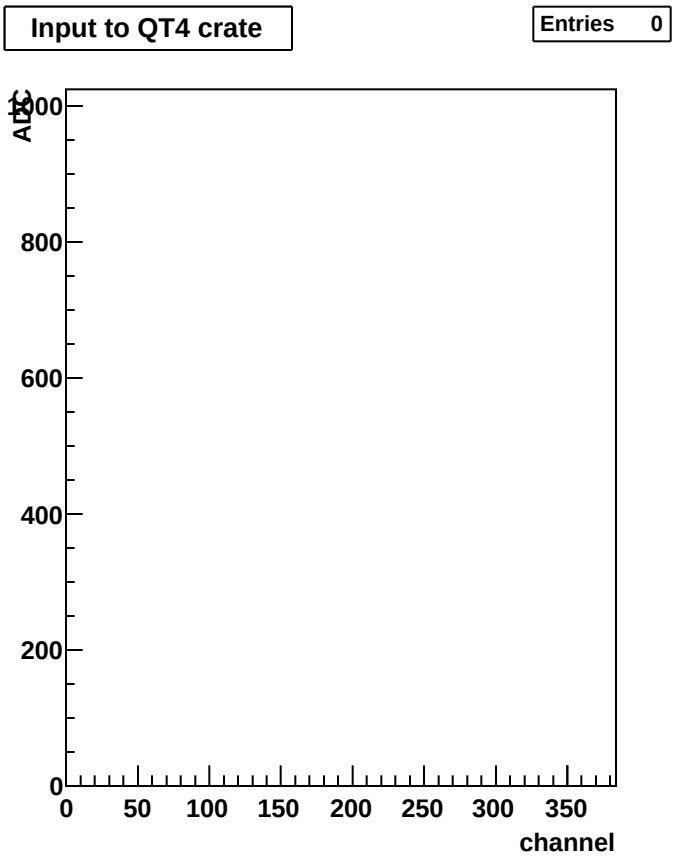

VPD-TAC-W
Entries 131828
Mean 1593
RMS 210.1

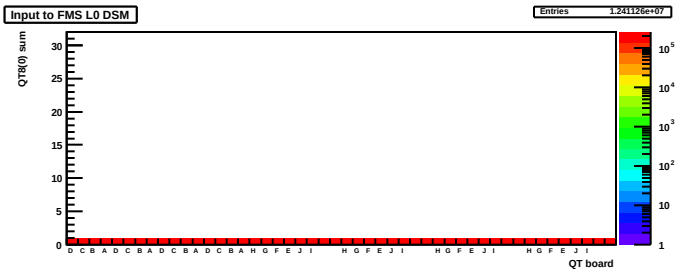

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 71815
Mean 4047
RMS 230.9

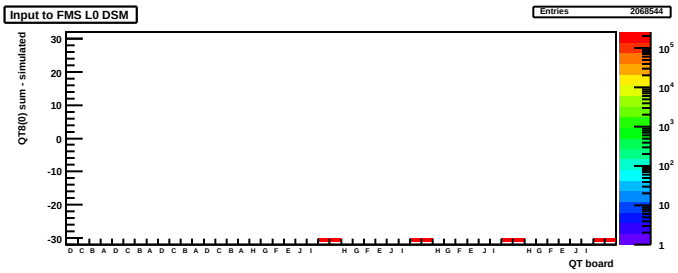

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

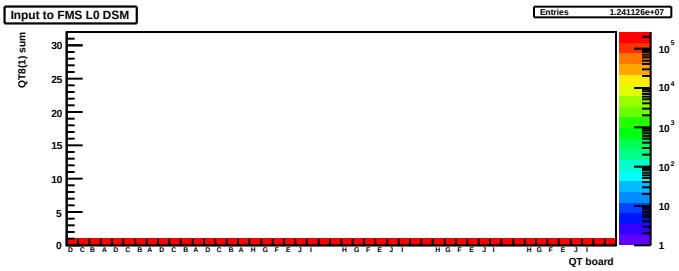

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

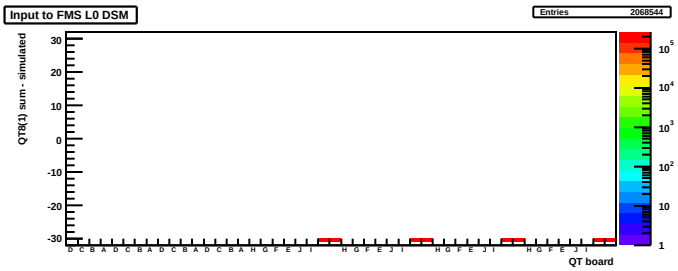

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

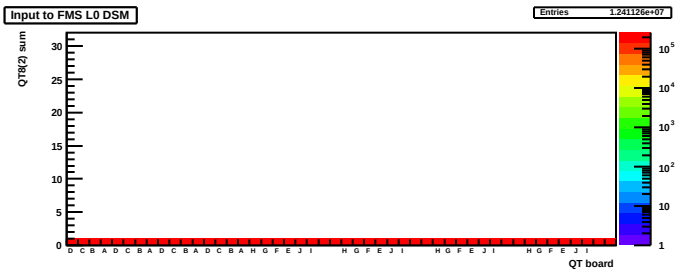

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

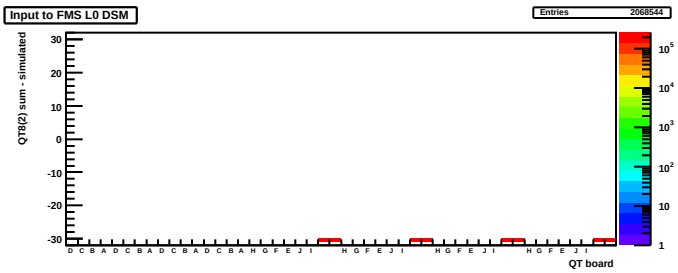

Input to FMS L0 DSM
Entries 1.241126e+07
QT8(0) sum
QT board

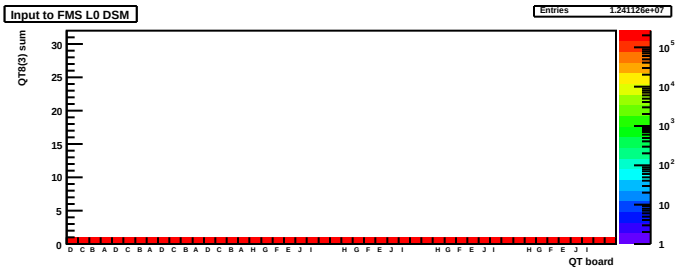

Input to FMS L0 DSM
Entries 2068544
QT8(0) sum - simulated
QT board

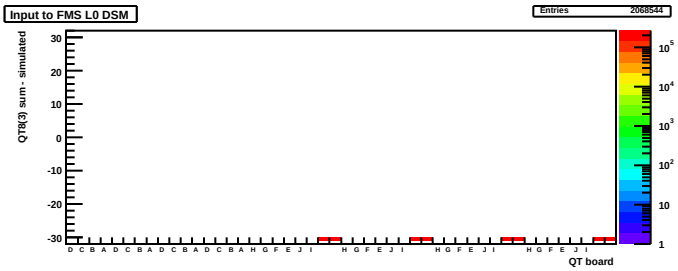

Input to FMS L0 DSM
Entries 1.241126e+07
QT8(1) sum
QT board

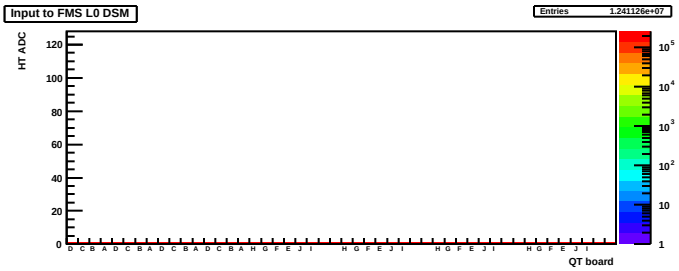

Input to FMS L0 DSM
Entries 2068544
QT8(1) sum - simulated
QT board

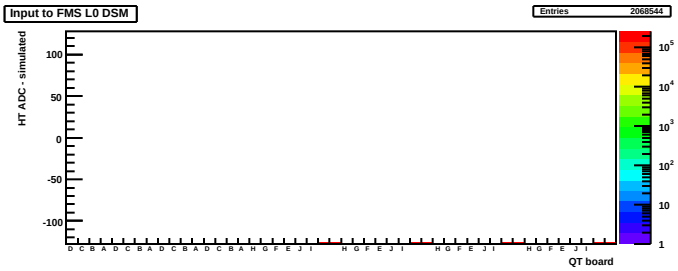

Input to FMS L0 DSM
Entries 1.241126e+07
QT8(2) sum
QT board

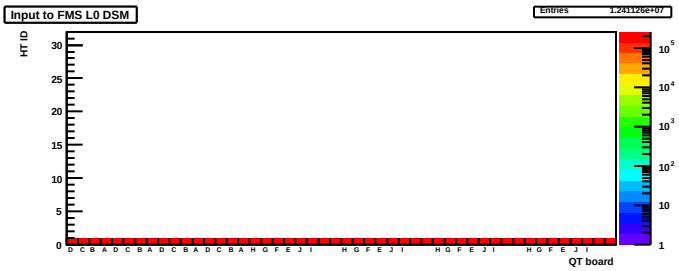

Input to FMS L0 DSM
Entries 2068544
QT8(2) sum - simulated
QT board

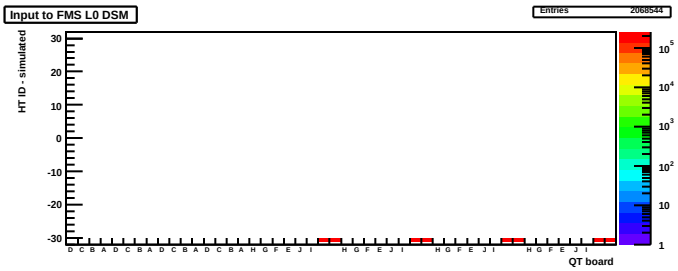

Input to FMS L0 DSM
Entries 1.241126e+07
QT8(3) sum
QT board

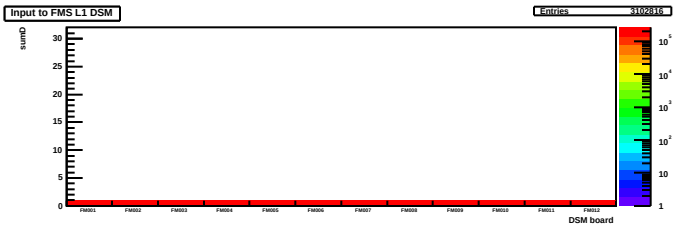

Input to FMS L0 DSM
Entries 2068544
QT8(3) sum - simulated
QT board

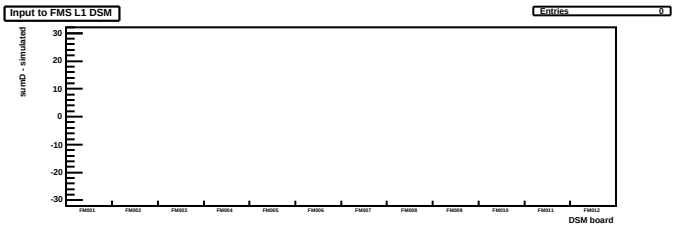

Input to FMS L0 DSM
Entries 1.241126e+07
HT ADC
QT board

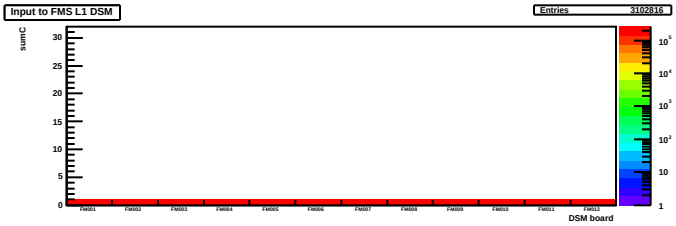

Input to FMS L0 DSM
Entries 2068544
HT ADC - simulated
QT board

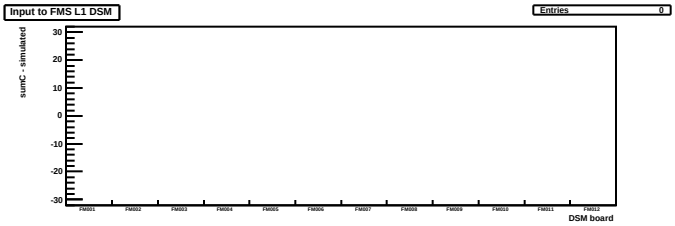

Input to FMS L0 DSM
Entries 1.241126e+07
HT ID
QT board

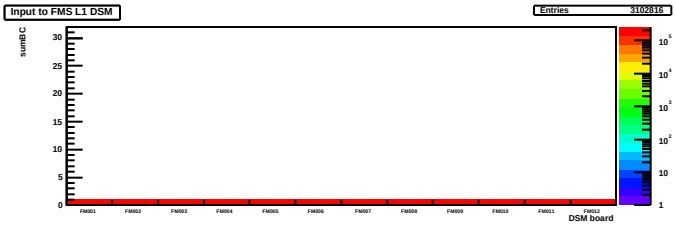

Input to FMS L0 DSM
Entries 2068544
HT ID - simulated
QT board

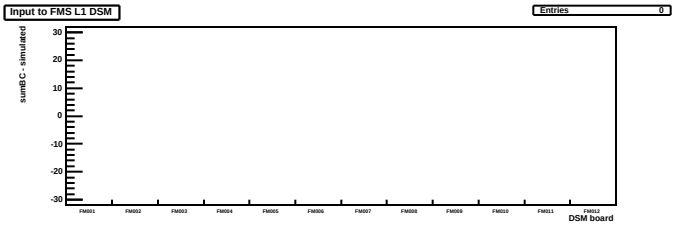

Input to FMS L1 DSM
Entries 3102816
sumD
DSM board

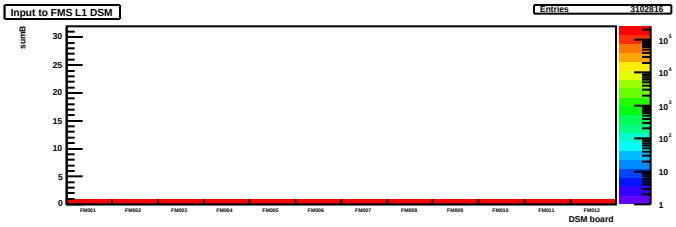

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

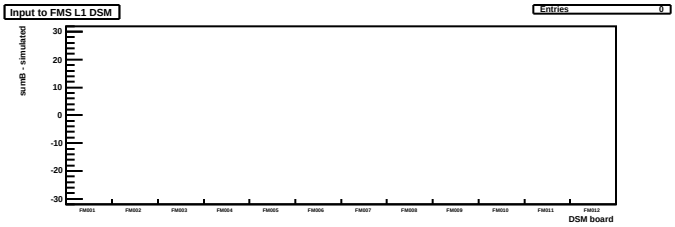

Input to FMS L1 DSM
Entries 3102816
sumC
DSM board

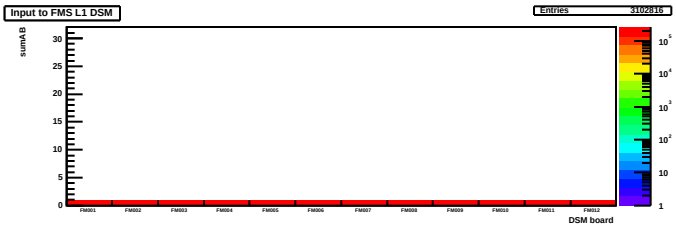

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

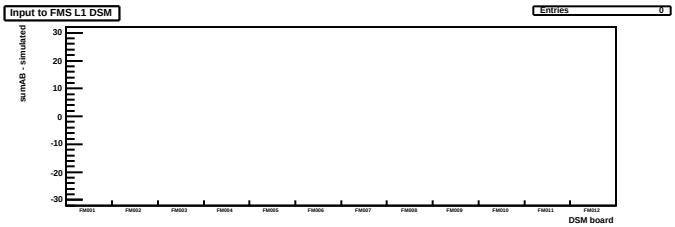

Input to FMS L1 DSM
Entries 3102816
sumBC
DSM board

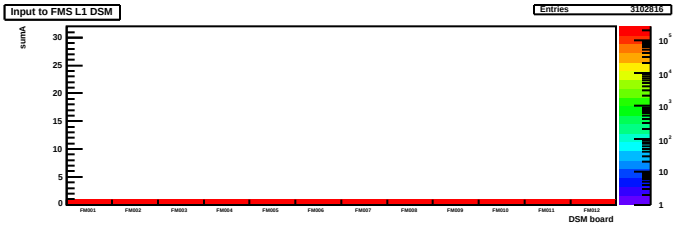

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

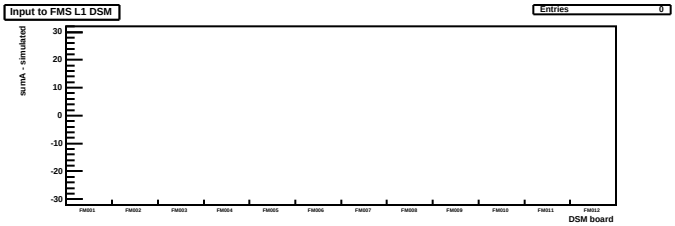

Input to FMS L1 DSM
Entries 3102816
sumB
DSM board

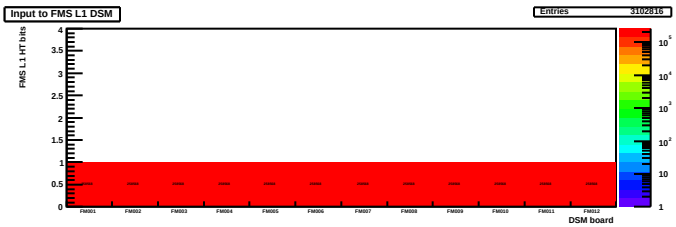

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

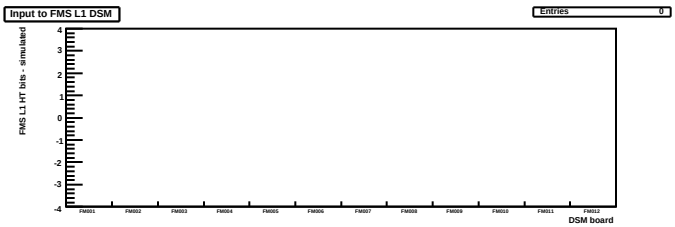

Input to FMS L1 DSM
Entries 3102816
sumAB
DSM board

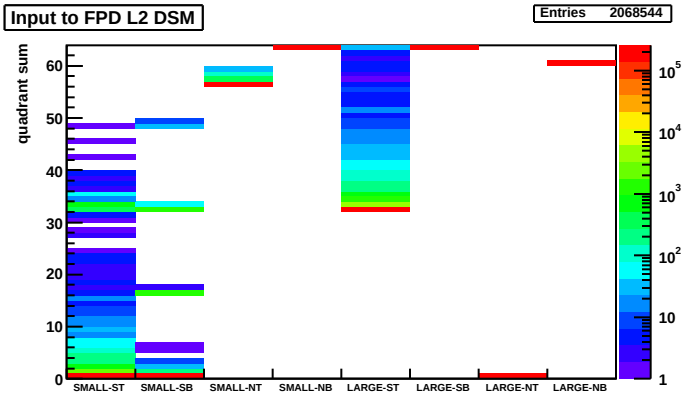

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

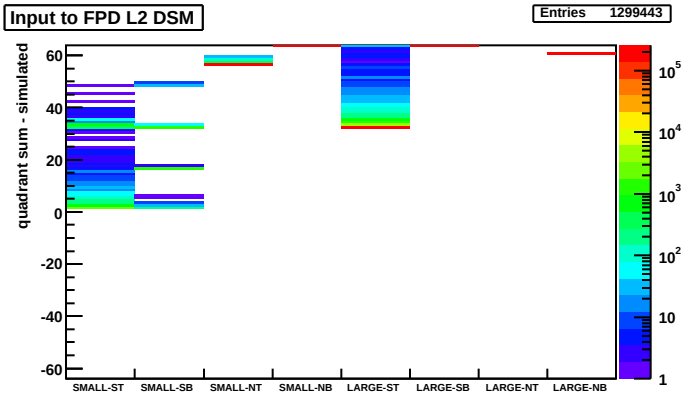

Input to FMS L1 DSM
Entries 3102816
sumA
DSM board

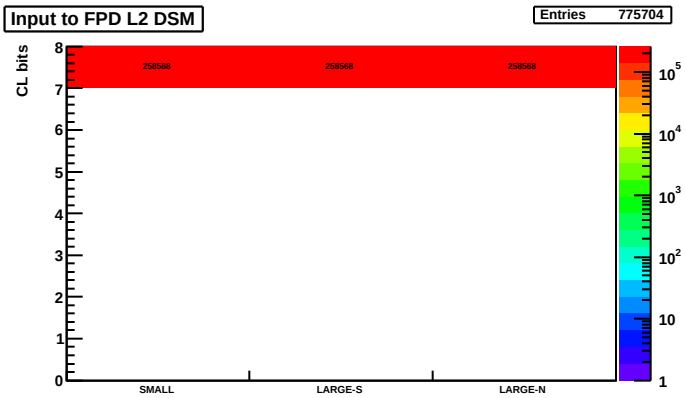

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

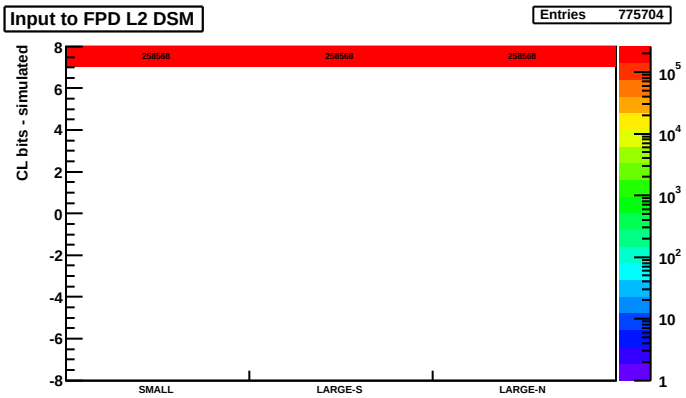

Input to FMS L1 DSM
Entries 3102816
FMS L1 HT bits
DSM board

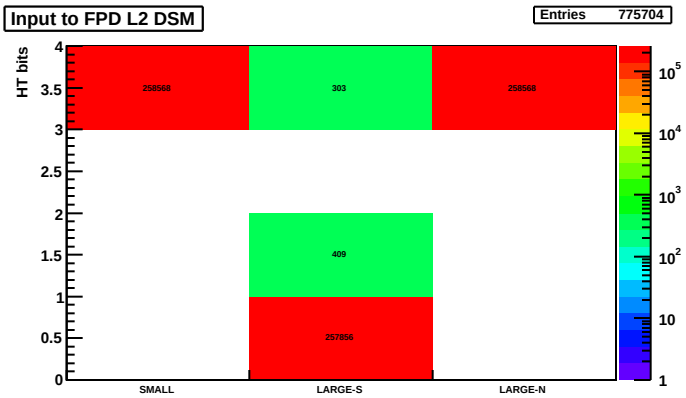

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

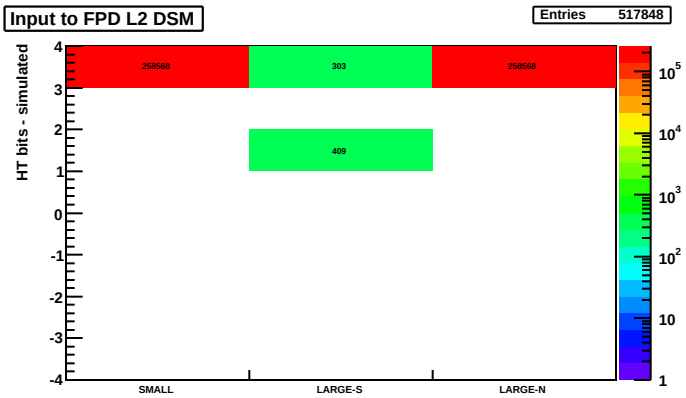

Input to FPD L2 DSM
Entries 2068544
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

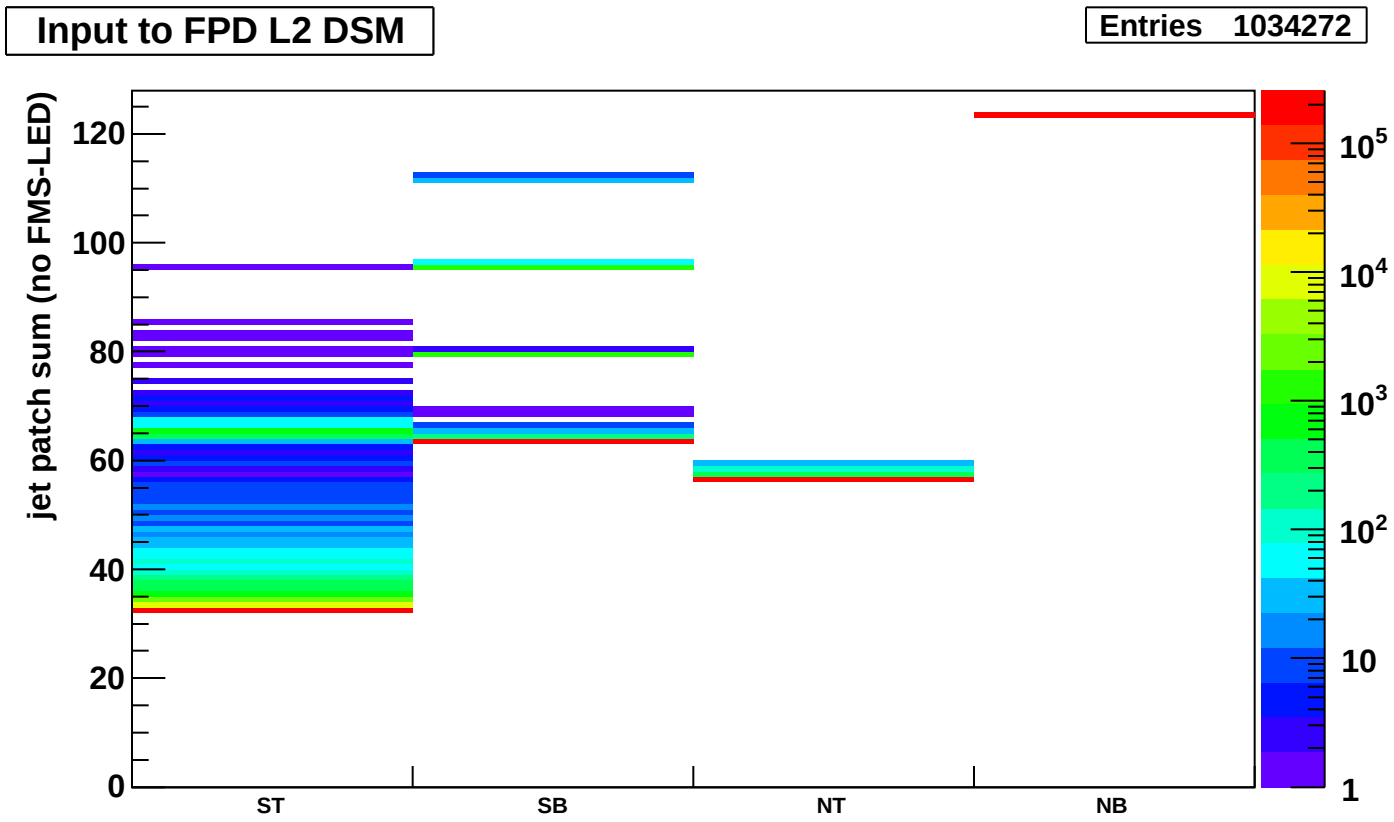

Input to FPD L2 DSM
Entries 1299443
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

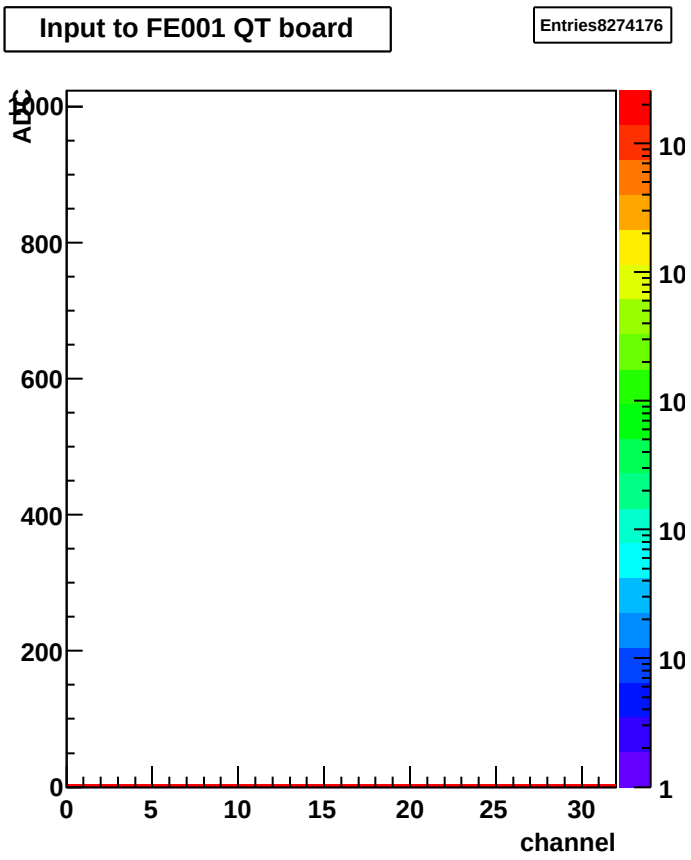

Input to FPD L2 DSM
Entries 775704
CL bits
258568
258568
258568
SMALL
LARGE-S
LARGE-N

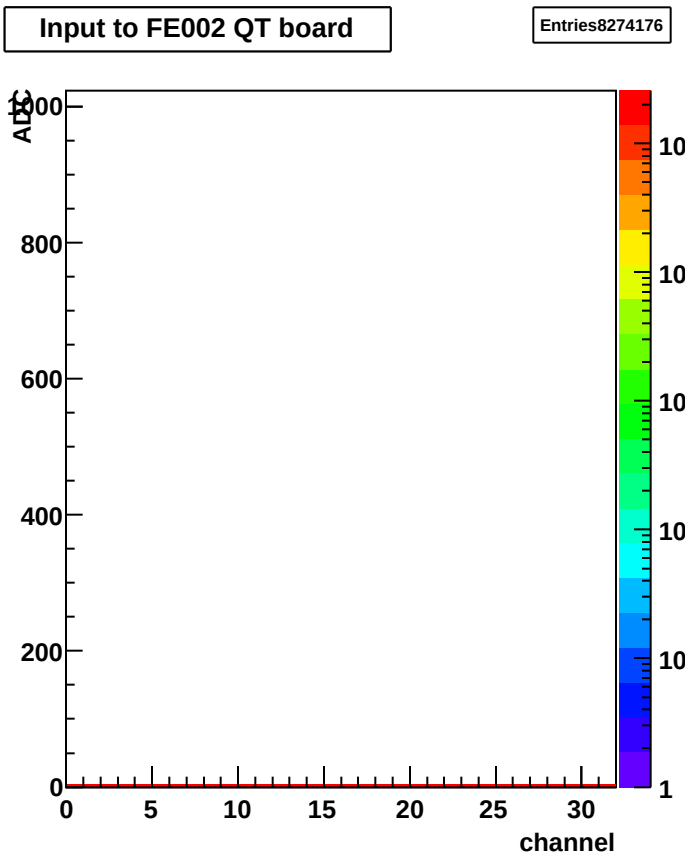

Input to FPD L2 DSM
Entries 775704
CL bits - simulated
258568
258568
258568
SMALL
LARGE-S
LARGE-N

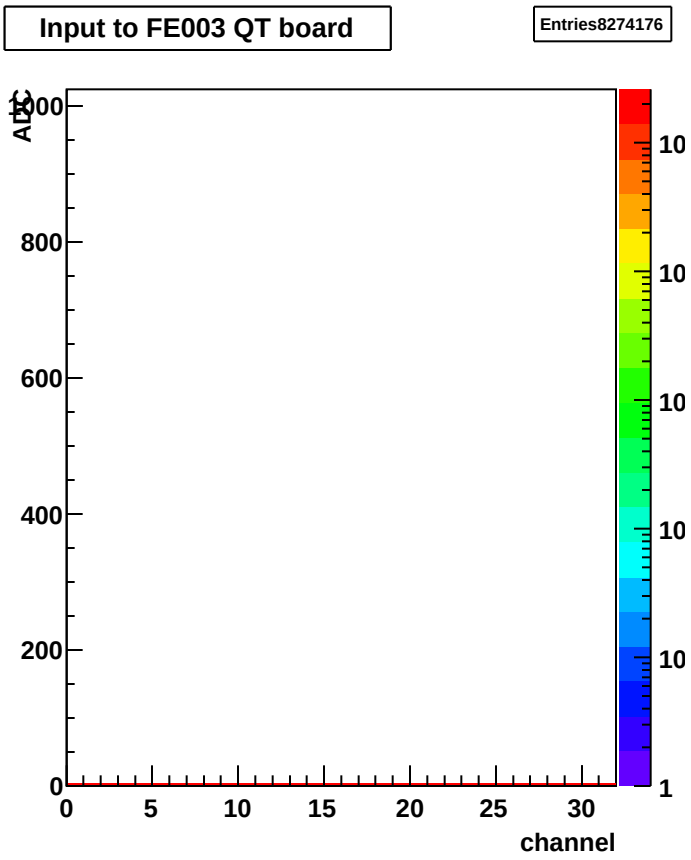

Input to FPD L2 DSM
Entries 775704
HT bits
258568
303
258568
409
257856
SMALL
LARGE-S
LARGE-N

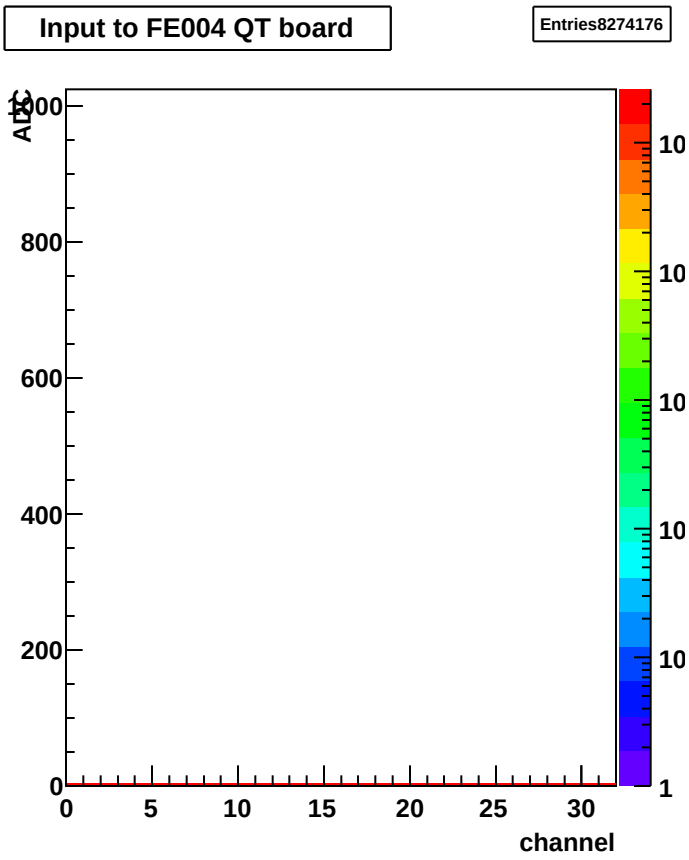

Input to FPD L2 DSM
Entries 517848
HT bits - simulated
258568
303
258568
409
SMALL
LARGE-S
LARGE-N

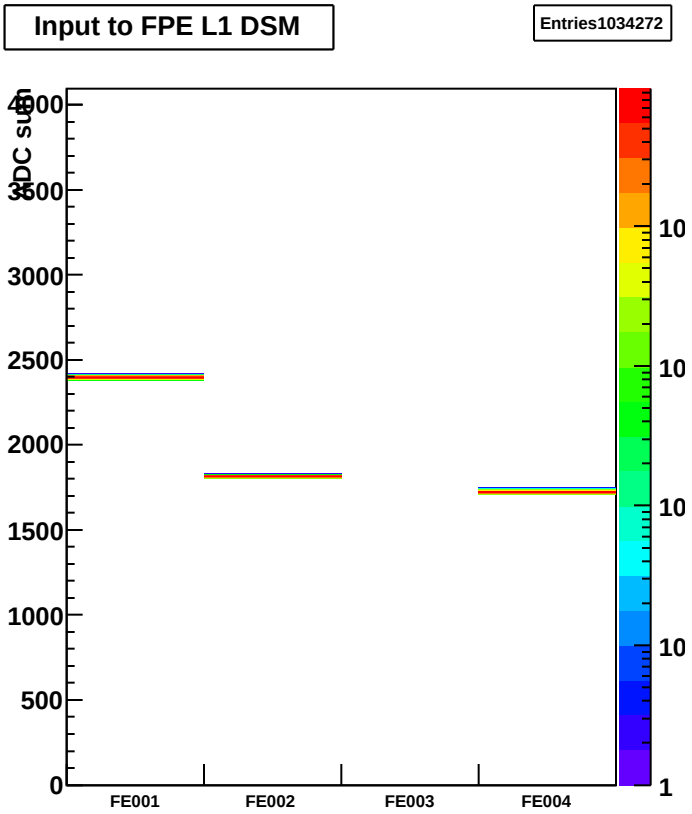

Input to FPD L2 DSM
Entries 1034272
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

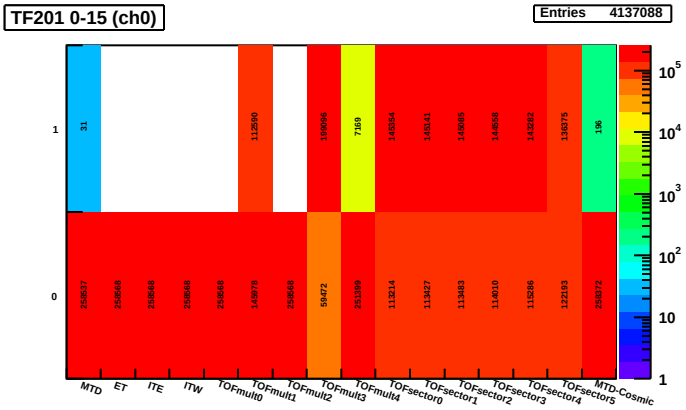

Input to FE001 QT board
Entries8274176
ADC
channel

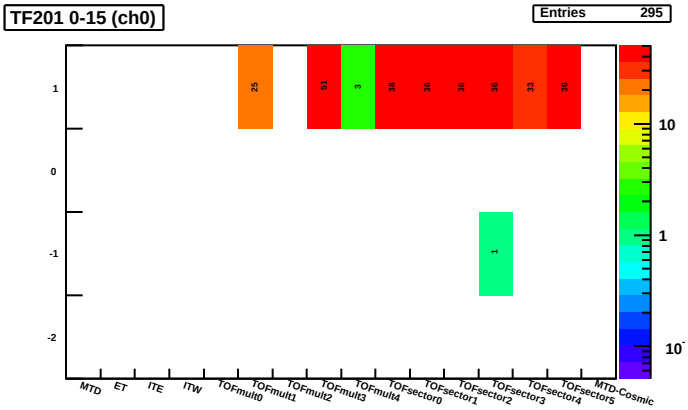

Input to FE002 QT board
Entries8274176
ADC
channel

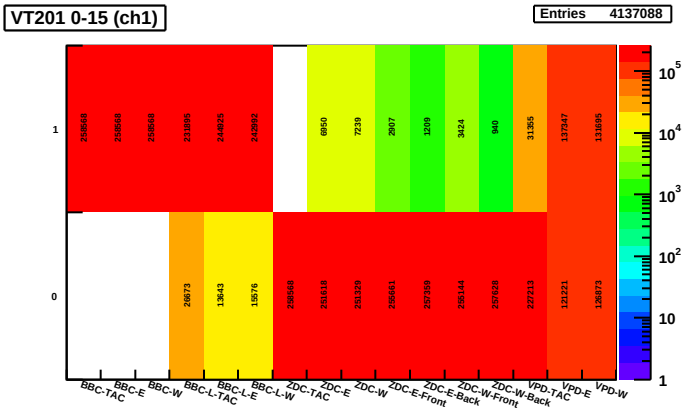

Input to FE003 QT board
Entries8274176
ADC
channel

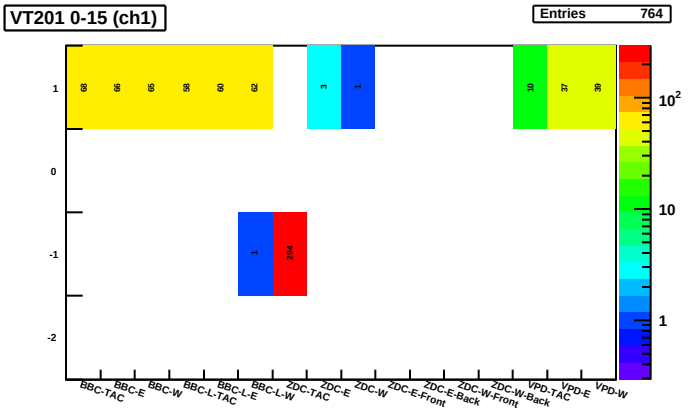

Input to FE004 QT board
Entries8274176
ADC
channel

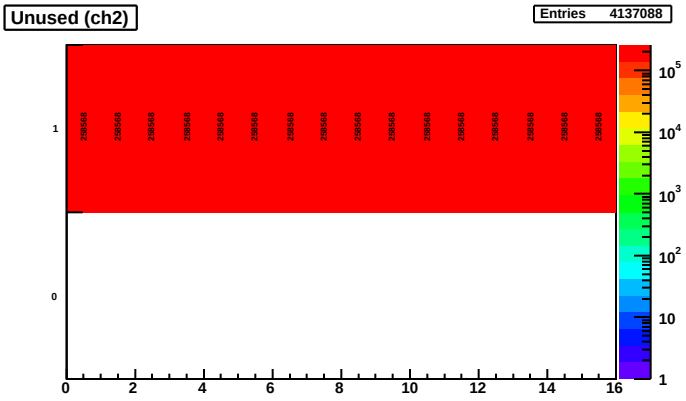

Input to FPE L1 DSM
Entries1034272
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

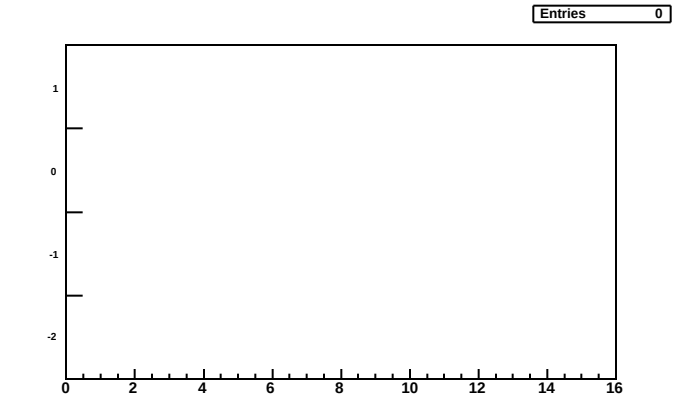

TF201 0-15 (ch0)
Entries 4137088
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

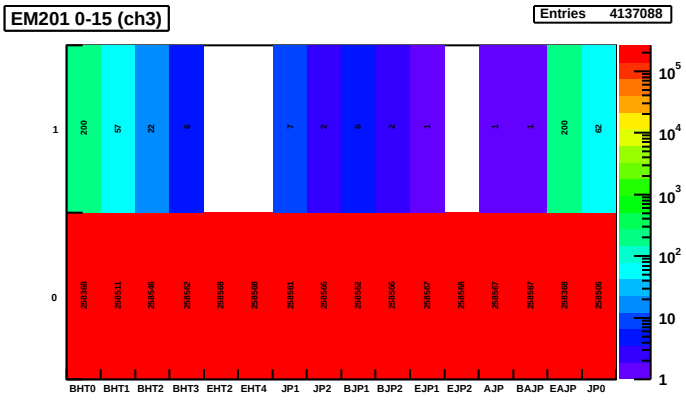

TF201 0-15 (ch0)
Entries 295
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

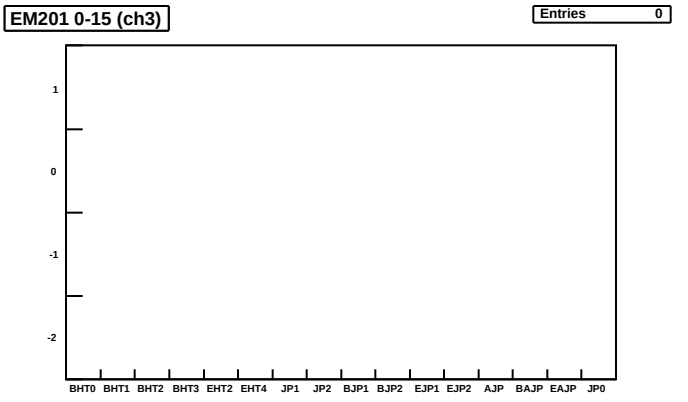

VT201 0-15 (ch1)
Entries 4137088
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 764
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

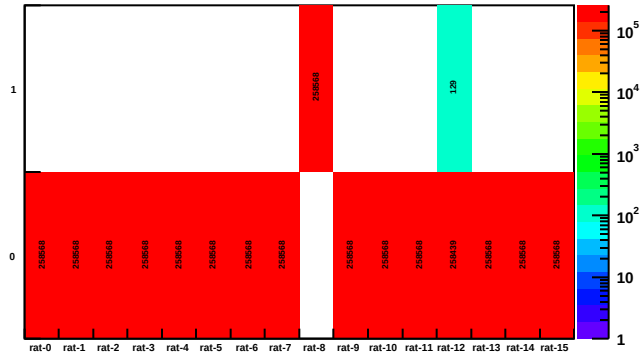

Unused (ch2)
Entries 4137088

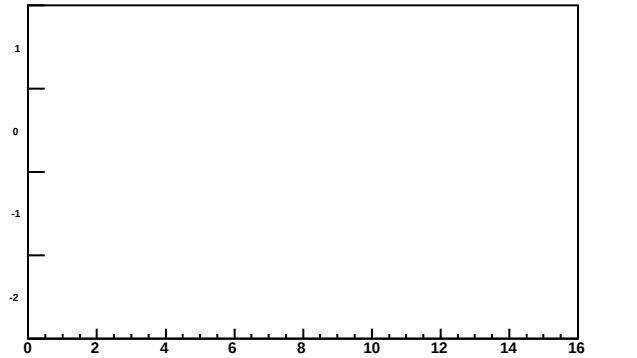

Entries 0


EM201 0-15 (ch3)
Entries 4137088
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

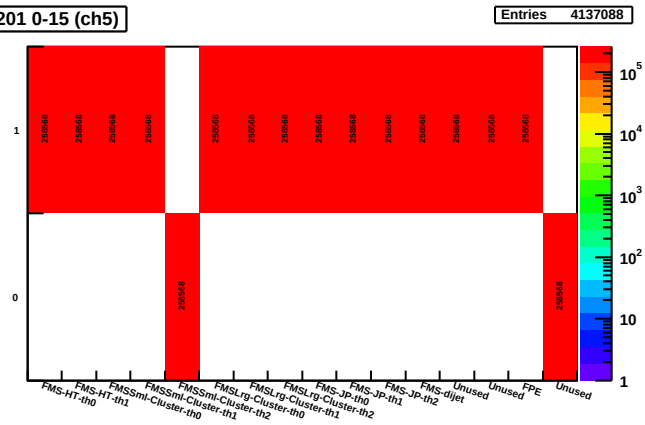

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

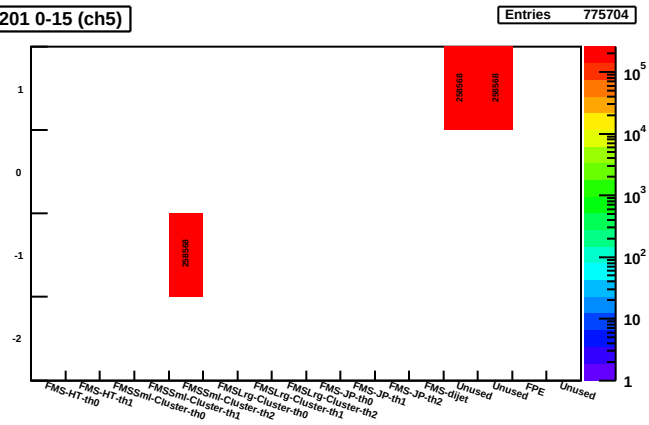




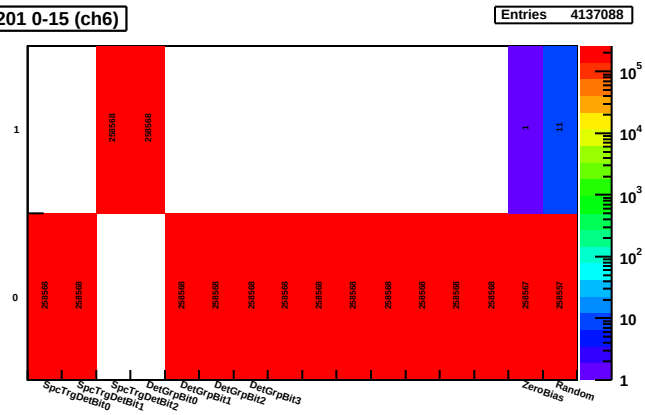
FP201 0-15 (ch5)

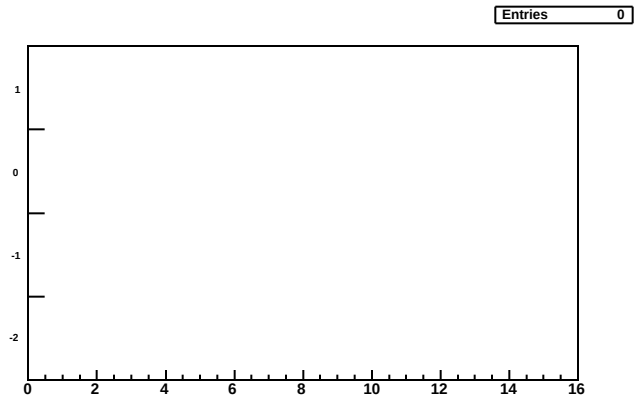
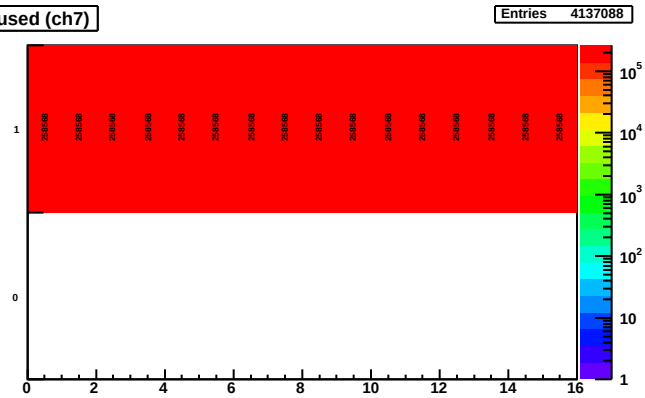
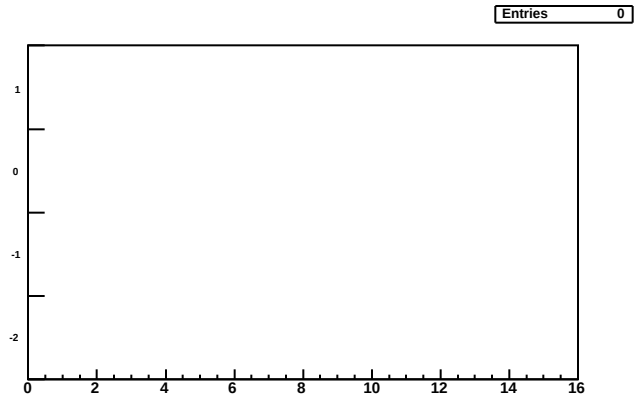


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)