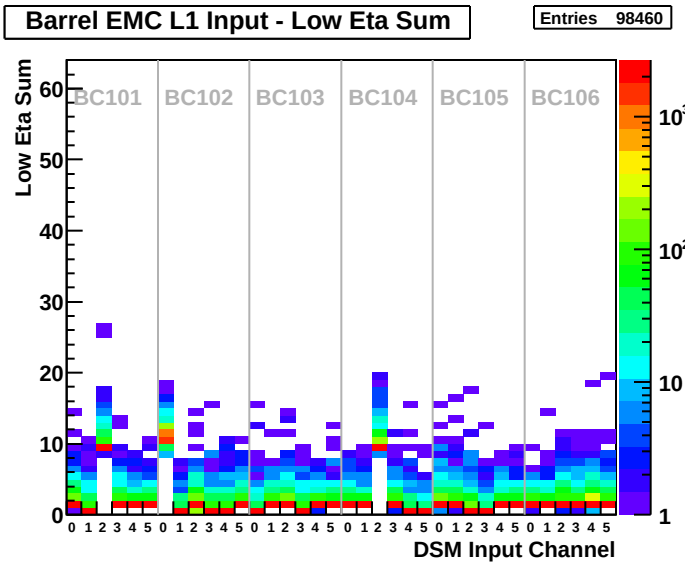
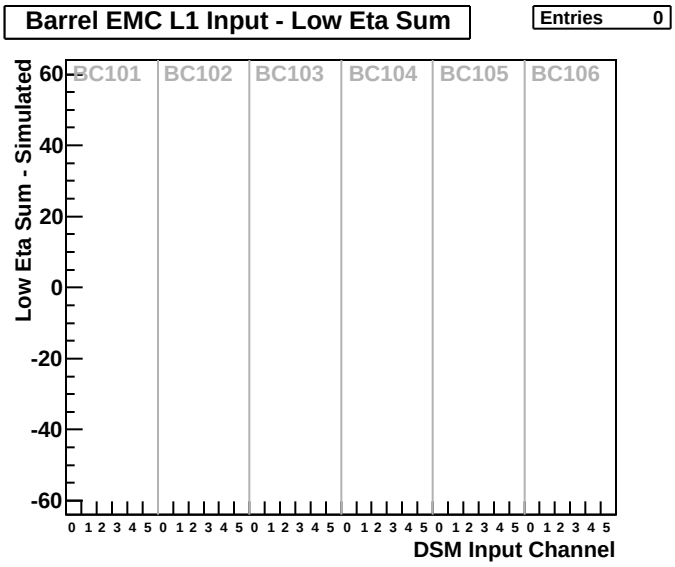
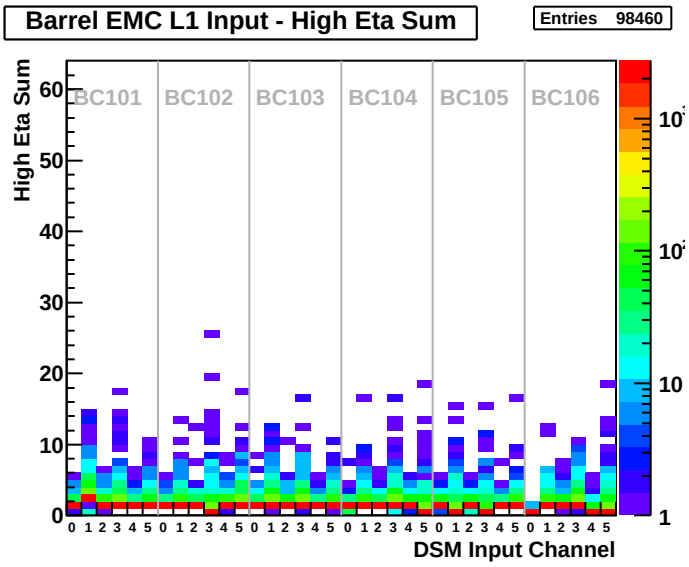
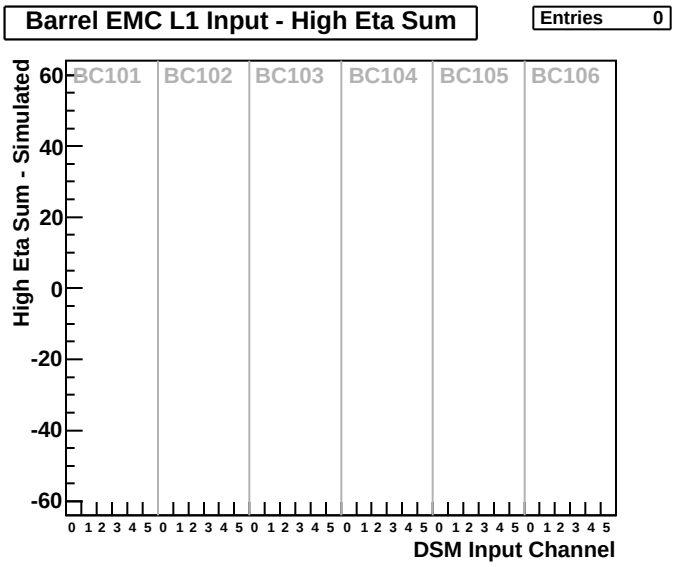
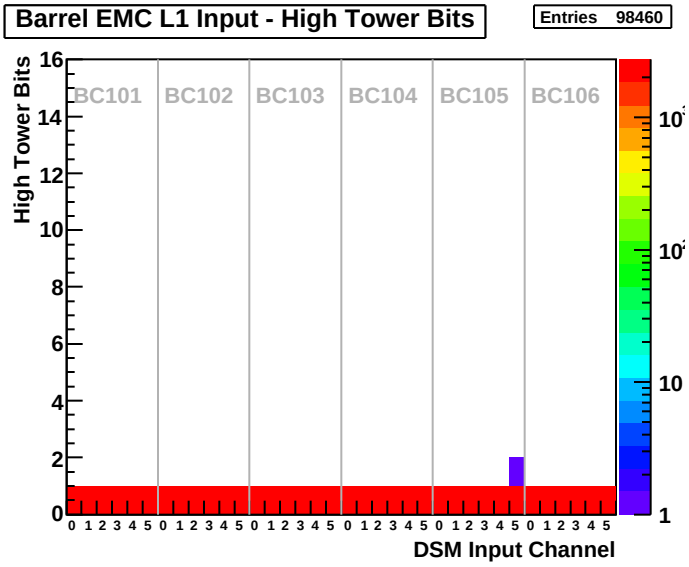
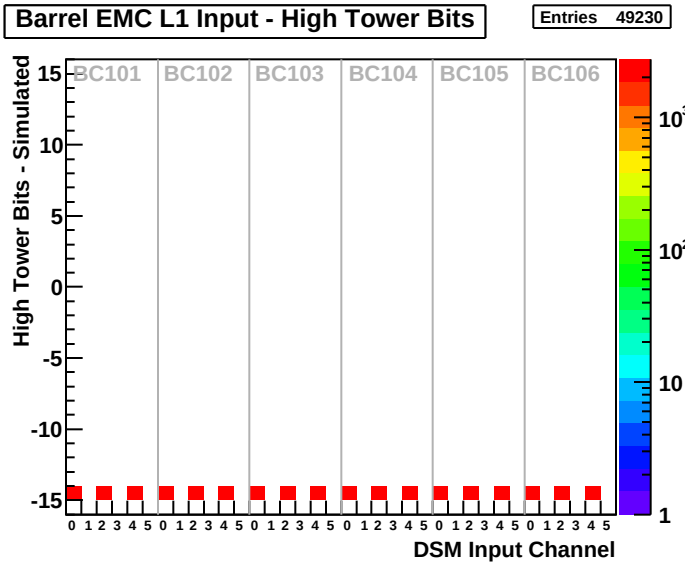

Barrel EMC L1 Input - Low Eta Sum
Entries 98460
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

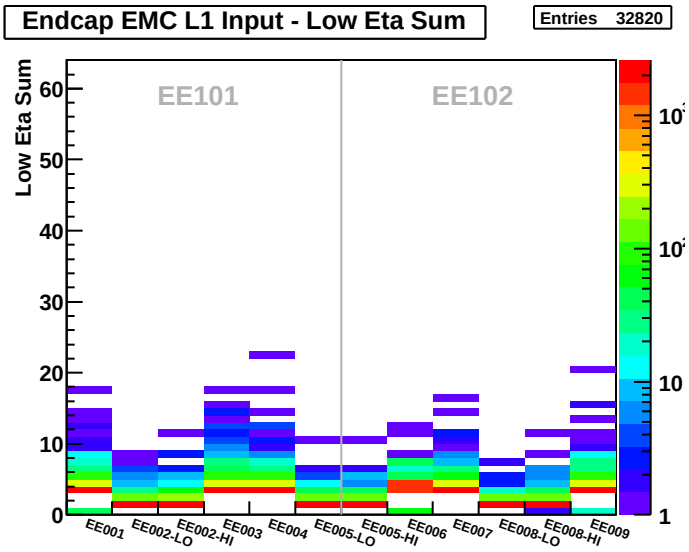

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

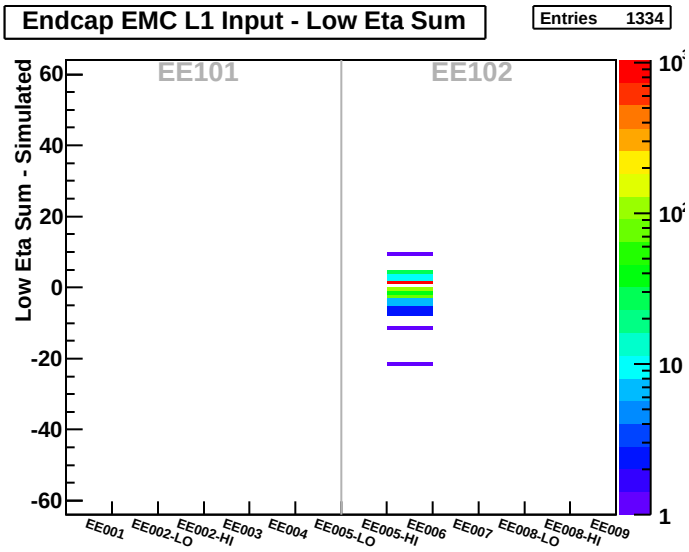

Barrel EMC L1 Input - High Eta Sum
Entries 98460
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

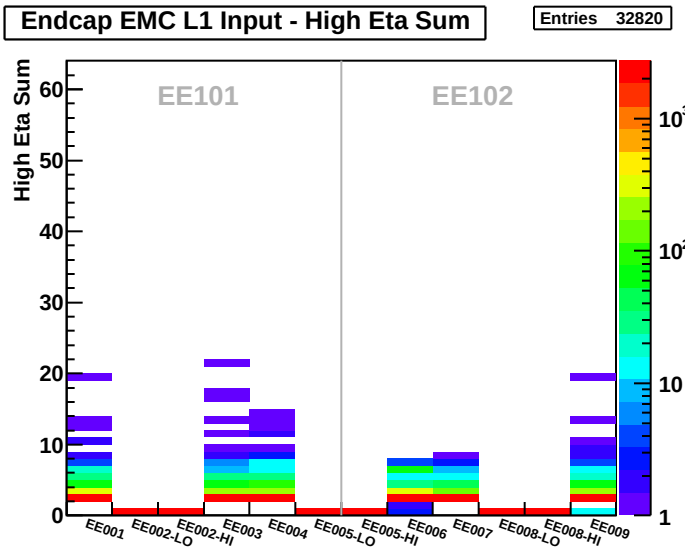

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

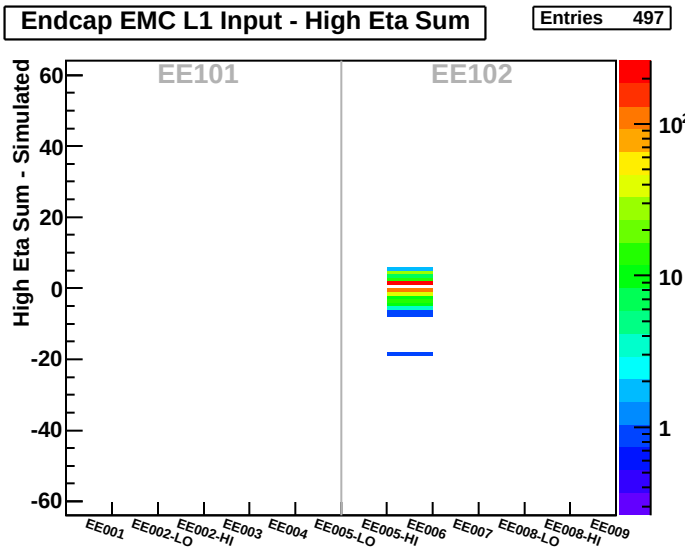

Barrel EMC L1 Input - High Tower Bits
Entries 98460
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

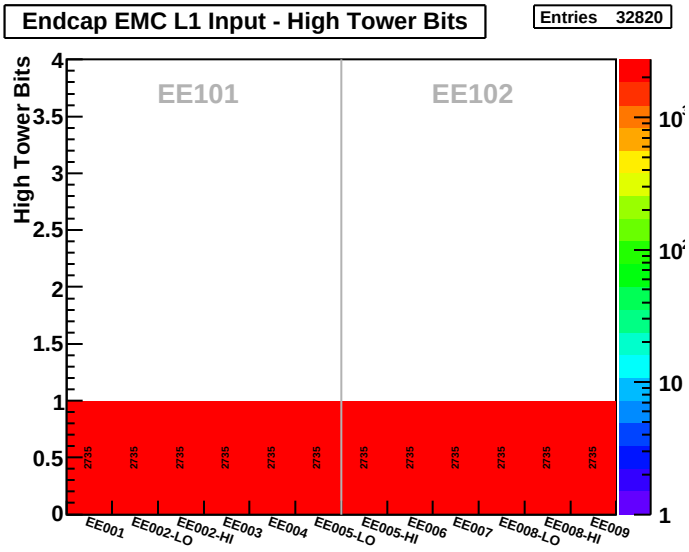

Barrel EMC L1 Input - High Tower Bits
Entries 49230
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

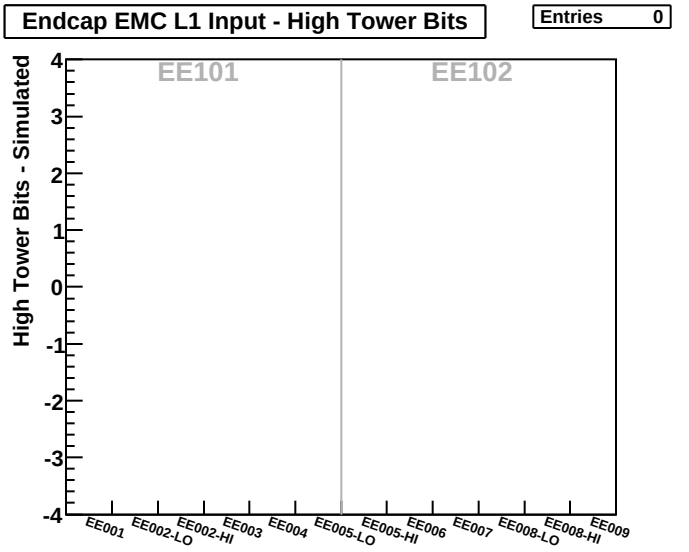

Endcap EMC L1 Input - Low Eta Sum
Entries 32820
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

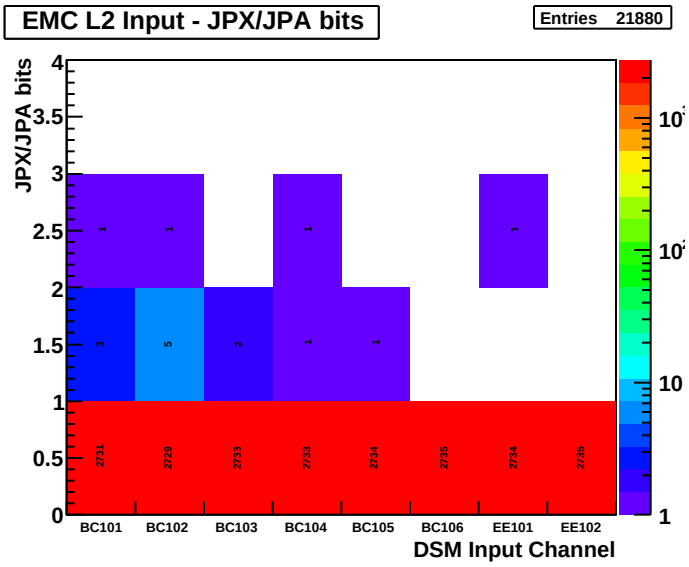

Endcap EMC L1 Input - Low Eta Sum
Entries 1334
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

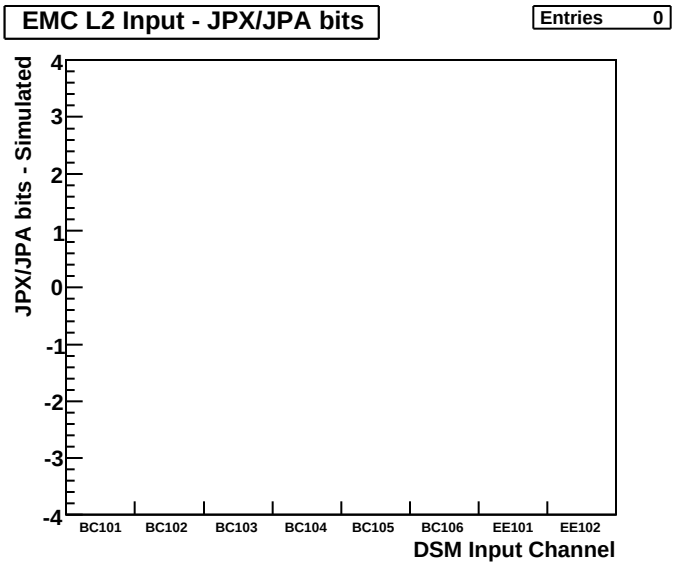

Endcap EMC L1 Input - High Eta Sum
Entries 32820
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

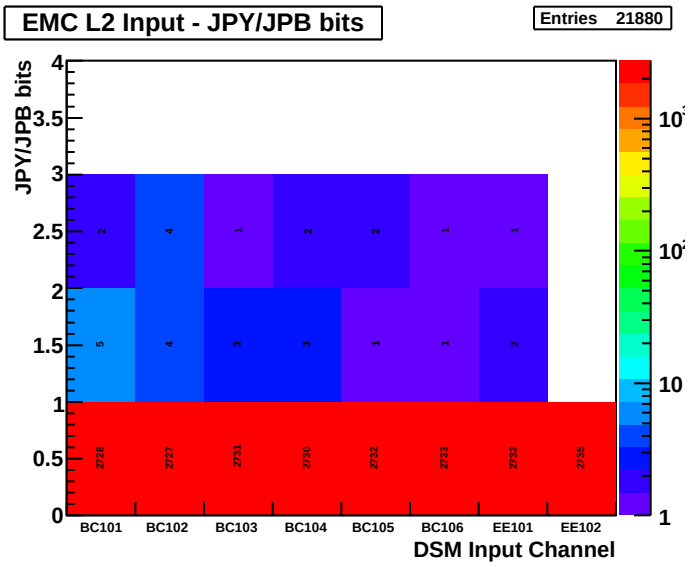

Endcap EMC L1 Input - High Eta Sum
Entries 497
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

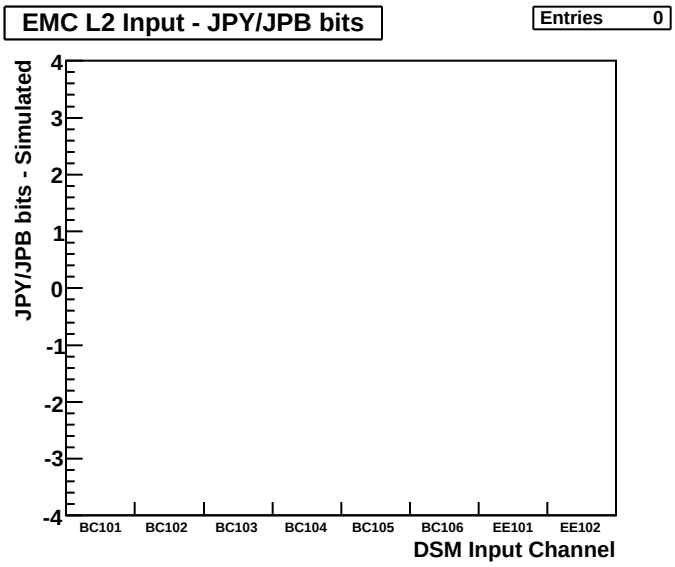

Endcap EMC L1 Input - High Tower Bits
Entries 32820
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

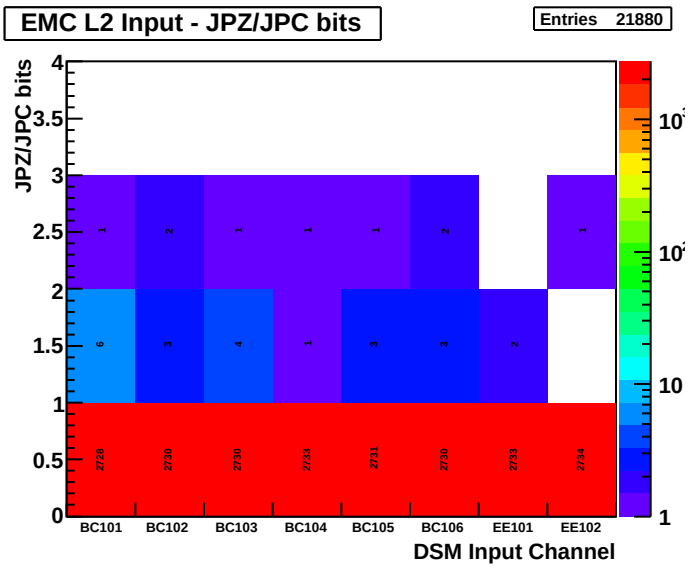

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

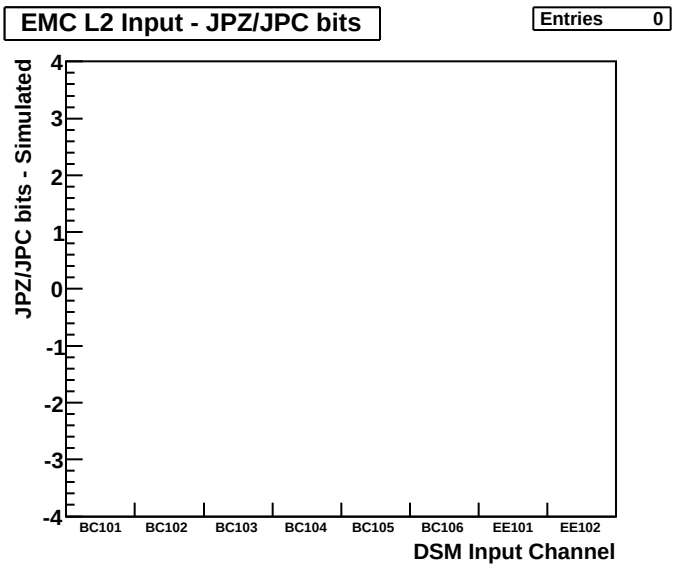

EMC L2 Input - JPX/JPA bits
Entries 21880
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

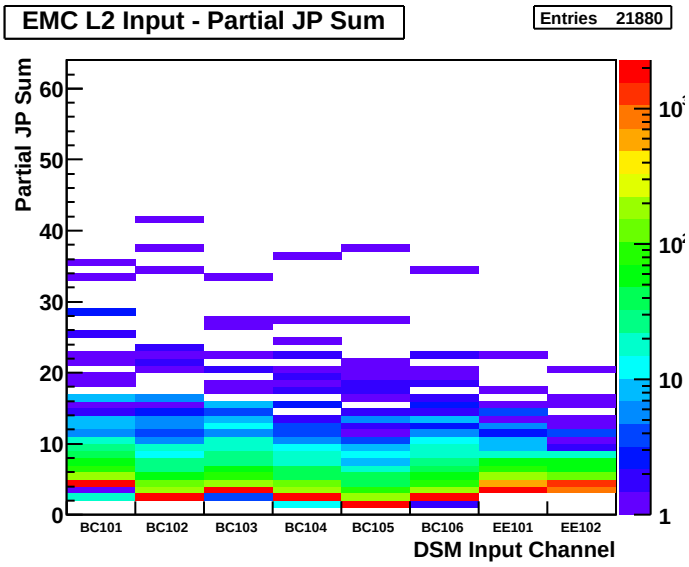

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

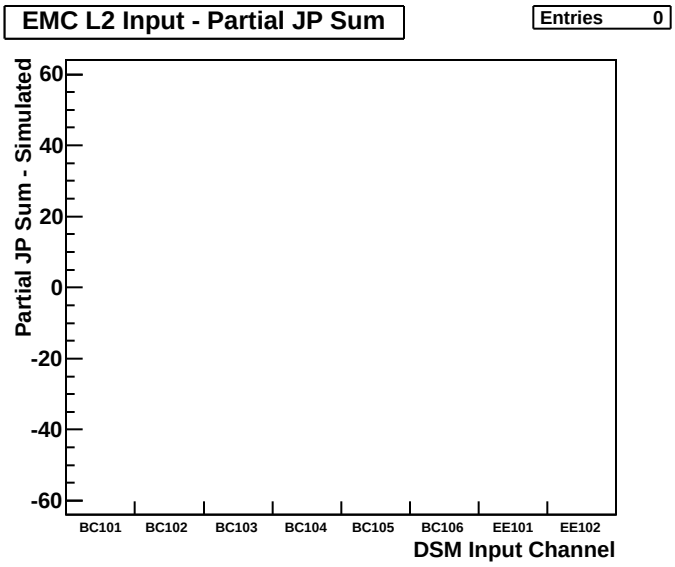

EMC L2 Input - JPY/JPB bits
Entries 21880
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

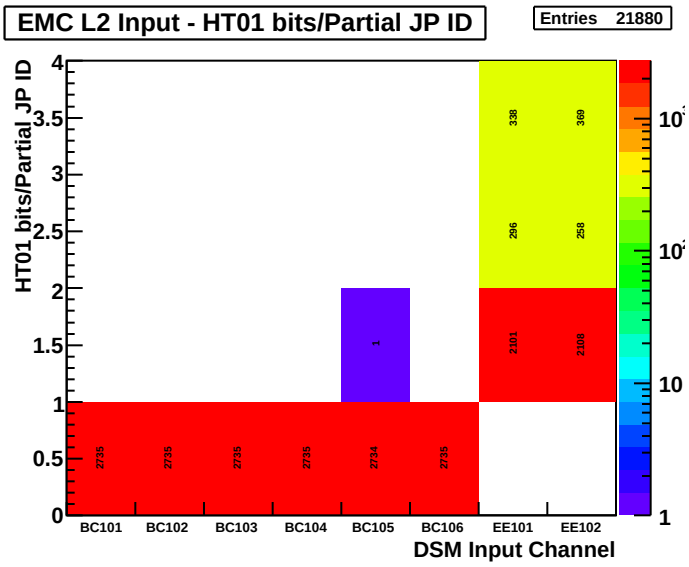

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

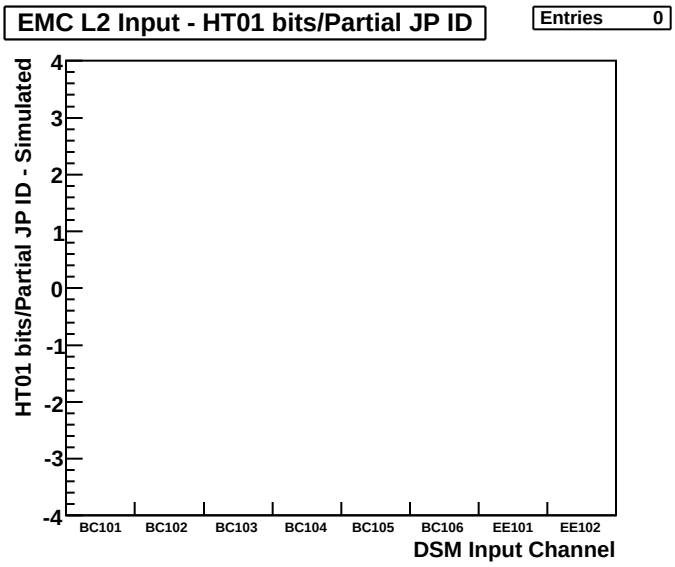

EMC L2 Input - JPZ/JPC bits
Entries 21880
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

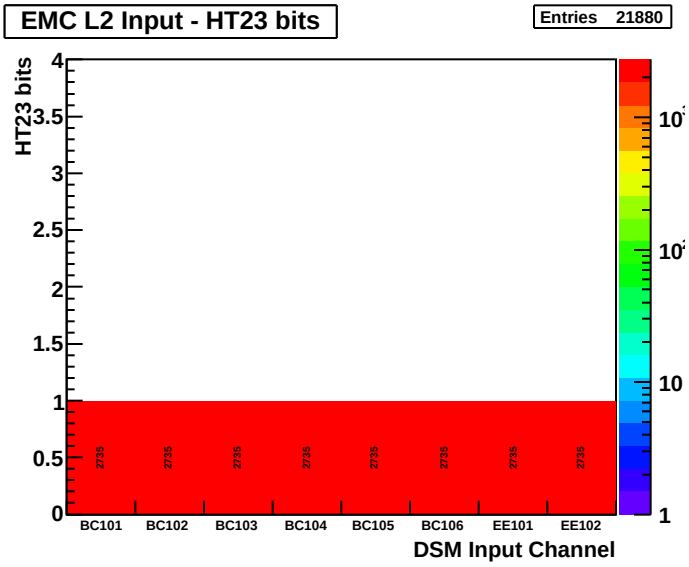

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

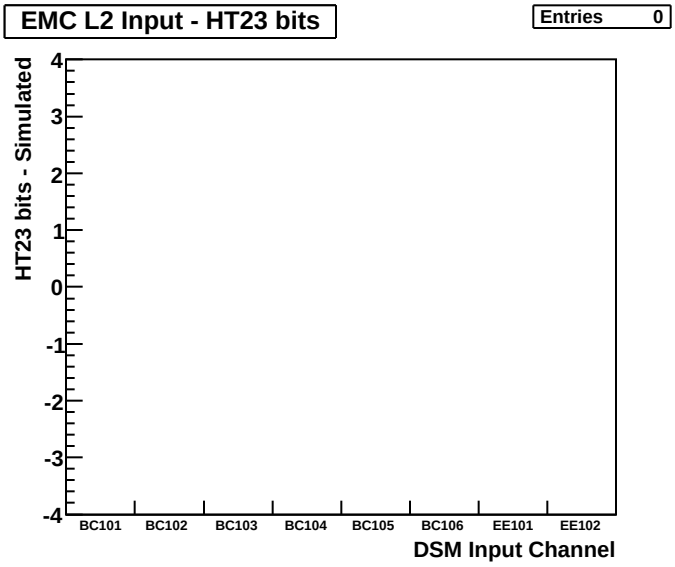

EMC L2 Input - Partial JP Sum
Entries 21880
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

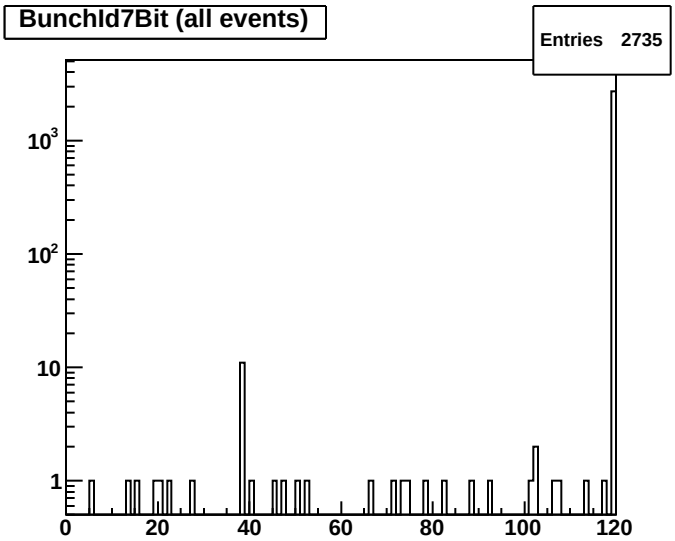

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

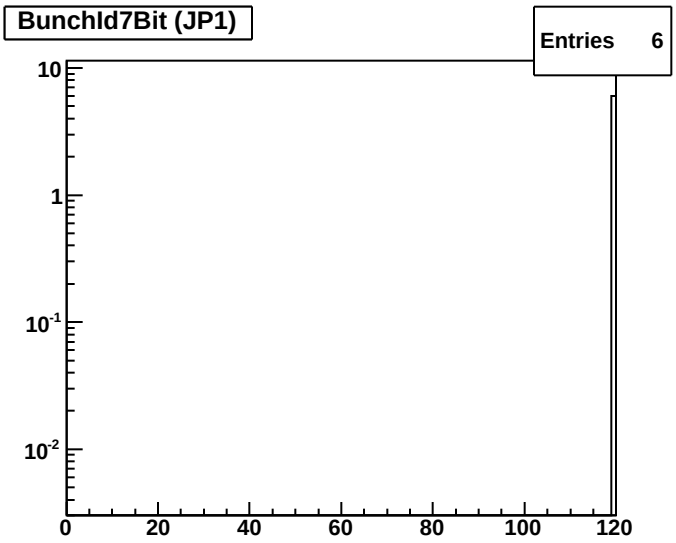

EMC L2 Input - HT01 bits/Partial JP ID
Entries 21880
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

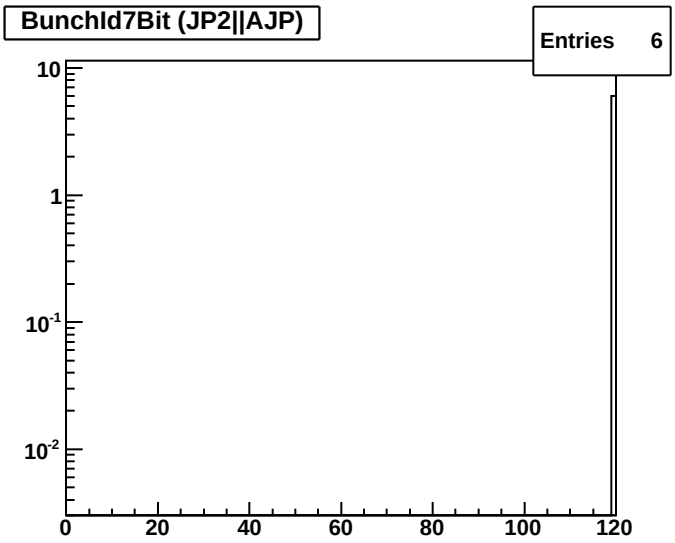

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

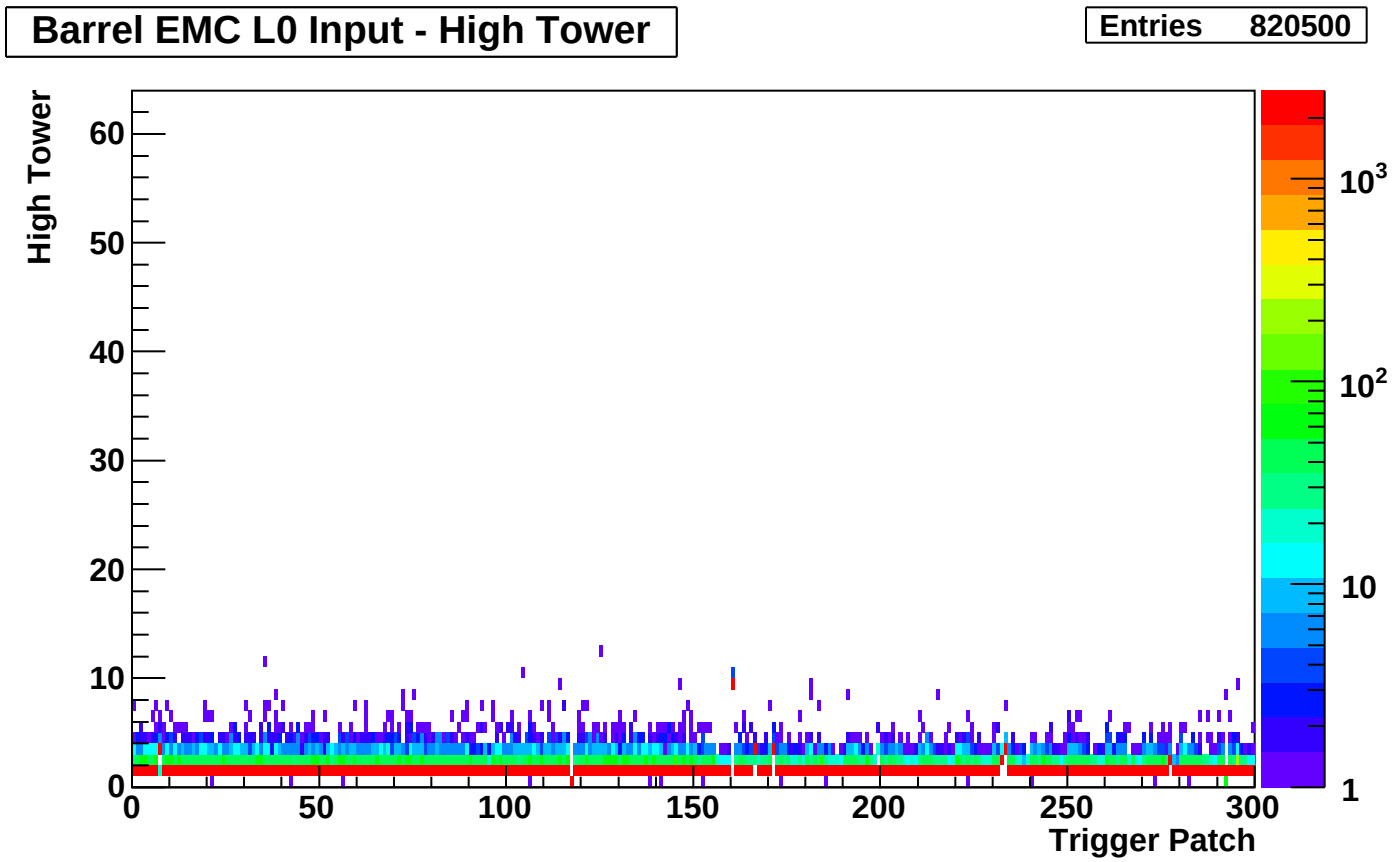

EMC L2 Input - HT23 bits
Entries 21880
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

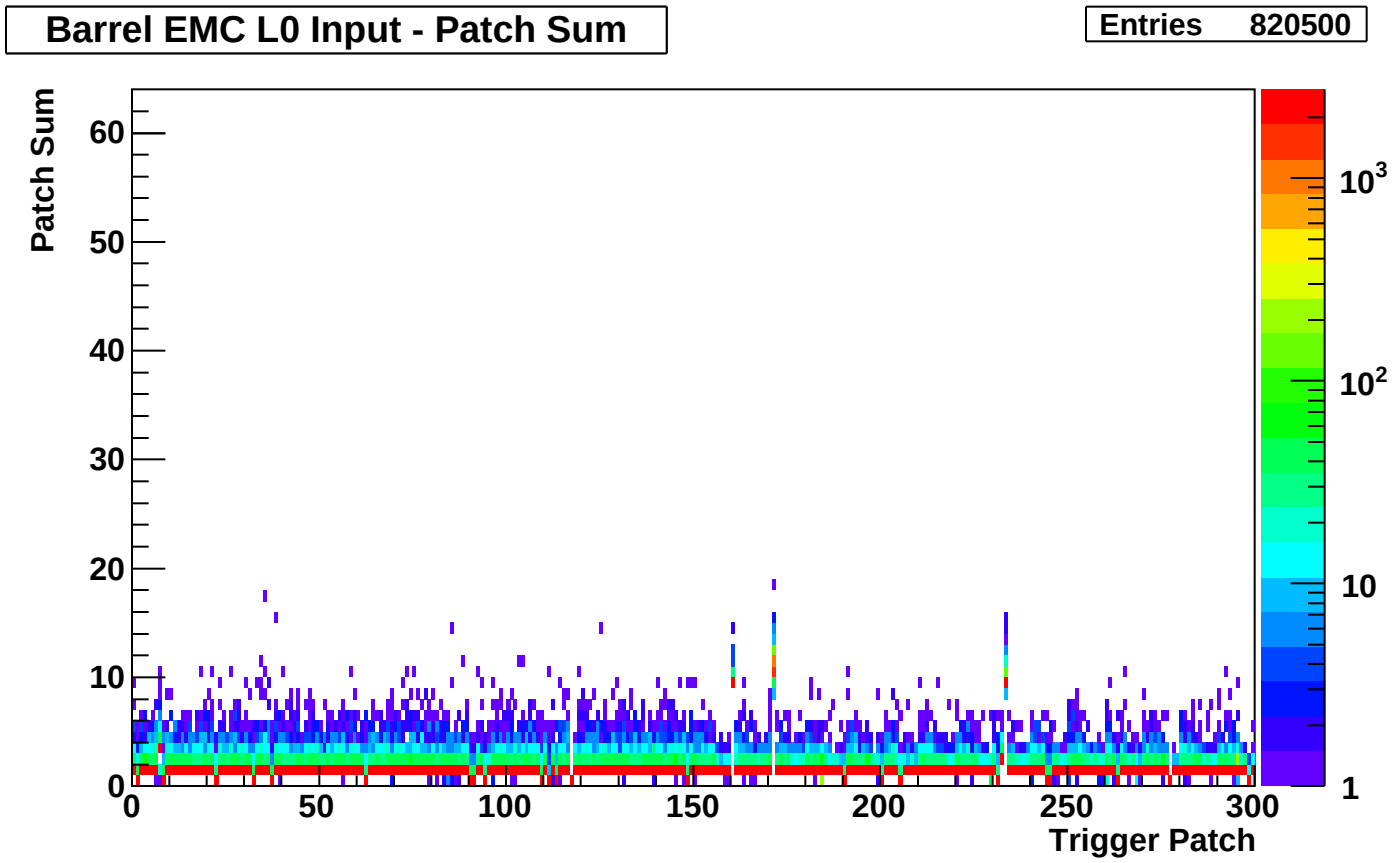

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

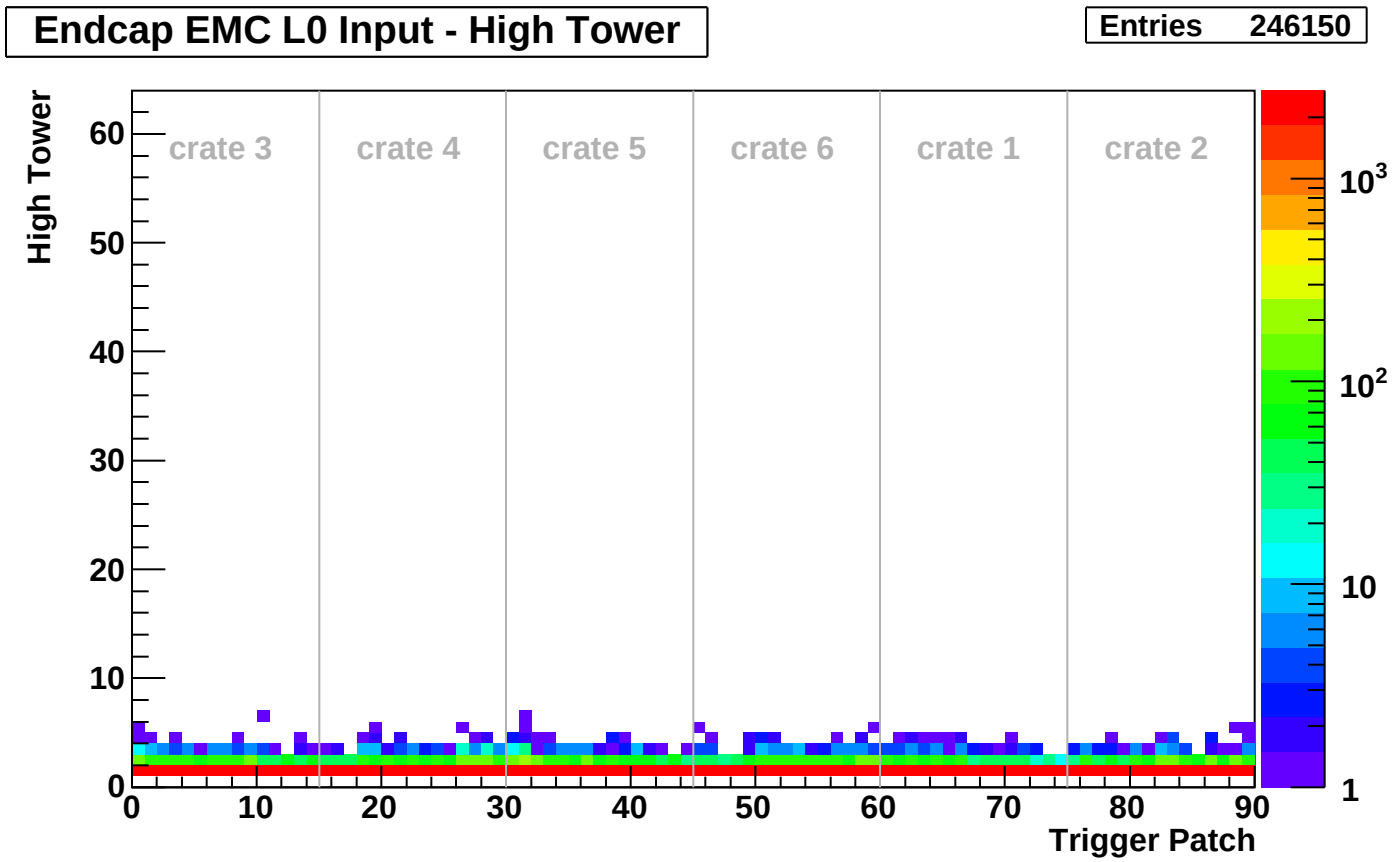

BunchId7Bit (all events)
Entries 2735
10^3
10^2
10
1
0
20
40
60
80
100
120

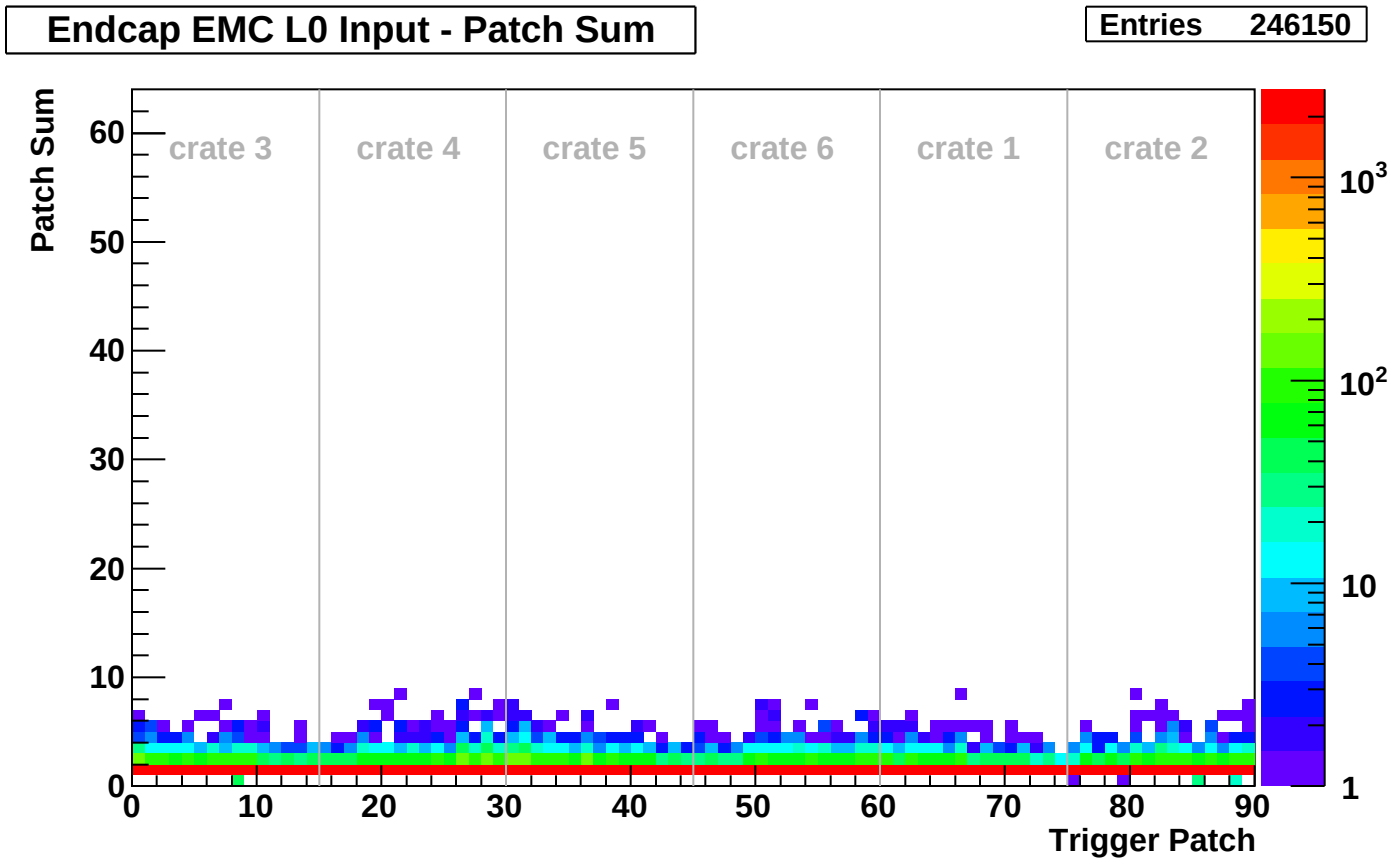

BunchId7Bit (JP1)
Entries 6
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

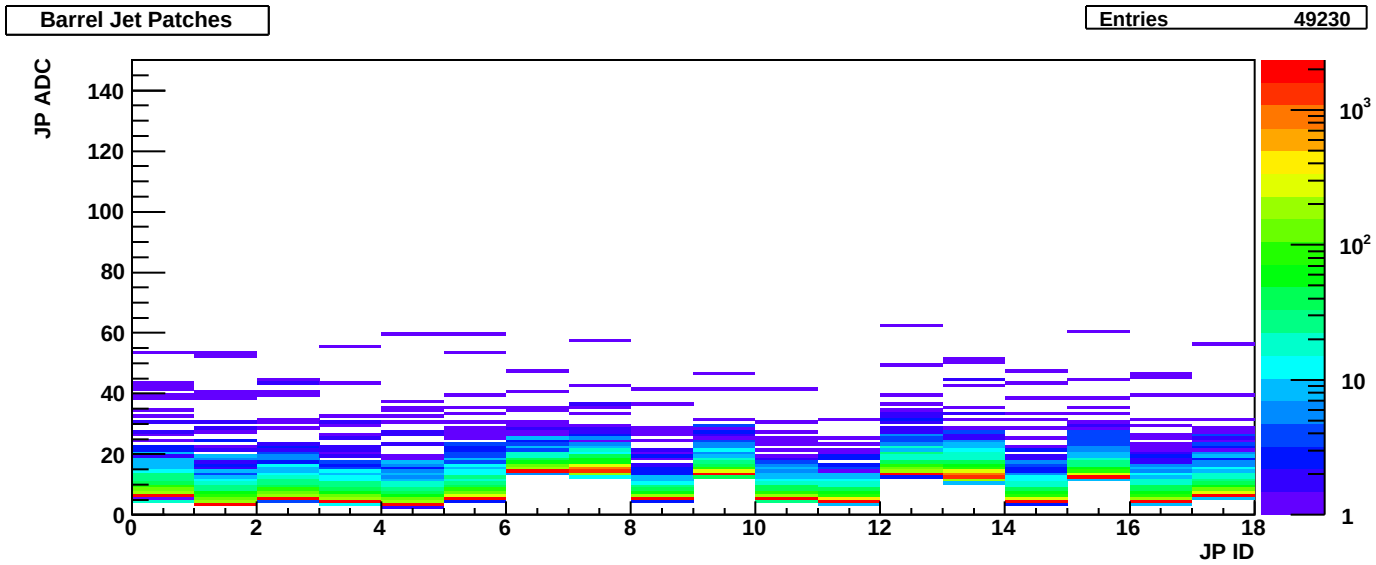

BunchId7Bit (JP2||AJP)
Entries 6
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

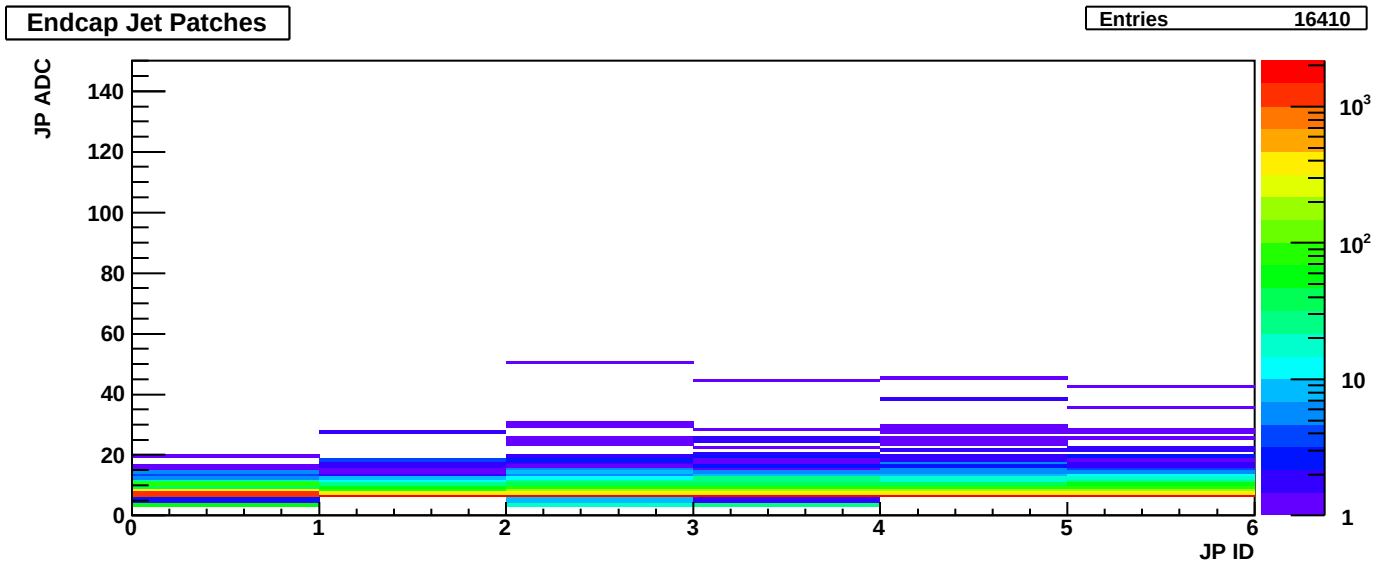

Barrel EMC L0 Input - High Tower
Entries 820500
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

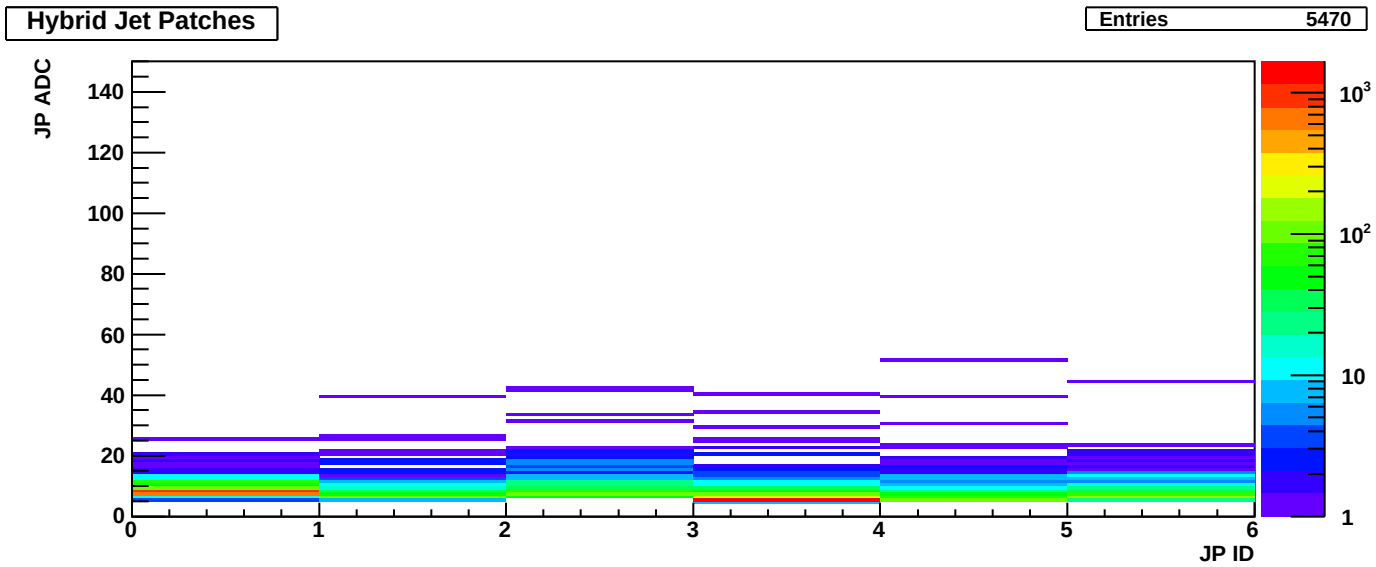

Barrel EMC L0 Input - Patch Sum
Entries 820500
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

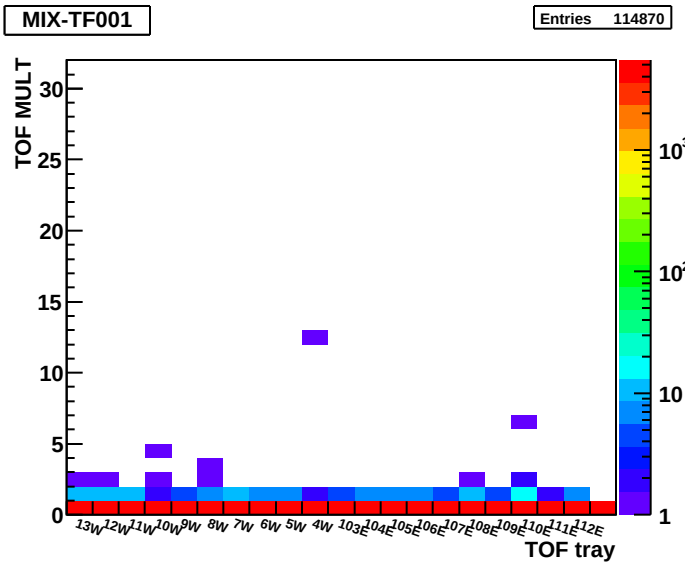

Endcap EMC L0 Input - High Tower
Entries 246150
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

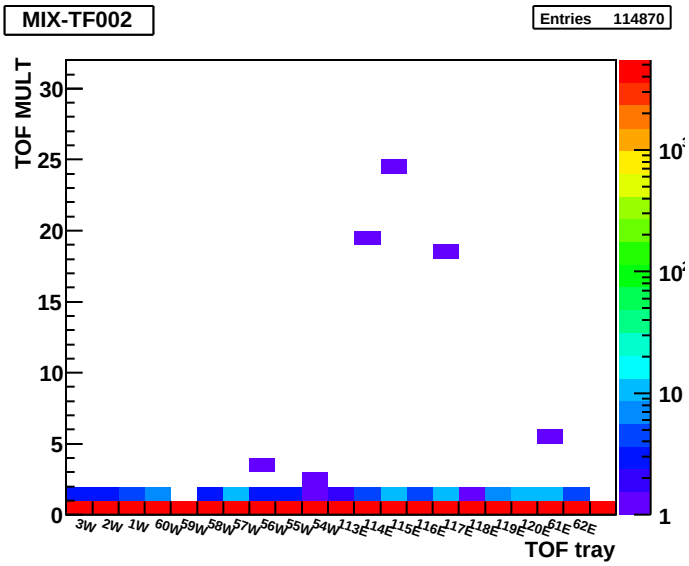

Endcap EMC L0 Input - Patch Sum
Entries 246150
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

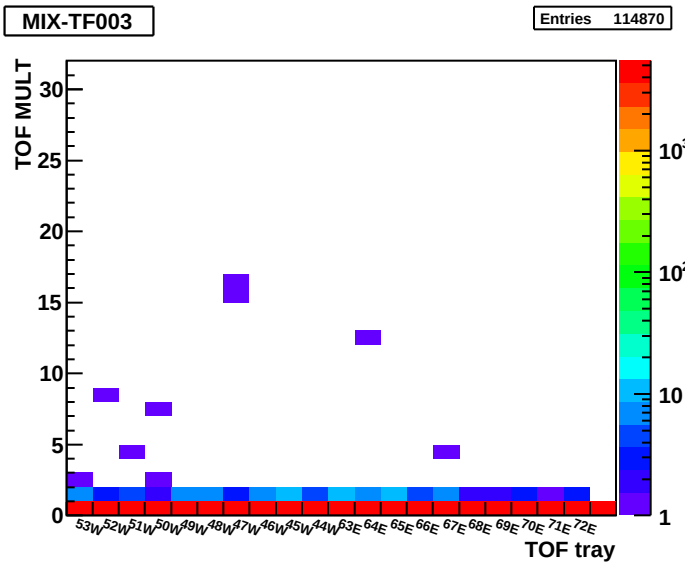

Barrel Jet Patches
Entries 49230
JP ADC
JP ID

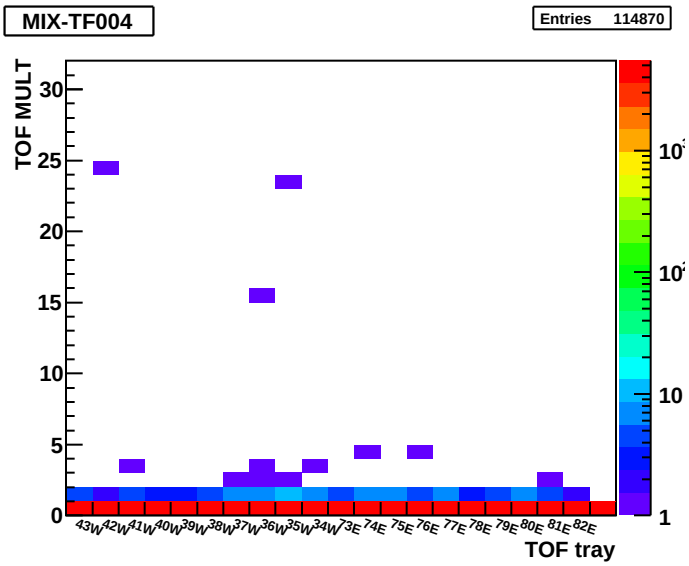

Endcap Jet Patches
Entries 16410
JP ADC
JP ID

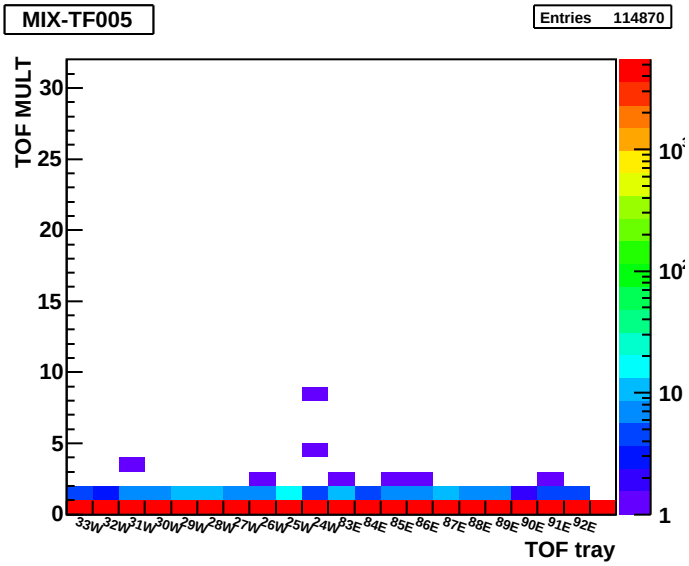

Hybrid Jet Patches
Entries 5470
JP ADC
JP ID

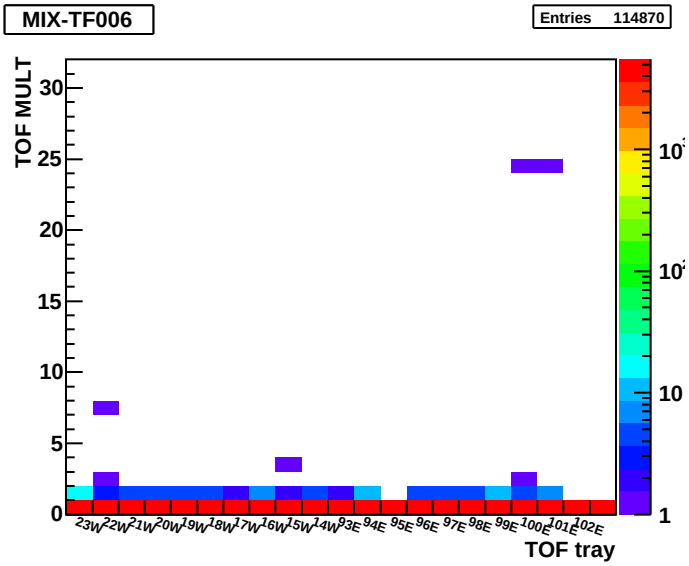

MIX-TF001
Entries 114870
TOF MULT
TOF tray

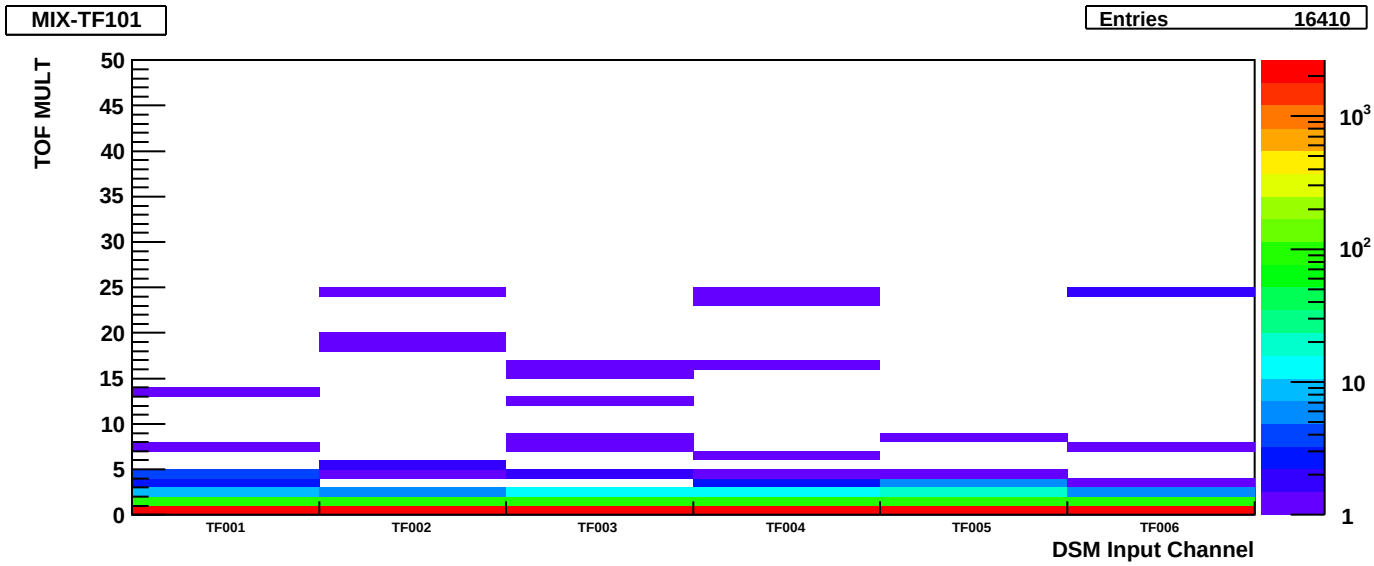

MIX-TF002
Entries 114870
TOF MULT
TOF tray

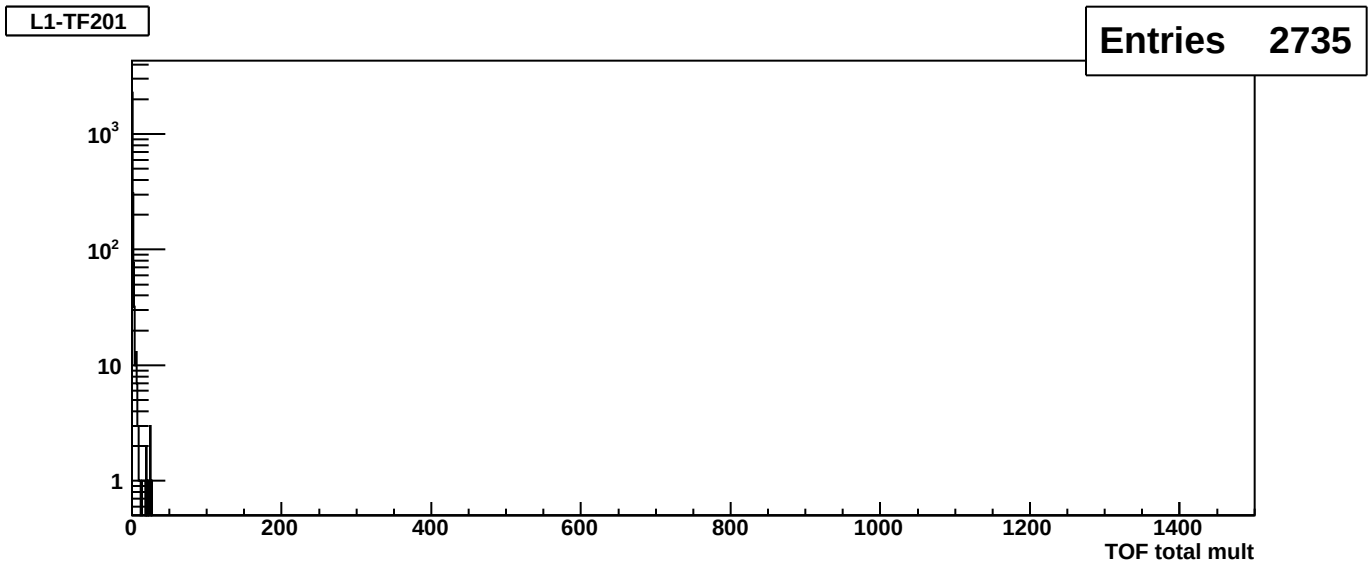

MIX-TF003
Entries 114870
TOF MULT
TOF tray

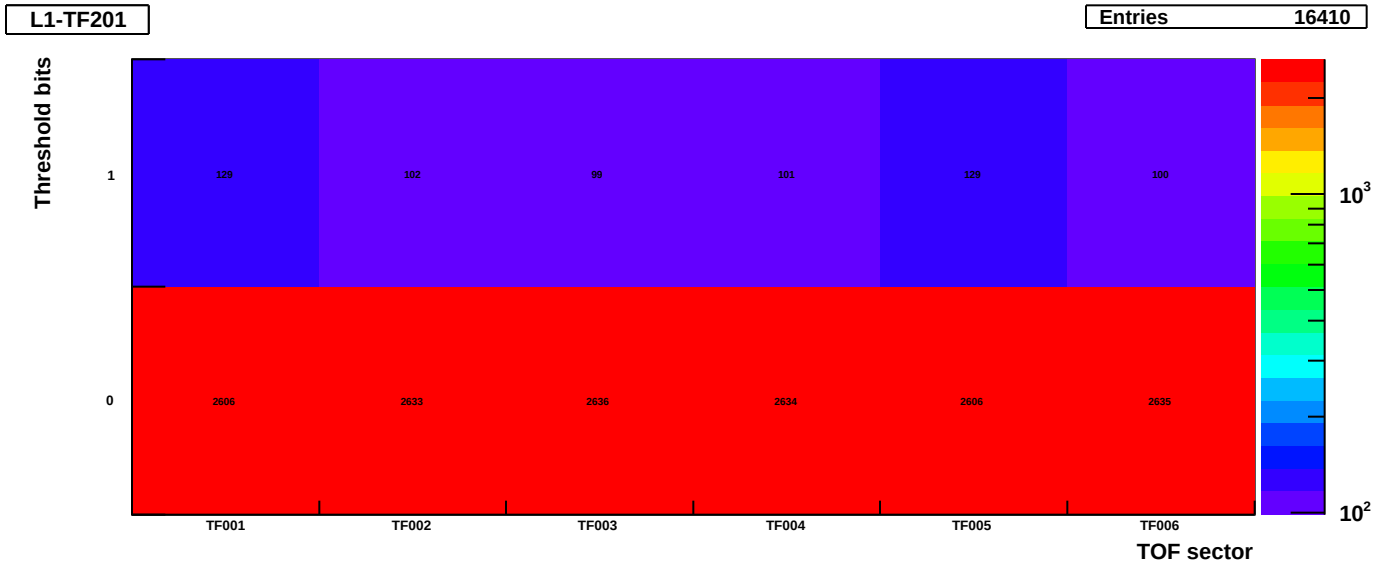

MIX-TF004
Entries 114870
TOF MULT
TOF tray

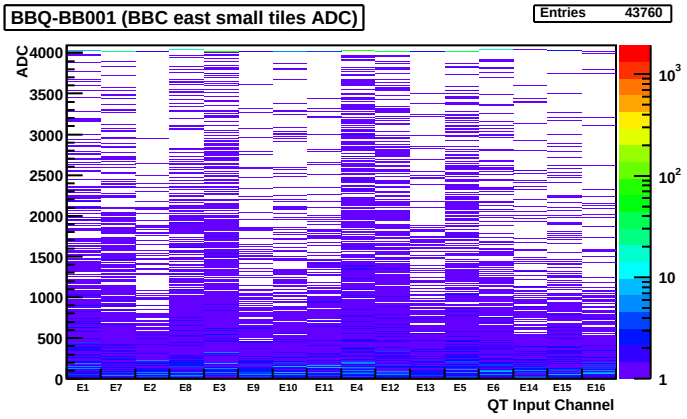

MIX-TF005
Entries 114870
TOF MULT
TOF tray

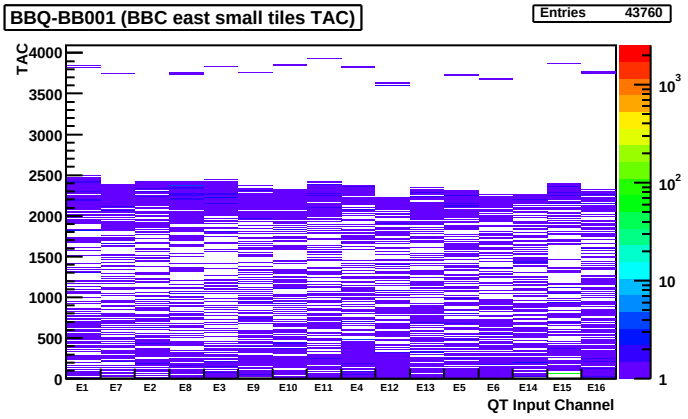

MIX-TF006
Entries 114870
TOF MULT
TOF tray

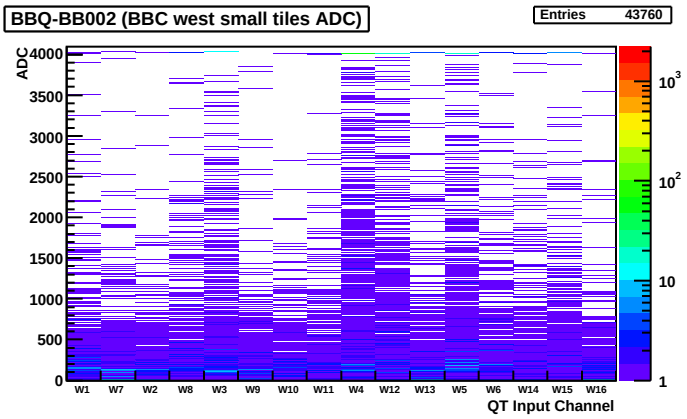

MIX-TF101
Entries 16410
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10
1

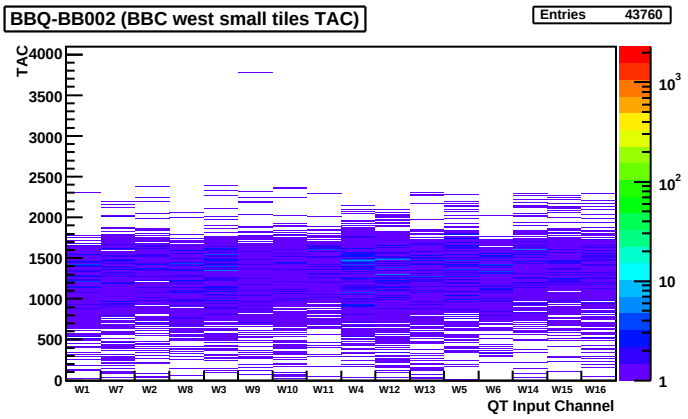

L1-TF201
Entries 2735
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

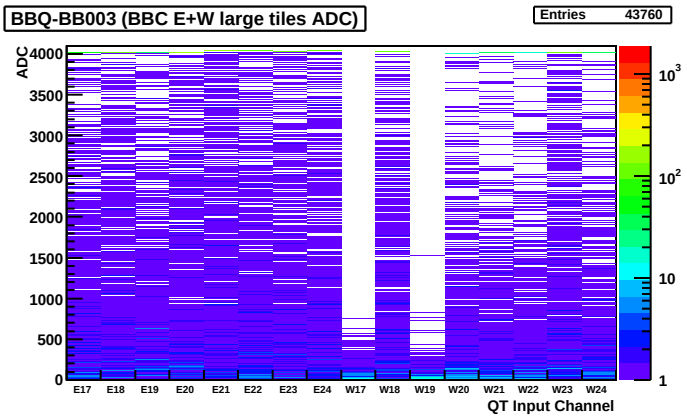

L1-TF201
Entries 16410
Threshold bits
1
0
129
102
99
101
129
100
2606
2633
2636
2634
2606
2635
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^3
10^2

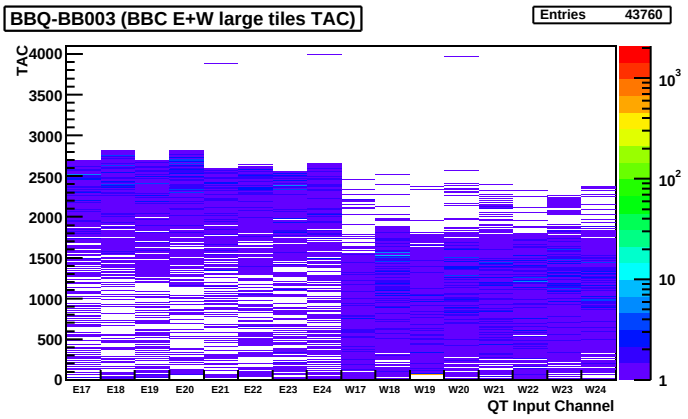

BBQ-BB001 (BBC east small tiles ADC)
Entries 43760
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

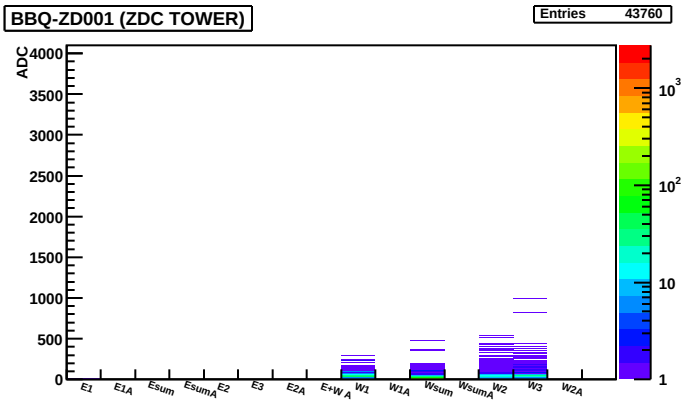

BBQ-BB001 (BBC east small tiles TAC)
Entries 43760
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

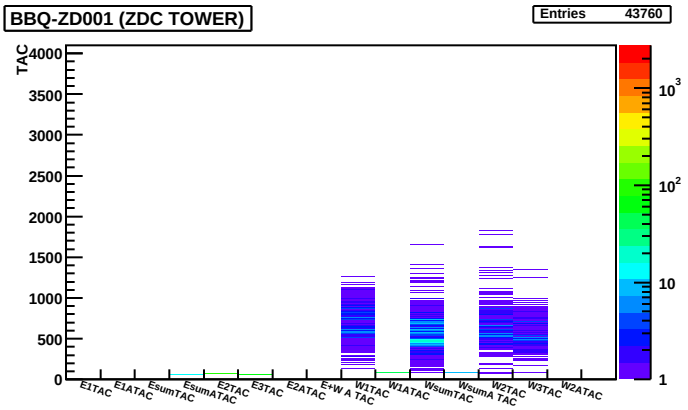

BBQ-BB002 (BBC west small tiles ADC)
Entries 43760
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

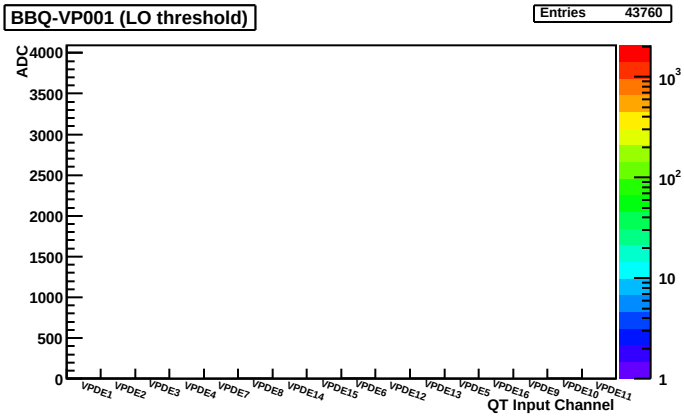

BBQ-BB002 (BBC west small tiles TAC)
Entries 43760
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

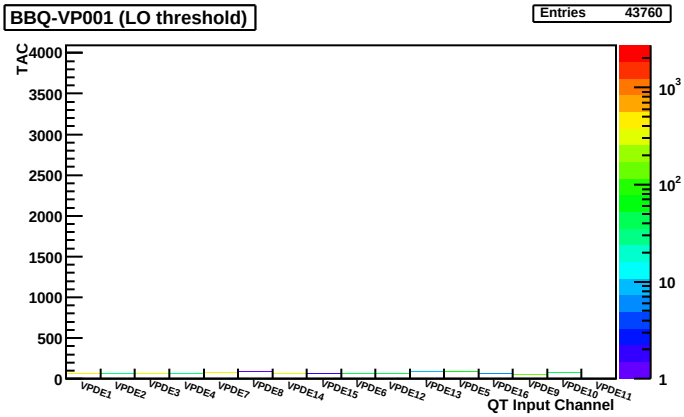

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 43760
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

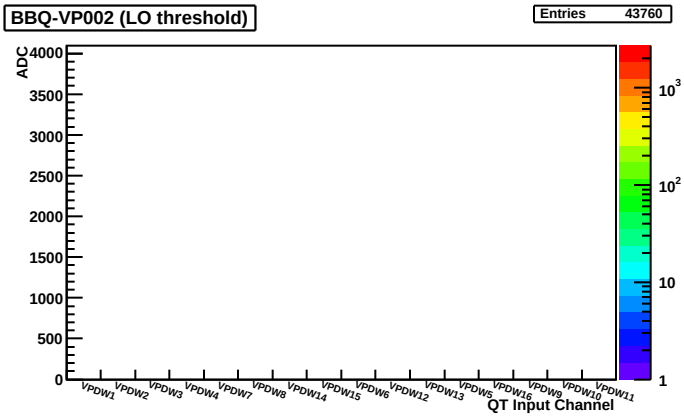

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 43760
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

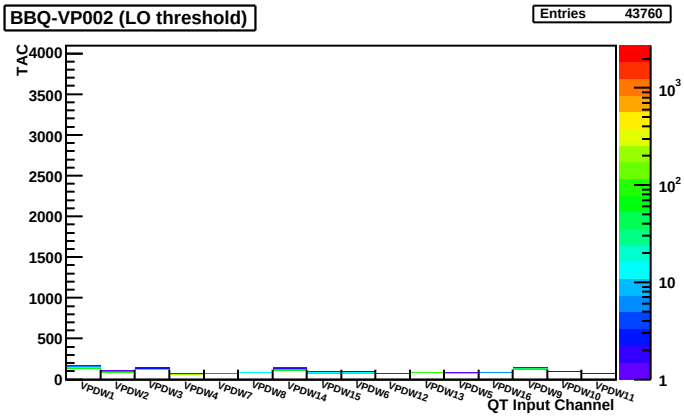

BBQ-ZD001 (ZDC TOWER)
Entries 43760
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

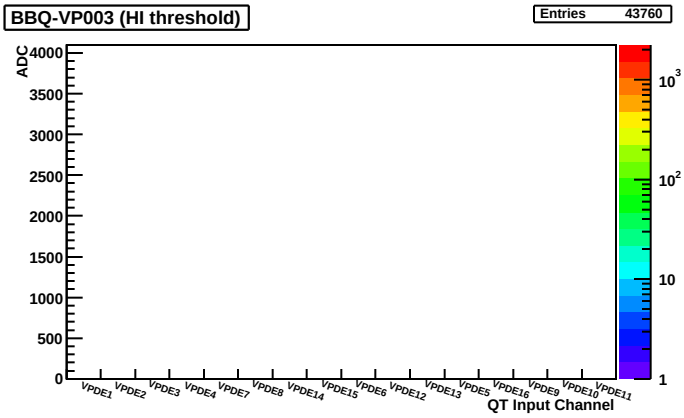

BBQ-ZD001 (ZDC TOWER)
Entries 43760
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

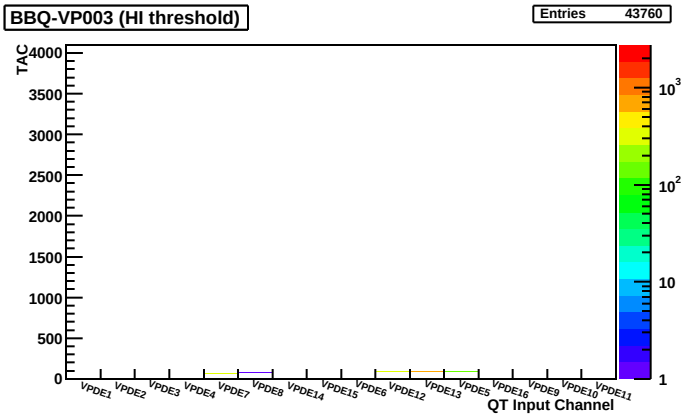

BBQ-VP001 (LO threshold)
Entries 43760
ADC
QT Input Channel

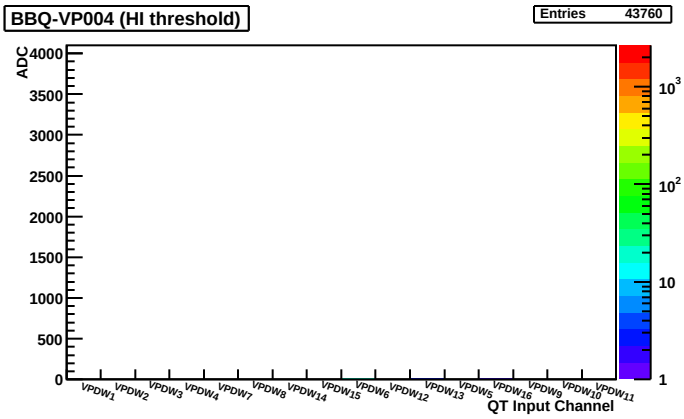

BBQ-VP001 (LO threshold)
Entries 43760
TAC
QT Input Channel

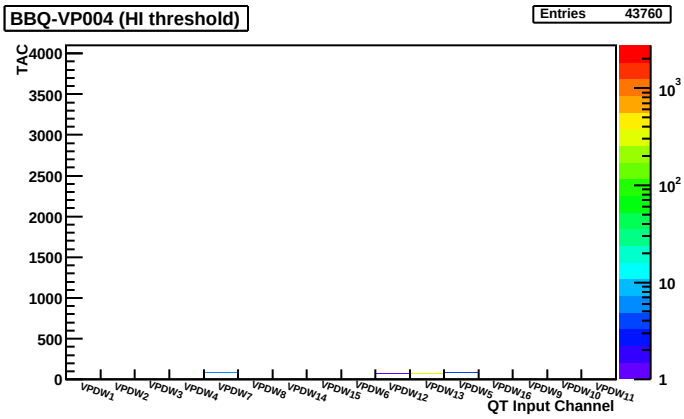

BBQ-VP002 (LO threshold)
Entries 43760
ADC
QT Input Channel

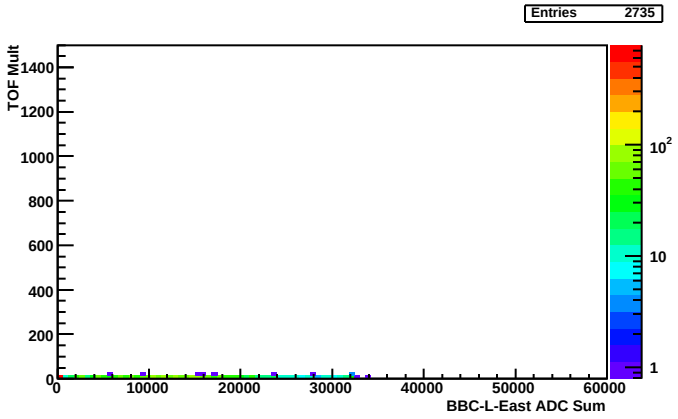

BBQ-VP002 (LO threshold)
Entries 43760
TAC
QT Input Channel

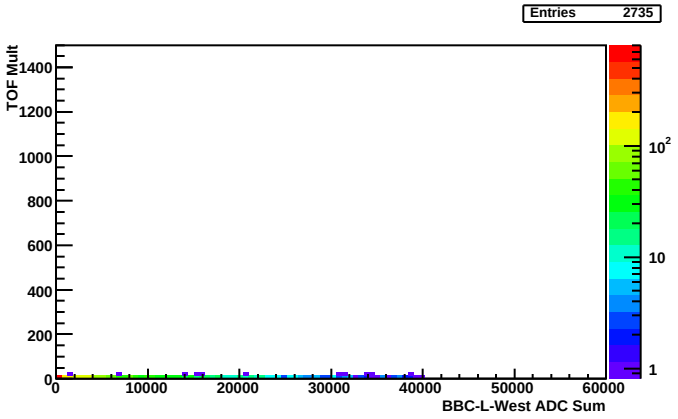

BBQ-VP003 (HI threshold)
Entries 43760
ADC
QT Input Channel

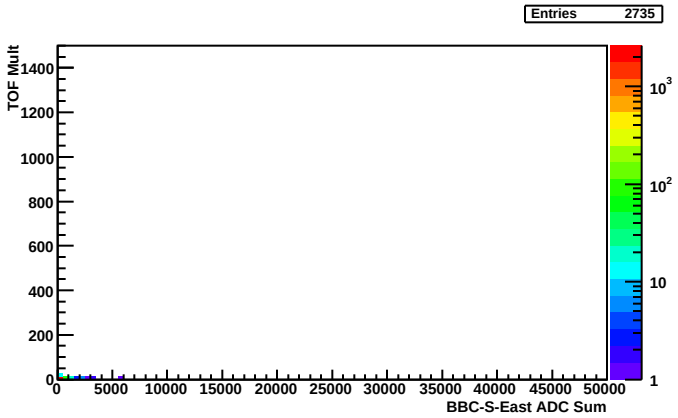

BBQ-VP003 (HI threshold)
Entries 43760
TAC
QT Input Channel

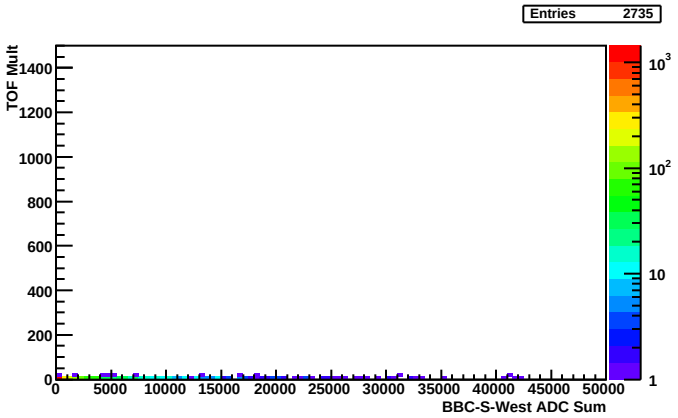

BBQ-VP004 (HI threshold)
Entries 43760
ADC
QT Input Channel

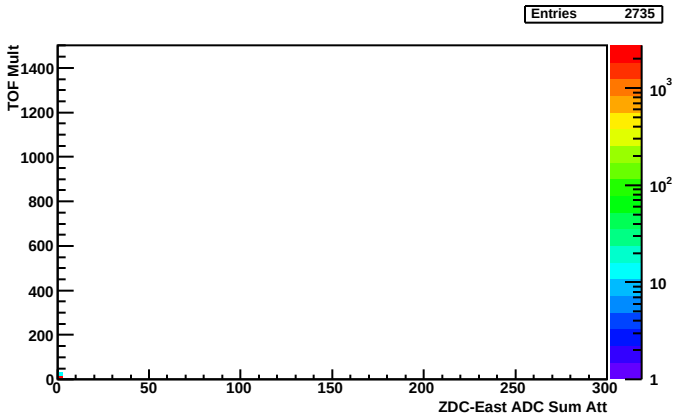

BBQ-VP004 (HI threshold)
Entries 43760
TAC
QT Input Channel

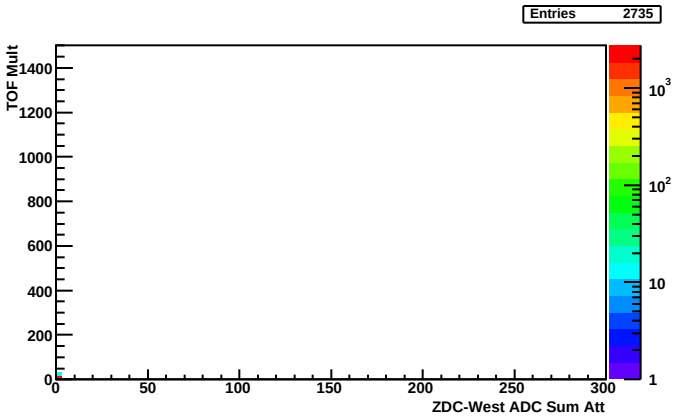

Entries 2735
TOF Mult
BBC-L-East ADC Sum

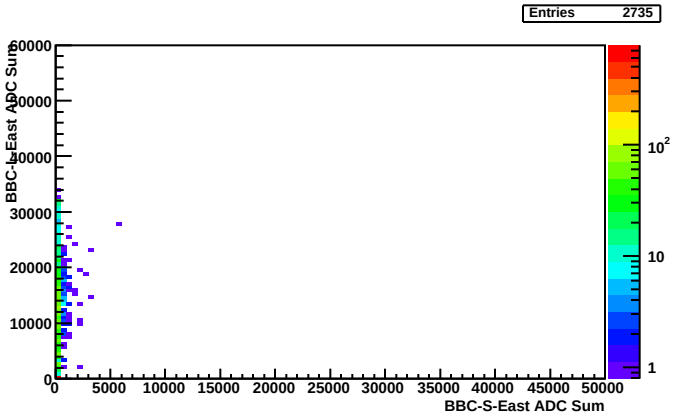

Entries 2735
TOF Mult
BBC-L-West ADC Sum

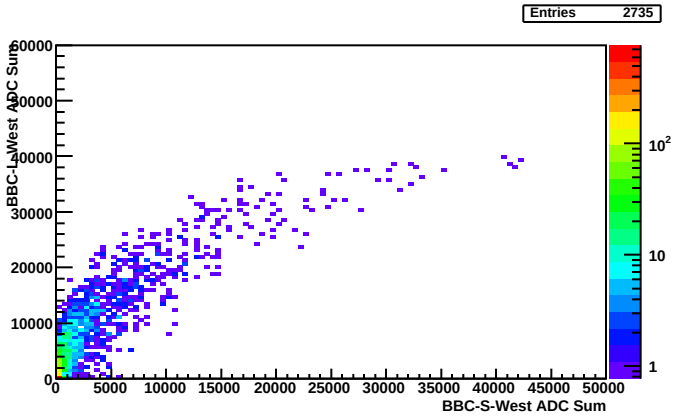

Entries 2735
TOF Mult
BBC-S-East ADC Sum

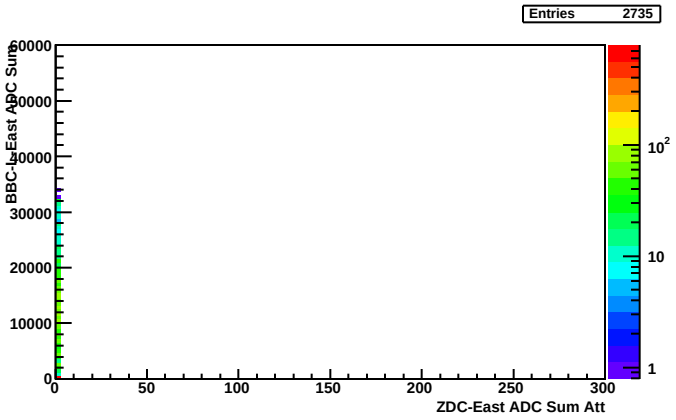

Entries 2735
TOF Mult
BBC-S-West ADC Sum

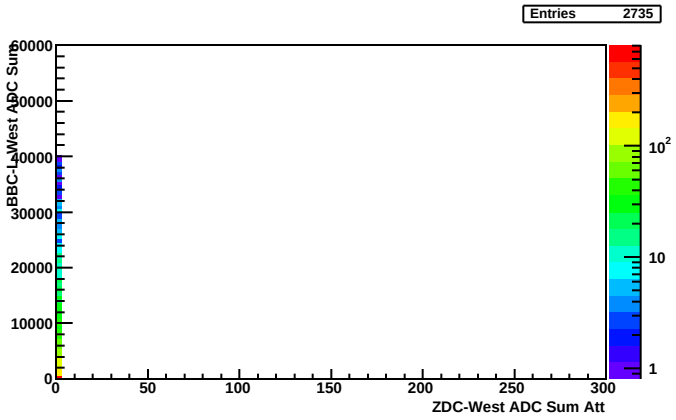

Entries 2735
TOF Mult
ZDC-East ADC Sum Att

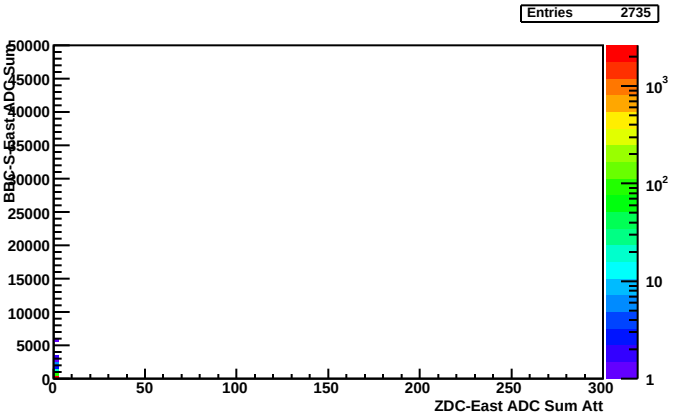

Entries 2735
TOF Mult
ZDC-West ADC Sum Att

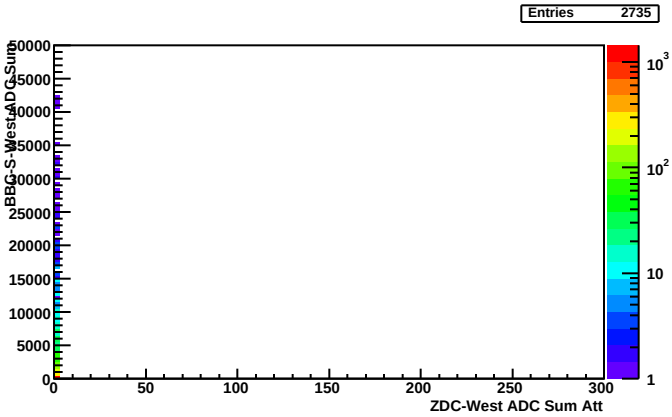

Entries 2735
BBC-L-East ADC Sum
BBC-S-East ADC Sum

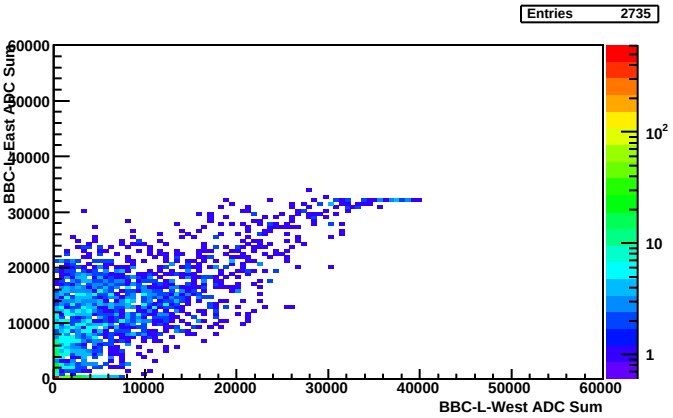

Entries 2735
BBC-L-West ADC Sum
BBC-S-West ADC Sum

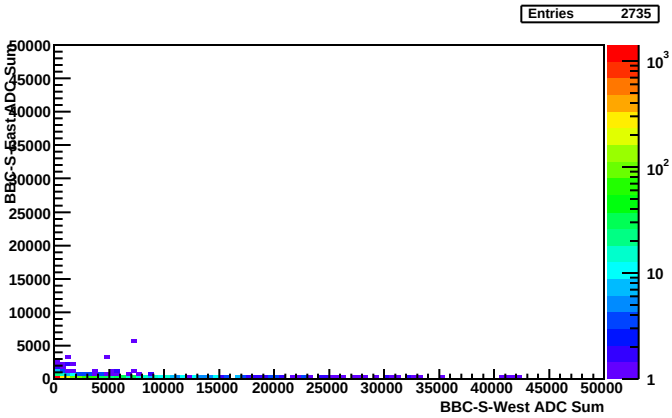

Entries 2735
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

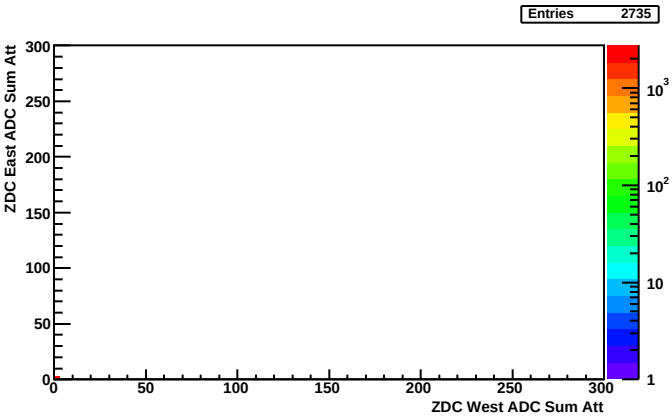

Entries 2735
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

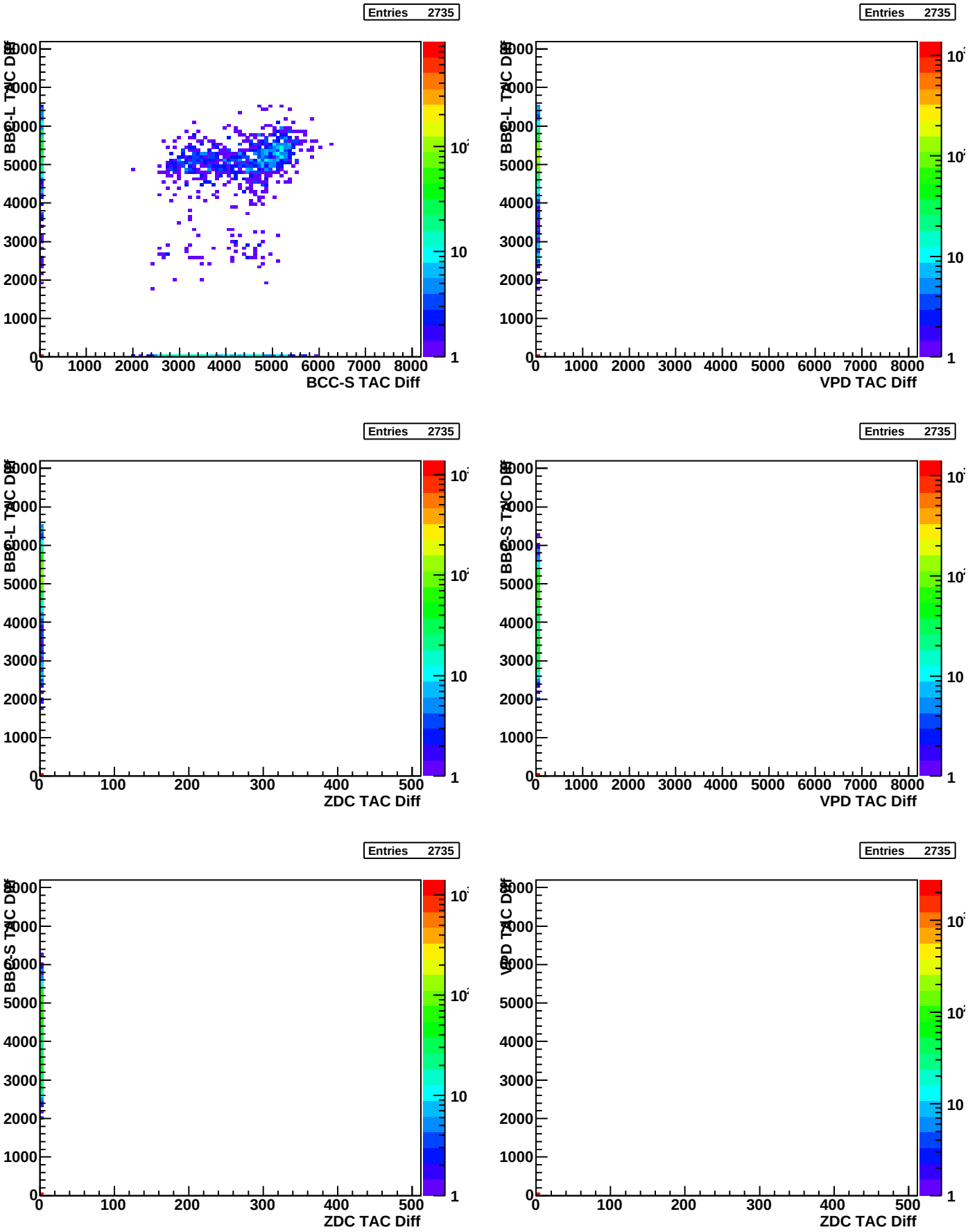

Entries 2735
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

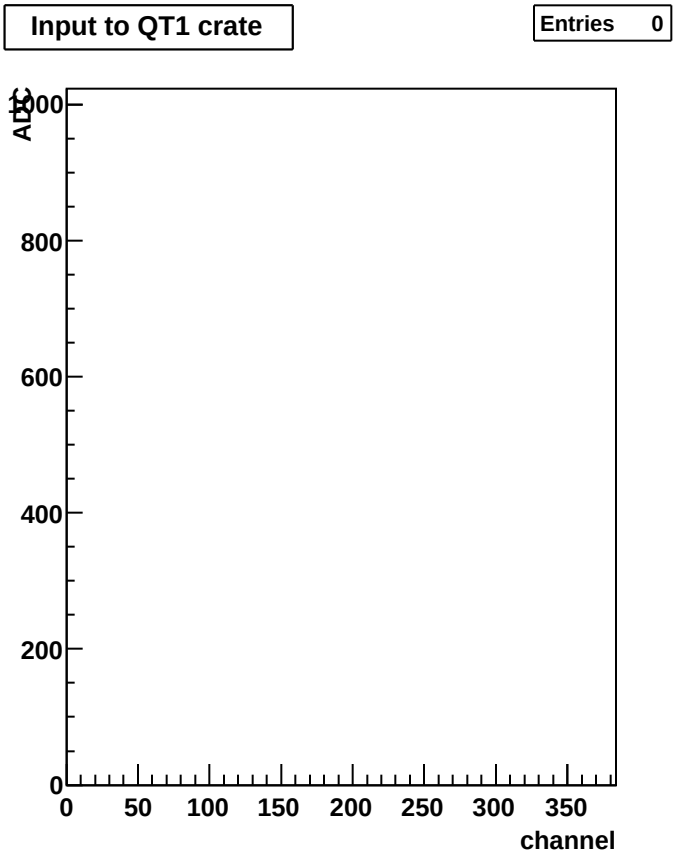

Entries 2735
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

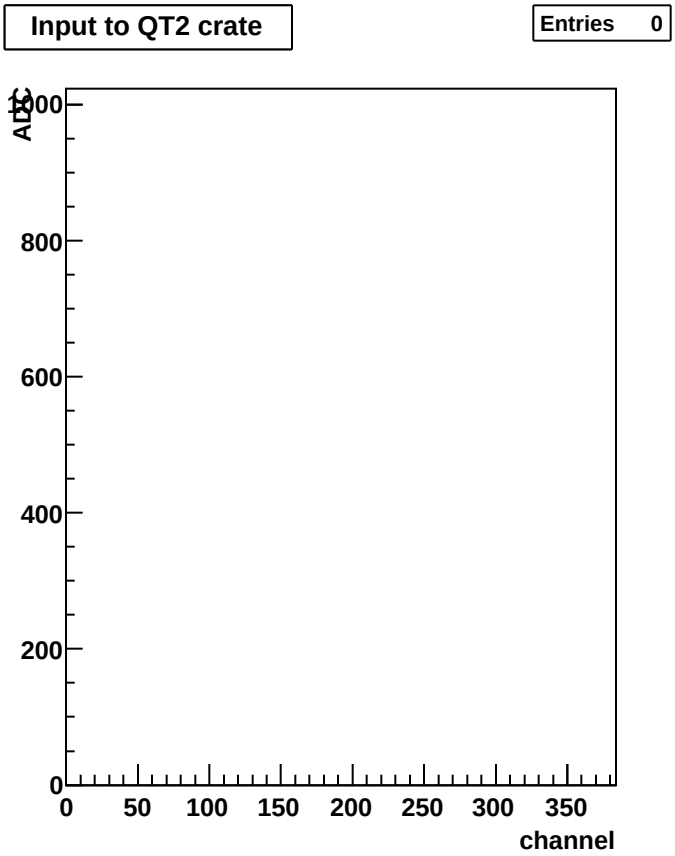

Entries 2735
BBC-L-East ADC Sum
BBC-L-West ADC Sum

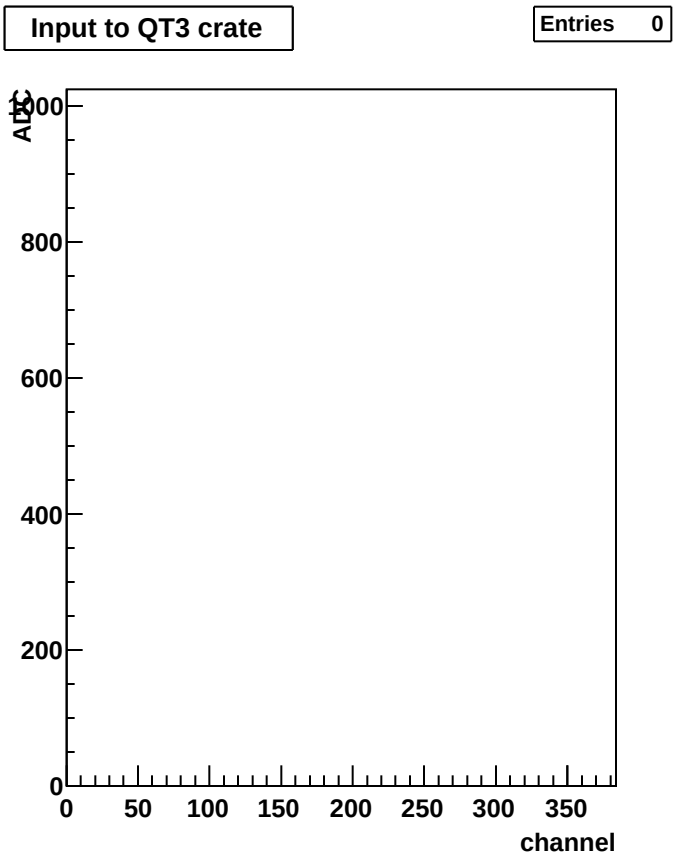

Entries 2735
BBC-S-East ADC Sum
BBC-S-West ADC Sum

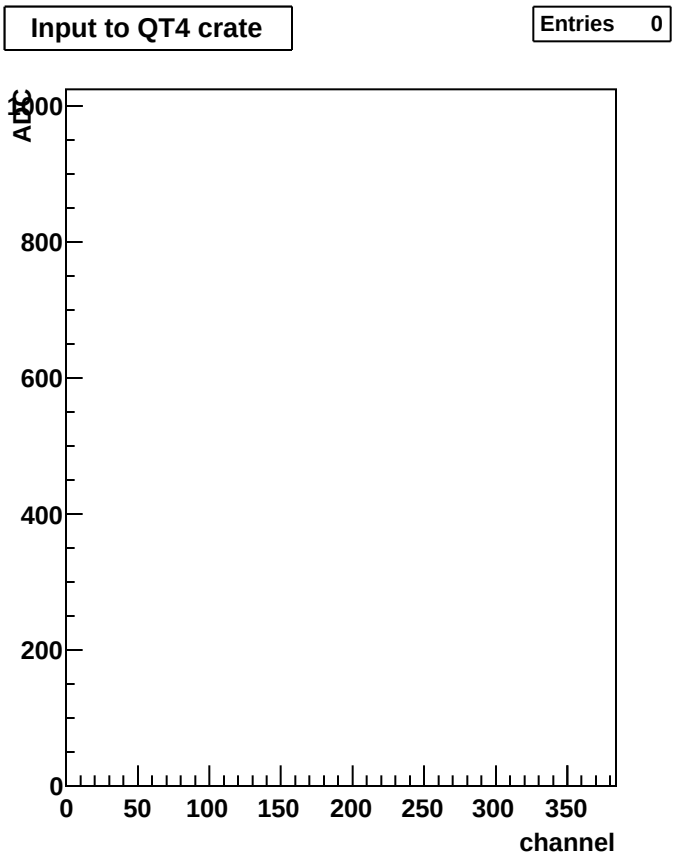

Entries 2735
ZDC East ADC Sum Att
ZDC West ADC Sum Att

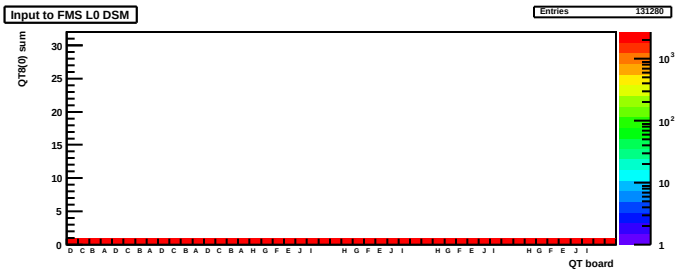

Entries 2735
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2735
BBC-L TAC Diff
VPD TAC Diff
Entries 2735
BBC-L TAC Diff
ZDC TAC Diff
Entries 2735
BBC-S TAC Diff
VPD TAC Diff
Entries 2735
BBC-S TAC Diff
ZDC TAC Diff
Entries 2735
VPD TAC Diff
ZDC TAC Diff

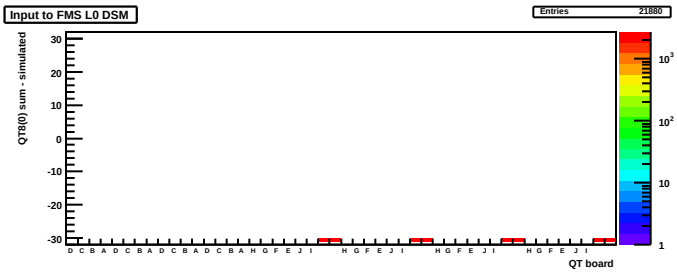

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

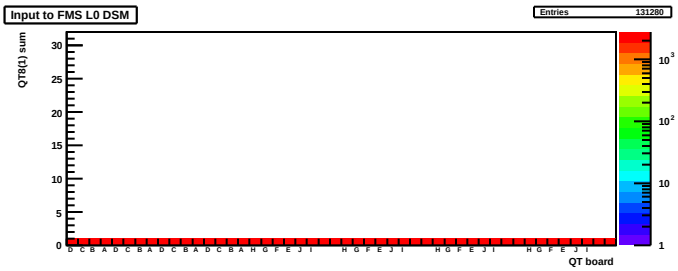

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

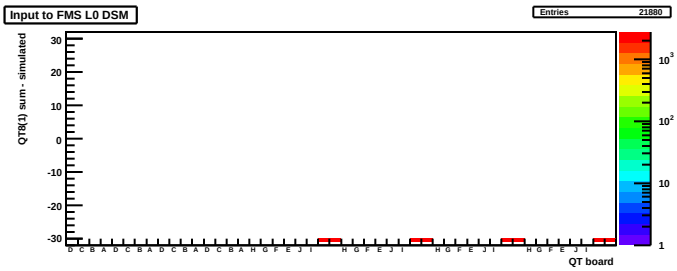

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

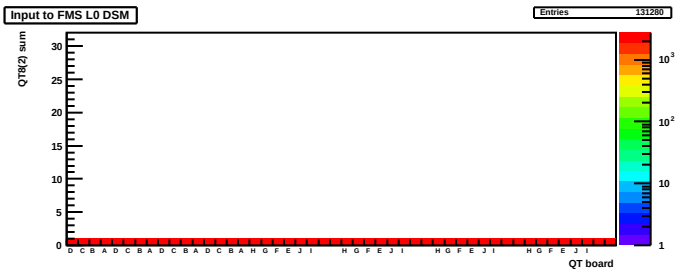

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

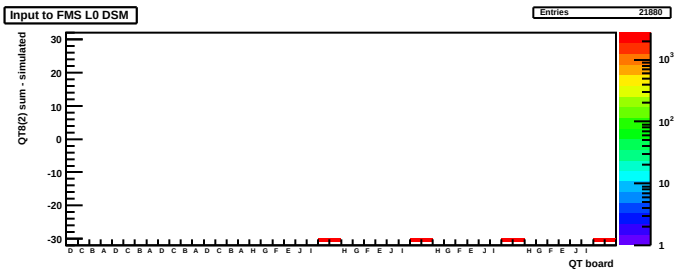

Input to FMS L0 DSM
Entries 131280
QT8(0) sum
QT board

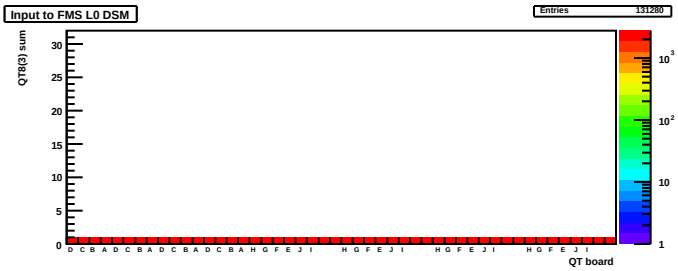

Input to FMS L0 DSM
Entries 21880
QT8(0) sum - simulated
QT board

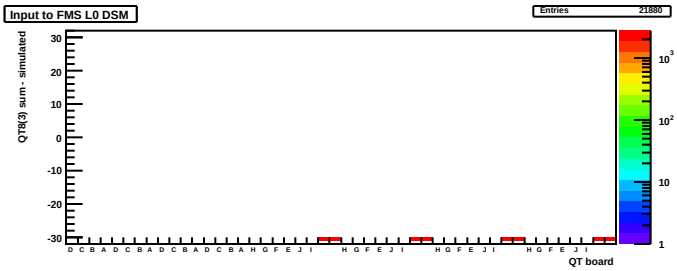

Input to FMS L0 DSM
Entries 131280
QT8(1) sum
QT board

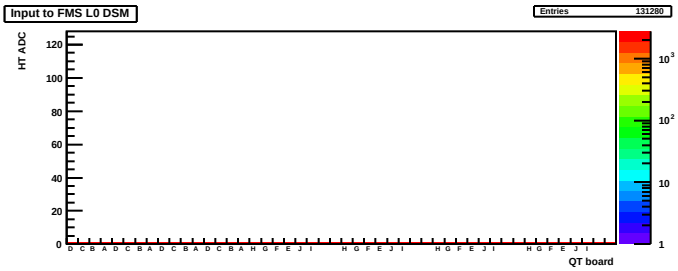

Input to FMS L0 DSM
Entries 21880
QT8(1) sum - simulated
QT board

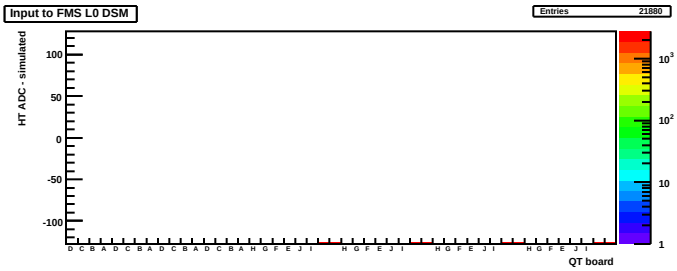

Input to FMS L0 DSM
Entries 131280
QT8(2) sum
QT board

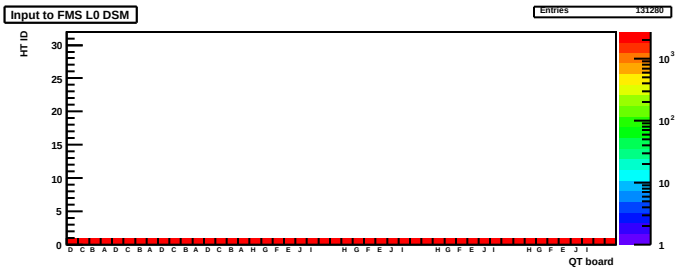

Input to FMS L0 DSM
Entries 21880
QT8(2) sum - simulated
QT board

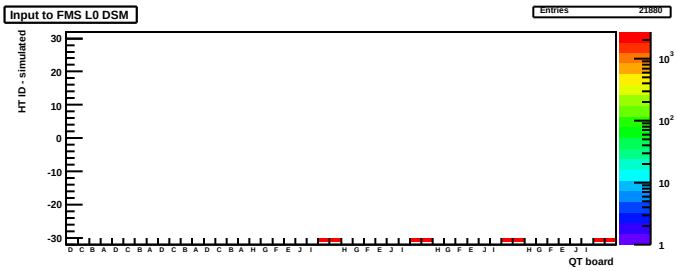

Input to FMS L0 DSM
Entries 131280
QT8(3) sum
QT board

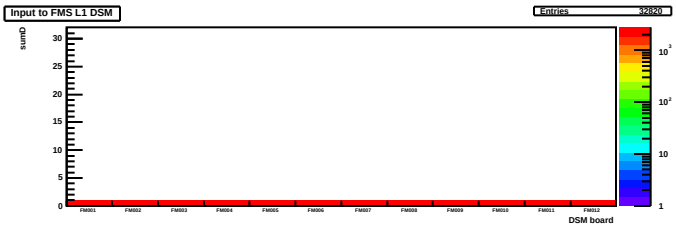

Input to FMS L0 DSM
Entries 21880
QT8(3) sum - simulated
QT board

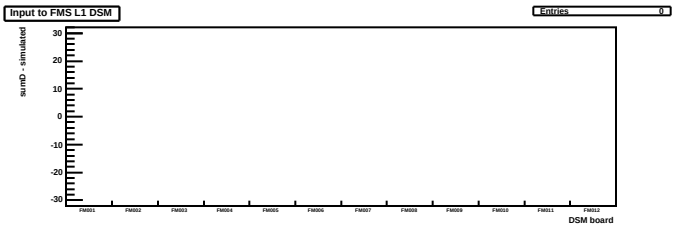

Input to FMS L0 DSM
Entries 131280
HT ADC
QT board

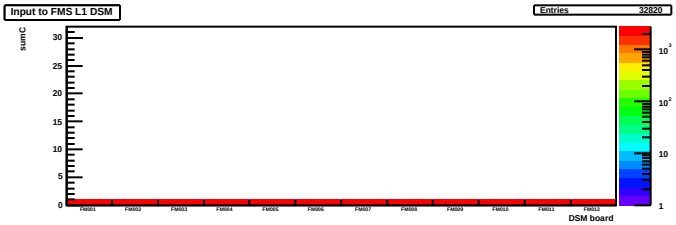

Input to FMS L0 DSM
Entries 21880
HT ADC - simulated
QT board

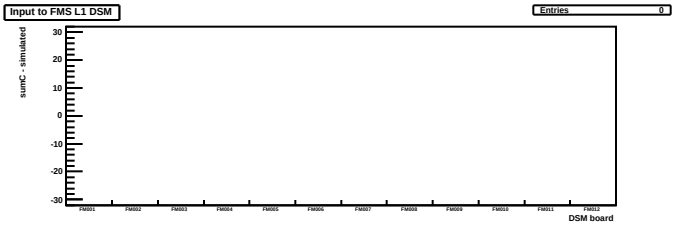

Input to FMS L0 DSM
Entries 131280
HT ID
QT board

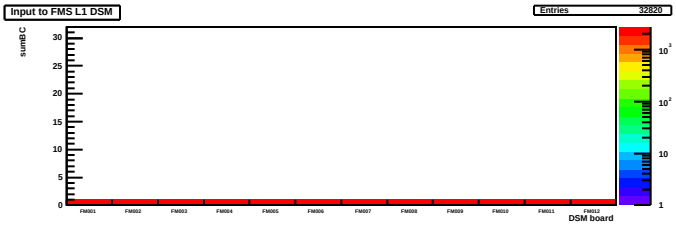

Input to FMS L0 DSM
Entries 21880
HT ID - simulated
QT board

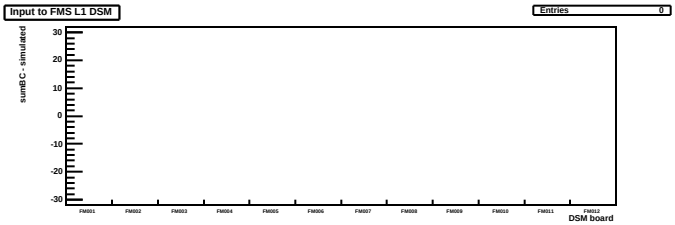

Input to FMS L1 DSM
Entries 32820
sumD
DSM board

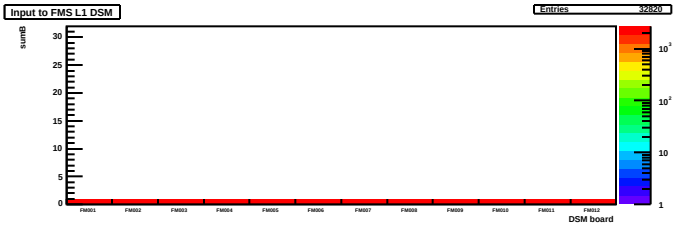

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

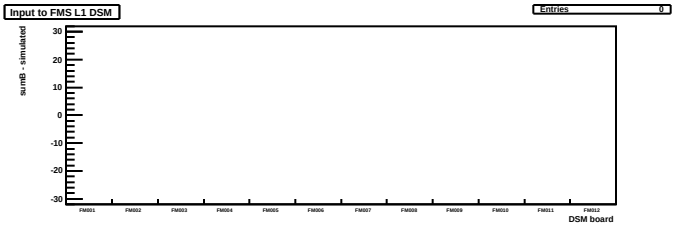

Input to FMS L1 DSM
Entries 32820
sumC
DSM board

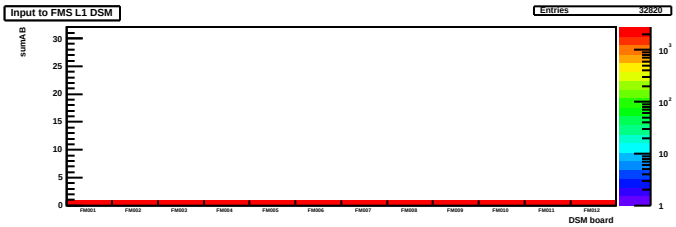

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

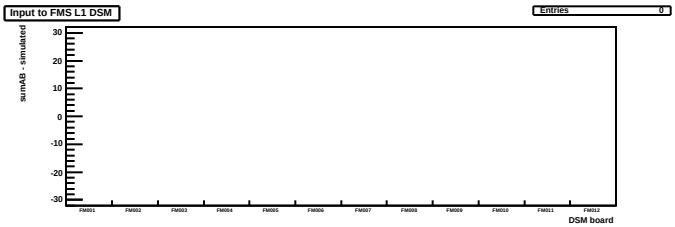

Input to FMS L1 DSM
Entries 32820
sumBC
DSM board

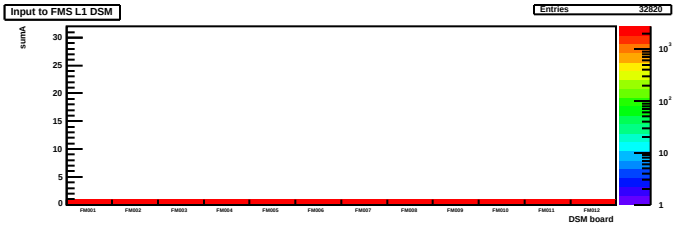

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

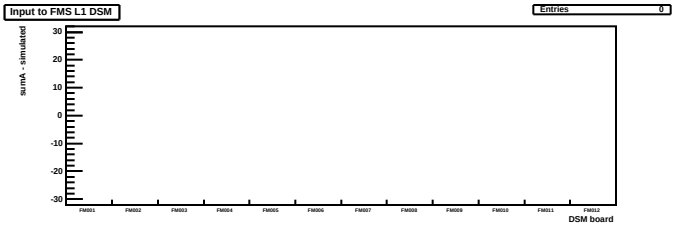

Input to FMS L1 DSM
Entries 32820
sumB
DSM board

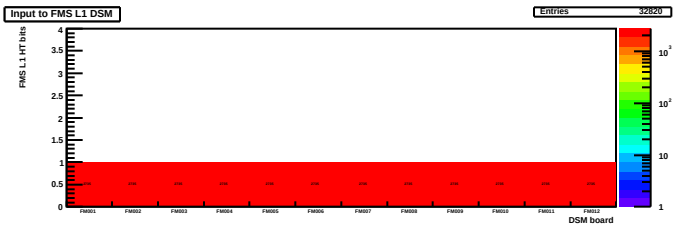

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

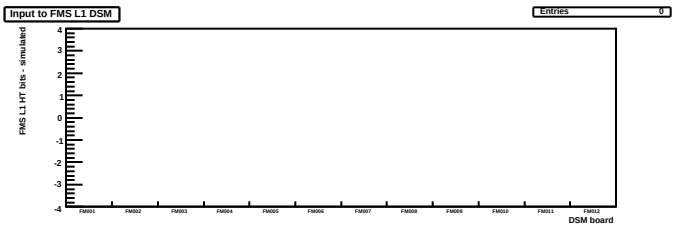

Input to FMS L1 DSM
Entries 32820
sumAB
DSM board

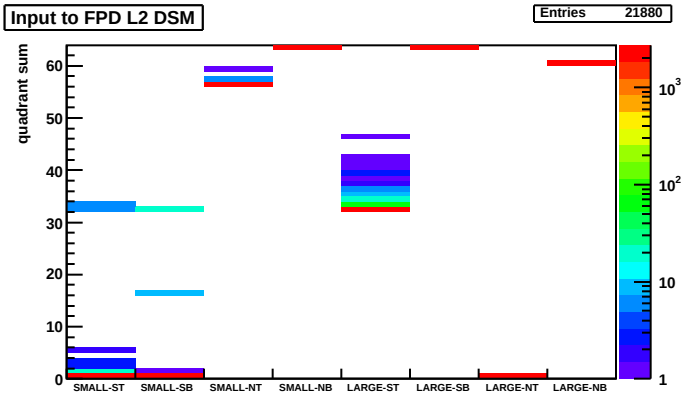

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

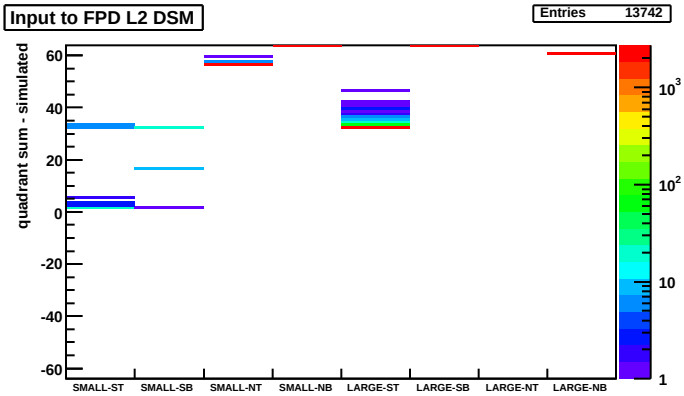

Input to FMS L1 DSM
Entries 32820
sumA
DSM board

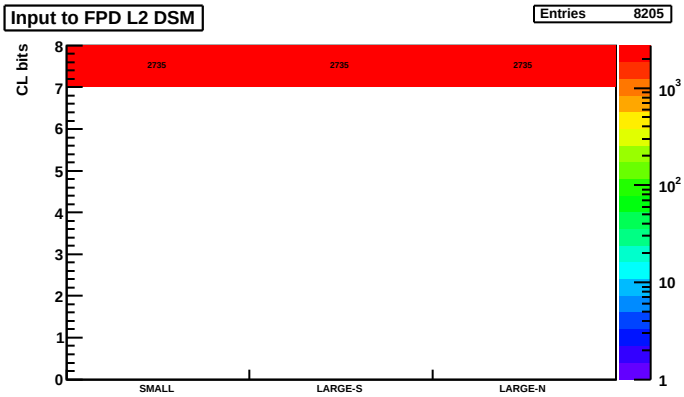

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

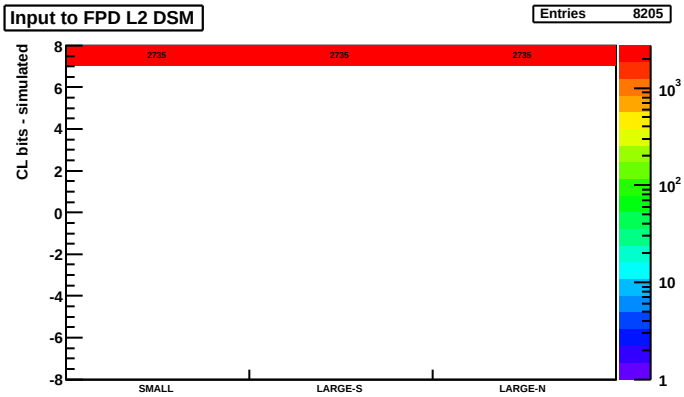

Input to FMS L1 DSM
Entries 32820
FMS L1 HT bits
DSM board

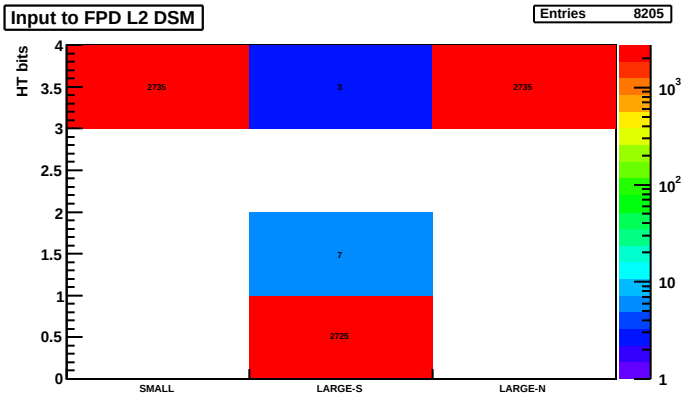

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

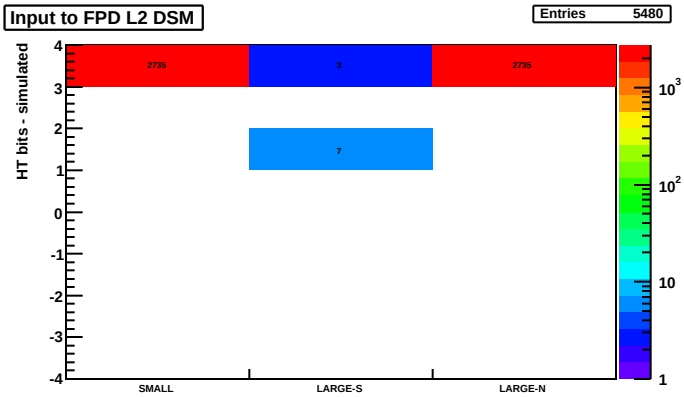

Input to FPD L2 DSM
Entries 21880
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

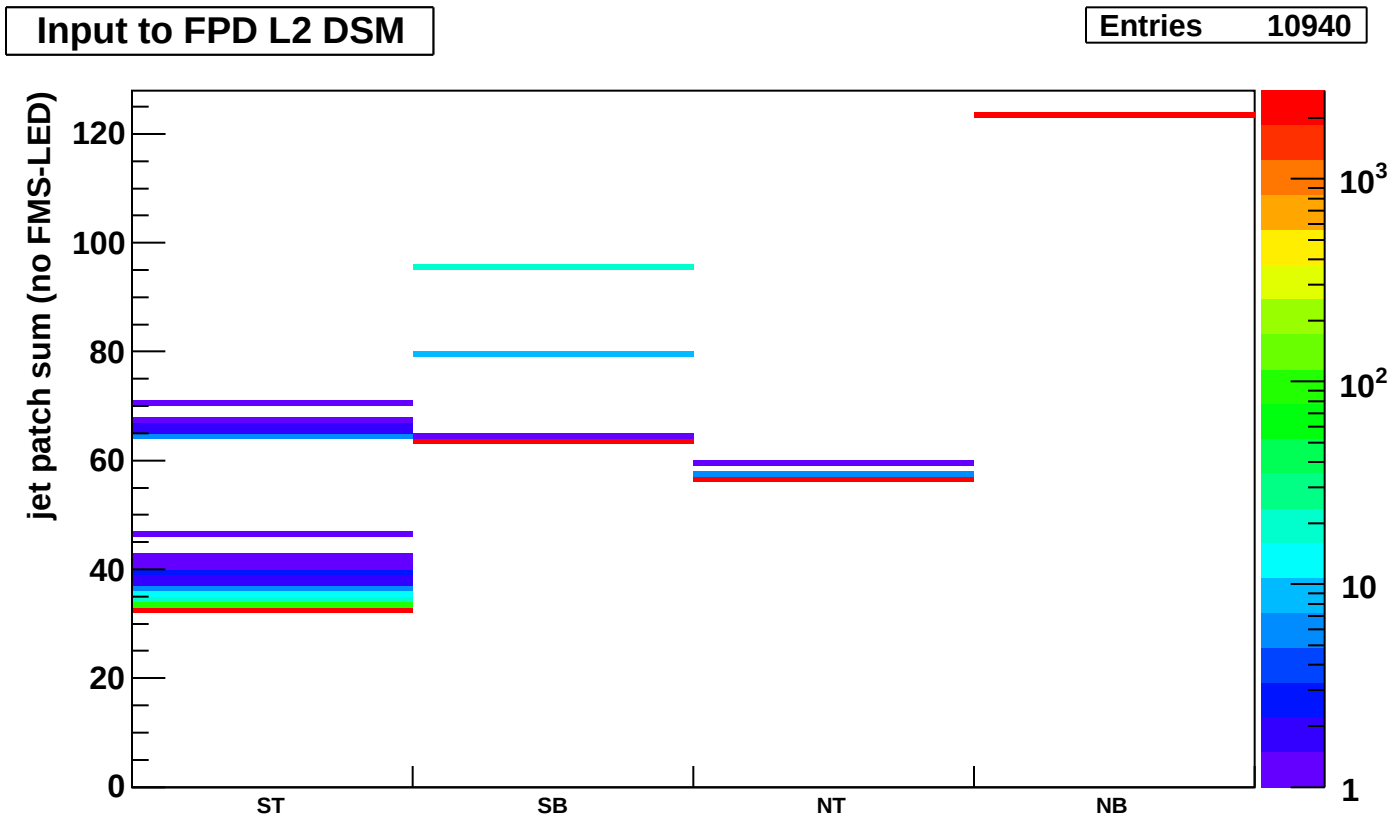

Input to FPD L2 DSM
Entries 13742
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

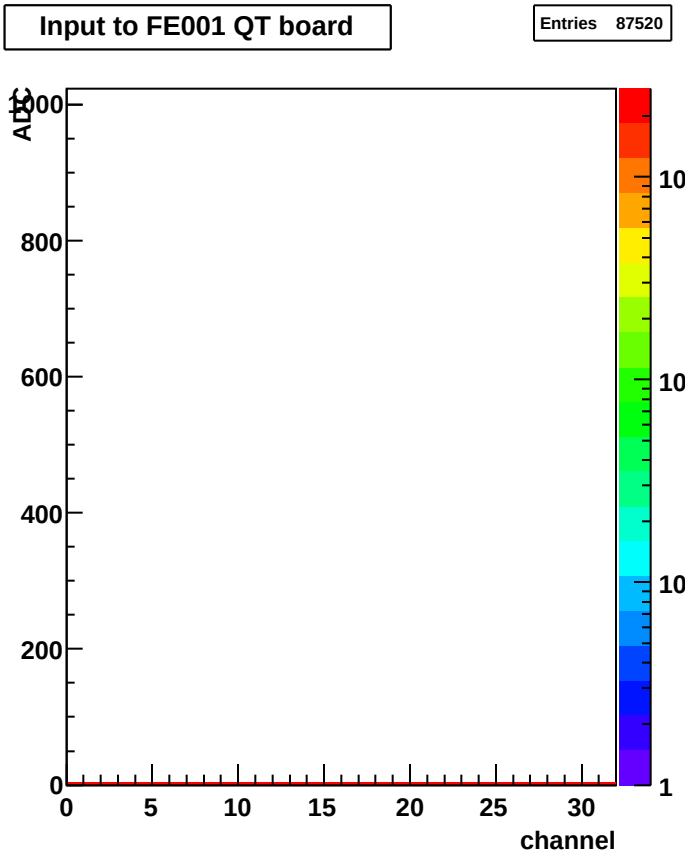

Input to FPD L2 DSM
Entries 8205
CL bits
2735
2735
2735
SMALL
LARGE-S
LARGE-N

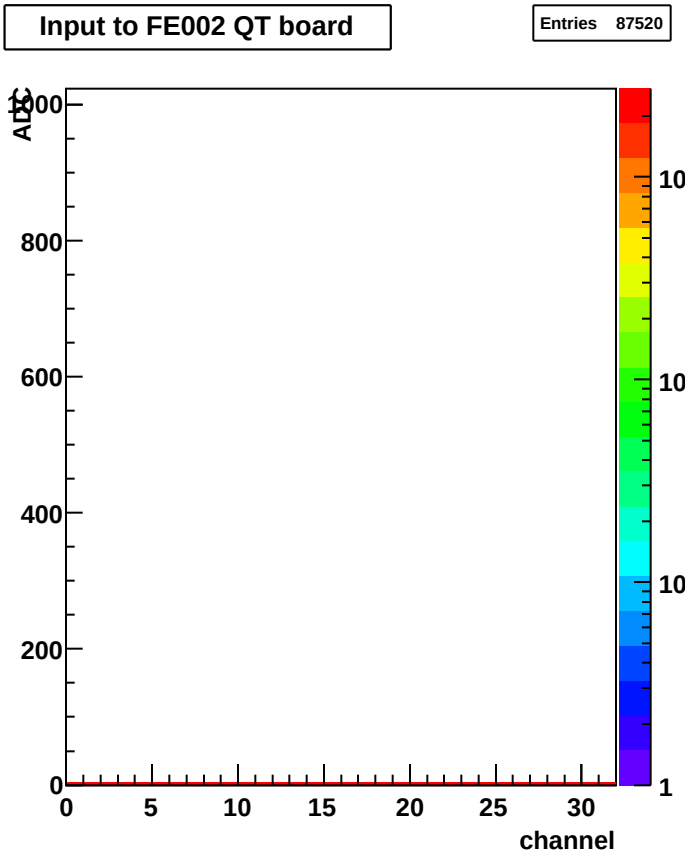

Input to FPD L2 DSM
Entries 8205
CL bits - simulated
2735
2735
2735
SMALL
LARGE-S
LARGE-N

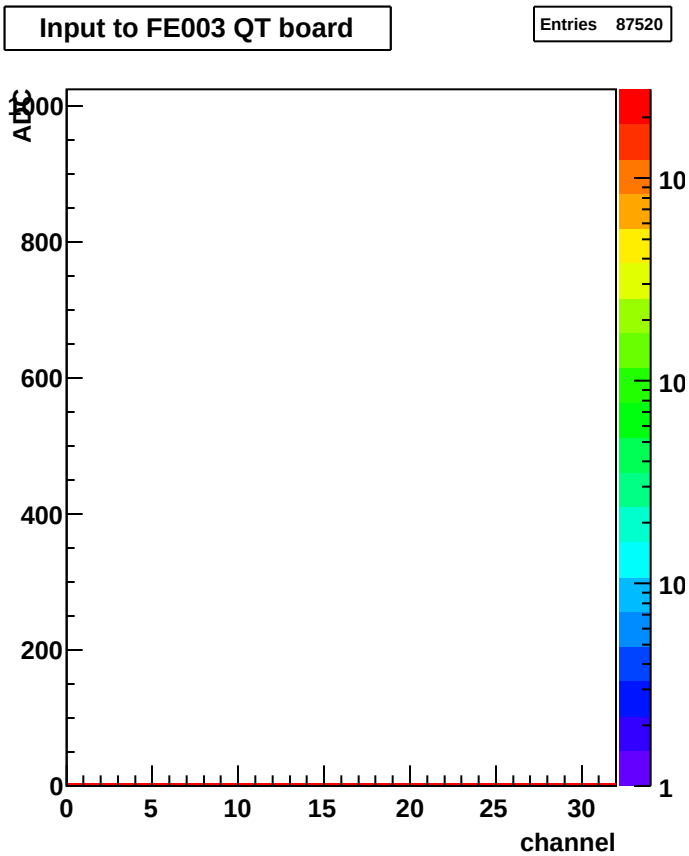

Input to FPD L2 DSM
Entries 8205
HT bits
2735
3
2735
7
2725
SMALL
LARGE-S
LARGE-N

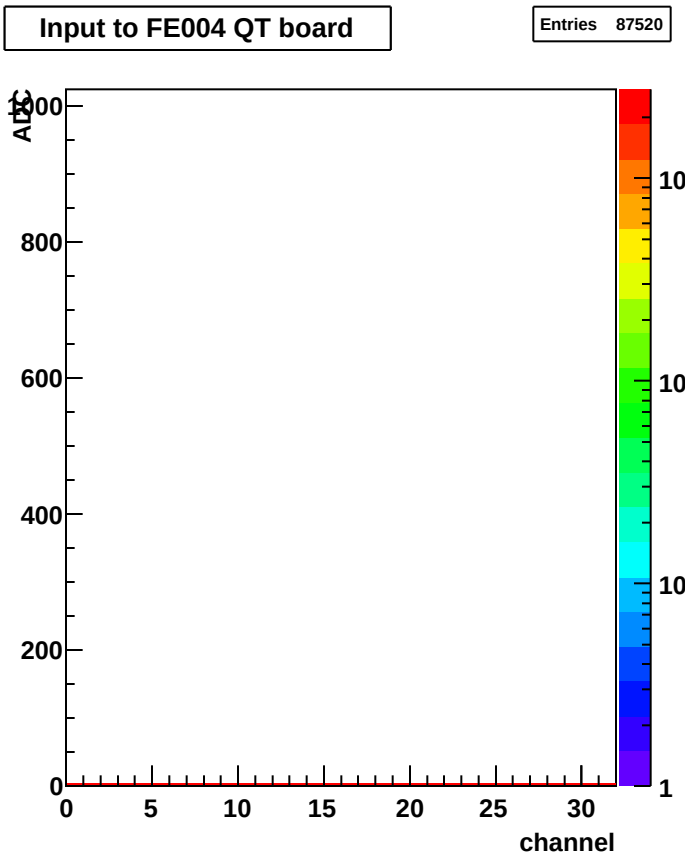

Input to FPD L2 DSM
Entries 5480
HT bits - simulated
2735
3
2735
7
SMALL
LARGE-S
LARGE-N

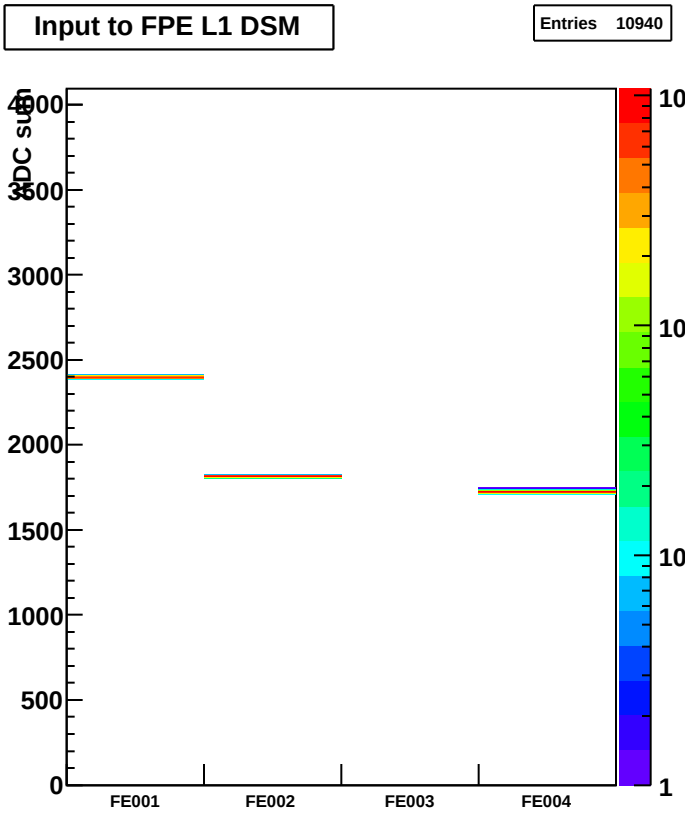

Input to FPD L2 DSM
Entries 10940
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3

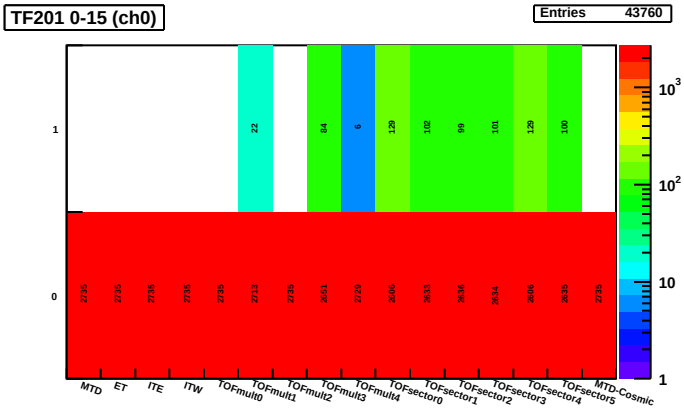

Input to FE001 QT board
Entries 87520
ADC
channel

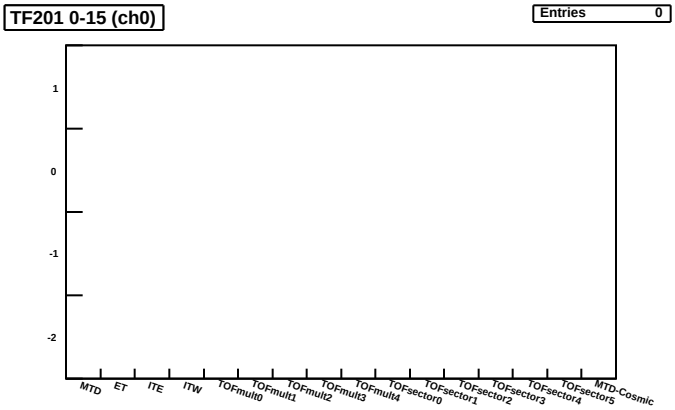

Input to FE002 QT board
Entries 87520
ADC
channel

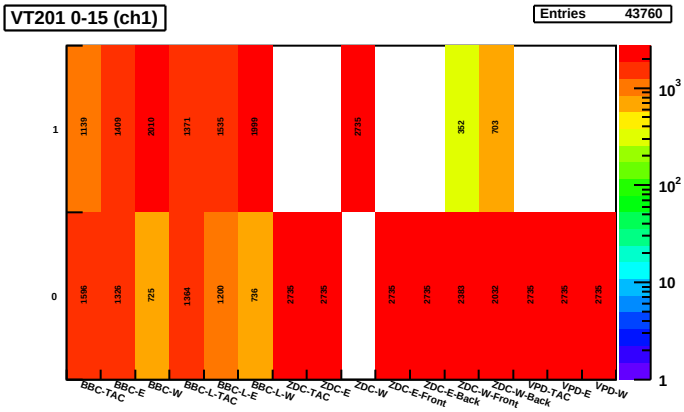

Input to FE003 QT board
Entries 87520
ADC
channel

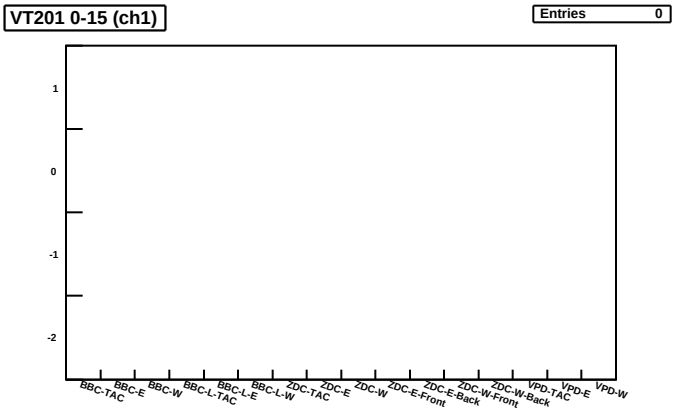

Input to FE004 QT board
Entries 87520
ADC
channel

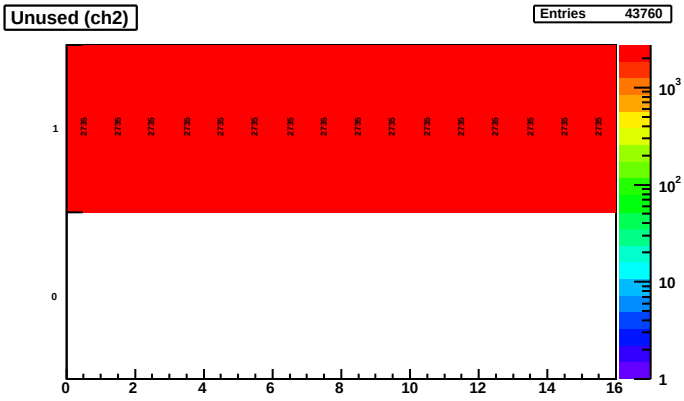

Input to FPE L1 DSM
Entries 10940
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
1

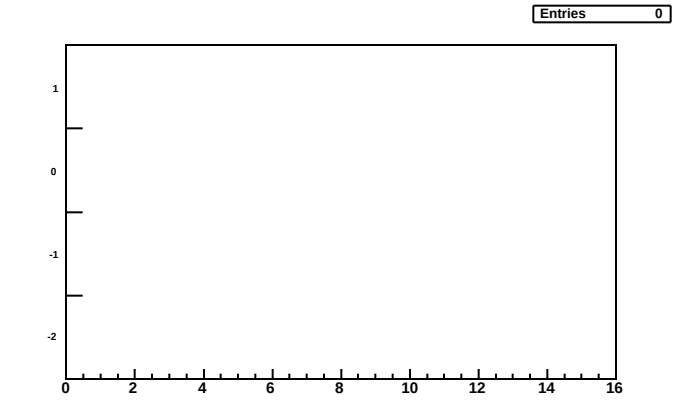

TF201 0-15 (ch0)
Entries 43760
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

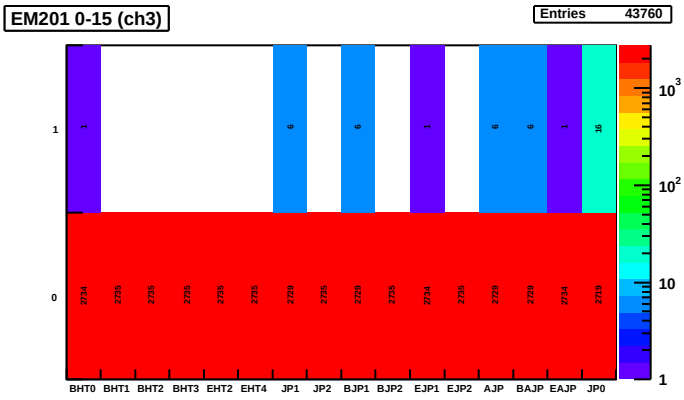

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

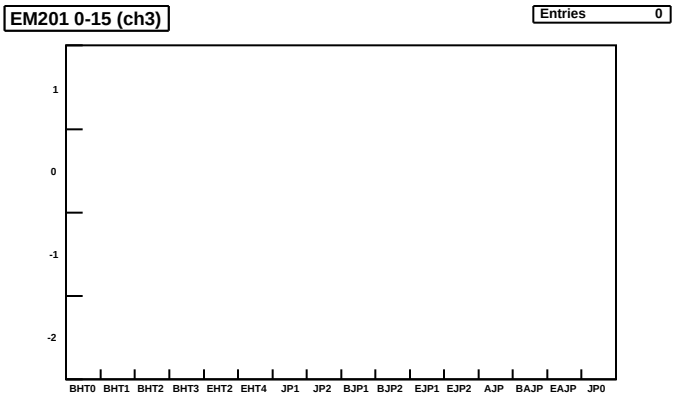

VT201 0-15 (ch1)
Entries 43760
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

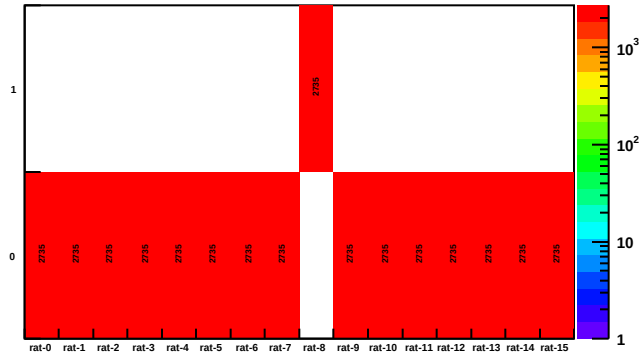

Unused (ch2)
Entries 43760

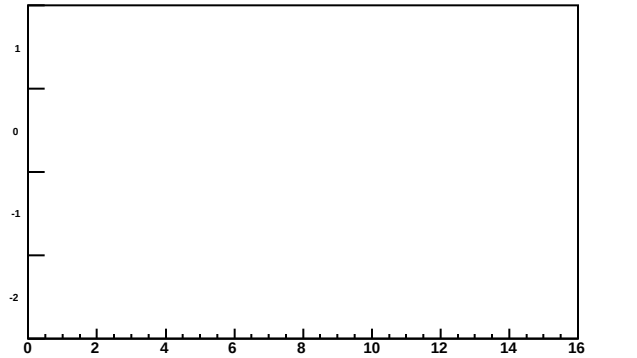

Entries 0


EM201 0-15 (ch3)
Entries 43760
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

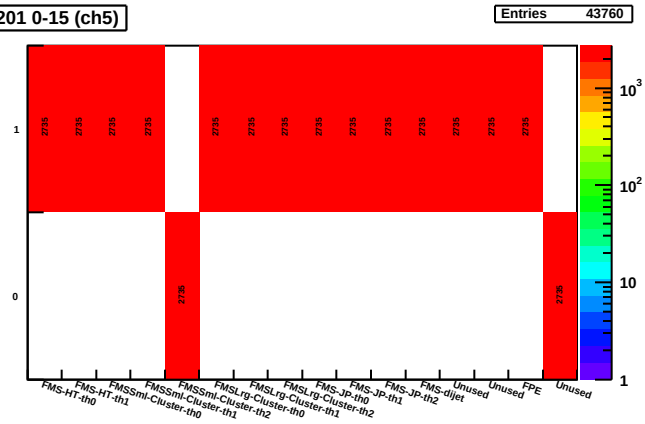

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

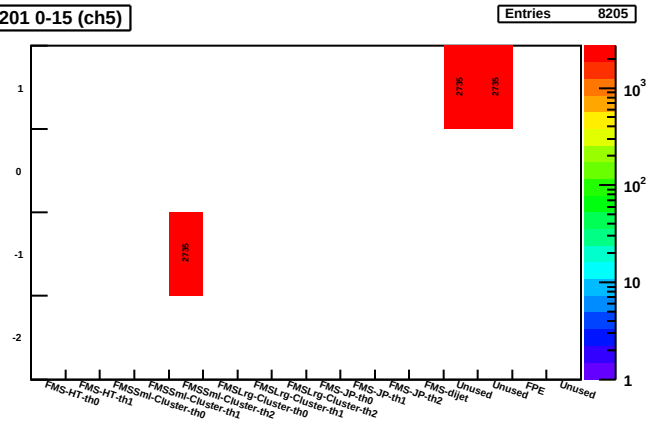




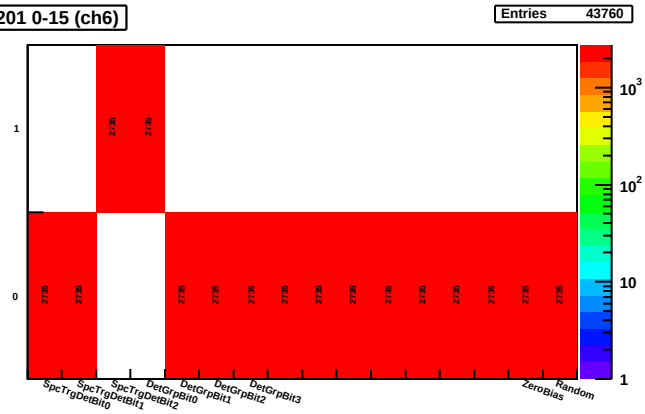
FP201 0-15 (ch5)

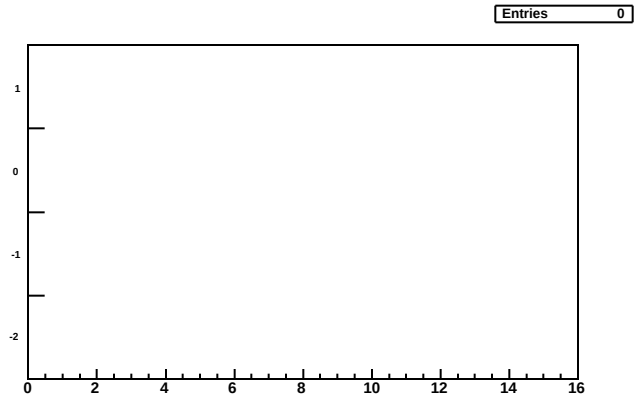
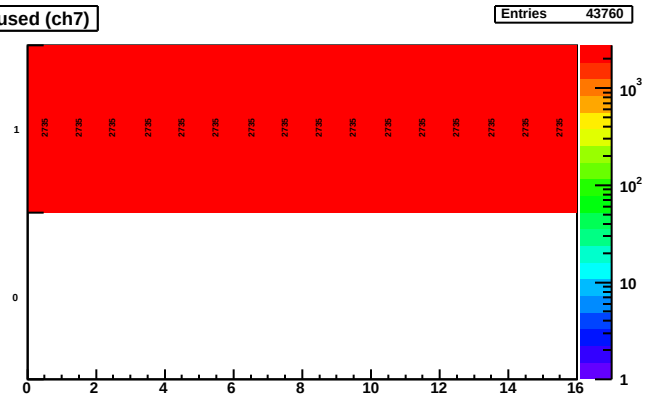
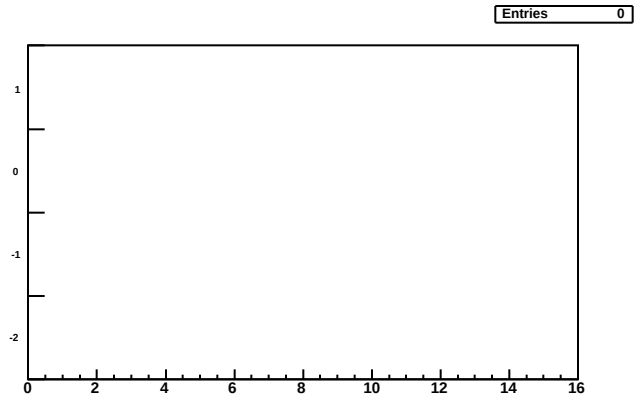


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)