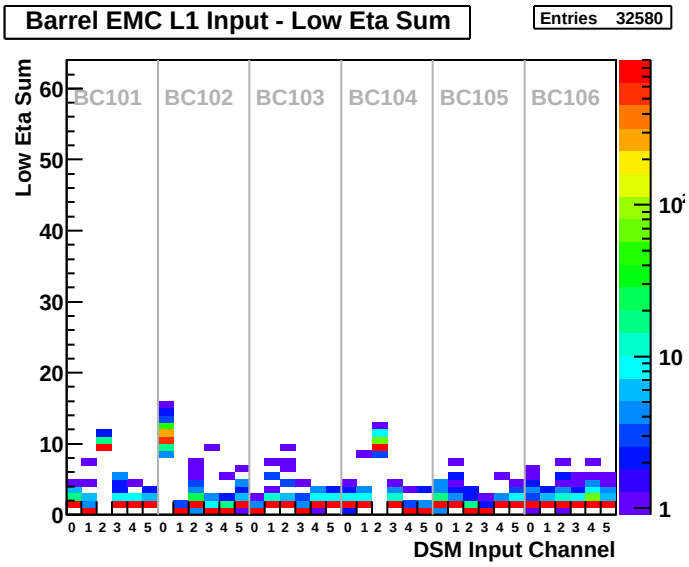
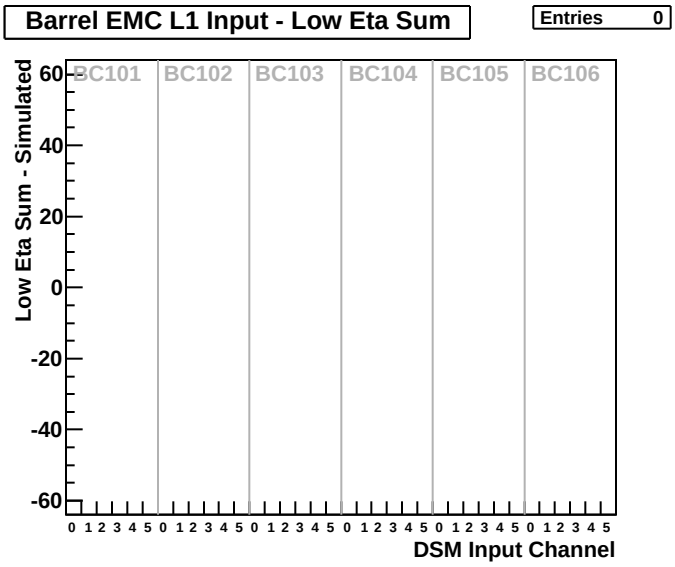
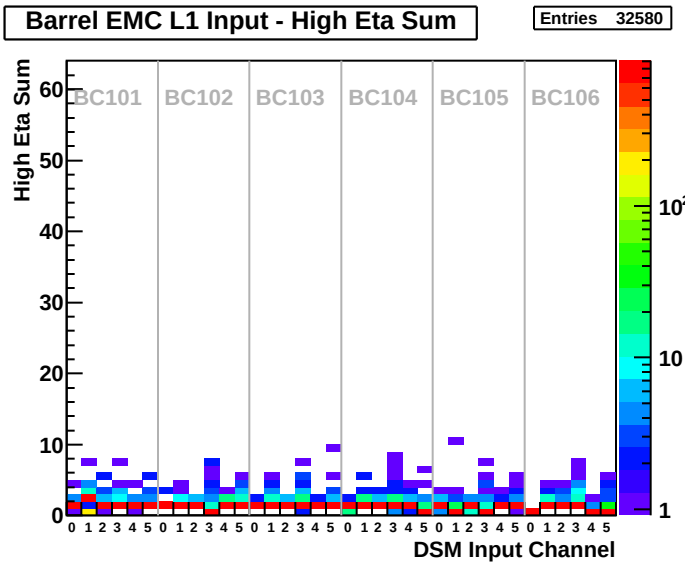
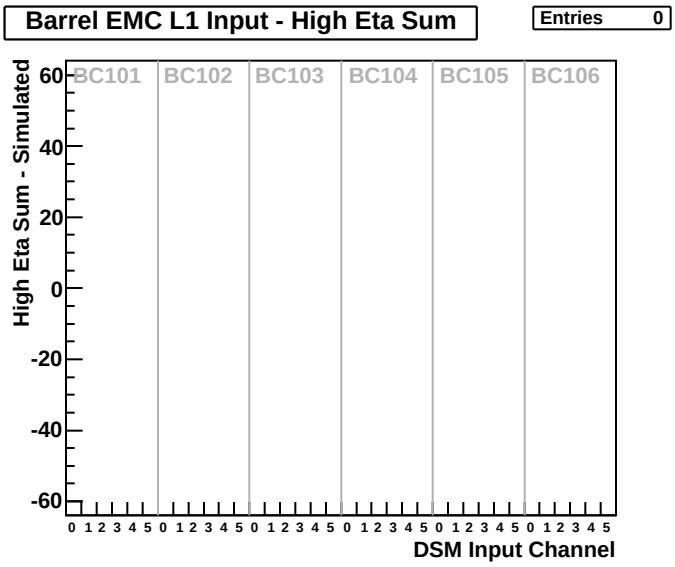
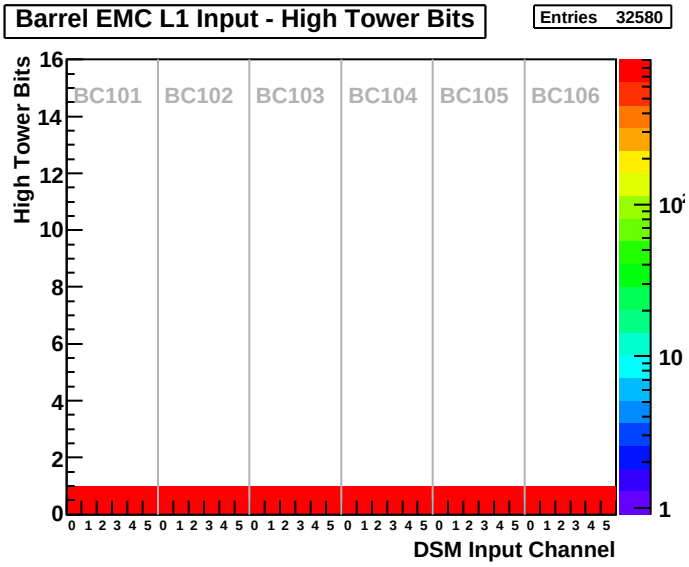
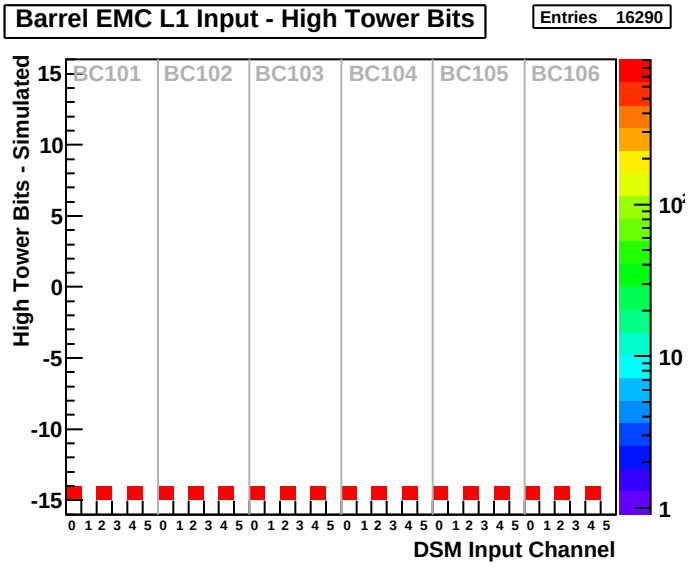

Barrel EMC L1 Input - Low Eta Sum
Entries 32580
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

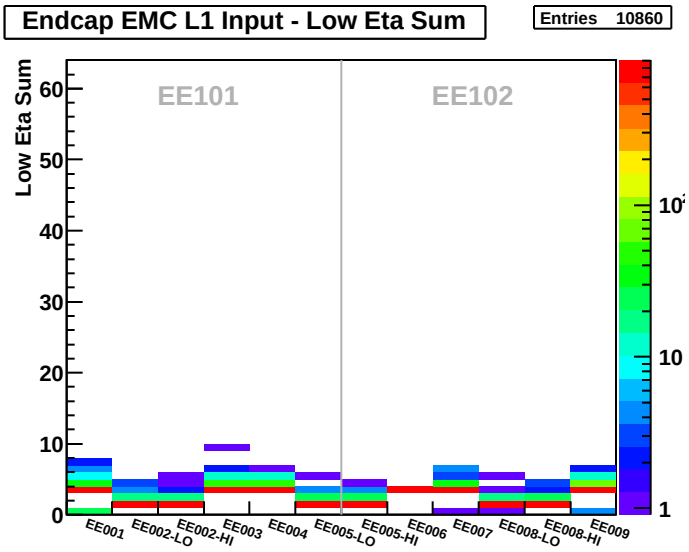

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

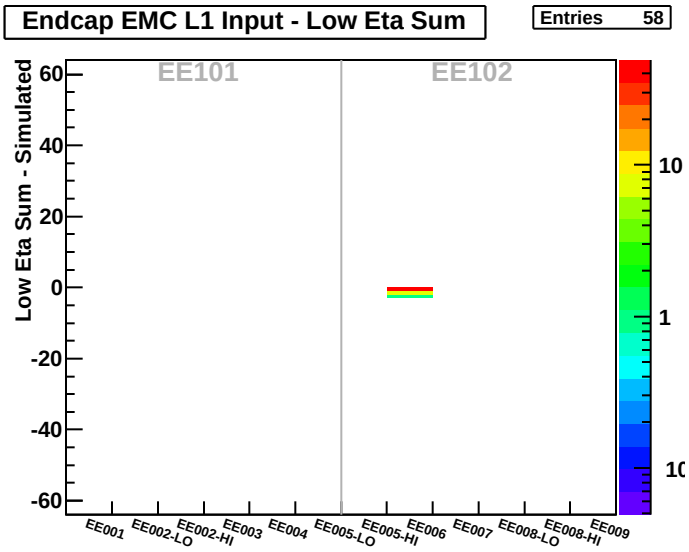

Barrel EMC L1 Input - High Eta Sum
Entries 32580
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

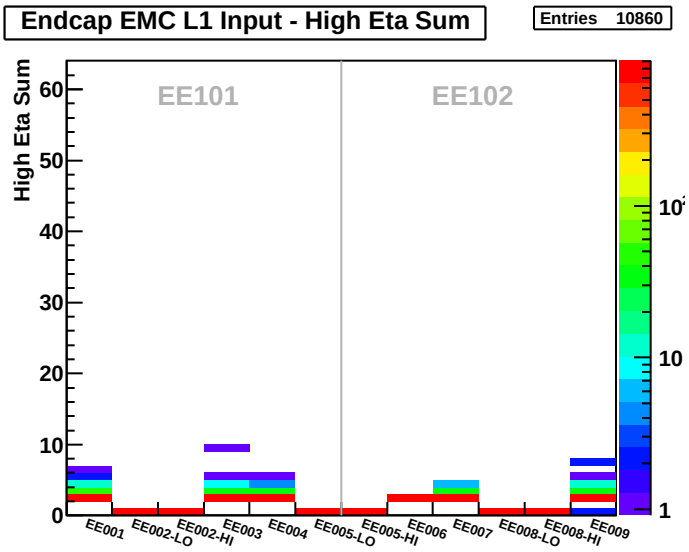

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

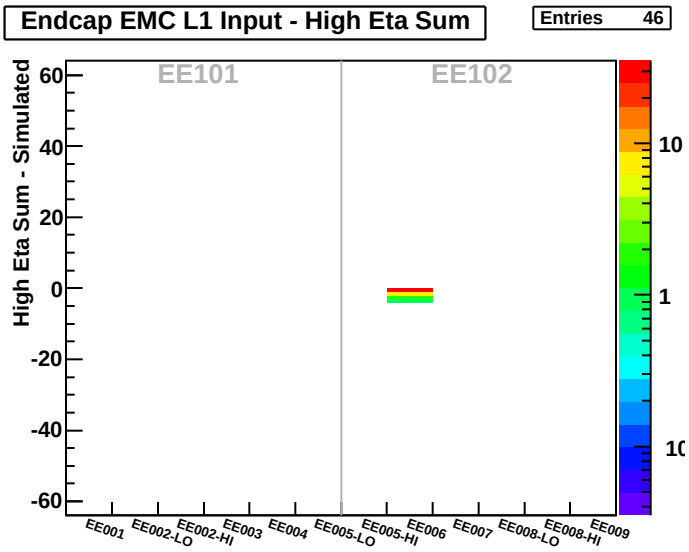

Barrel EMC L1 Input - High Tower Bits
Entries 32580
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

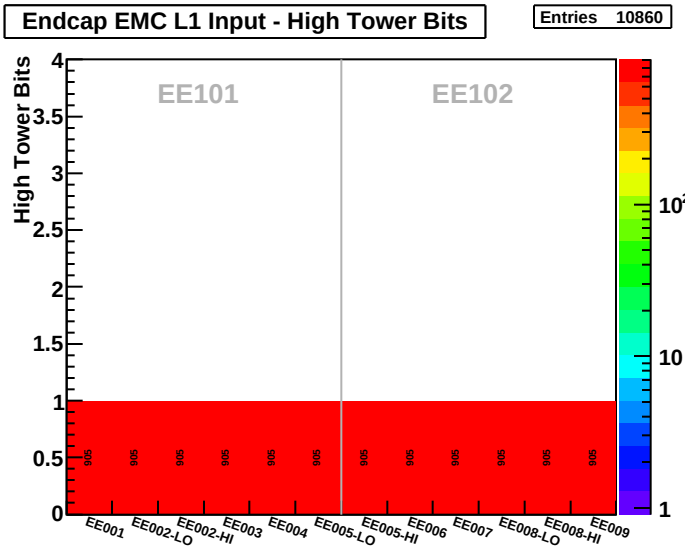

Barrel EMC L1 Input - High Tower Bits
Entries 16290
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

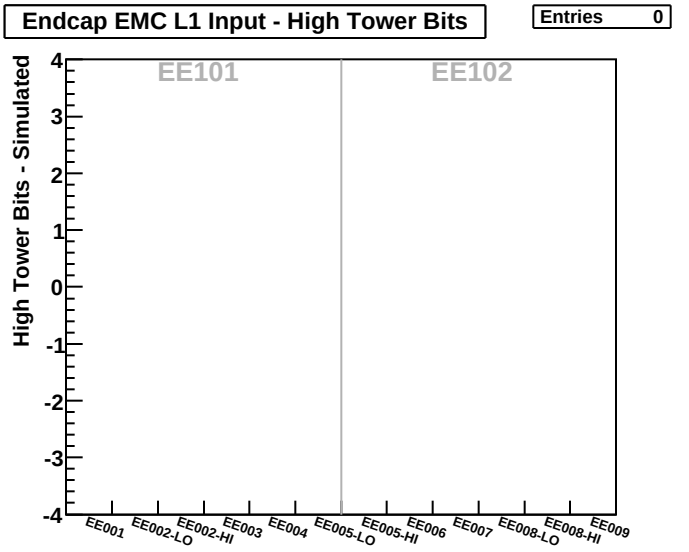

Endcap EMC L1 Input - Low Eta Sum
Entries 10860
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

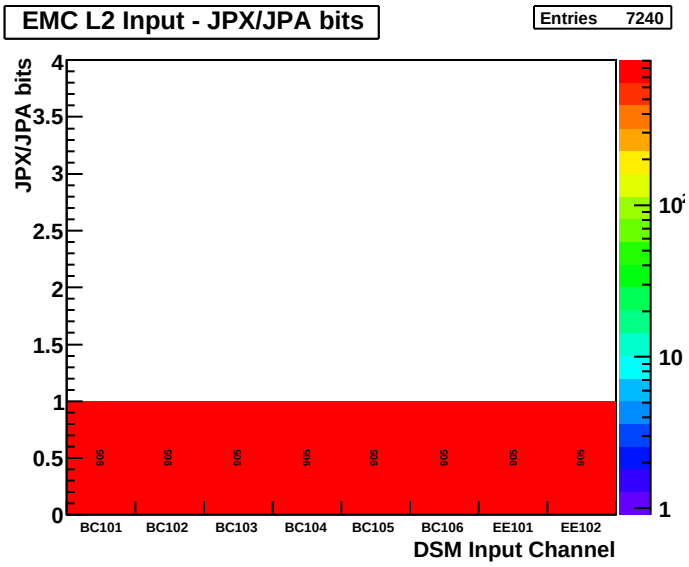

Endcap EMC L1 Input - Low Eta Sum
Entries 58
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

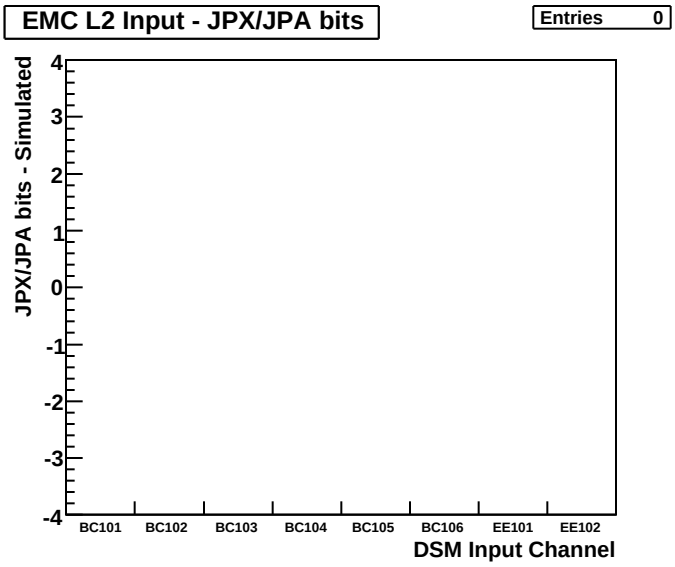

Endcap EMC L1 Input - High Eta Sum
Entries 10860
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

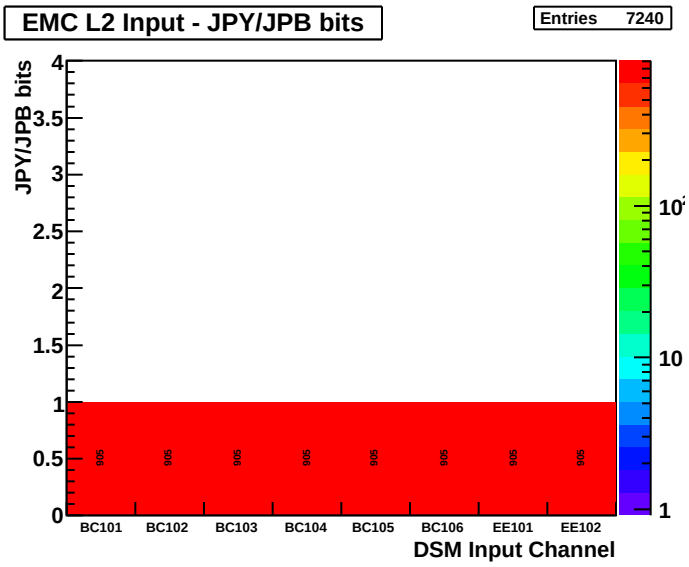

Endcap EMC L1 Input - High Eta Sum
Entries 46
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

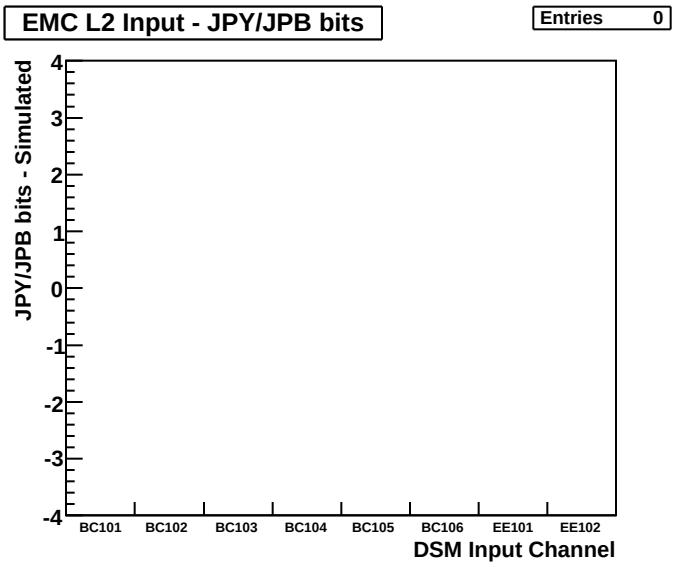

Endcap EMC L1 Input - High Tower Bits
Entries 10860
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

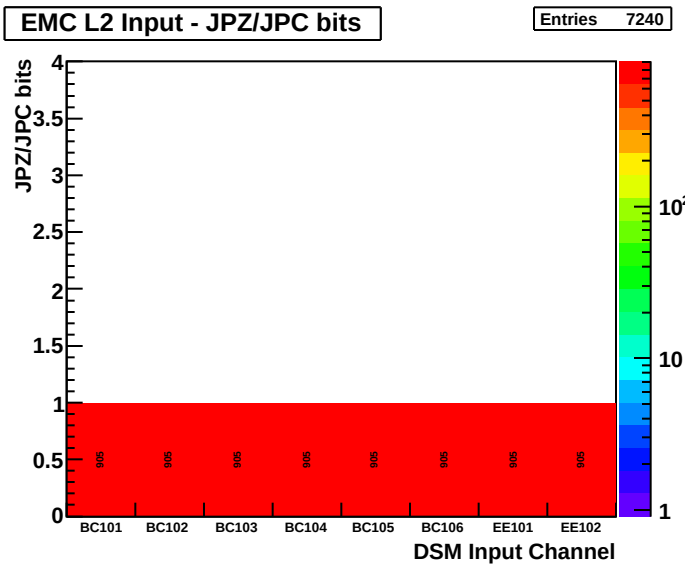

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

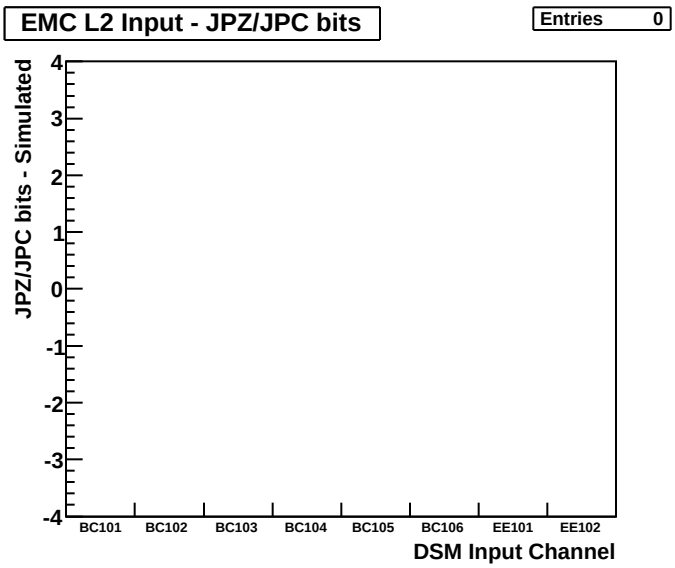

EMC L2 Input - JPX/JPA bits
Entries 7240
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

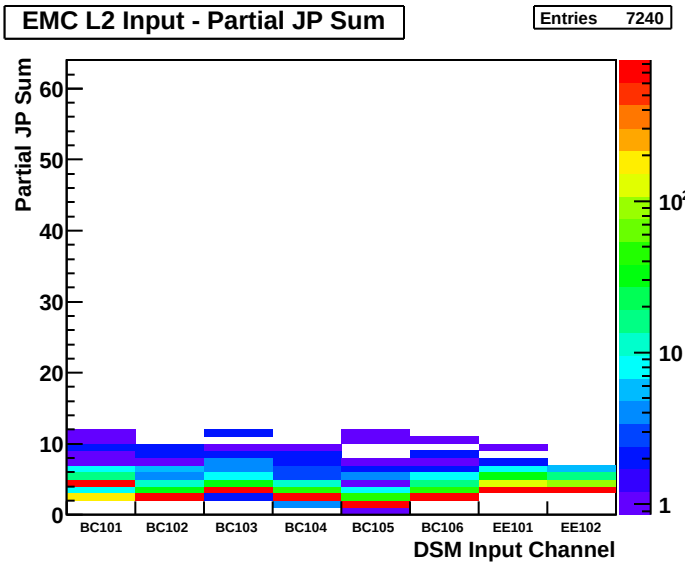

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

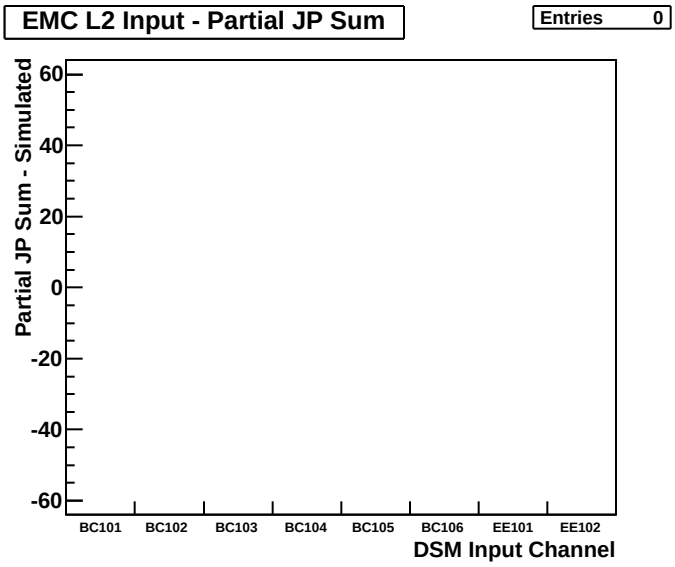

EMC L2 Input - JPY/JPB bits
Entries 7240
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

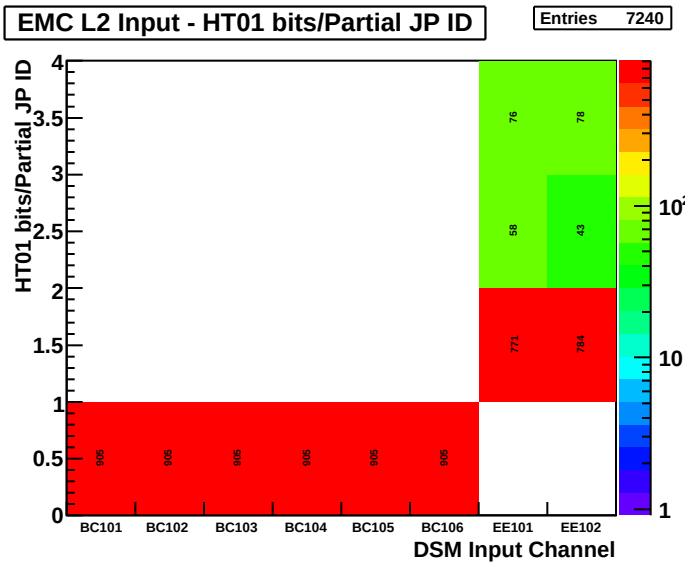

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

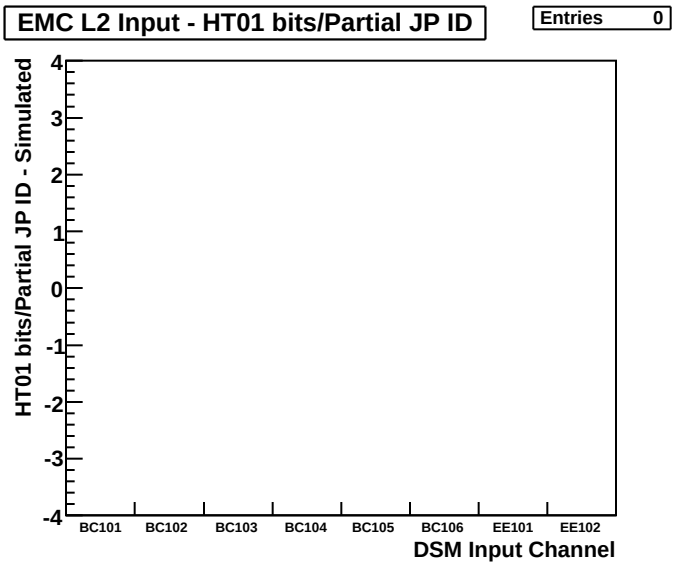

EMC L2 Input - JPZ/JPC bits
Entries 7240
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

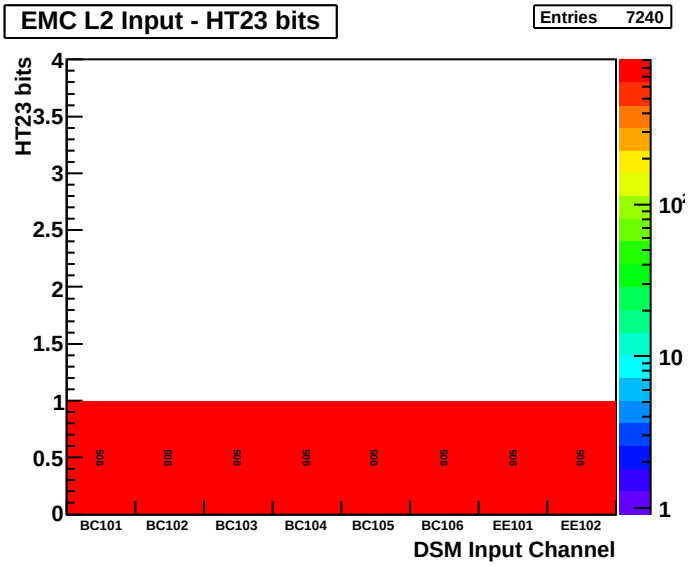

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

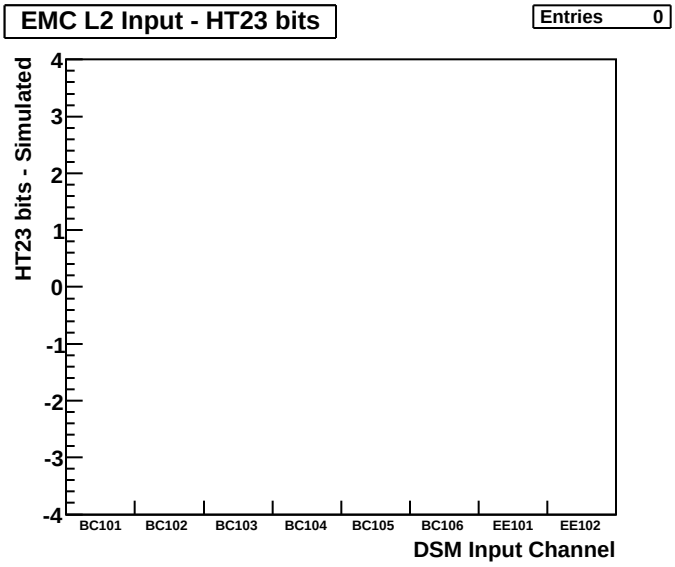

EMC L2 Input - Partial JP Sum
Entries 7240
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

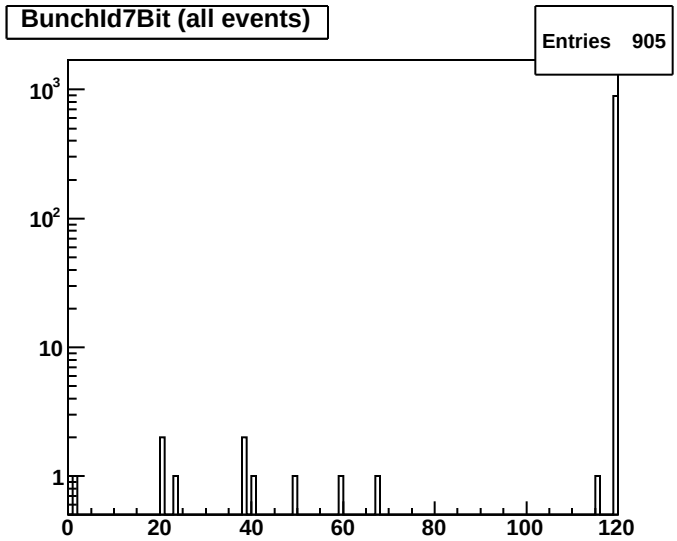

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

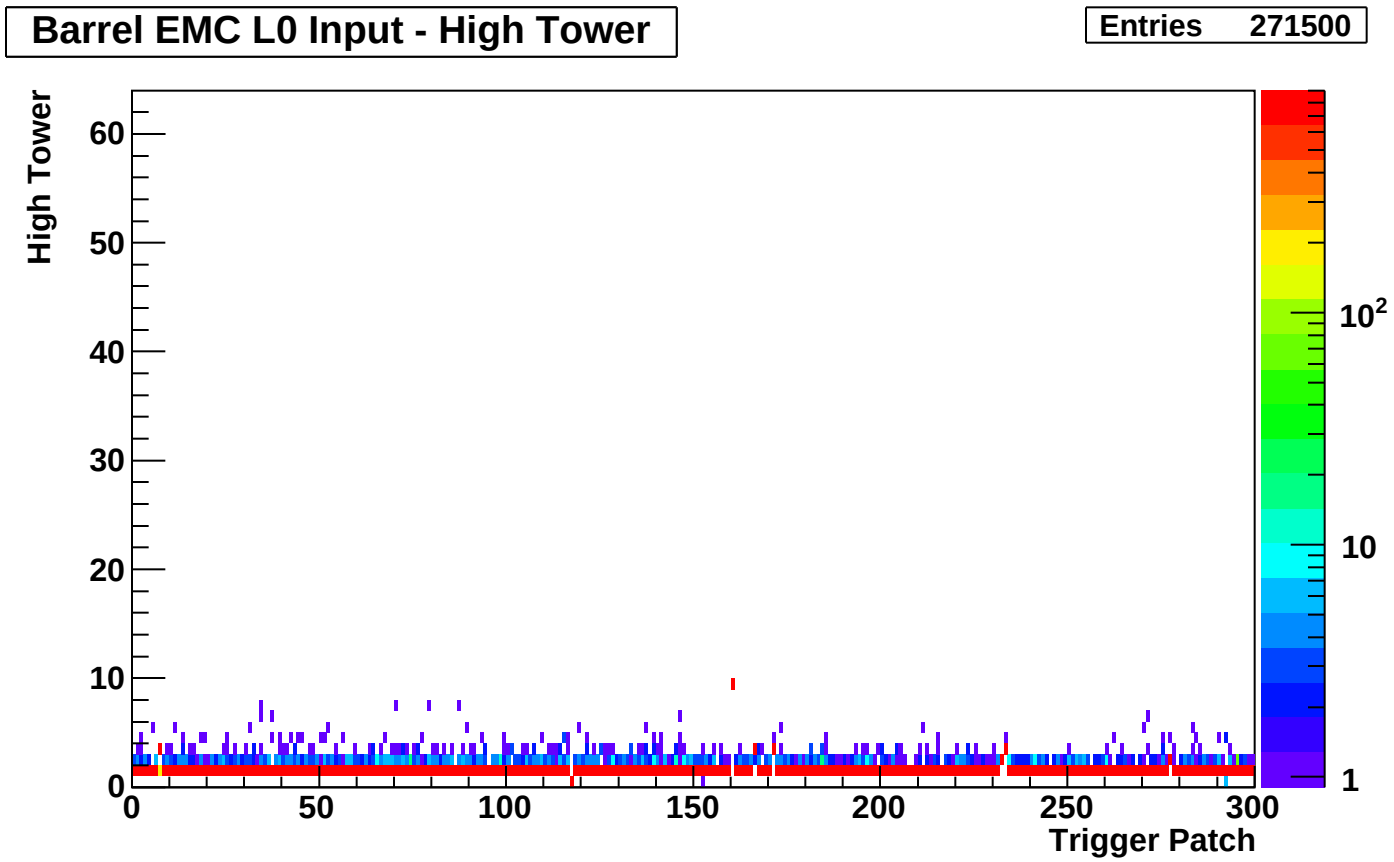

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7240
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

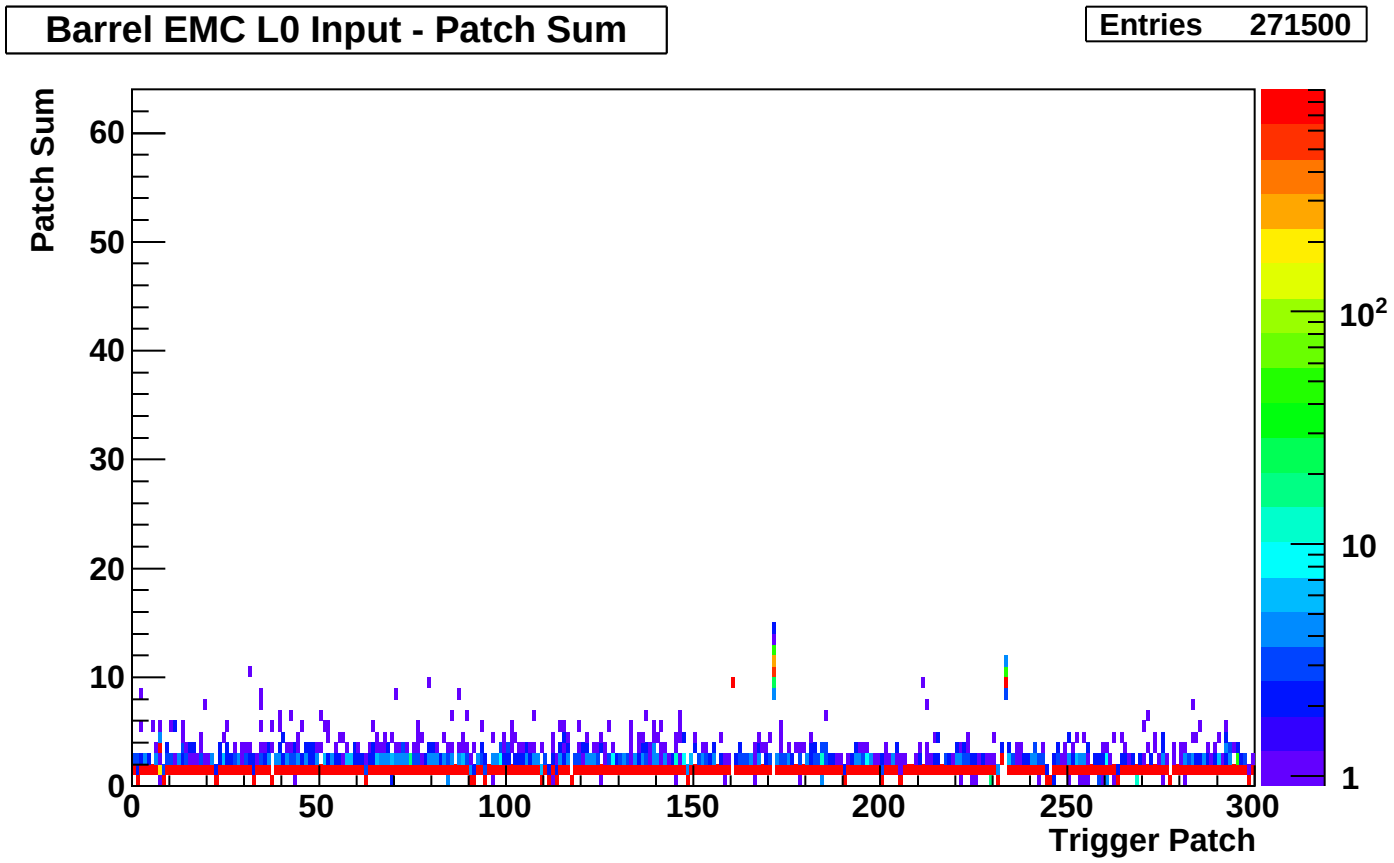

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

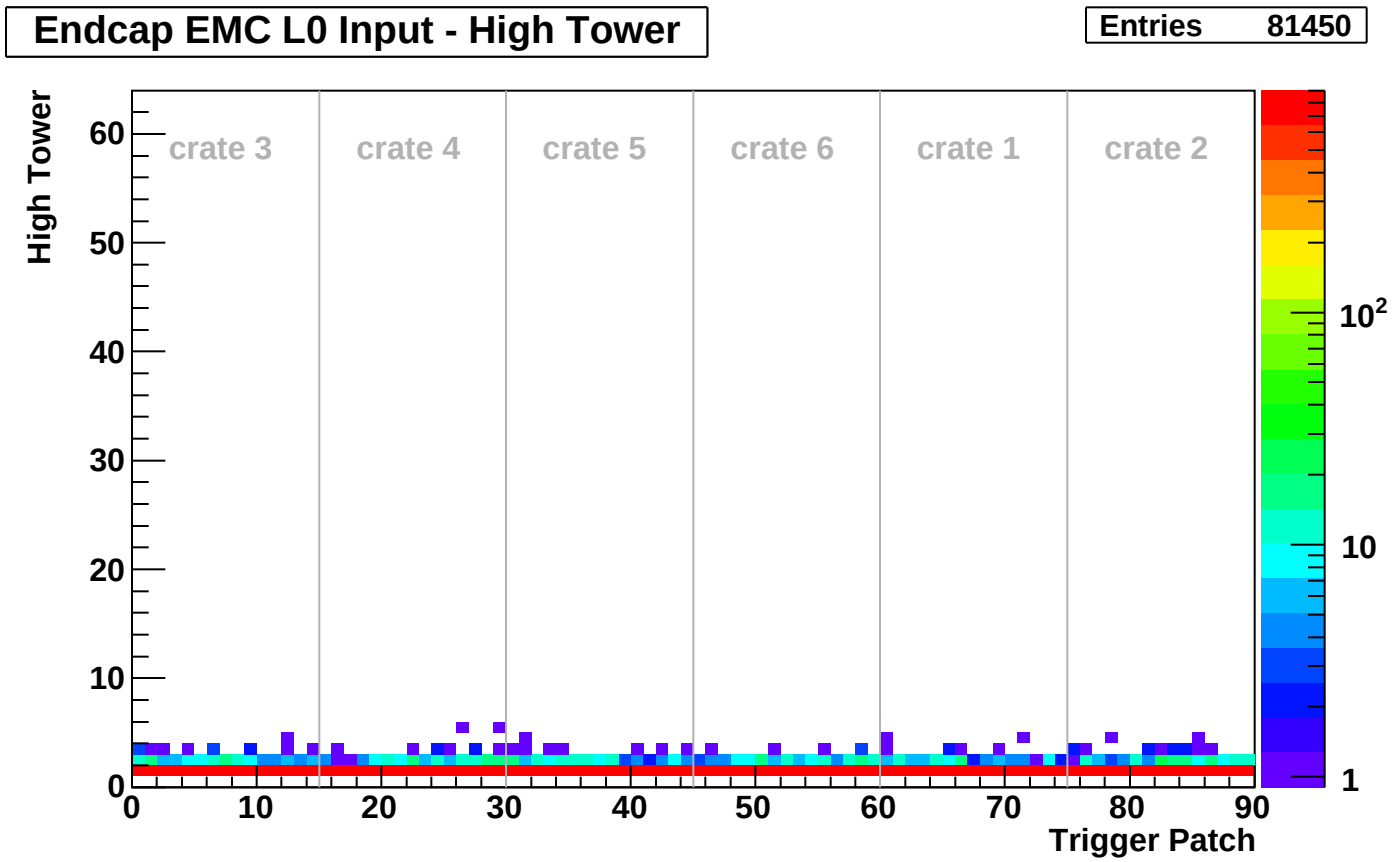

EMC L2 Input - HT23 bits
Entries 7240
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

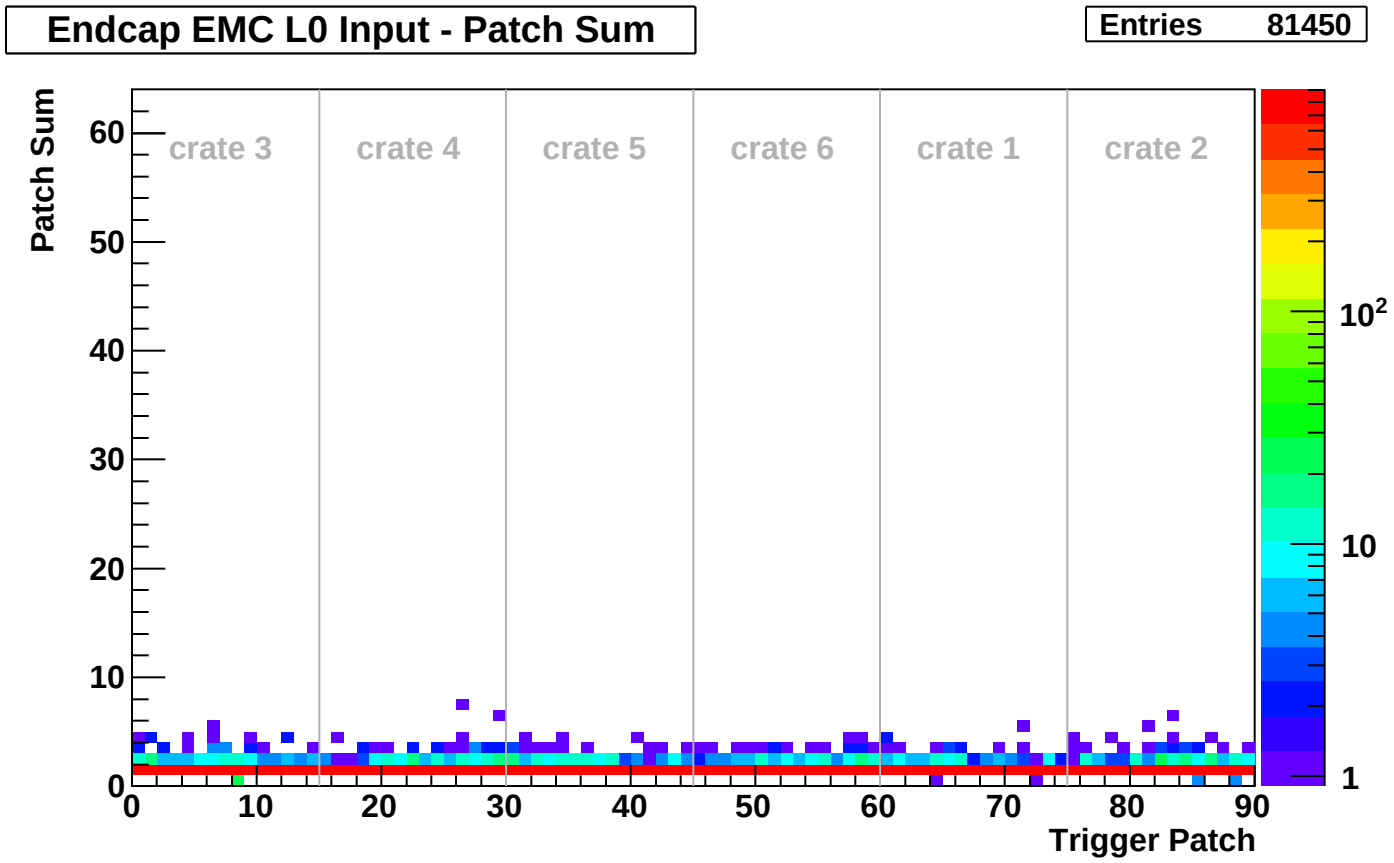

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

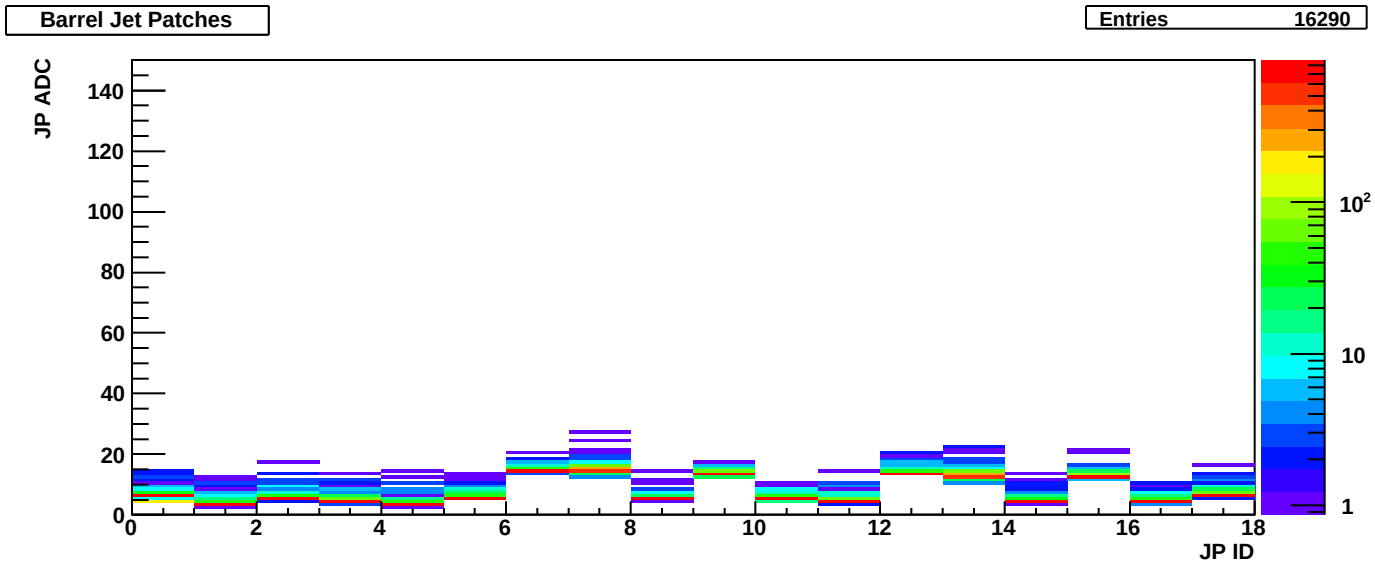

BunchId7Bit (all events)
Entries 905
10^3
10^2
10
1
0
20
40
60
80
100
120

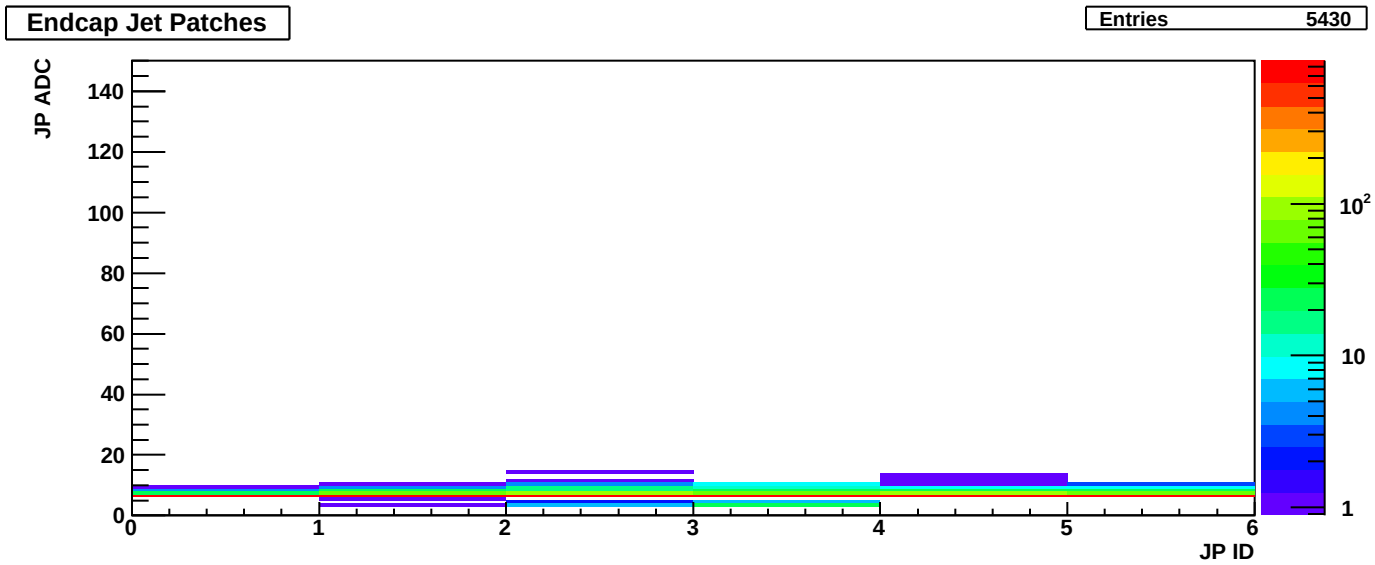

Barrel EMC L0 Input - High Tower
Entries 271500
High Tower
0
10
20
30
40
50
60
0
50
100
150
200
250
300
Trigger Patch
1
10
10^2

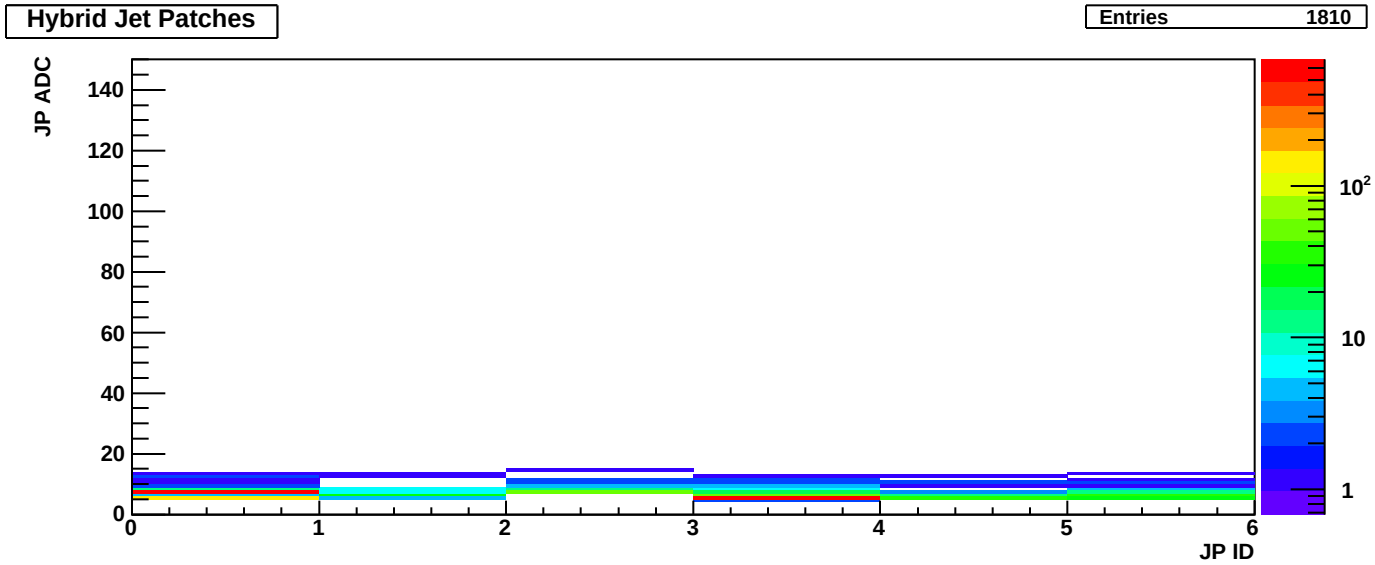

Barrel EMC L0 Input - Patch Sum
Entries 271500
Patch Sum
0
10
20
30
40
50
60
0
50
100
150
200
250
300
Trigger Patch
1
10
10^2

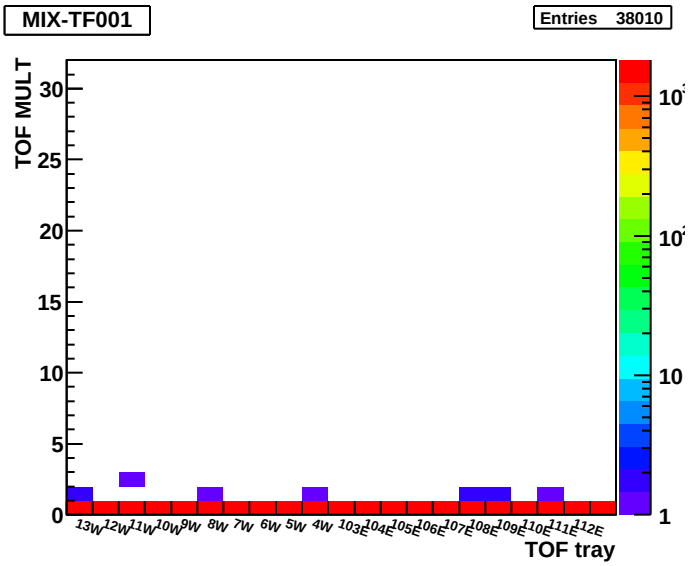

Endcap EMC L0 Input - High Tower
Entries 81450
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

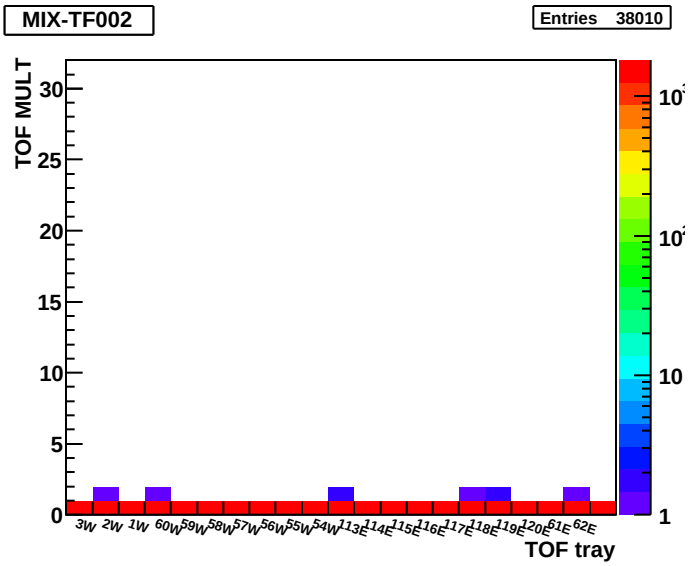

Endcap EMC L0 Input - Patch Sum
Entries 81450
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

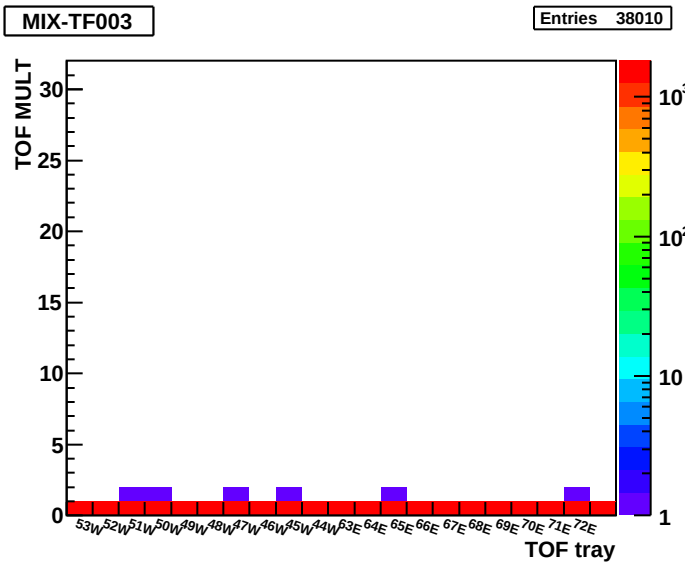

Barrel Jet Patches
Entries 16290
JP ADC
JP ID

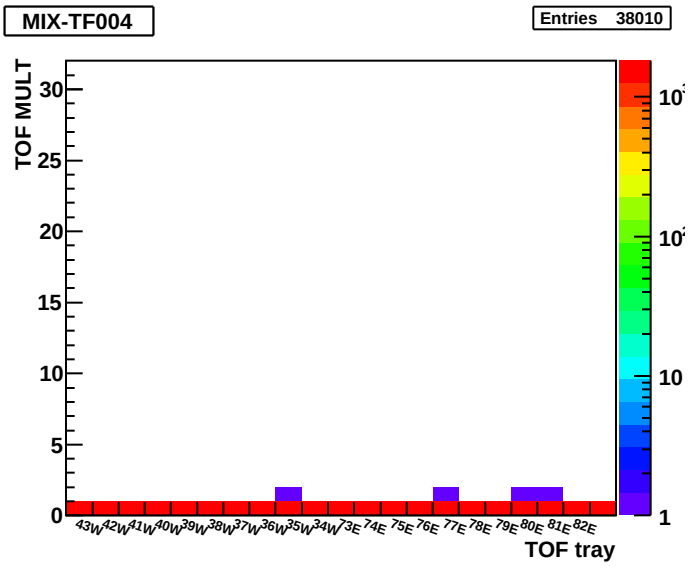

Endcap Jet Patches
Entries 5430
JP ADC
JP ID

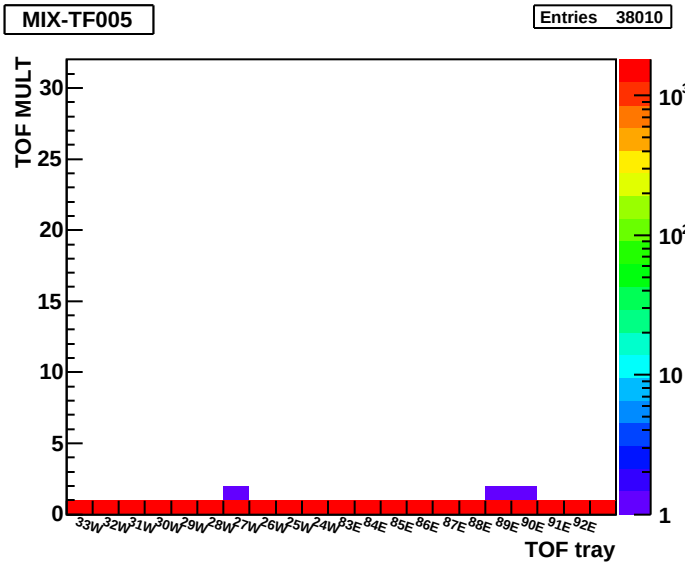

Hybrid Jet Patches
Entries 1810
JP ADC
JP ID

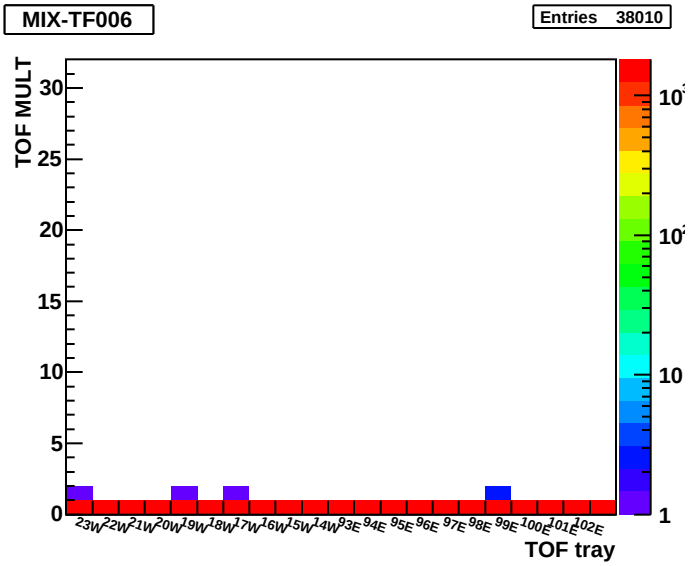

MIX-TF001
Entries 38010
TOF MULT
TOF tray

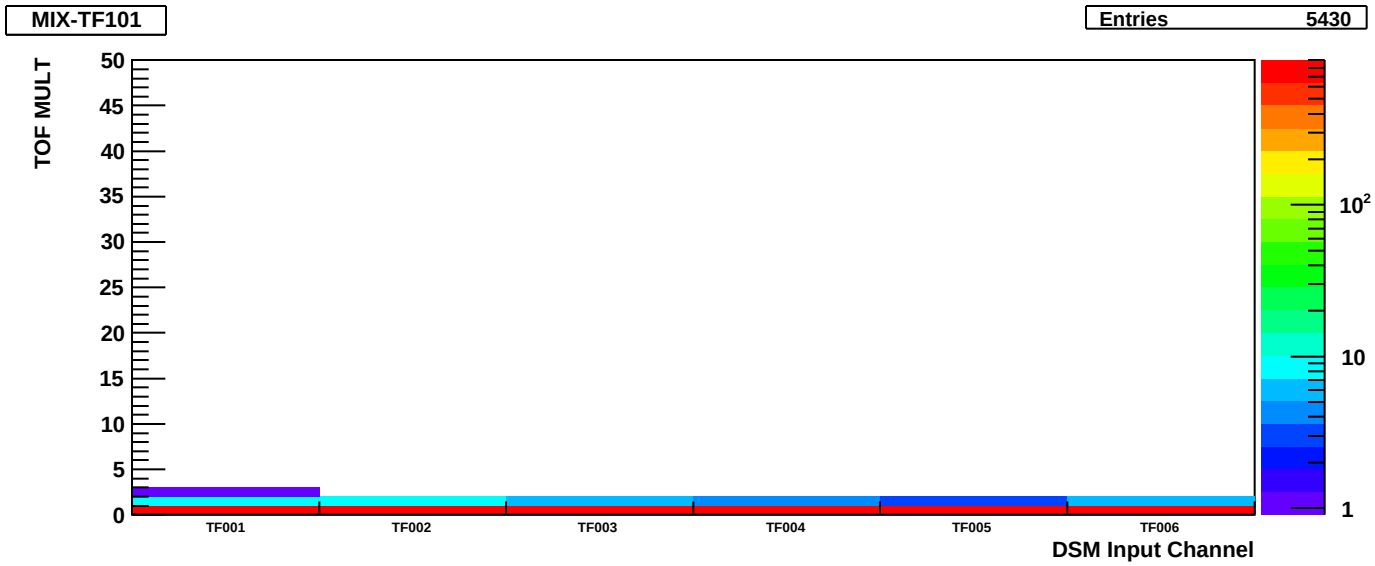

MIX-TF002
Entries 38010
TOF MULT
TOF tray

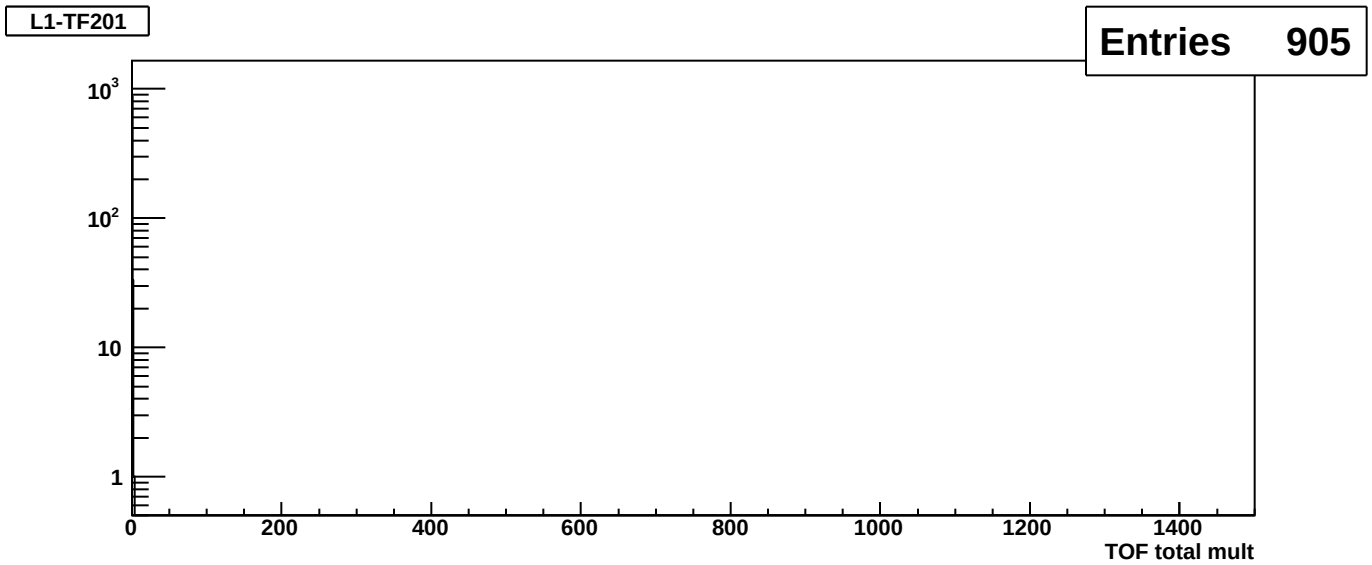

MIX-TF003
Entries 38010
TOF MULT
TOF tray

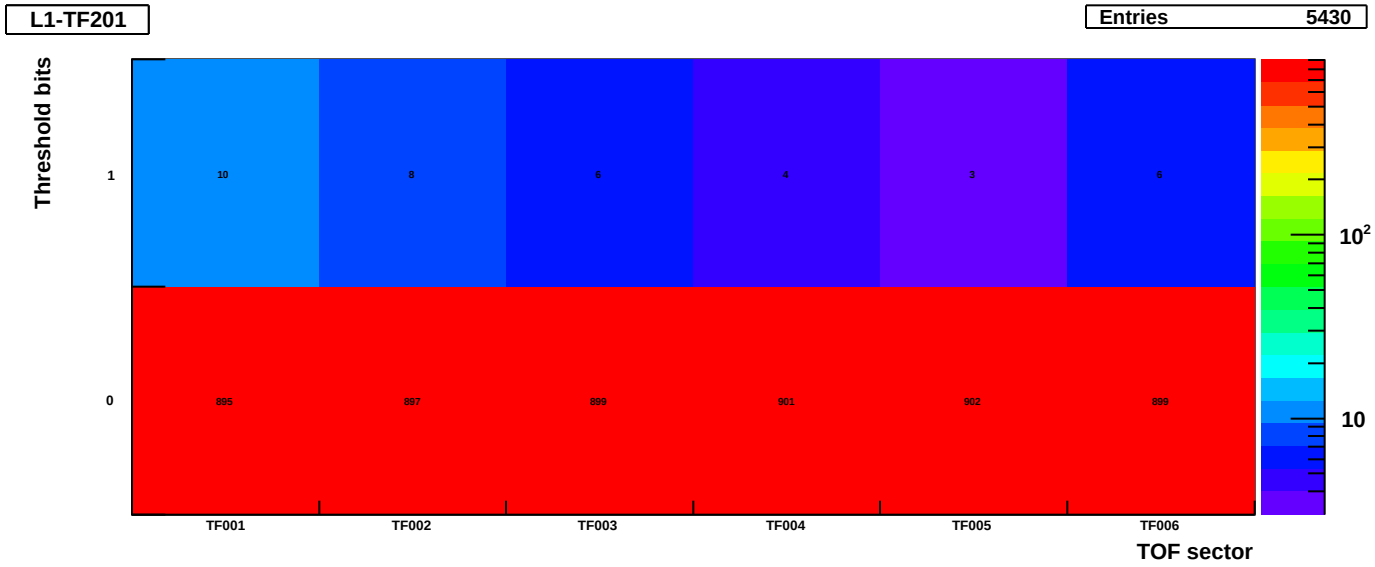

MIX-TF004
Entries 38010
TOF MULT
TOF tray

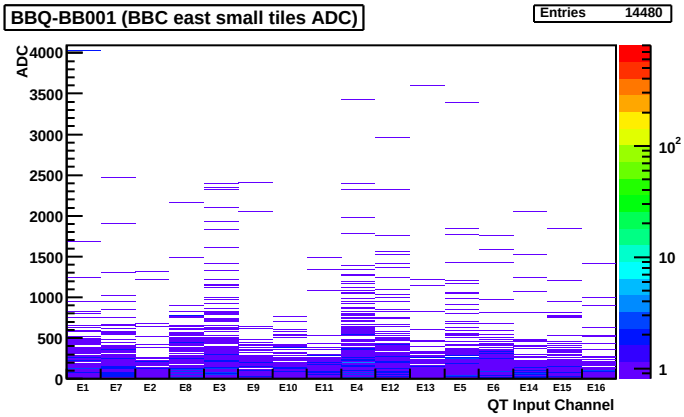

MIX-TF005
Entries 38010
TOF MULT
TOF tray

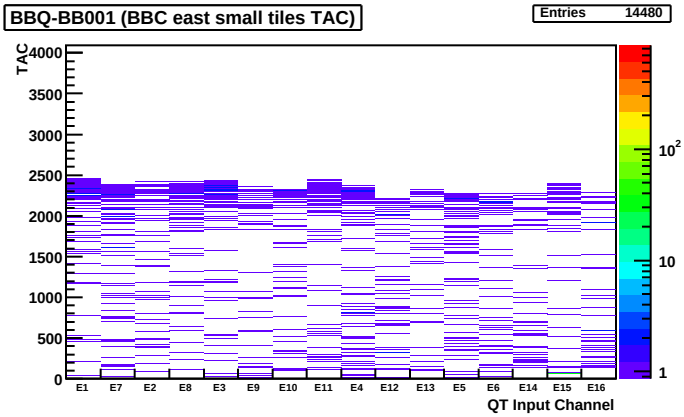

MIX-TF006
Entries 38010
TOF MULT
TOF tray

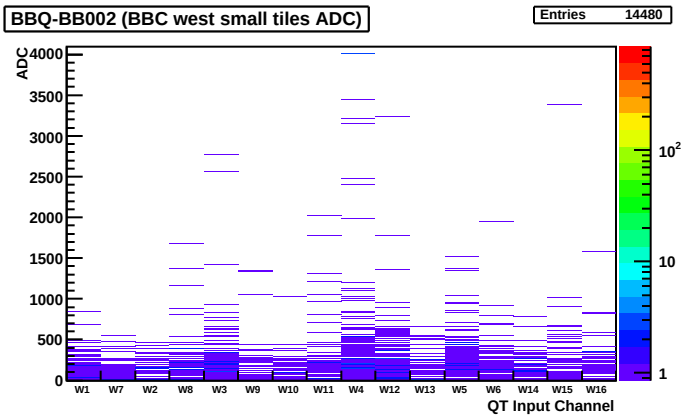

MIX-TF101
Entries 5430
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^2
10
1

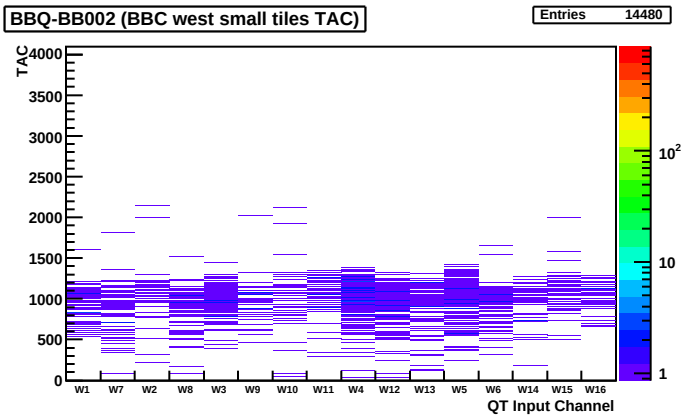

L1-TF201
Entries 905
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

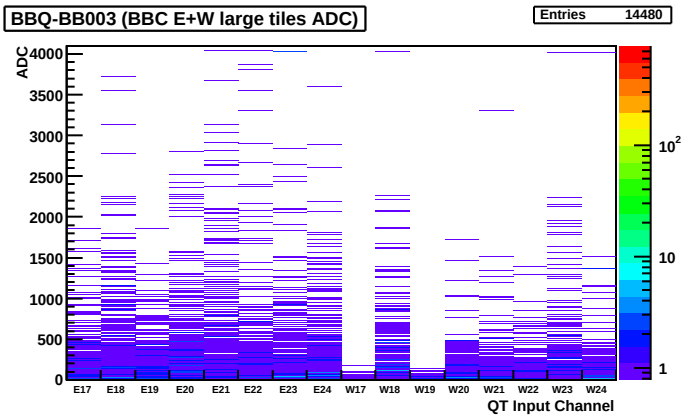

L1-TF201
Entries 5430
Threshold bits
1
0
10
8
6
4
3
6
895
897
899
901
902
899
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^2
10

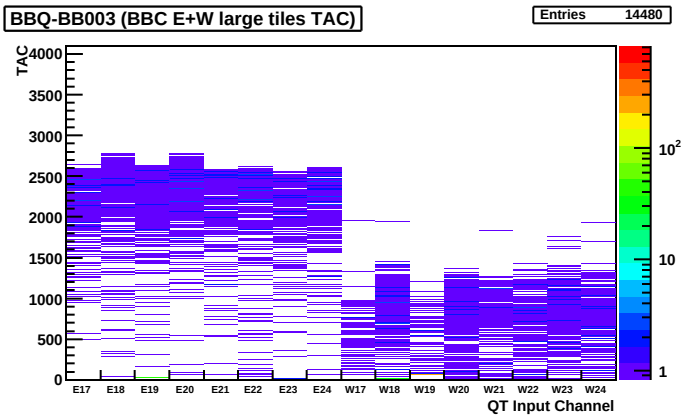

BBQ-BB001 (BBC east small tiles ADC)
Entries 14480
ADC
QT Input Channel

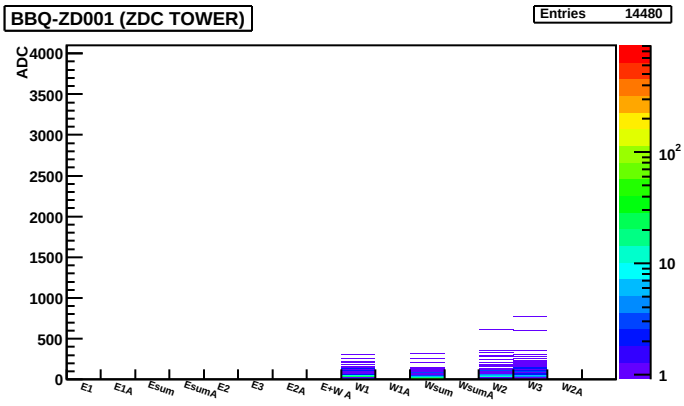

BBQ-BB001 (BBC east small tiles TAC)
Entries 14480
TAC
QT Input Channel

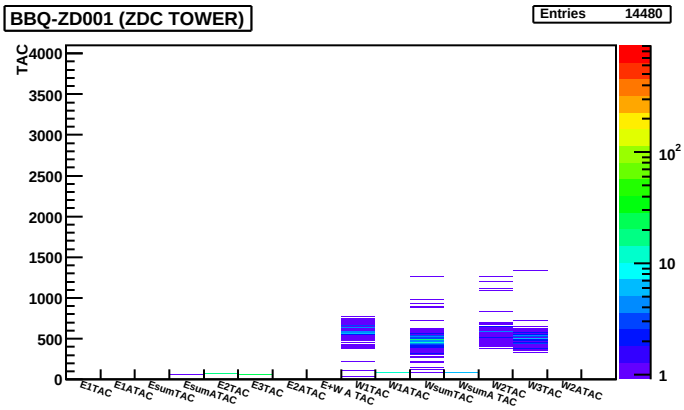

BBQ-BB002 (BBC west small tiles ADC)
Entries 14480
ADC
QT Input Channel

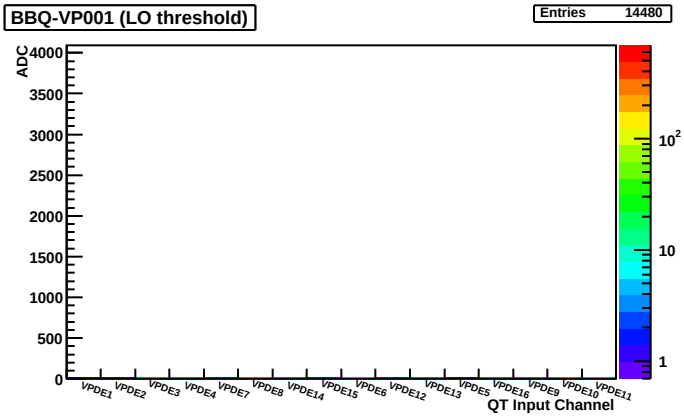

BBQ-BB002 (BBC west small tiles TAC)
Entries 14480
TAC
QT Input Channel

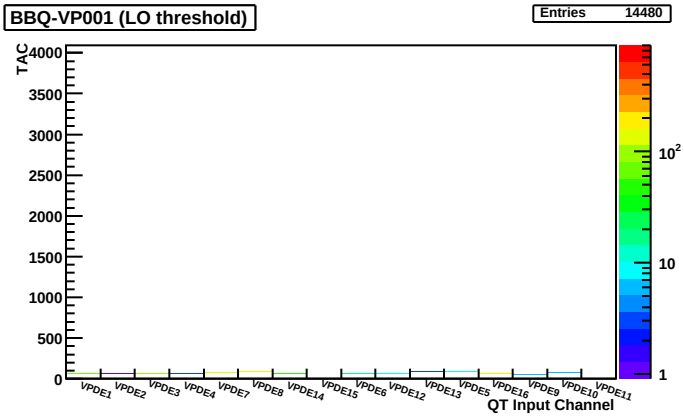

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 14480
ADC
QT Input Channel

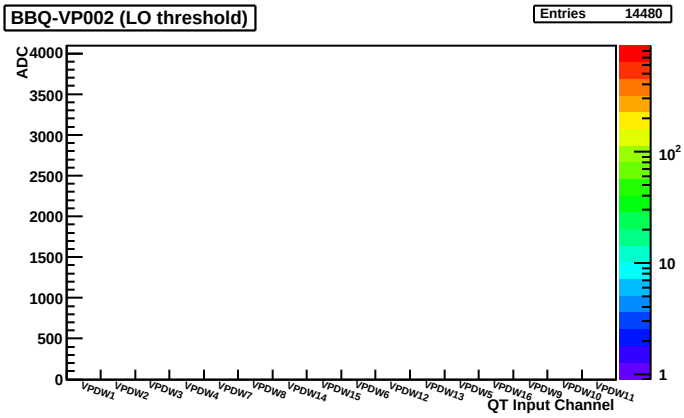

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 14480
TAC
QT Input Channel

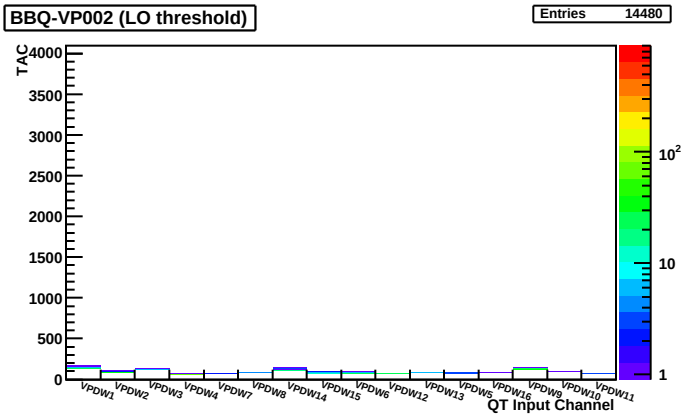

BBQ-ZD001 (ZDC TOWER)
Entries 14480
ADC

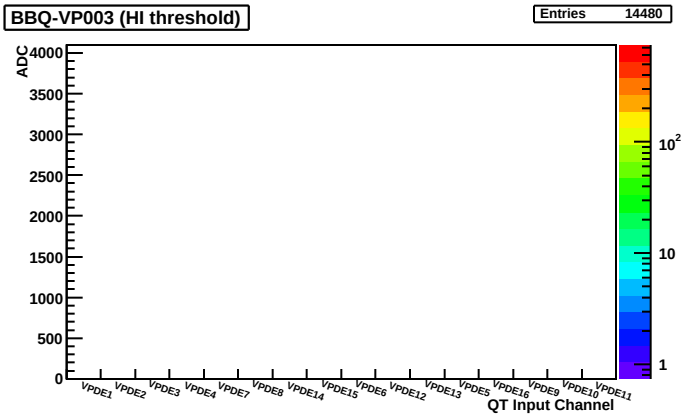

BBQ-ZD001 (ZDC TOWER)
Entries 14480
TAC

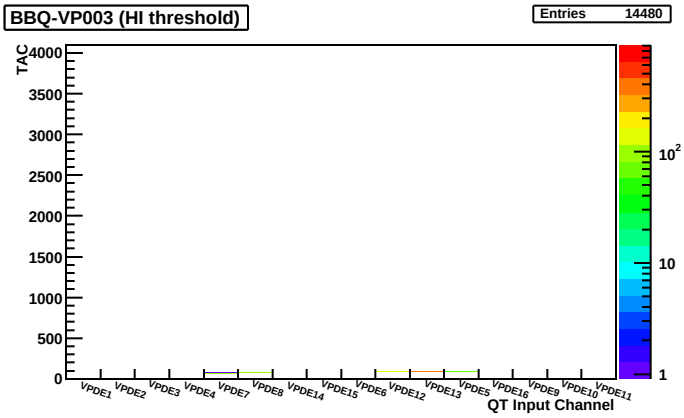

BBQ-VP001 (LO threshold)
Entries 14480
ADC
QT Input Channel

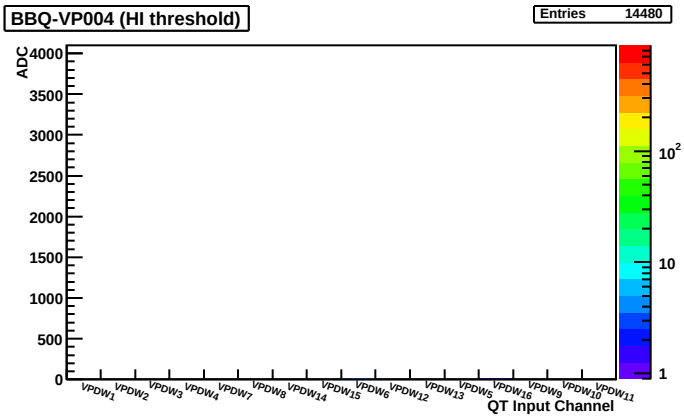

BBQ-VP001 (LO threshold)
Entries 14480
TAC
QT Input Channel

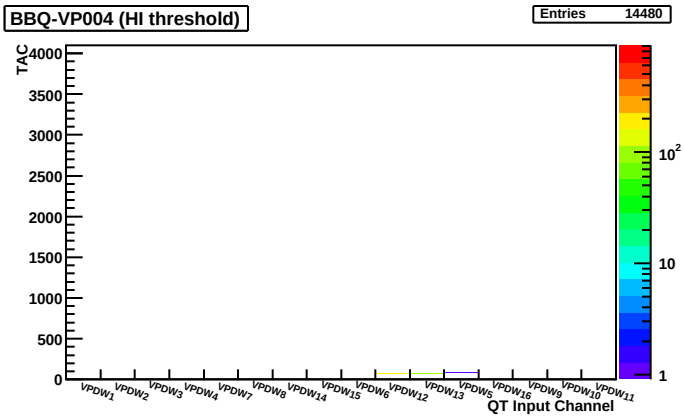

BBQ-VP002 (LO threshold)
Entries 14480
ADC
QT Input Channel

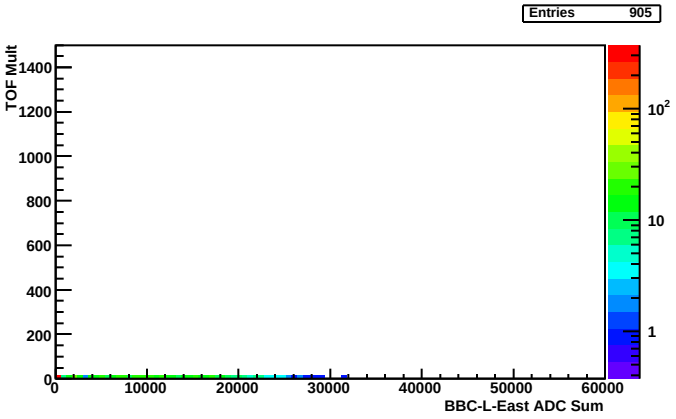

BBQ-VP002 (LO threshold)
Entries 14480
TAC
QT Input Channel

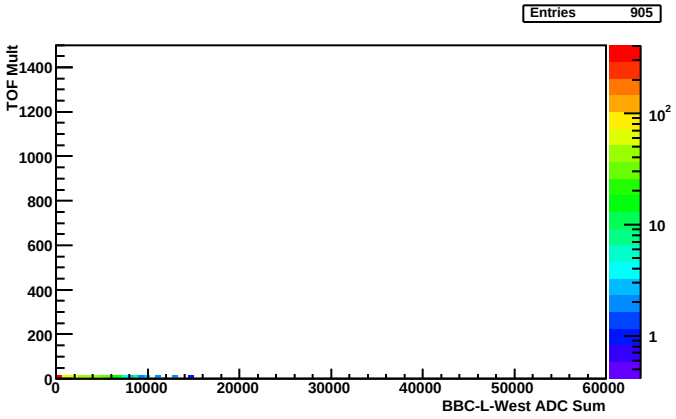

BBQ-VP003 (HI threshold)
Entries 14480
ADC
QT Input Channel

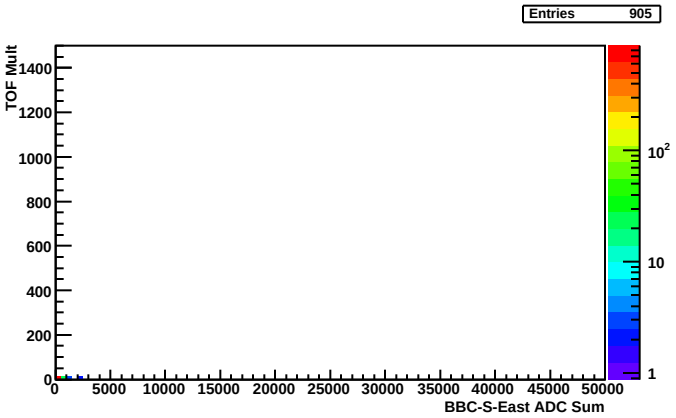

BBQ-VP003 (HI threshold)
Entries 14480
TAC
QT Input Channel

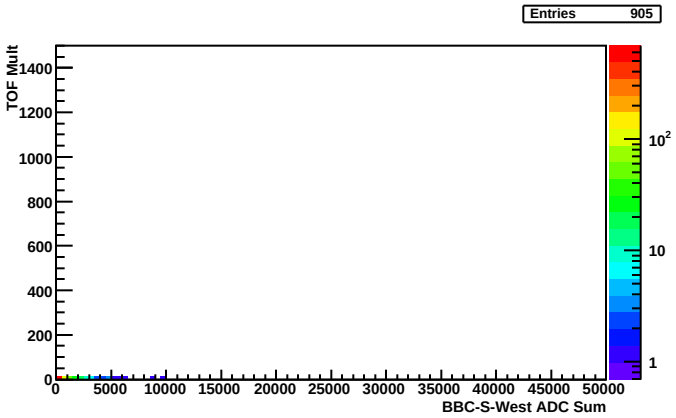

BBQ-VP004 (HI threshold)
Entries 14480
ADC
QT Input Channel

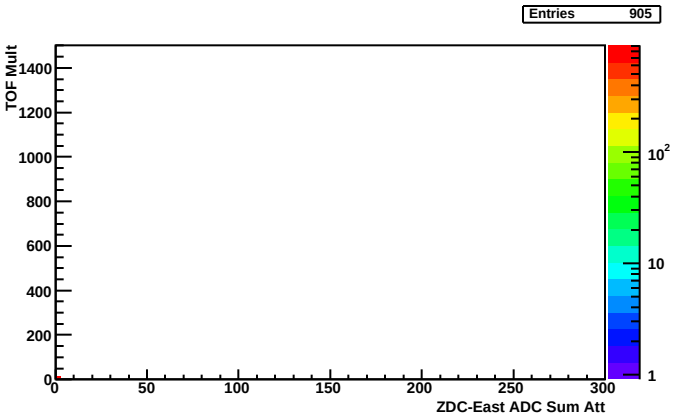

BBQ-VP004 (HI threshold)
Entries 14480
TAC
QT Input Channel

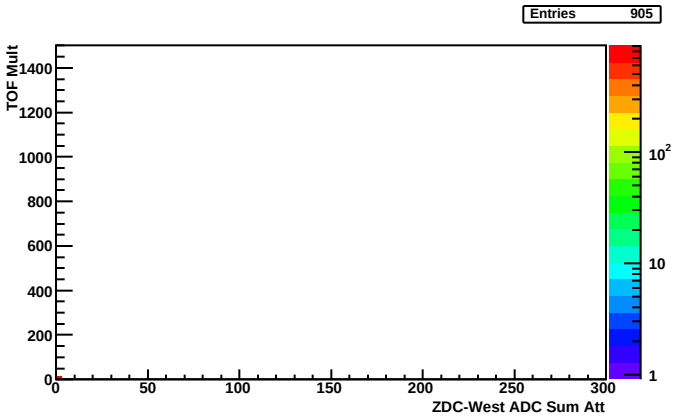

Entries 905
TOF Mult
BBC-L-East ADC Sum

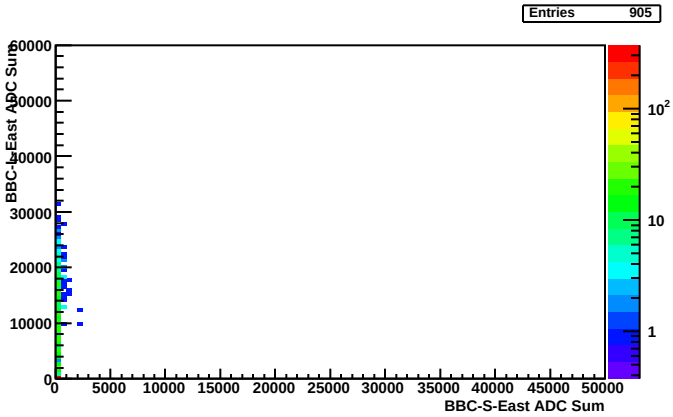

Entries 905
TOF Mult
BBC-L-West ADC Sum

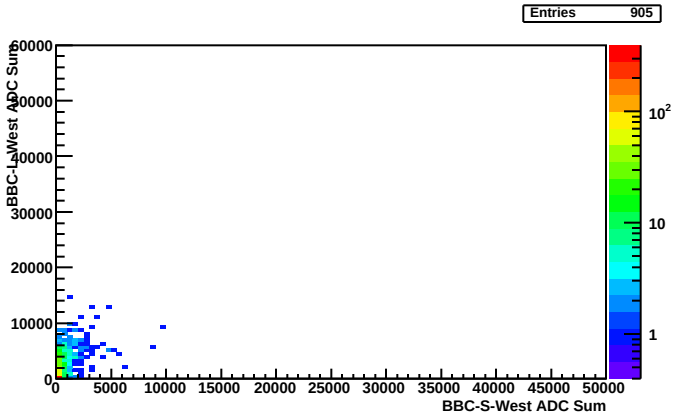

Entries 905
TOF Mult
BBC-S-East ADC Sum

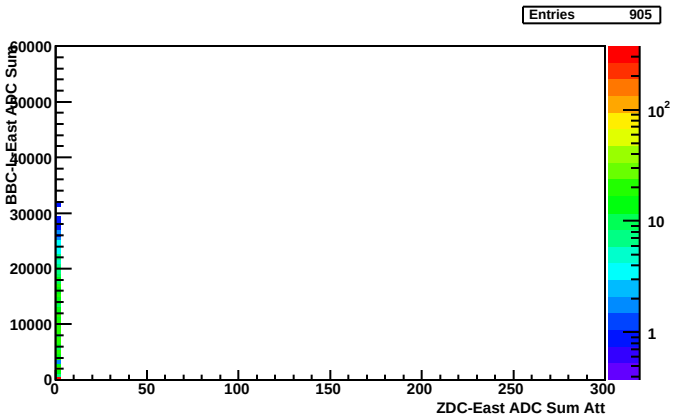

Entries 905
TOF Mult
BBC-S-West ADC Sum

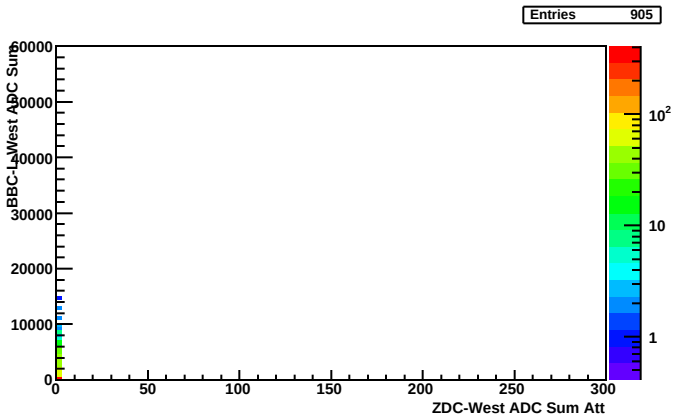

Entries 905
TOF Mult
ZDC-East ADC Sum Att


Entries 905
TOF Mult
ZDC-West ADC Sum Att


Entries 905
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 905
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 905
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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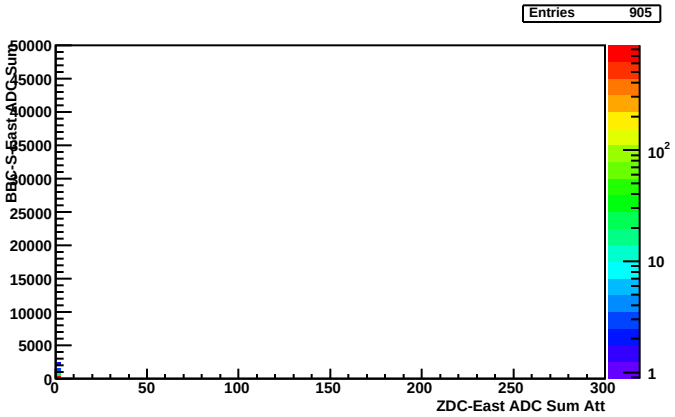
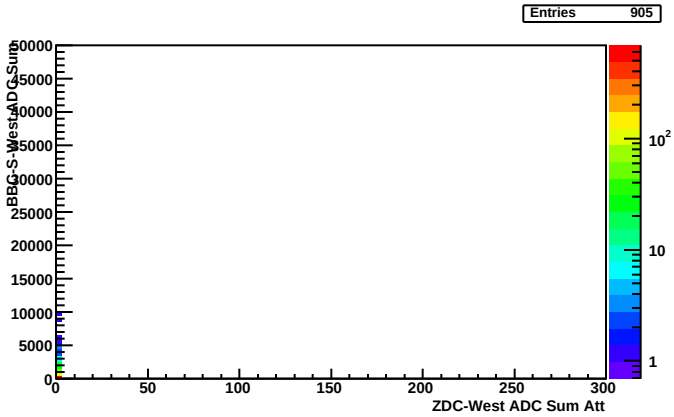
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 905
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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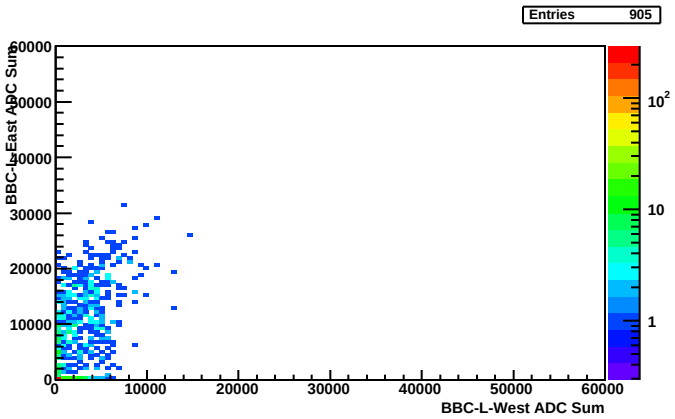
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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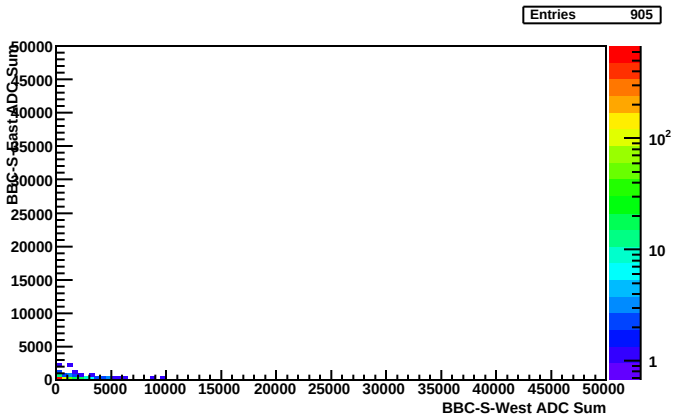
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 905
BBC-S-East ADC Sum
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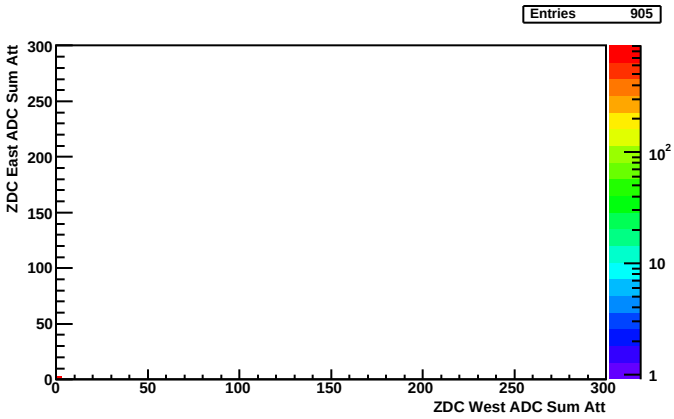
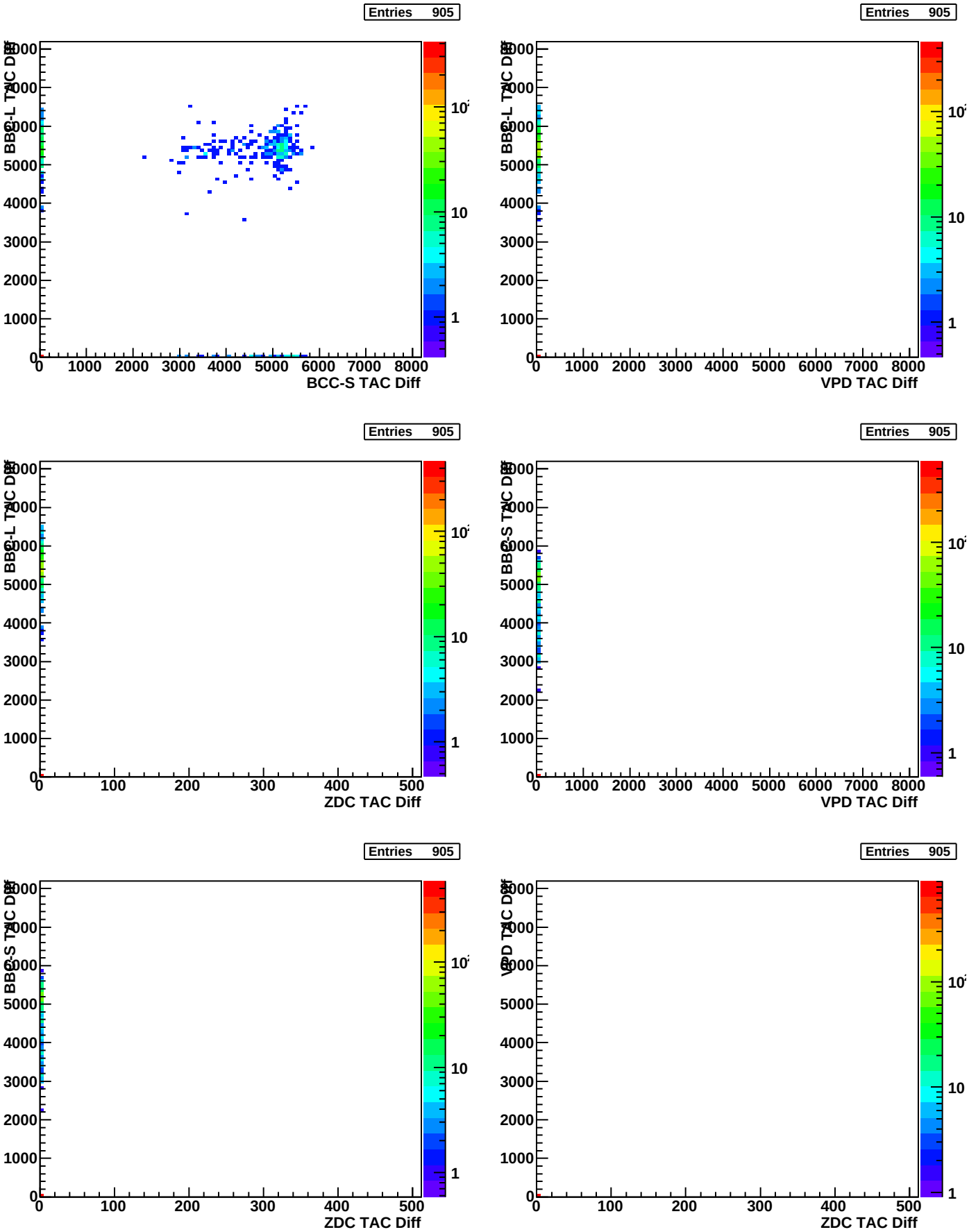
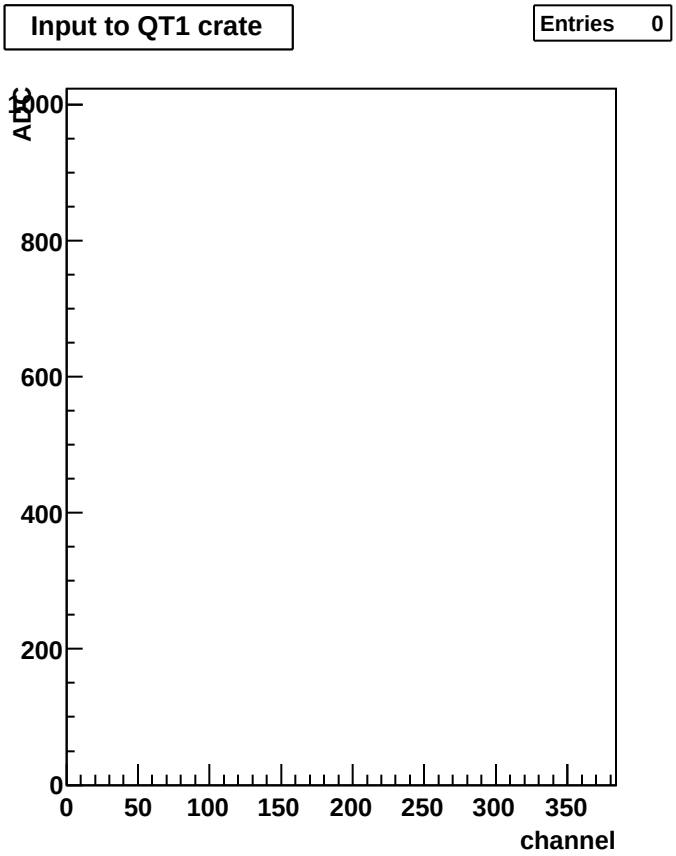
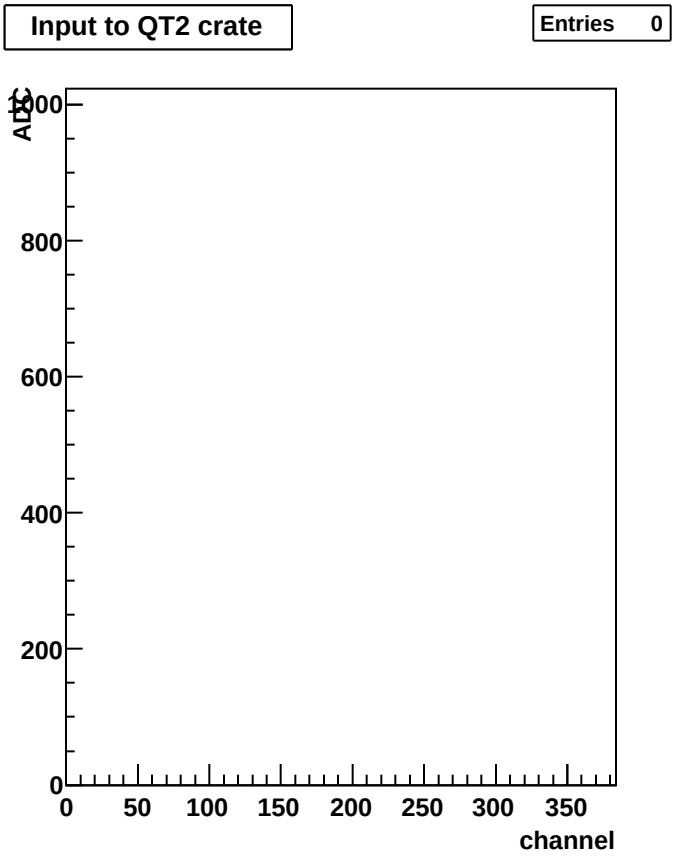
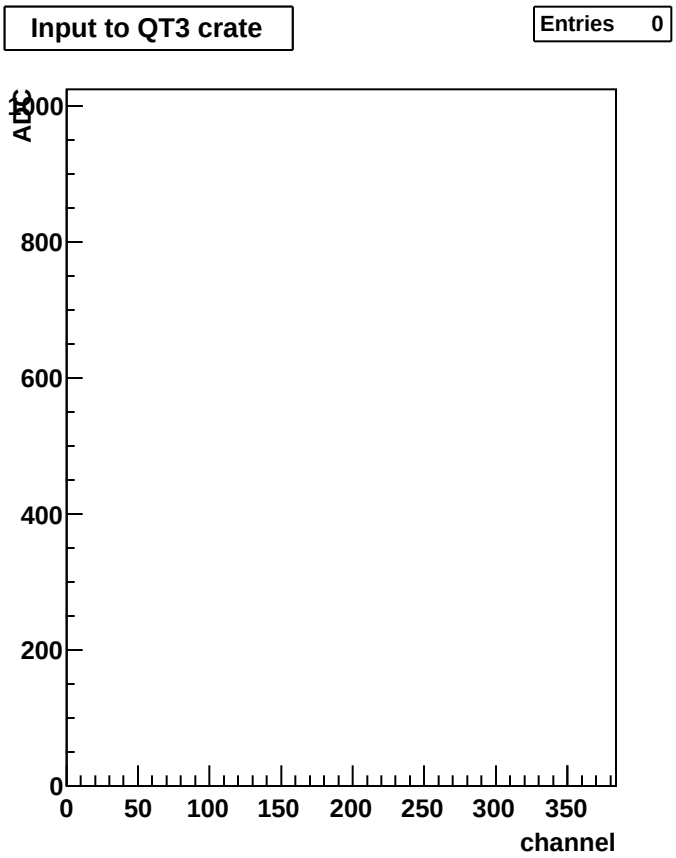
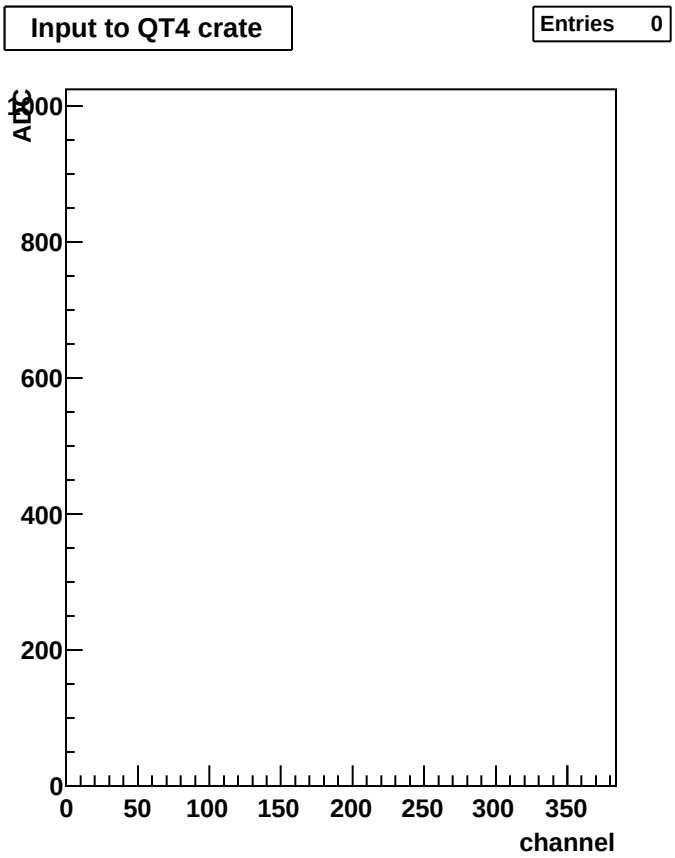

Entries 905
ZDC East ADC Sum Att
ZDC West ADC Sum Att

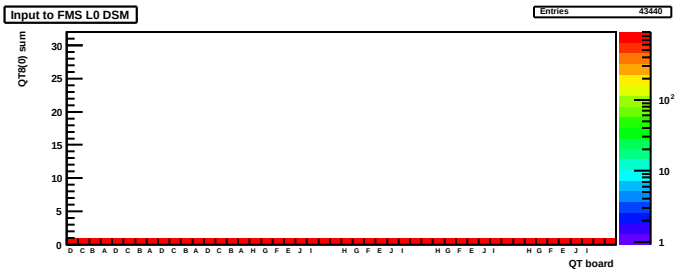

Entries 905
BBC-L TAC Diff
BCC-S TAC Diff
Entries 905
BBC-L TAC Diff
VPD TAC Diff
Entries 905
BBC-L TAC Diff
ZDC TAC Diff
Entries 905
BBC-S TAC Diff
VPD TAC Diff
Entries 905
BBC-S TAC Diff
ZDC TAC Diff
Entries 905
VPD TAC Diff
ZDC TAC Diff

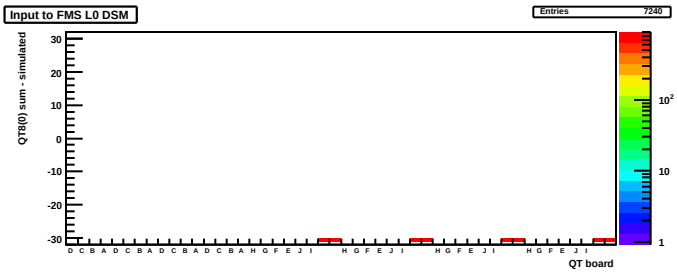

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

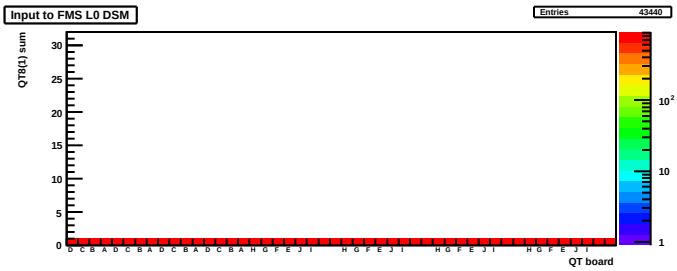

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

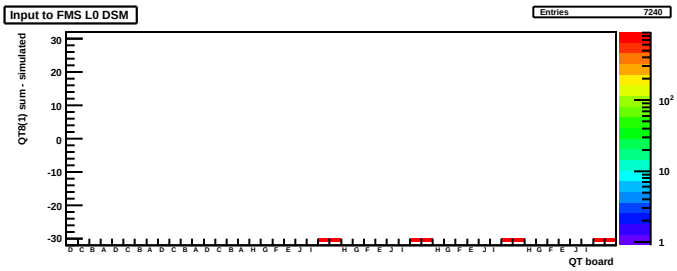

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

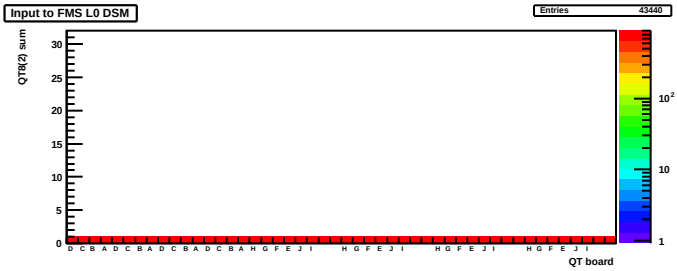

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

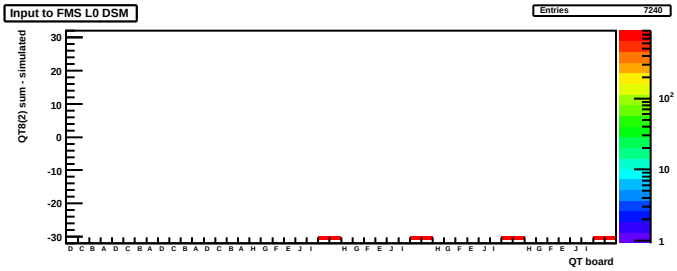

Input to FMS L0 DSM
Entries 43440
QT8(0) sum
QT board

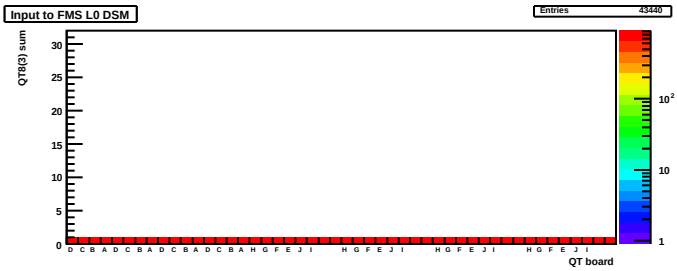

Input to FMS L0 DSM
Entries 7240
QT8(0) sum - simulated
QT board

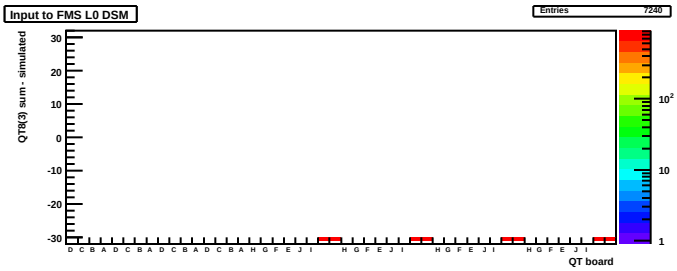

Input to FMS L0 DSM
Entries 43440
QT8(1) sum
QT board

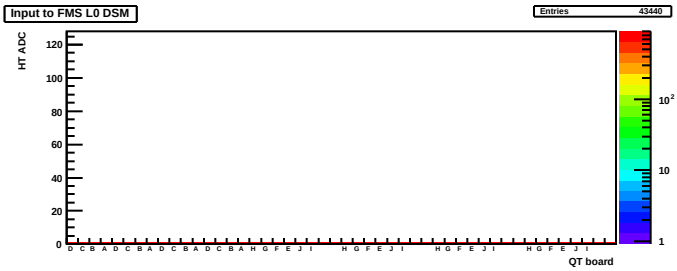

Input to FMS L0 DSM
Entries 7240
QT8(1) sum - simulated
QT board

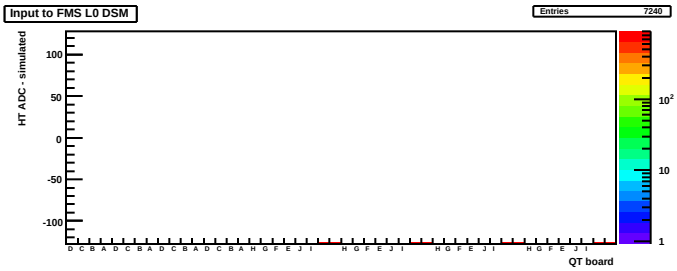

Input to FMS L0 DSM
Entries 43440
QT8(2) sum
QT board

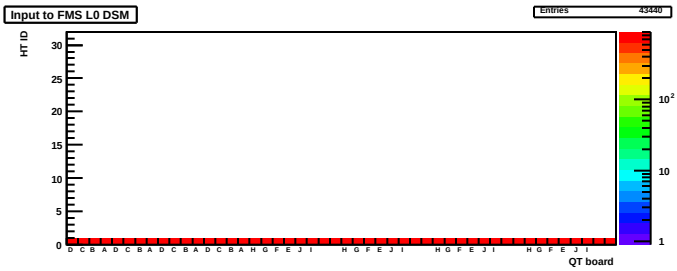

Input to FMS L0 DSM
Entries 7240
QT8(2) sum - simulated
QT board

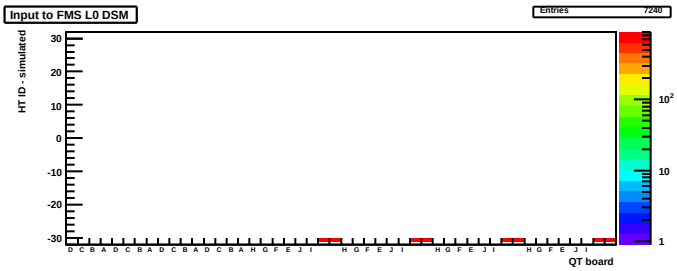

Input to FMS L0 DSM
Entries 43440
QT8(3) sum
QT board

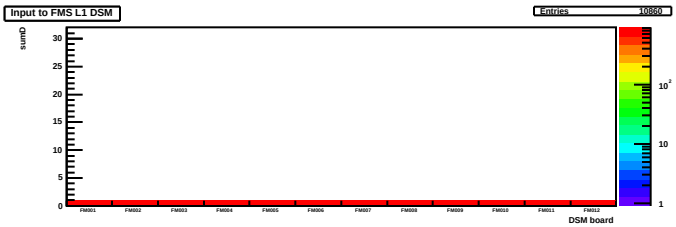

Input to FMS L0 DSM
Entries 7240
QT8(3) sum - simulated
QT board

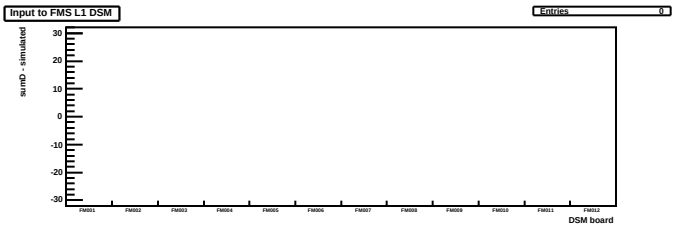

Input to FMS L0 DSM
Entries 43440
HT ADC
QT board

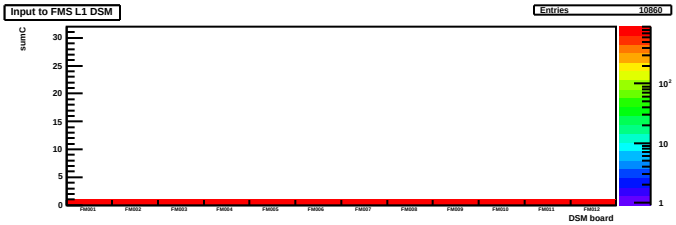

Input to FMS L0 DSM
Entries 7240
HT ADC - simulated
QT board

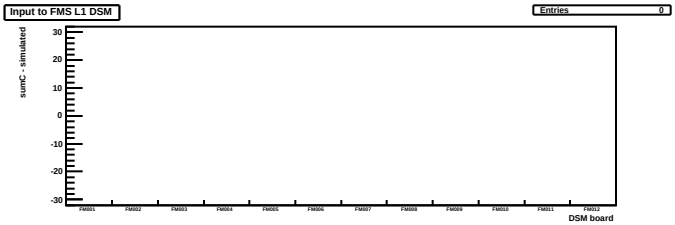

Input to FMS L0 DSM
Entries 43440
HT ID
QT board

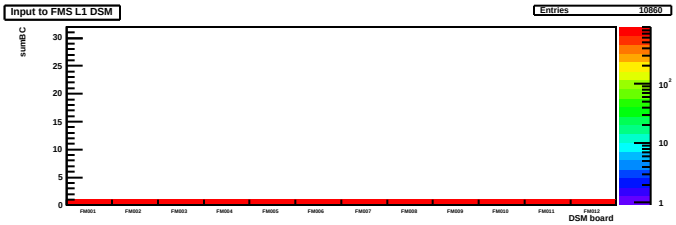

Input to FMS L0 DSM
Entries 7240
HT ID - simulated
QT board

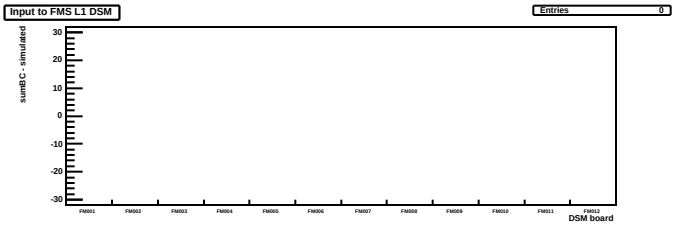

Input to FMS L1 DSM
Entries 10860
sumD
DSM board

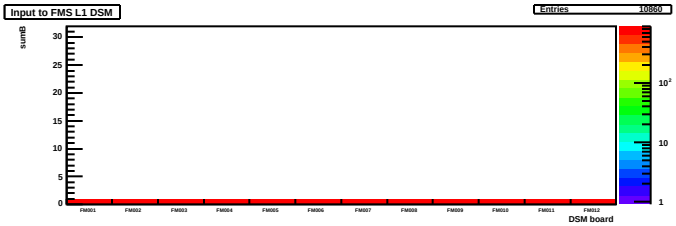

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

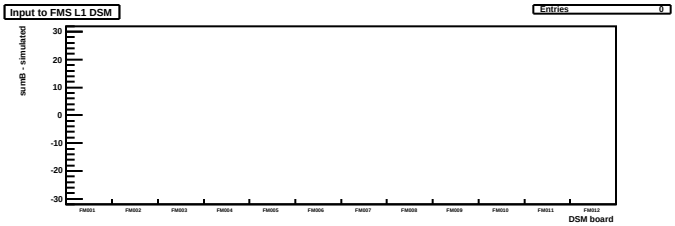

Input to FMS L1 DSM
Entries 10860
sumC
DSM board

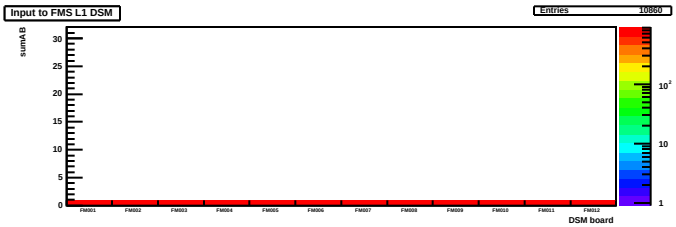

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

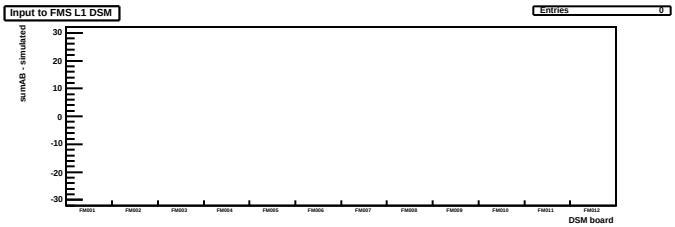

Input to FMS L1 DSM
Entries 10860
sumBC
DSM board

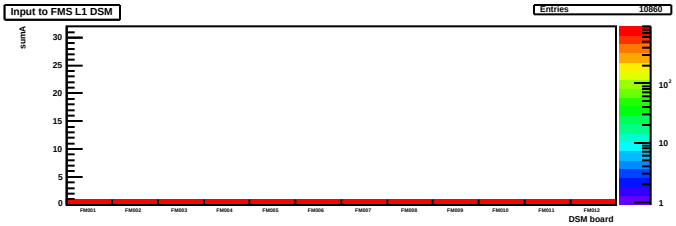

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

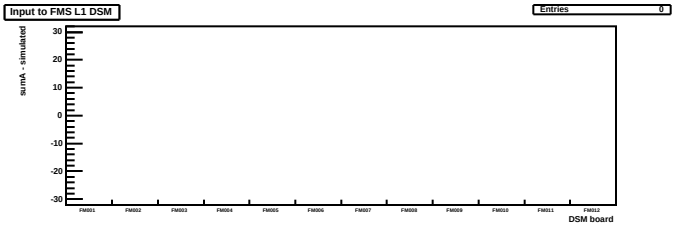

Input to FMS L1 DSM
Entries 10860
sumB
DSM board

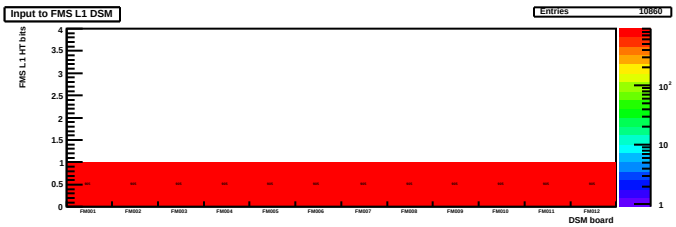

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

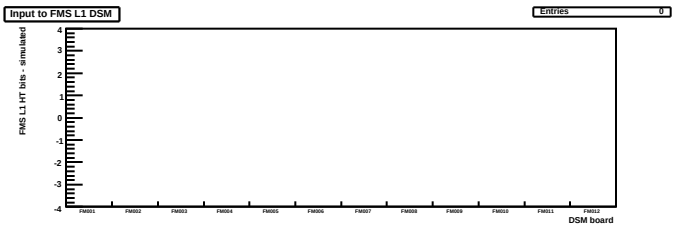

Input to FMS L1 DSM
Entries 10860
sumAB
DSM board

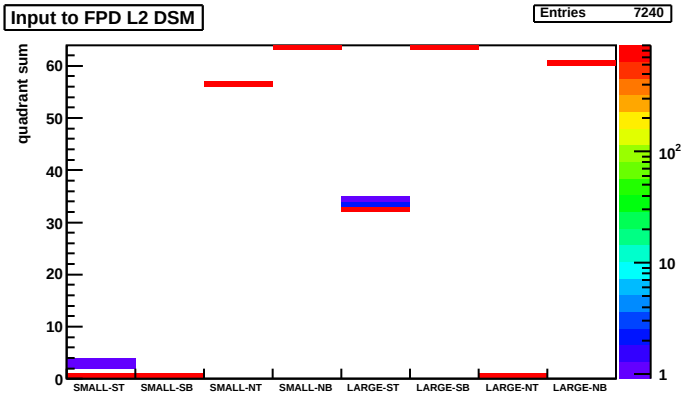

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

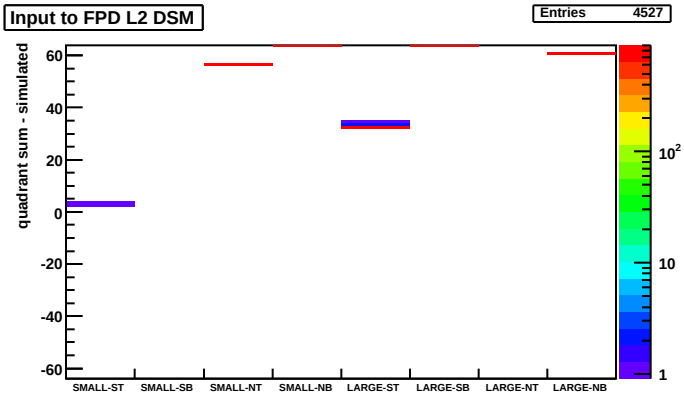

Input to FMS L1 DSM
Entries 10860
sumA
DSM board

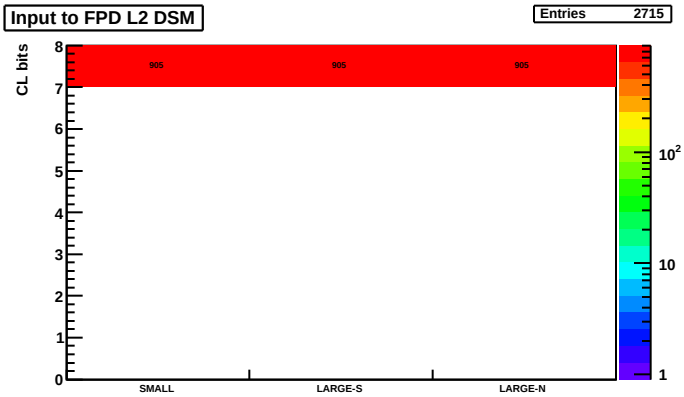

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

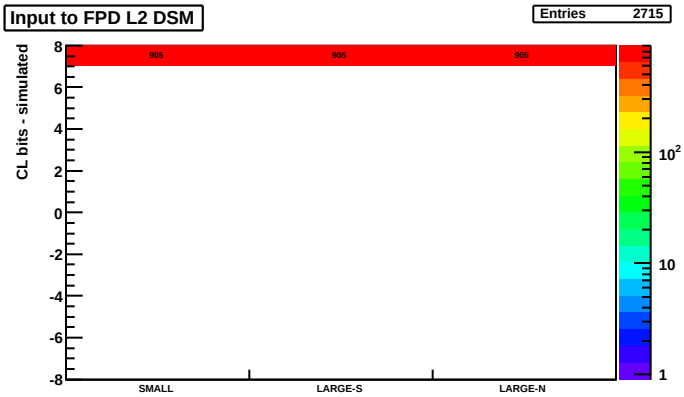

Input to FMS L1 DSM
Entries 10860
FMS L1 HT bits
DSM board

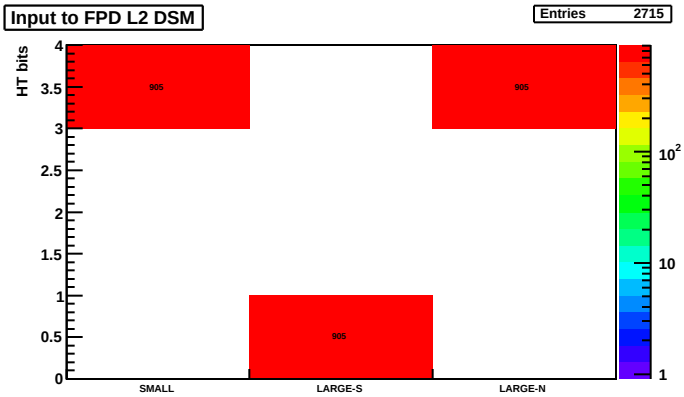

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

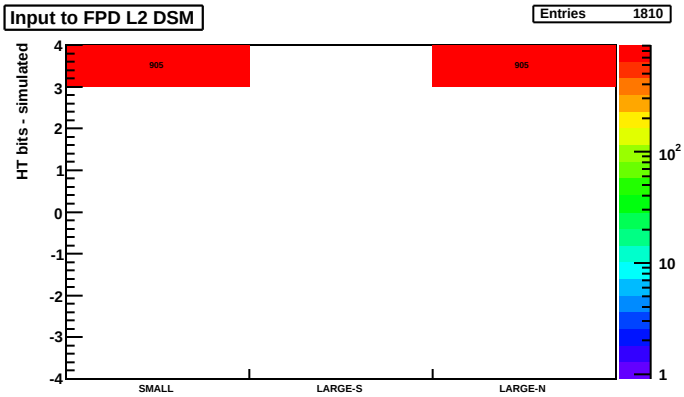

Input to FPD L2 DSM
Entries 7240
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

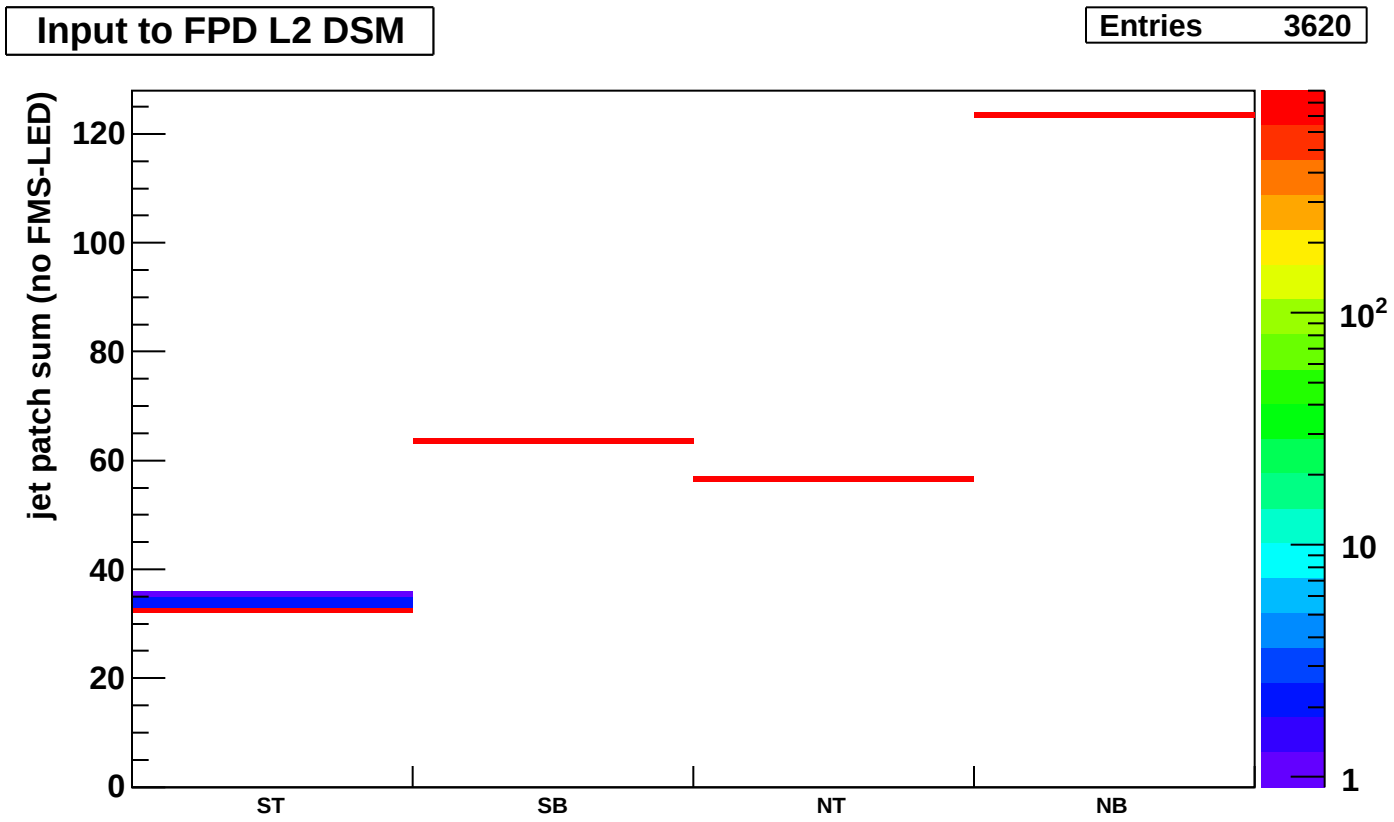

Input to FPD L2 DSM
Entries 4527
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

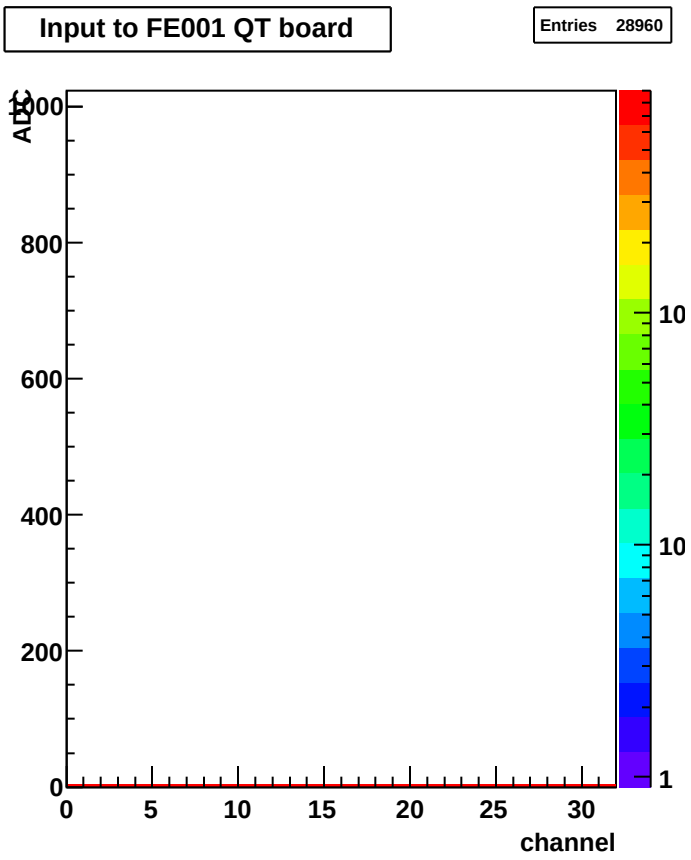

Input to FPD L2 DSM
Entries 2715
CL bits
905
905
905
SMALL
LARGE-S
LARGE-N

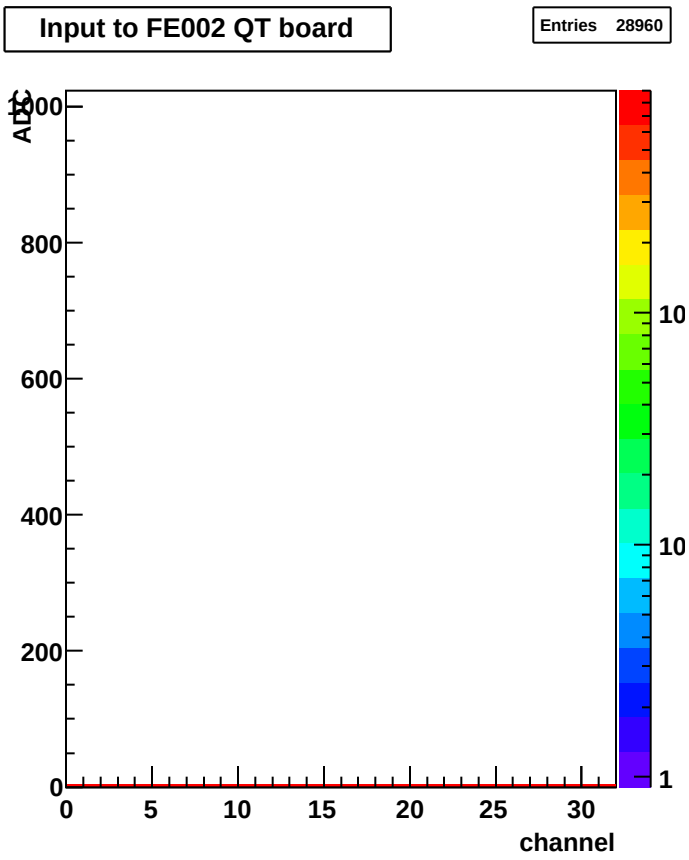

Input to FPD L2 DSM
Entries 2715
CL bits - simulated
905
905
905
SMALL
LARGE-S
LARGE-N

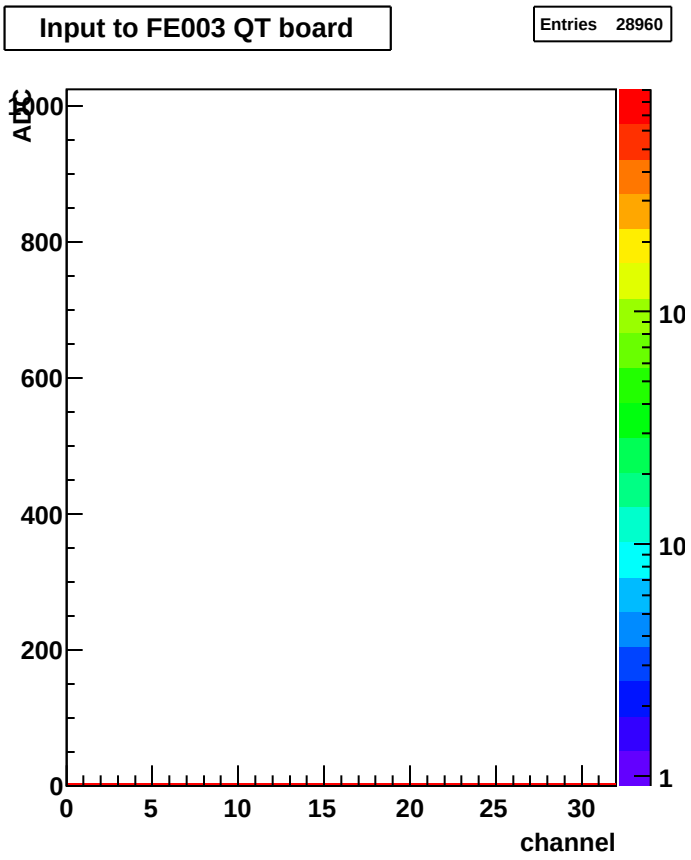

Input to FPD L2 DSM
Entries 2715
HT bits
905
905
905
SMALL
LARGE-S
LARGE-N

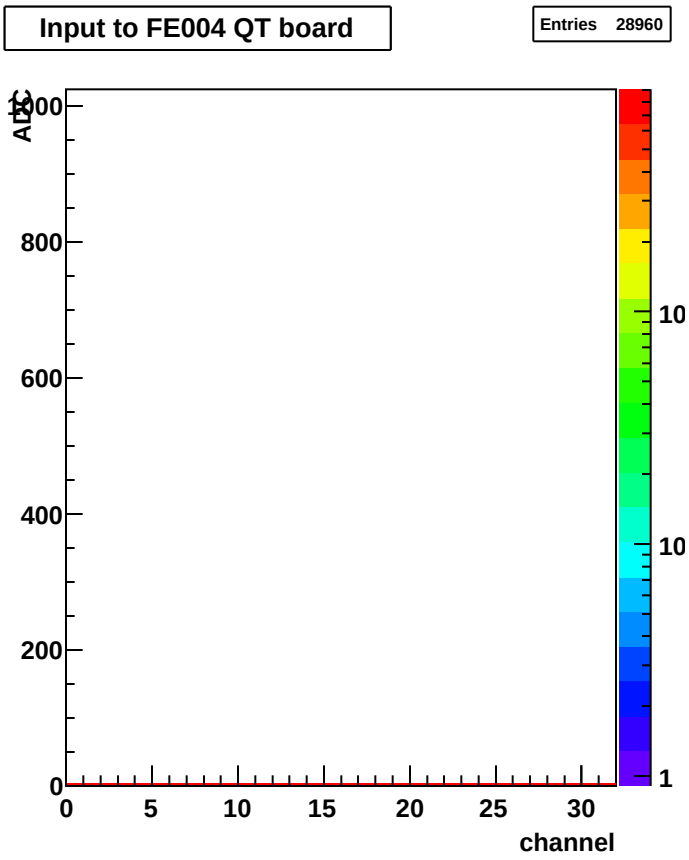

Input to FPD L2 DSM
Entries 1810
HT bits - simulated
905
905
SMALL
LARGE-S
LARGE-N

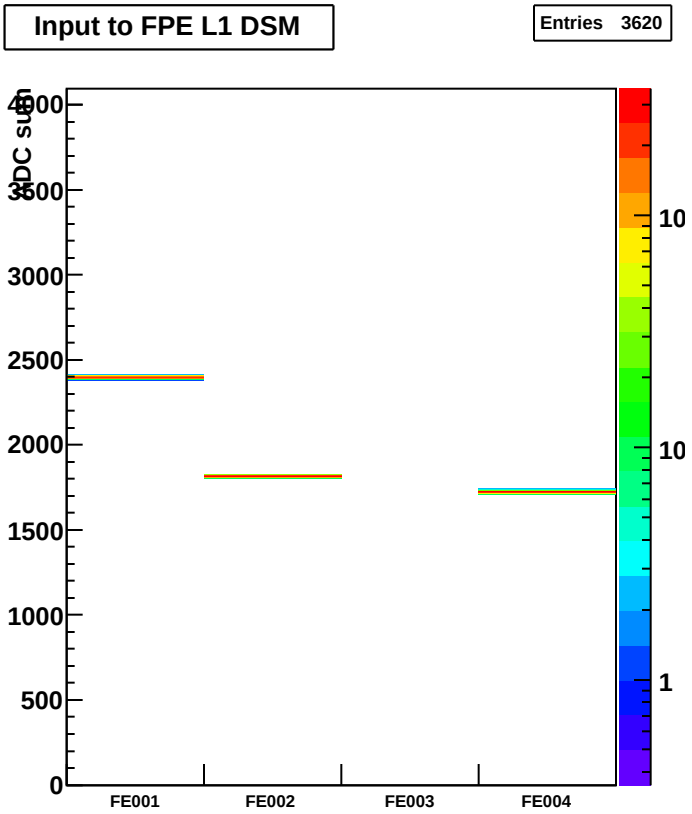

Input to FPD L2 DSM
Entries 3620
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2


Input to FE001 QT board
Entries 28960
ADC
channel

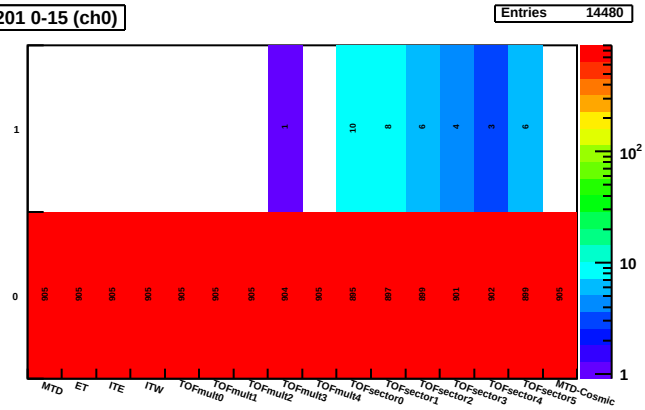

Input to FE002 QT board
Entries 28960
ADC
channel


Input to FE003 QT board
Entries 28960
ADC
channel

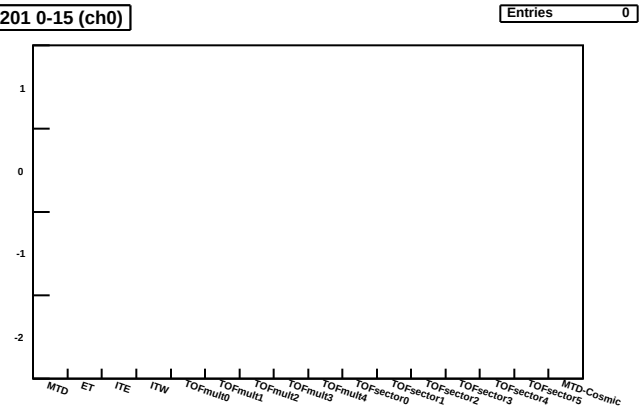

Input to FE004 QT board
Entries 28960
ADC
channel


Input to FPE L1 DSM
Entries 3620
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
1

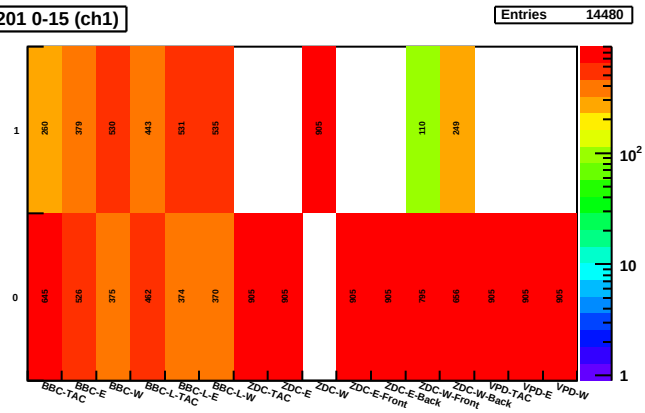
TF201 0-15 (ch0)



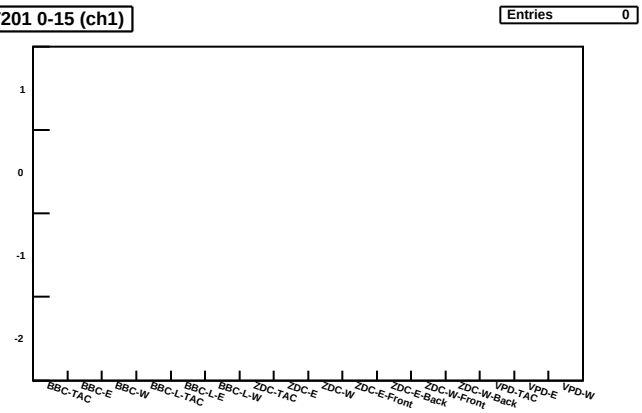
TF201 0-15 (ch0)



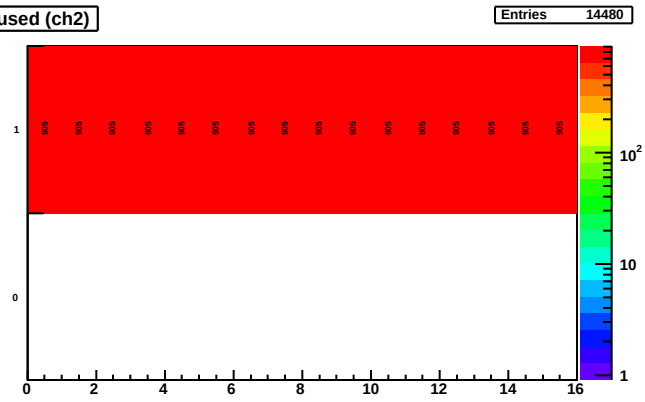
VT201 0-15 (ch1)

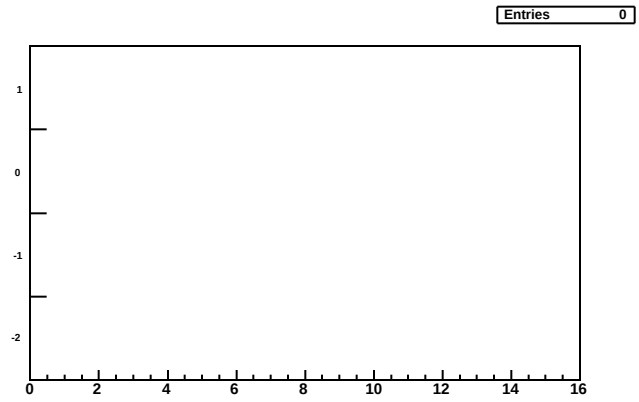


VT201 0-15 (ch1)

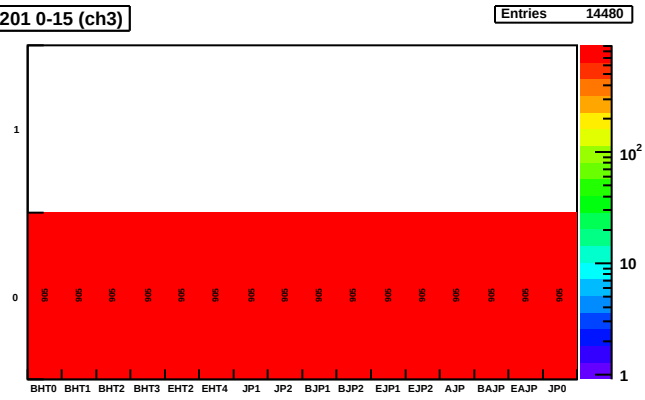


Unused (ch2)

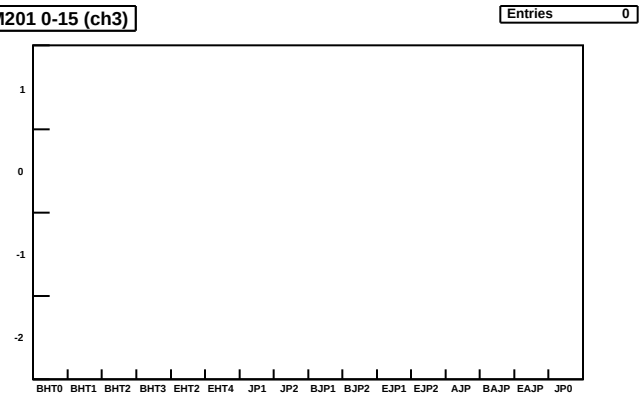




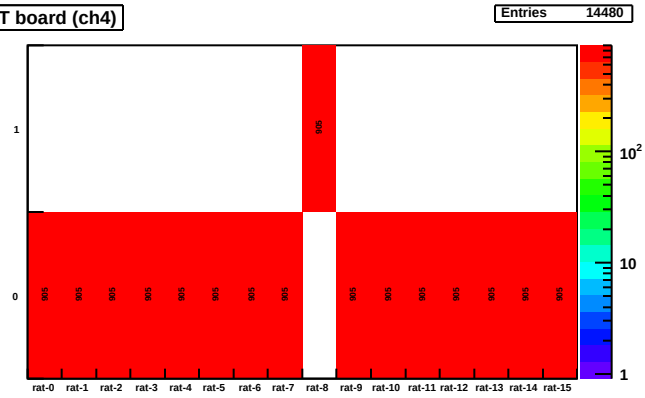
EM201 0-15 (ch3)

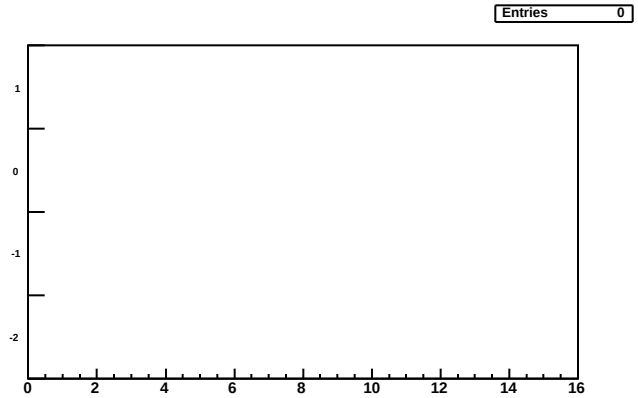


EM201 0-15 (ch3)

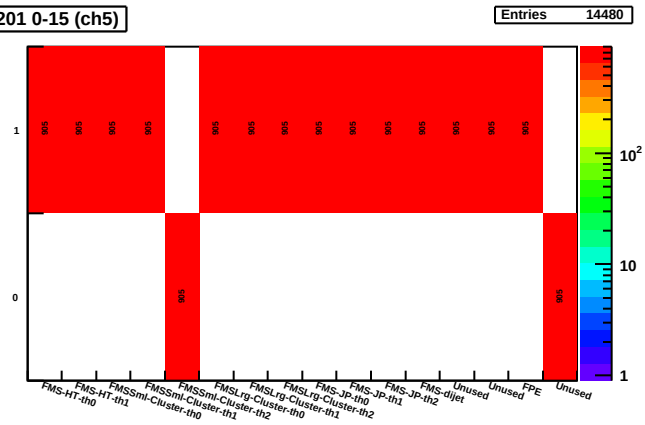


RAT board (ch4)

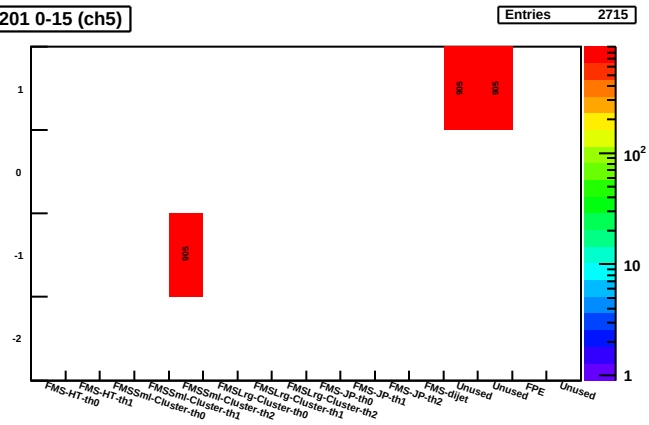




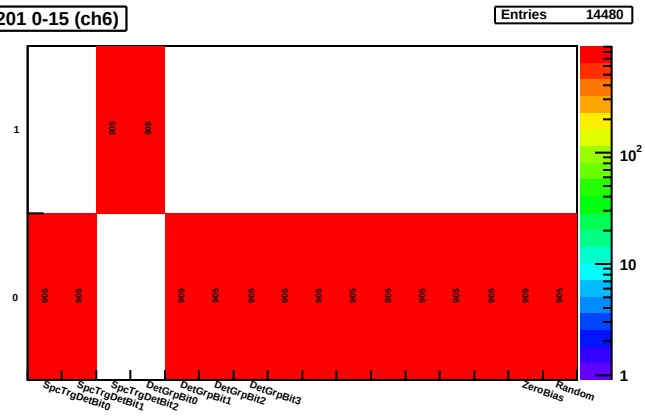
FP201 0-15 (ch5)

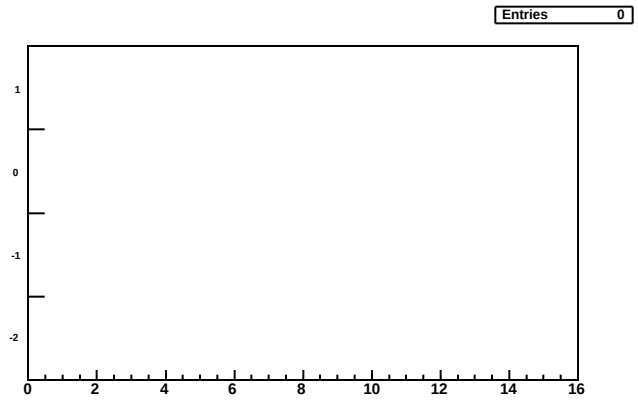
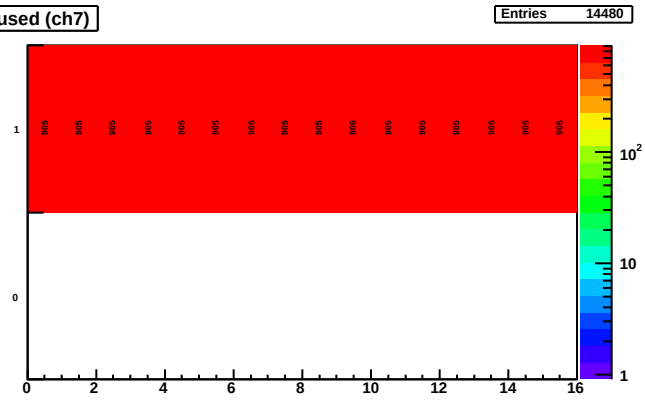
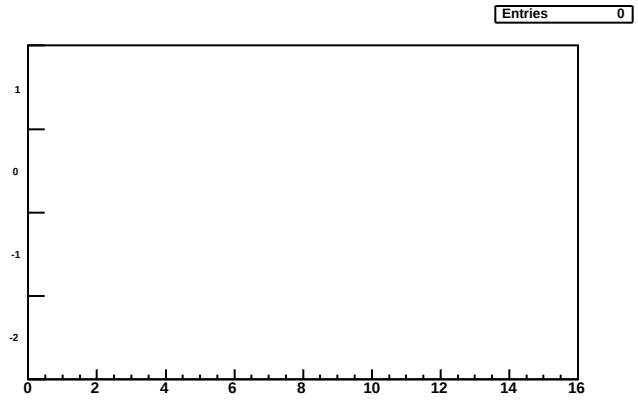


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)