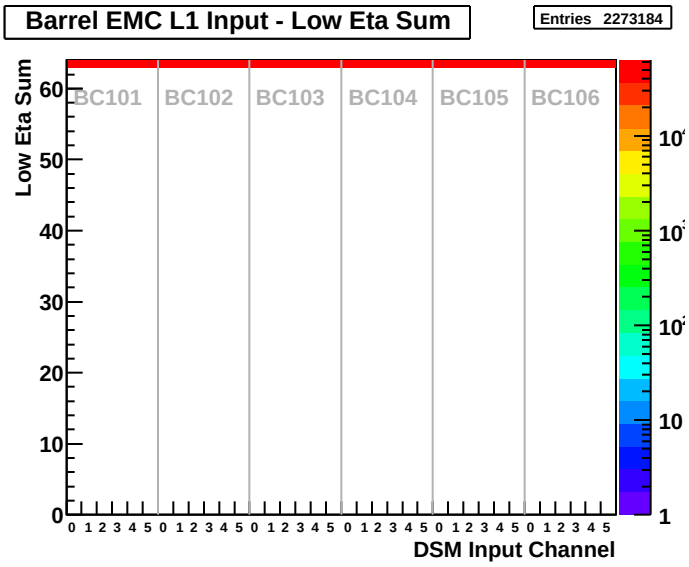
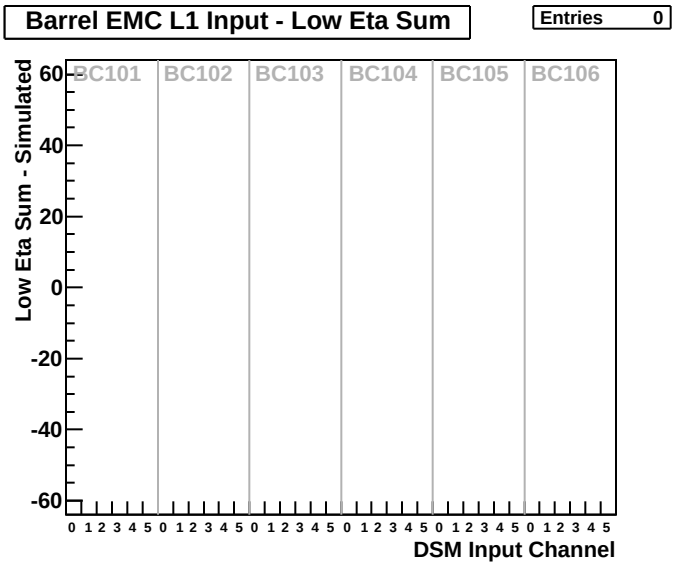
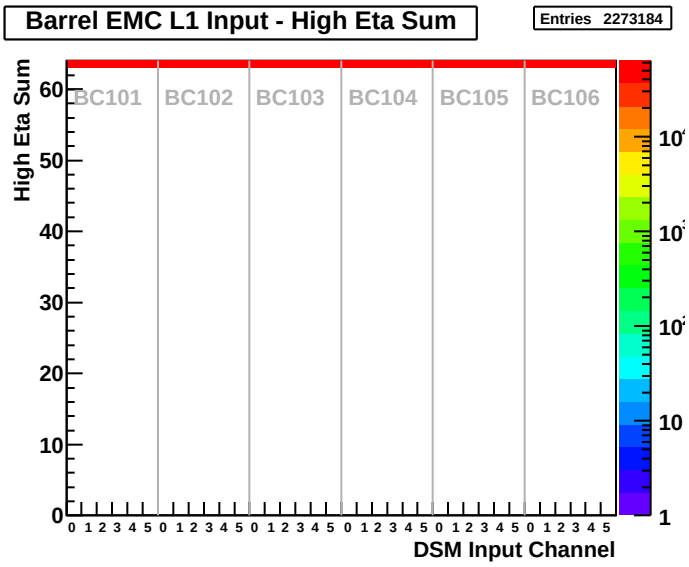
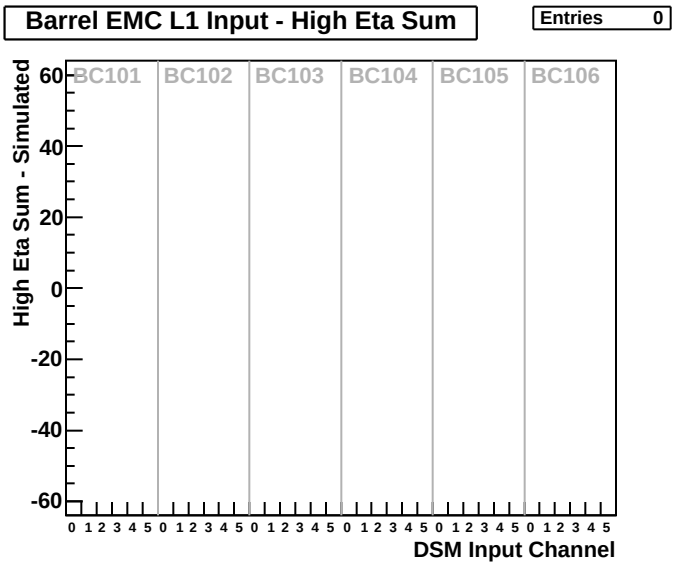
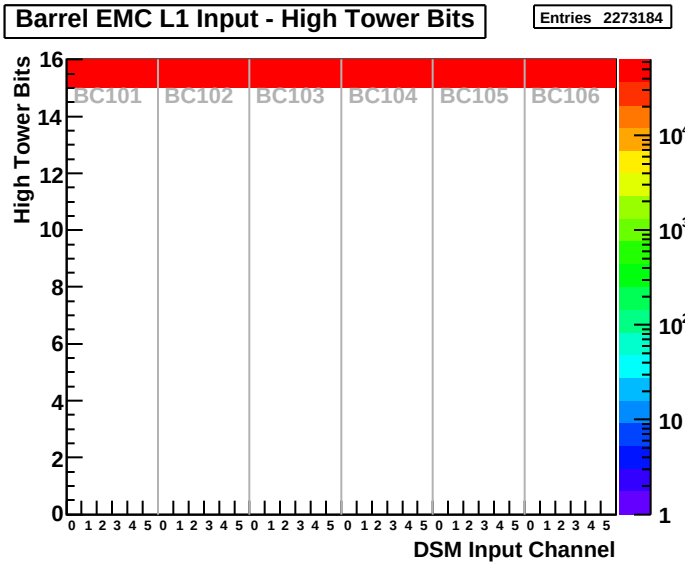
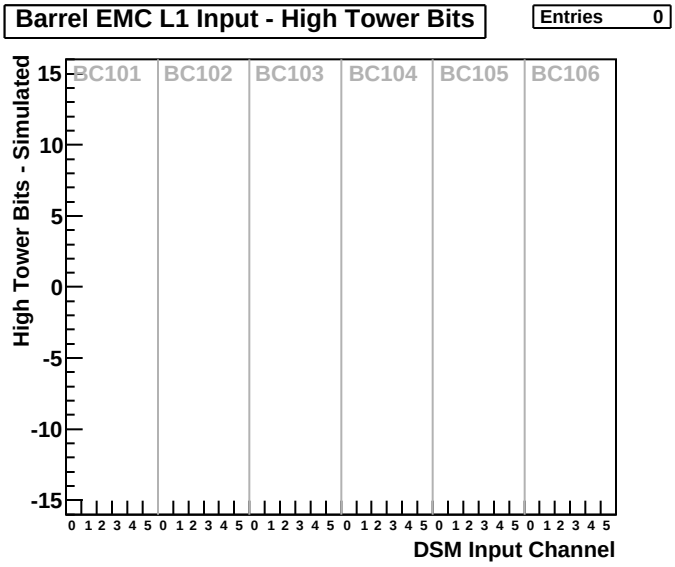

Barrel EMC L1 Input - Low Eta Sum
Entries 2273184
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

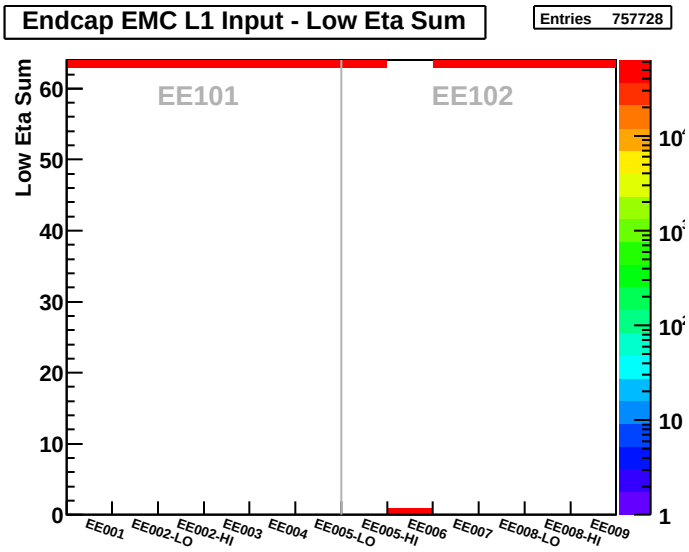

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

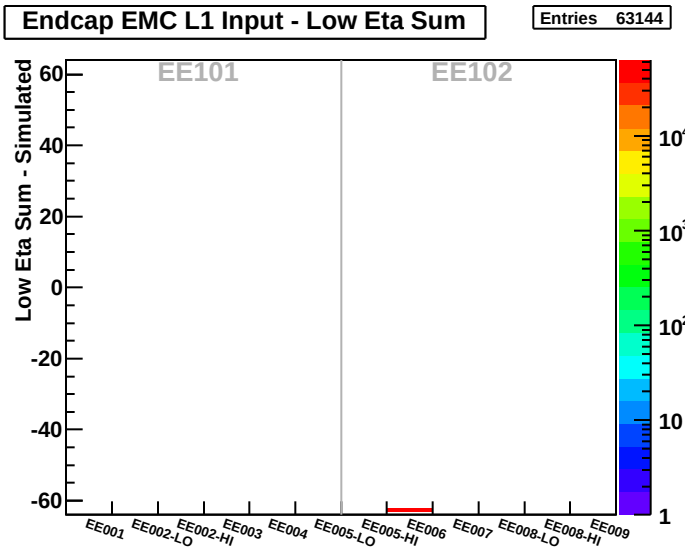

Barrel EMC L1 Input - High Eta Sum
Entries 2273184
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

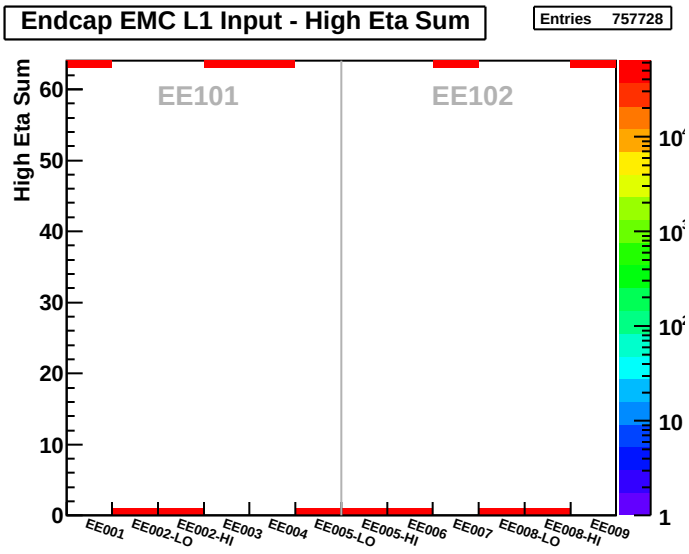

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

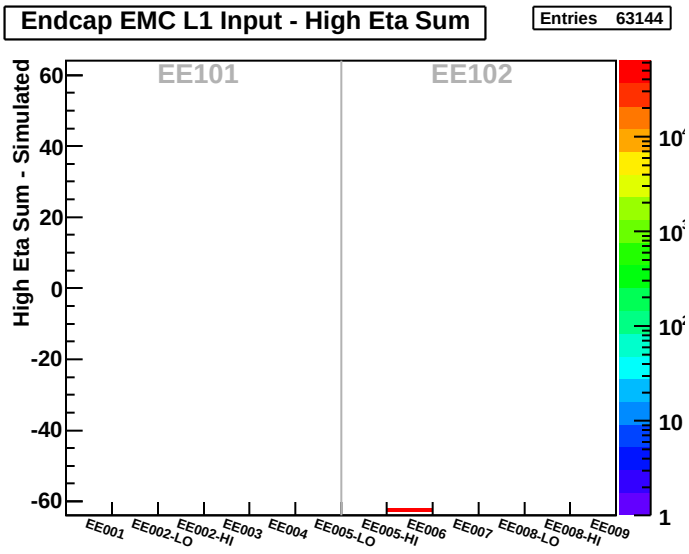

Barrel EMC L1 Input - High Tower Bits
Entries 2273184
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

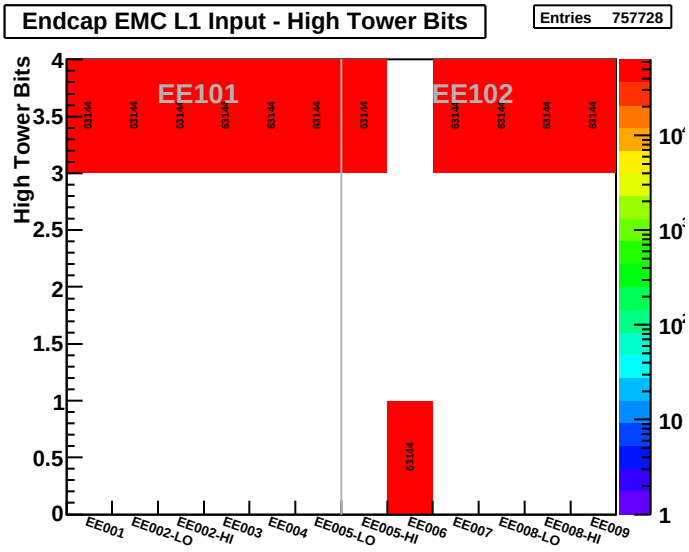

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

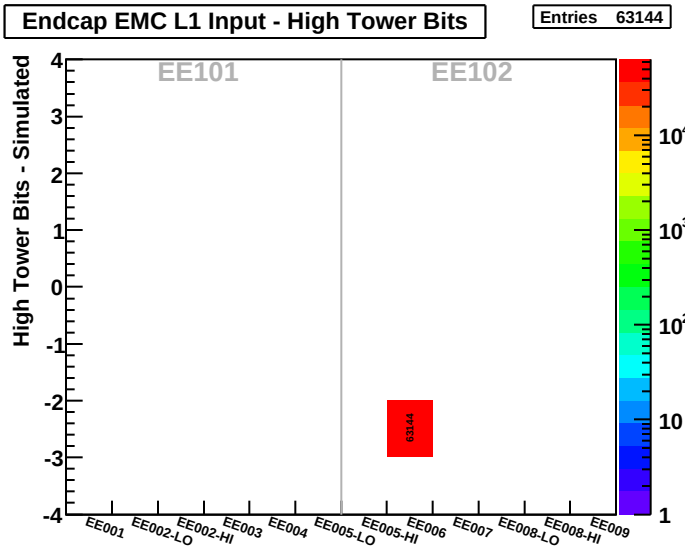

Endcap EMC L1 Input - Low Eta Sum
Entries 757728
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

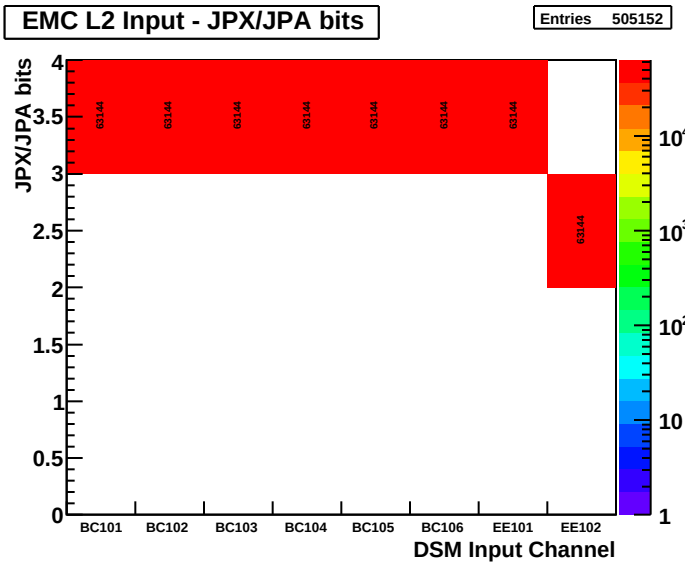

Endcap EMC L1 Input - Low Eta Sum
Entries 63144
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

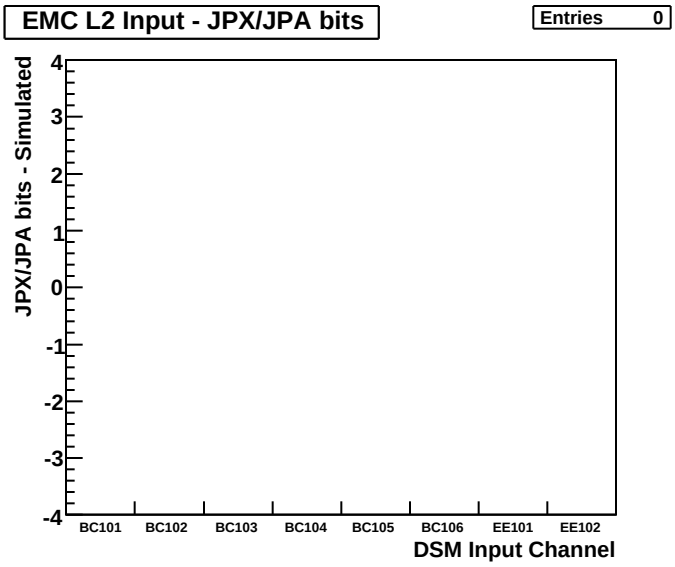

Endcap EMC L1 Input - High Eta Sum
Entries 757728
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

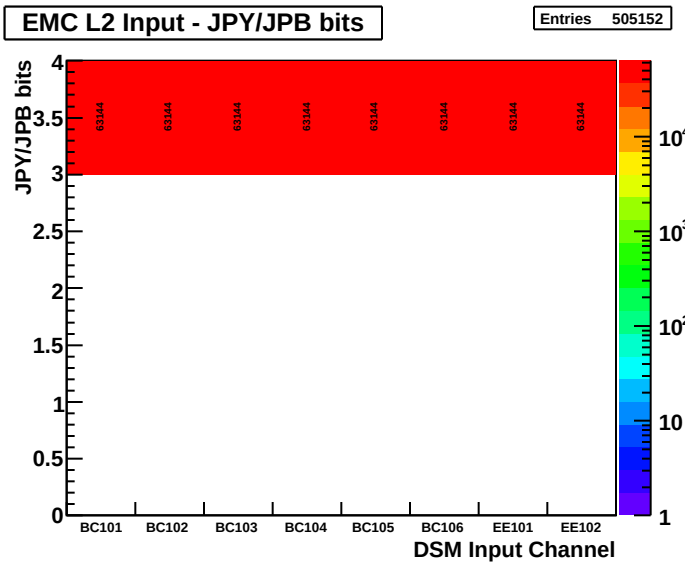

Endcap EMC L1 Input - High Eta Sum
Entries 63144
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

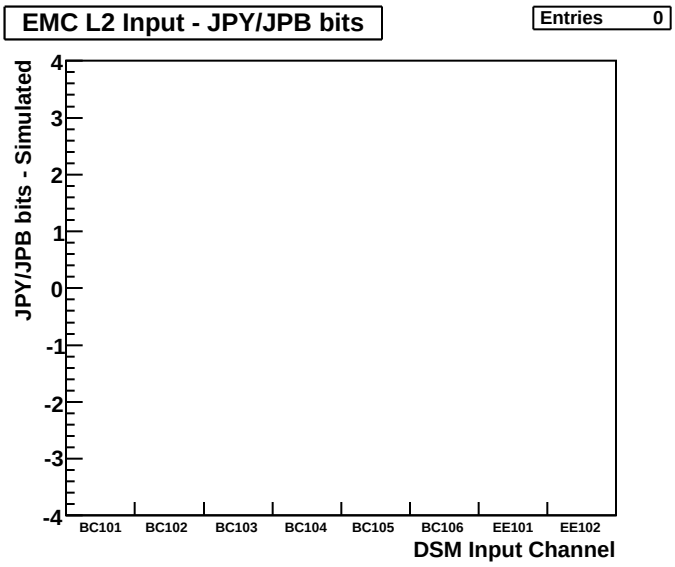

Endcap EMC L1 Input - High Tower Bits
Entries 757728
High Tower Bits
EE101
EE102
63144
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

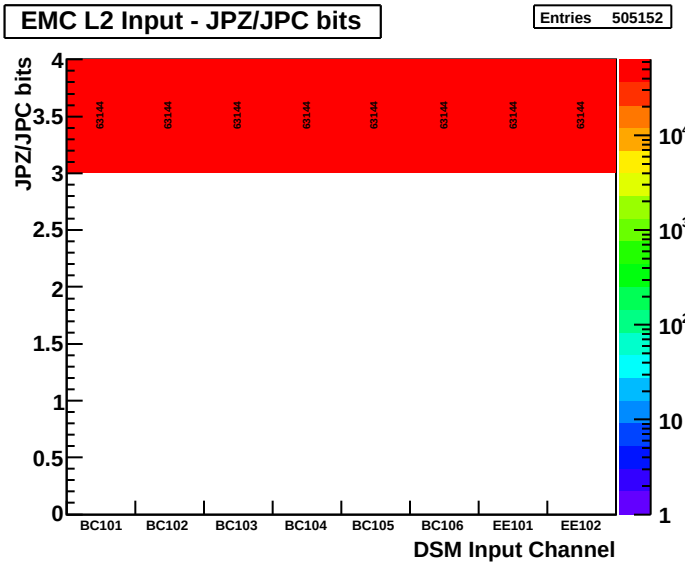

Endcap EMC L1 Input - High Tower Bits
Entries 63144
High Tower Bits - Simulated
EE101
EE102
63144
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

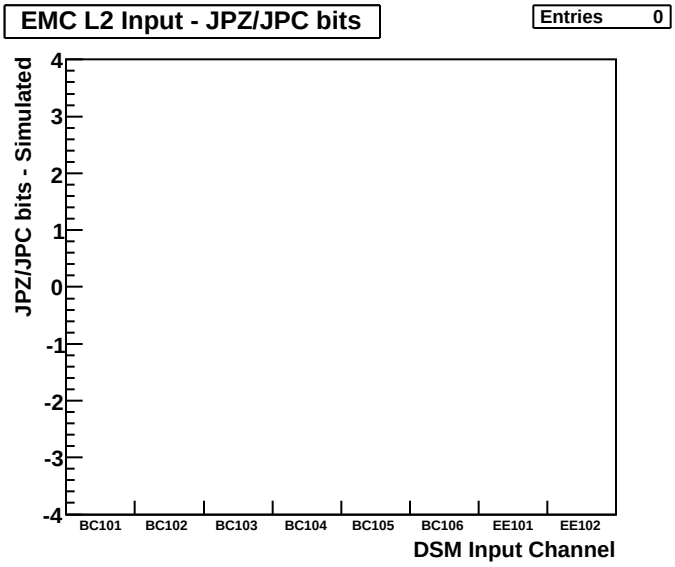

EMC L2 Input - JPX/JPA bits
Entries 505152
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
63144

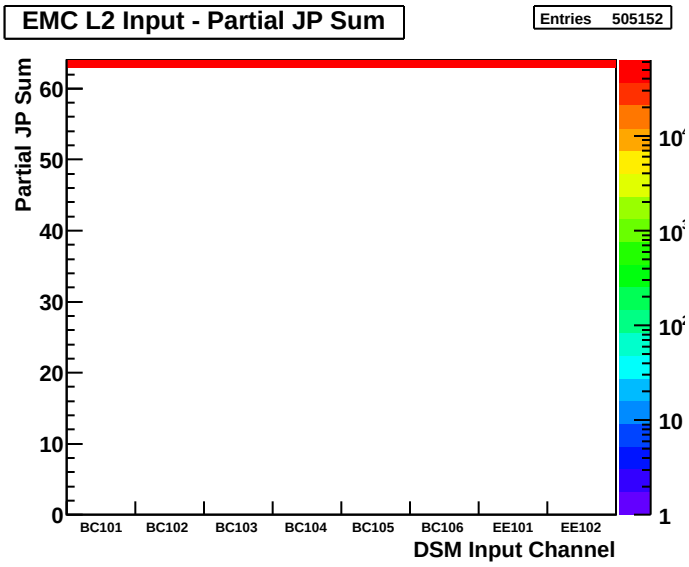

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

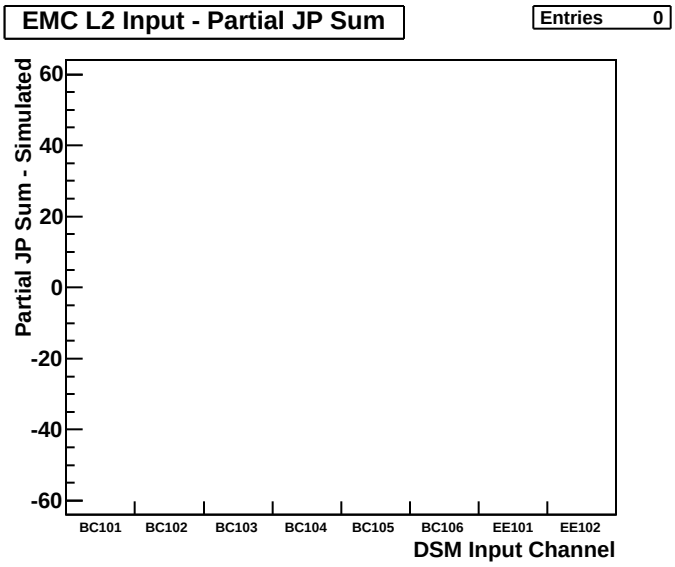

EMC L2 Input - JPY/JPB bits
Entries 505152
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
63144

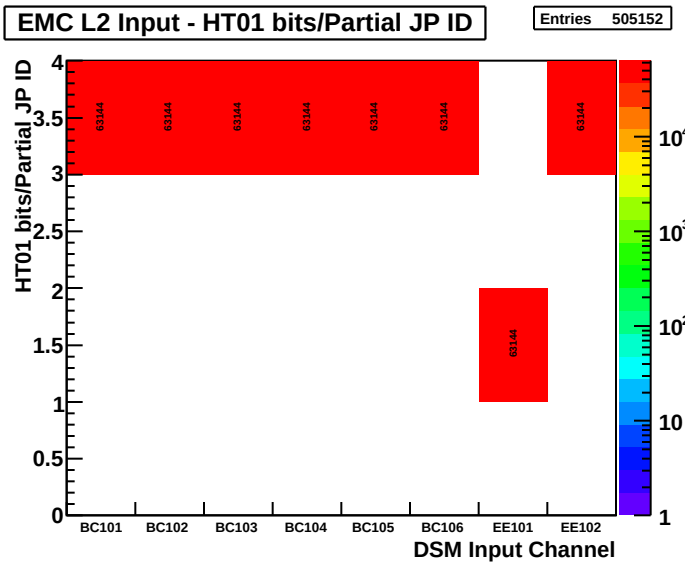

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

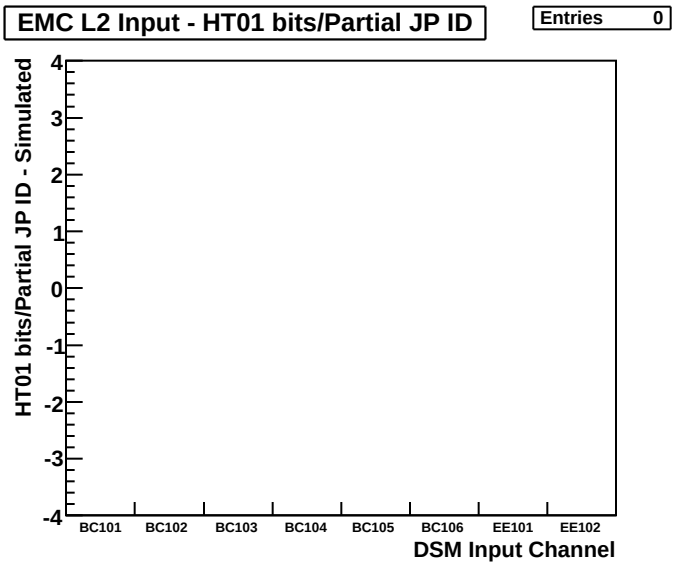

EMC L2 Input - JPZ/JPC bits
Entries 505152
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
63144

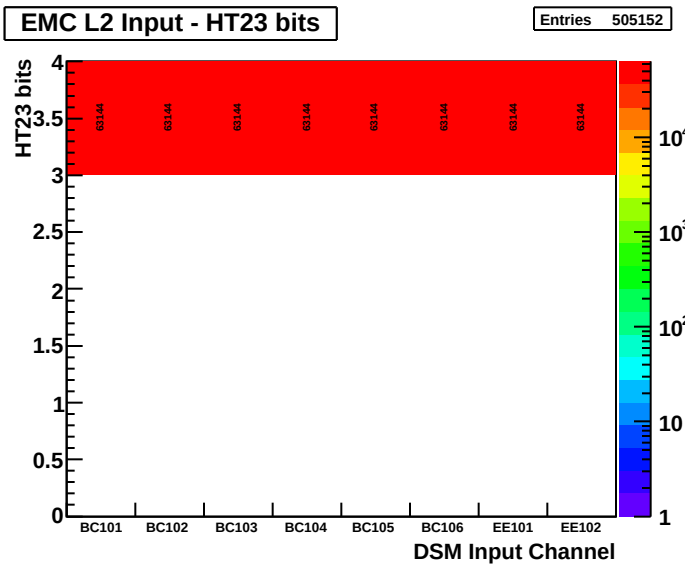

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

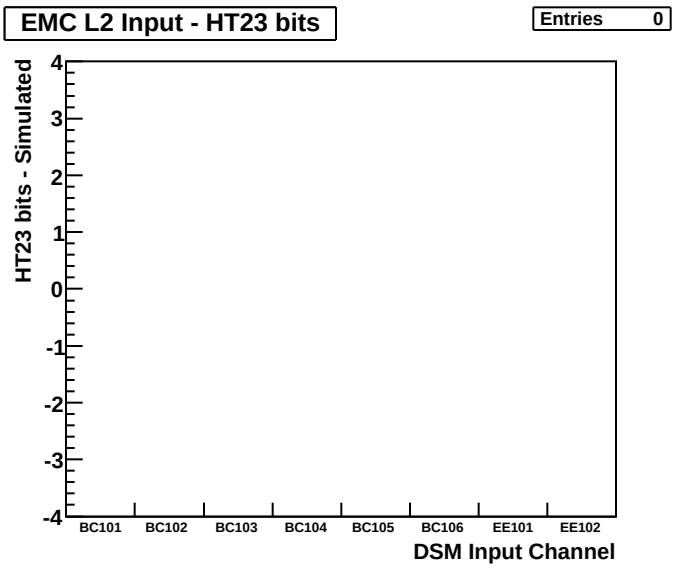

EMC L2 Input - Partial JP Sum
Entries 505152
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

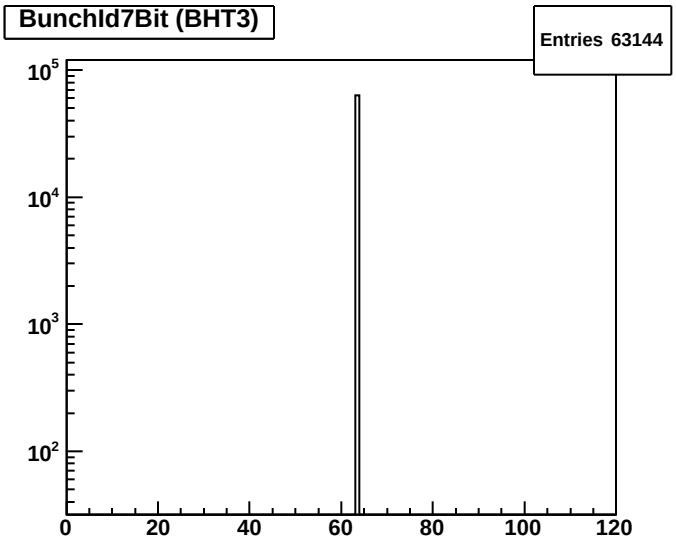

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

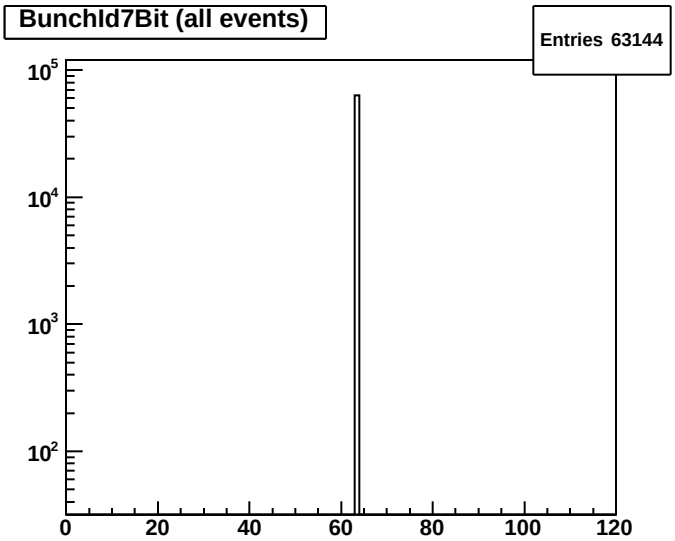

EMC L2 Input - HT01 bits/Partial JP ID
Entries 505152
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

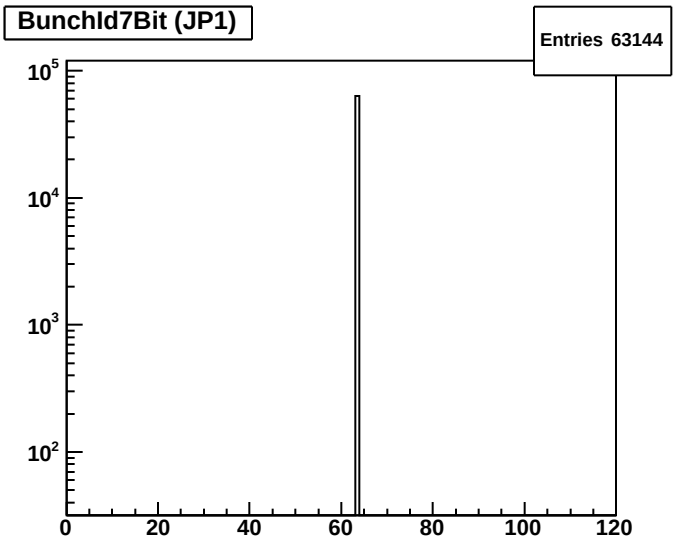

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

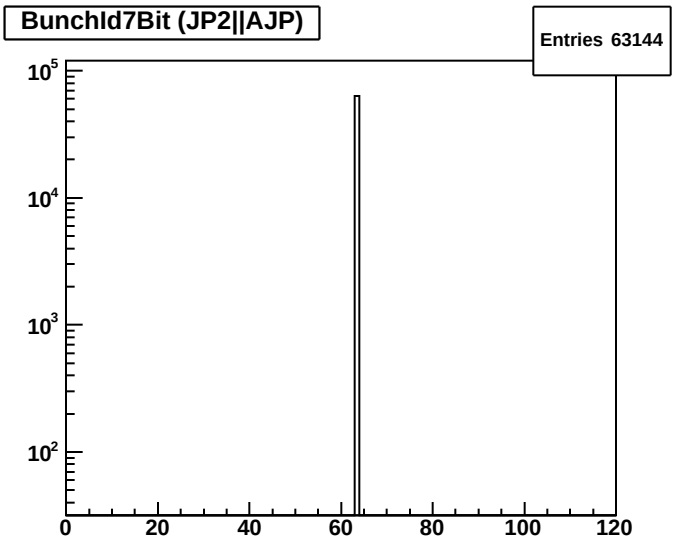

EMC L2 Input - HT23 bits
Entries 505152
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

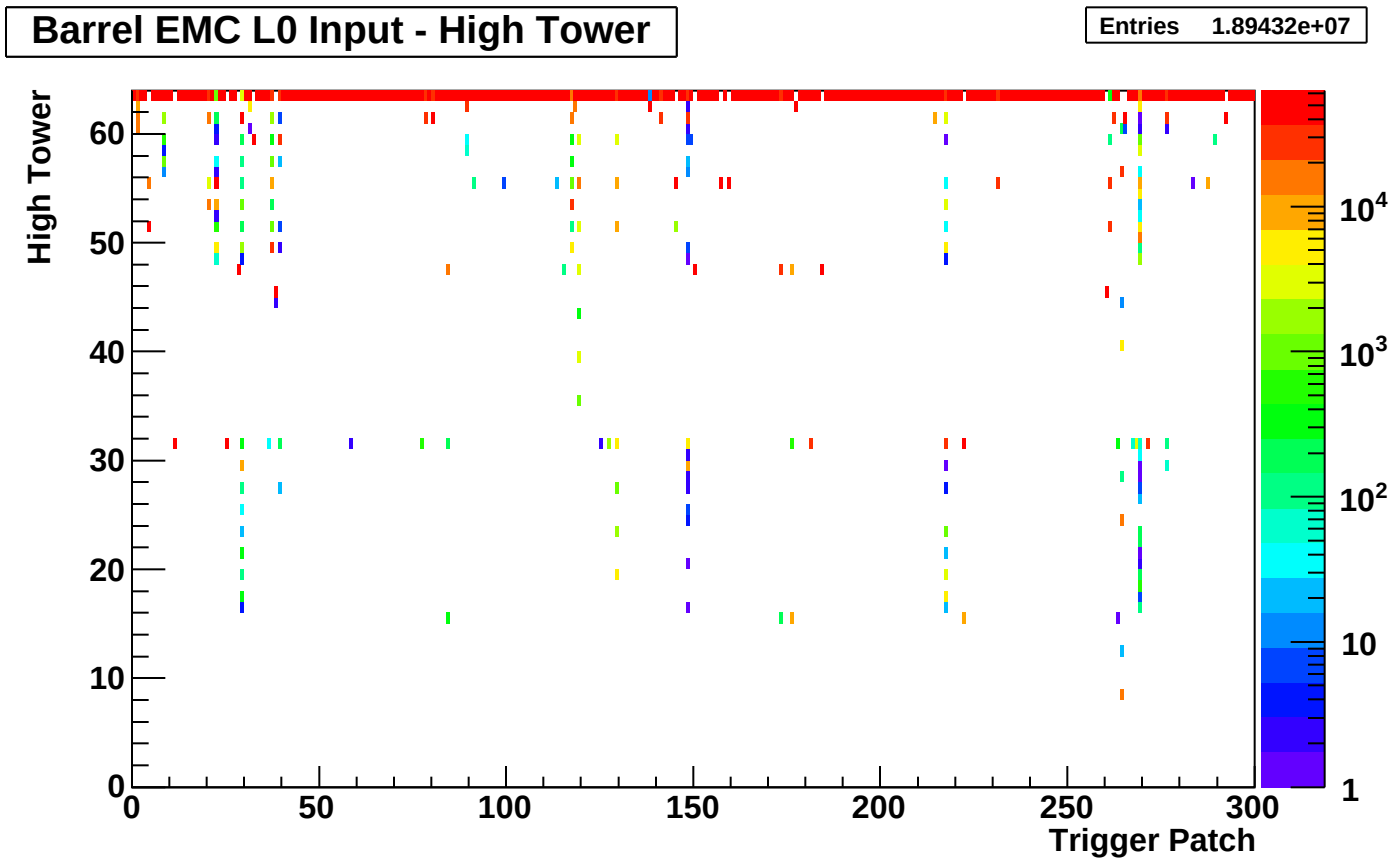

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

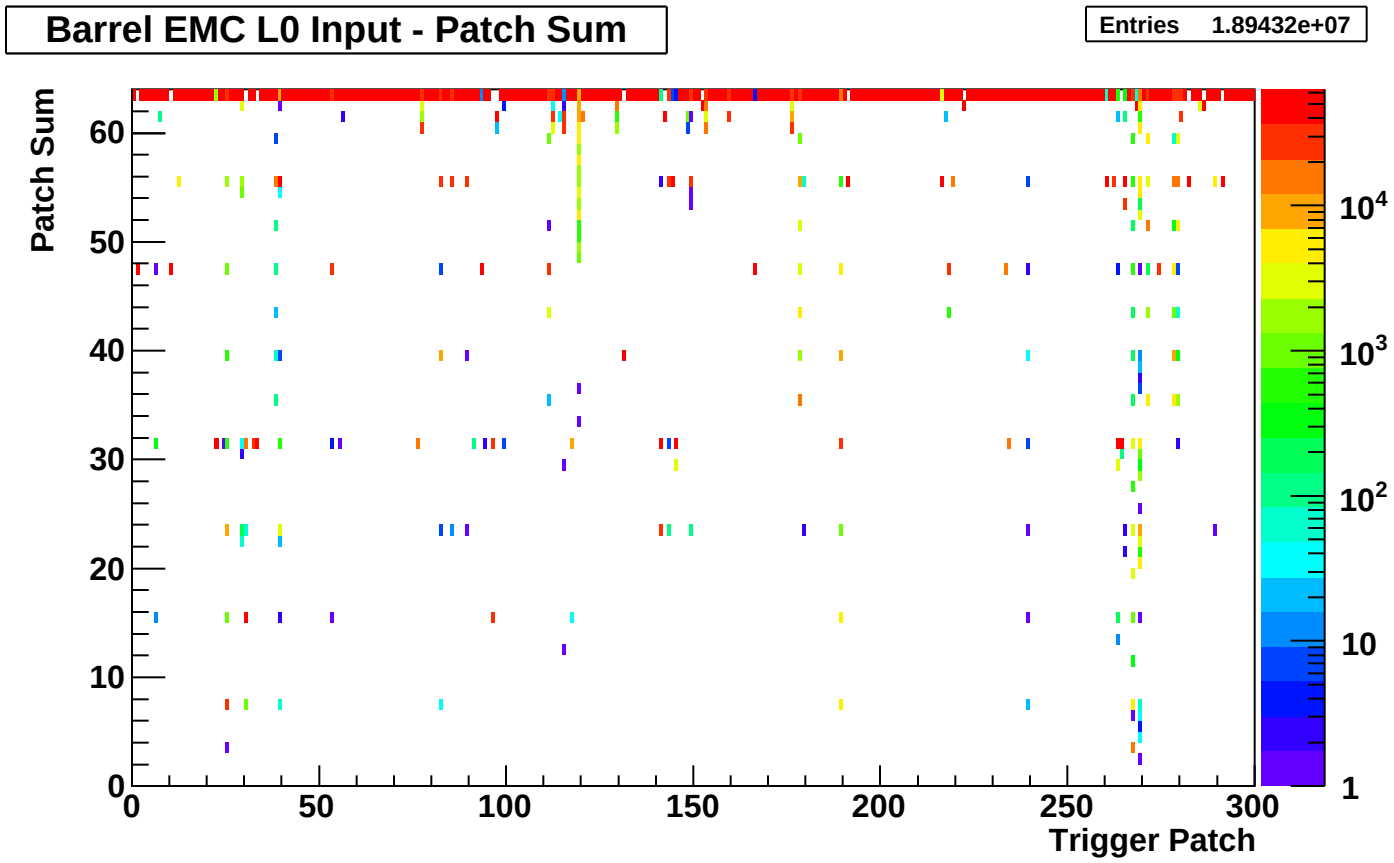

BunchId7Bit (BHT3)
Entries 63144
10^5
10^4
10^3
10^2
0
20
40
60
80
100
120

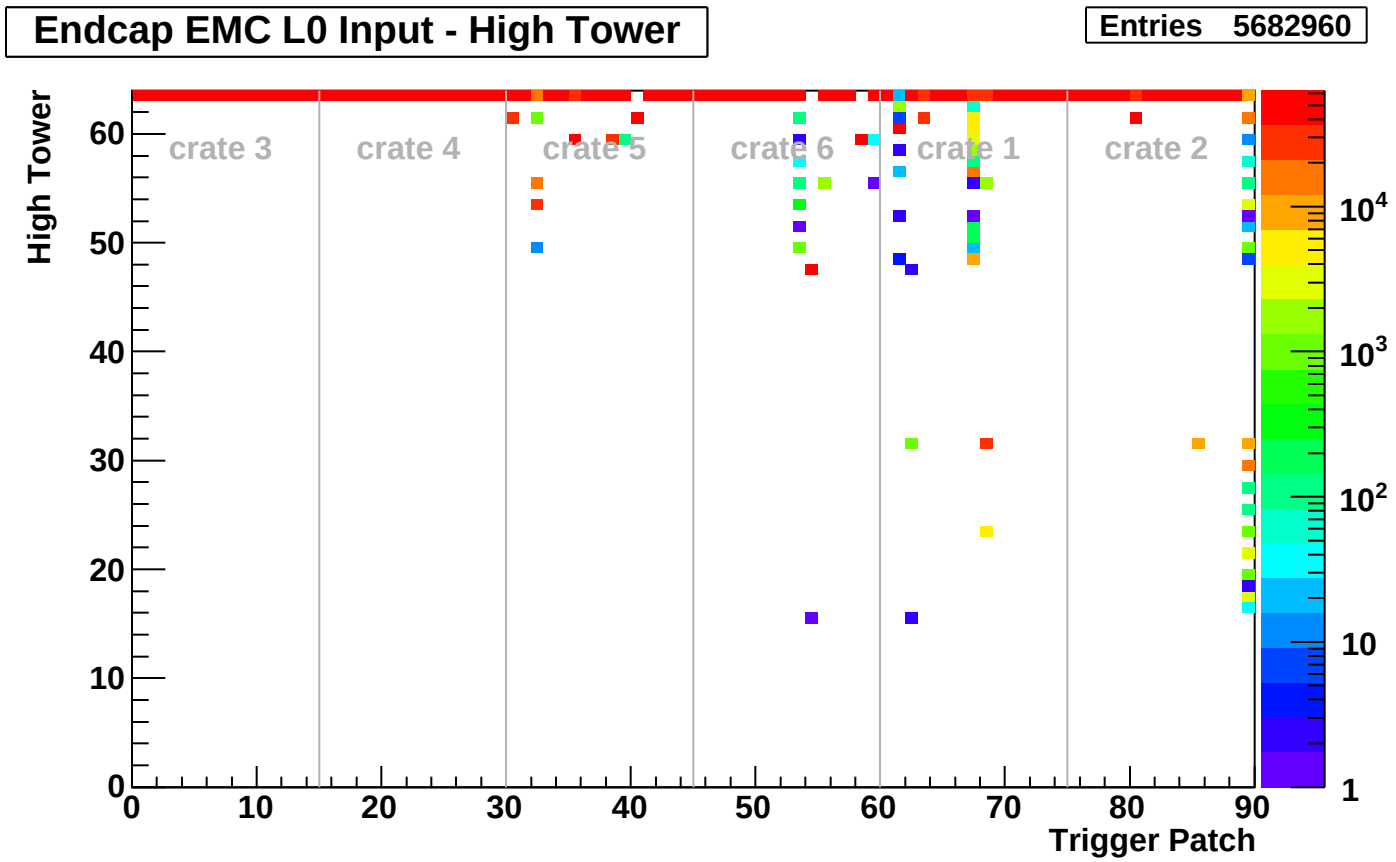

BunchId7Bit (all events)
Entries 63144
10^5
10^4
10^3
10^2
0
20
40
60
80
100
120

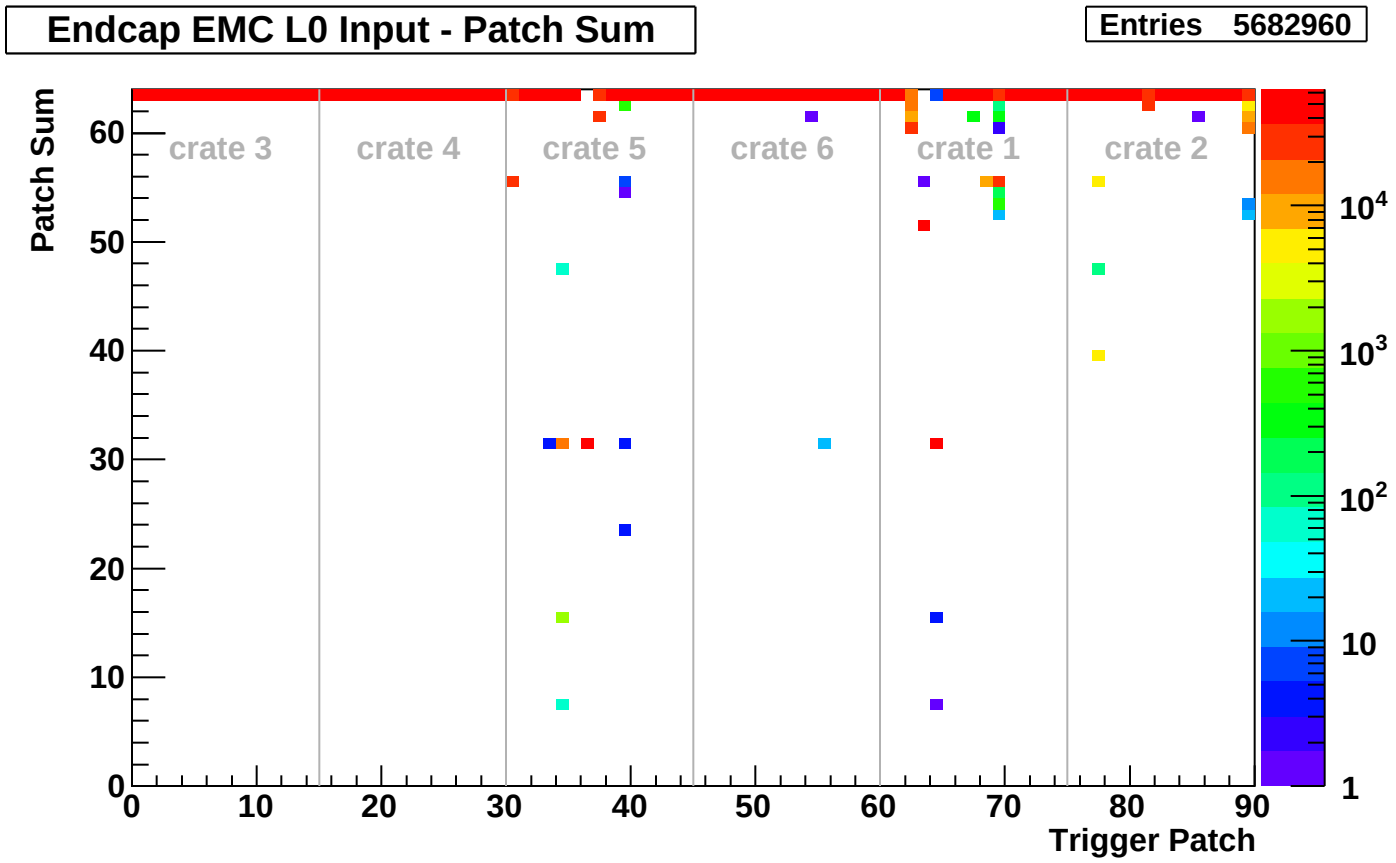

BunchId7Bit (JP1)
Entries 63144
10^5
10^4
10^3
10^2
0
20
40
60
80
100
120

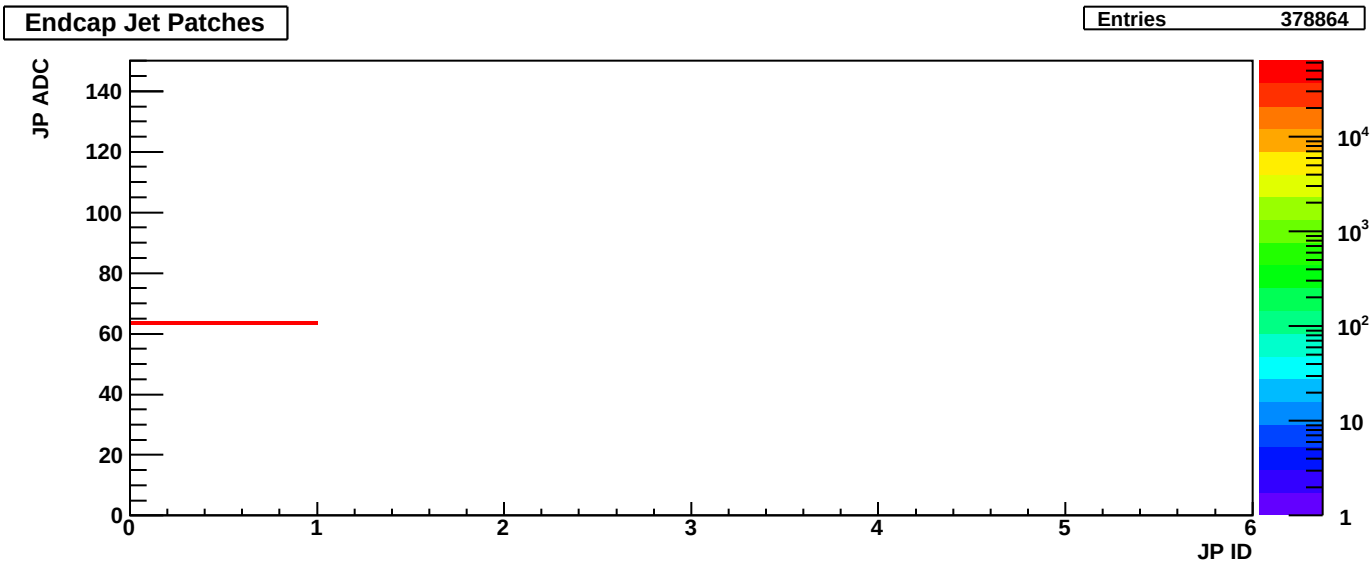

BunchId7Bit (JP2||AJP)
Entries 63144
10^5
10^4
10^3
10^2
0
20
40
60
80
100
120

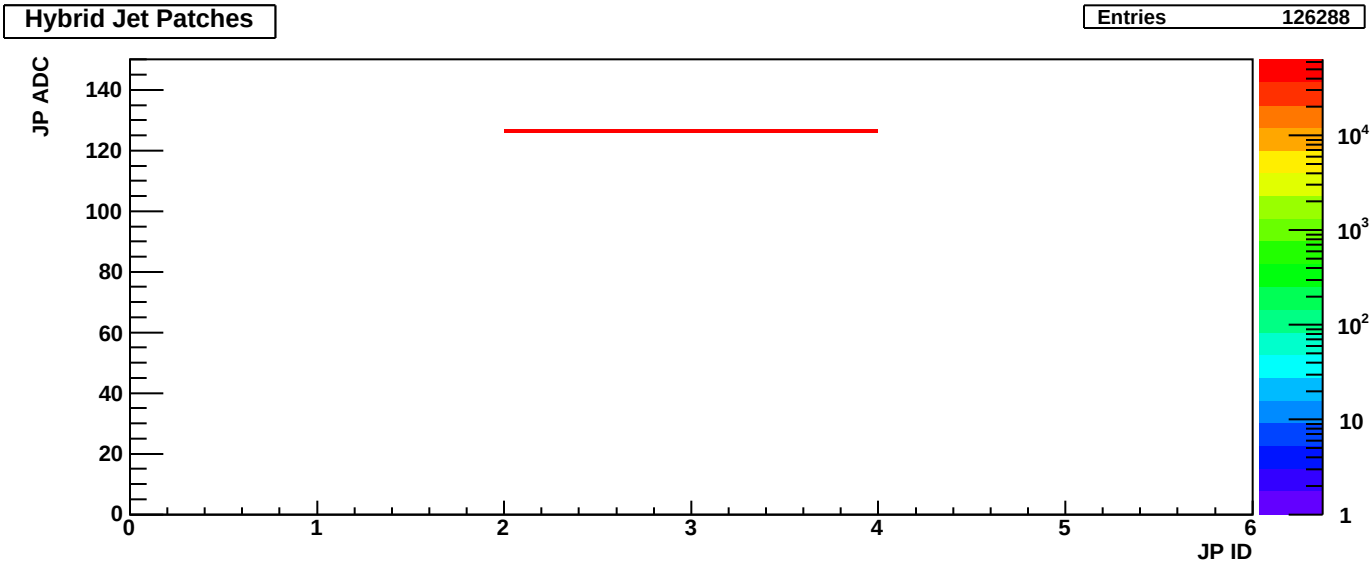

Barrel EMC L0 Input - High Tower
Entries 1.89432e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

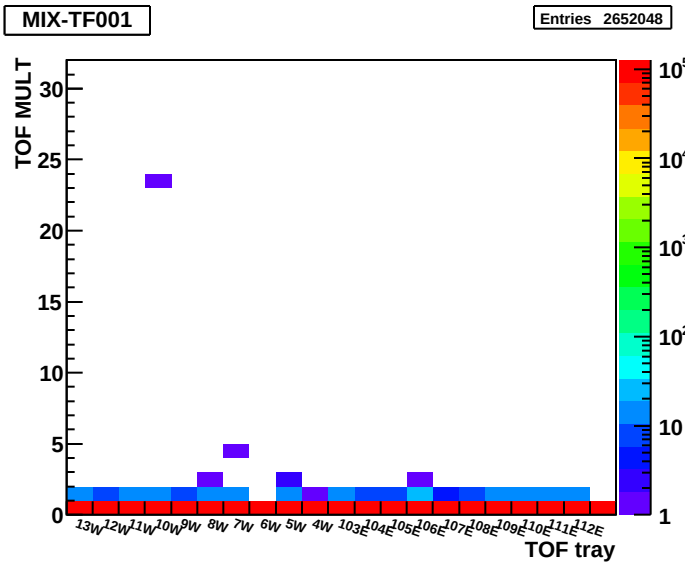

Barrel EMC L0 Input - Patch Sum
Entries 1.89432e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

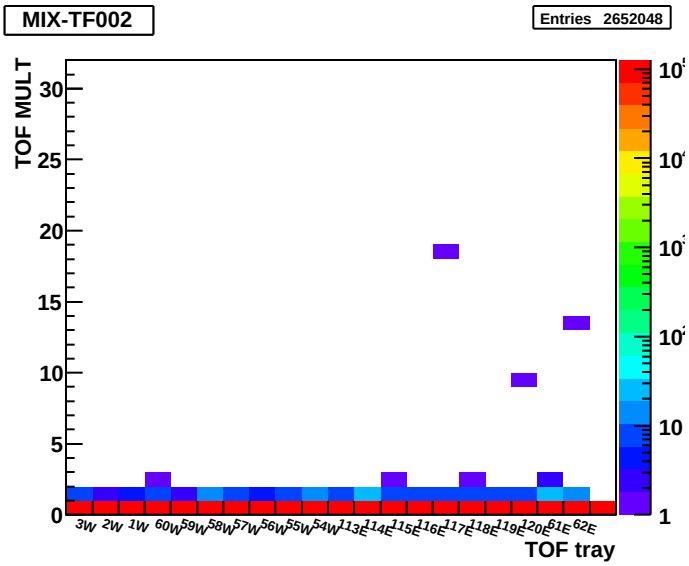

Endcap EMC L0 Input - High Tower
Entries 5682960
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

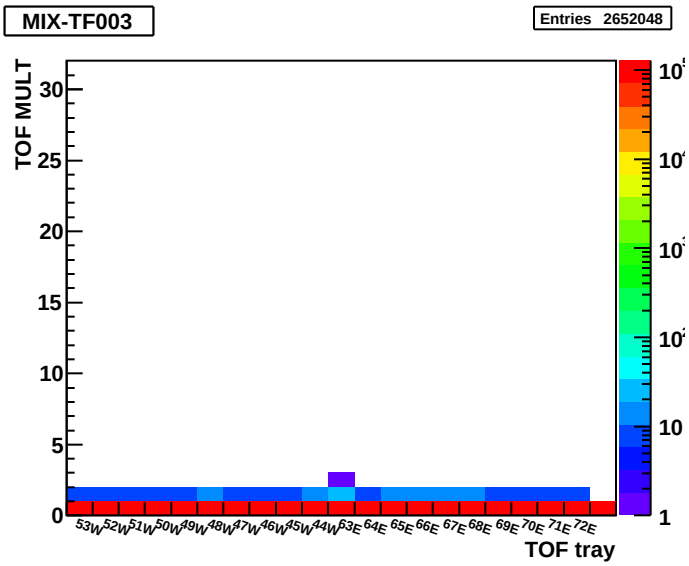

Endcap EMC L0 Input - Patch Sum
Entries 5682960
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

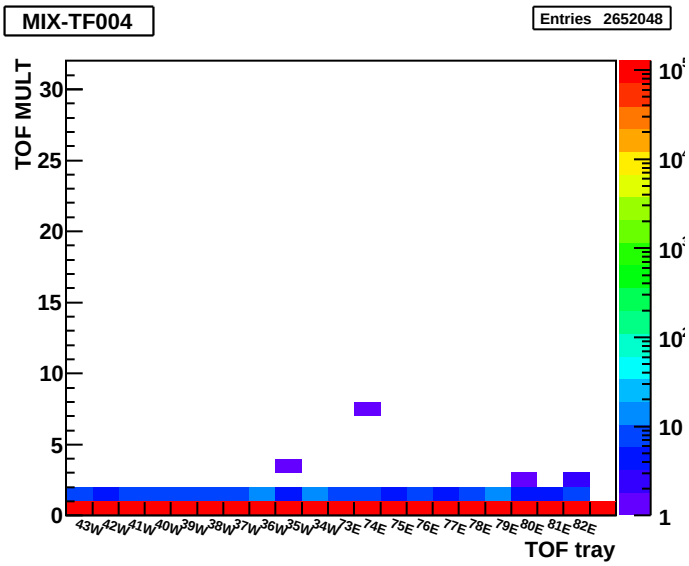

Endcap Jet Patches
Entries 378864
JP ADC
JP ID

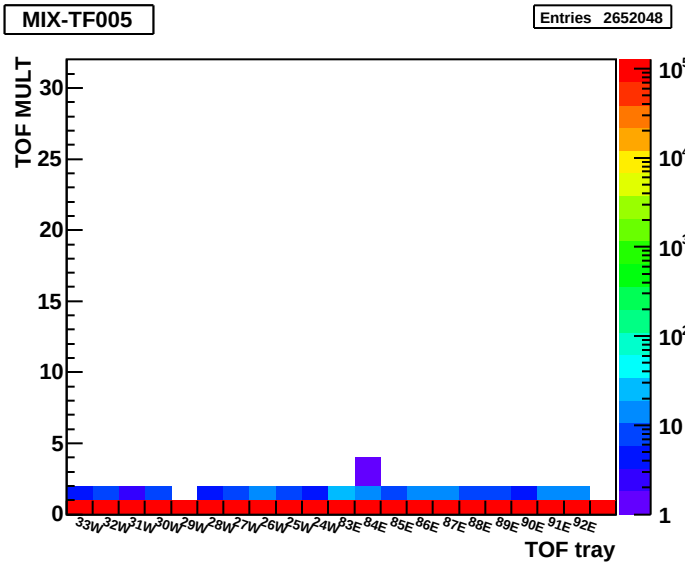

Hybrid Jet Patches
Entries 126288
JP ADC
JP ID

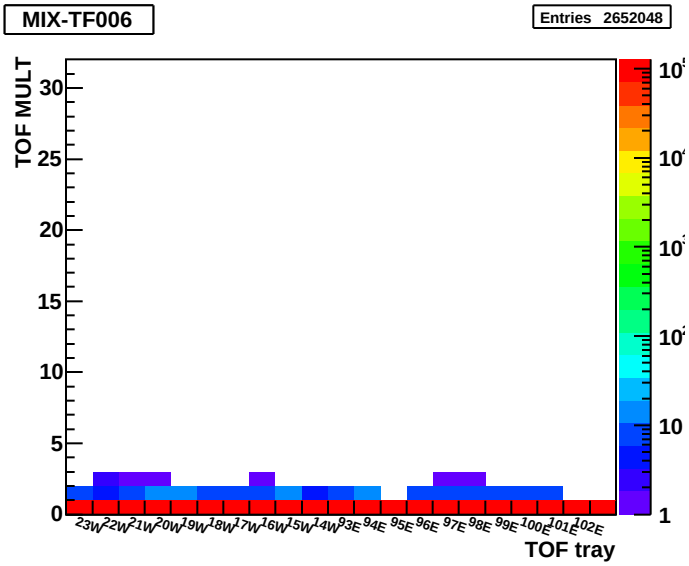

MIX-TF001
Entries 2652048
TOF MULT
TOF tray

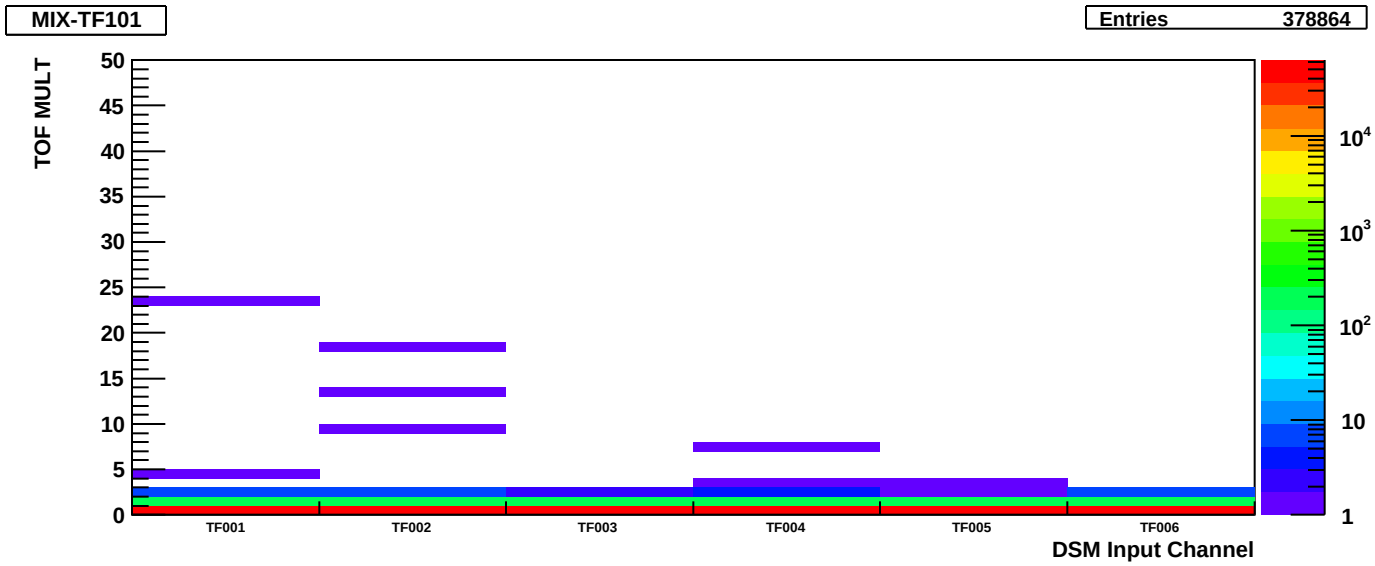

MIX-TF002
Entries 2652048
TOF MULT
TOF tray

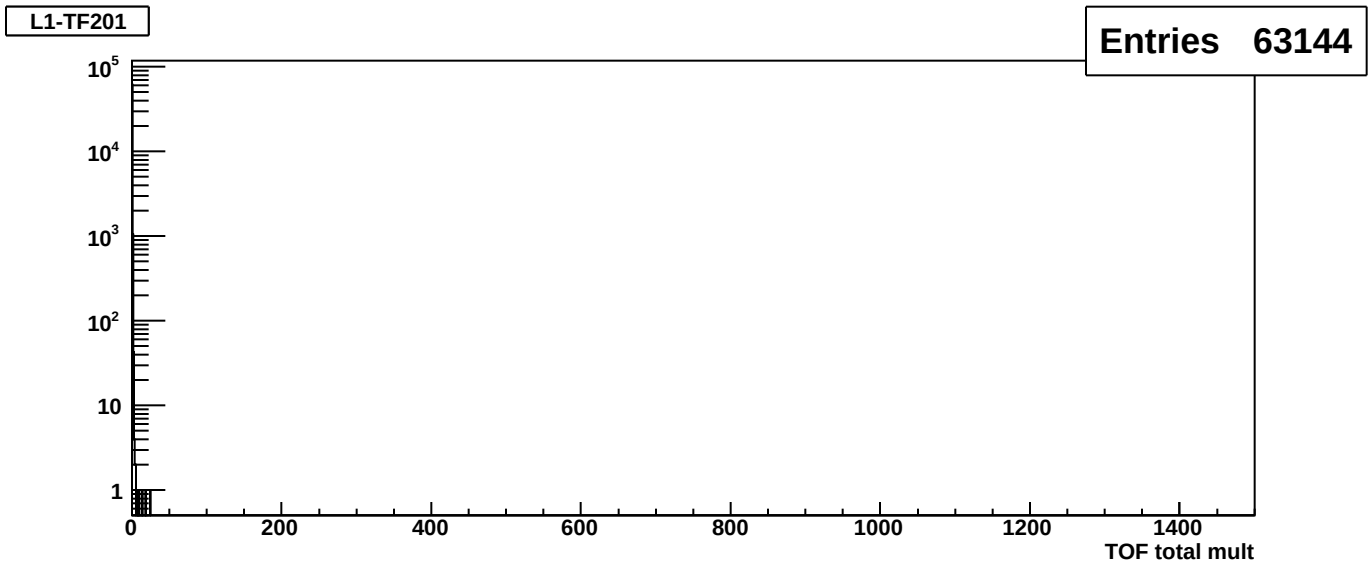

MIX-TF003
Entries 2652048
TOF MULT
TOF tray

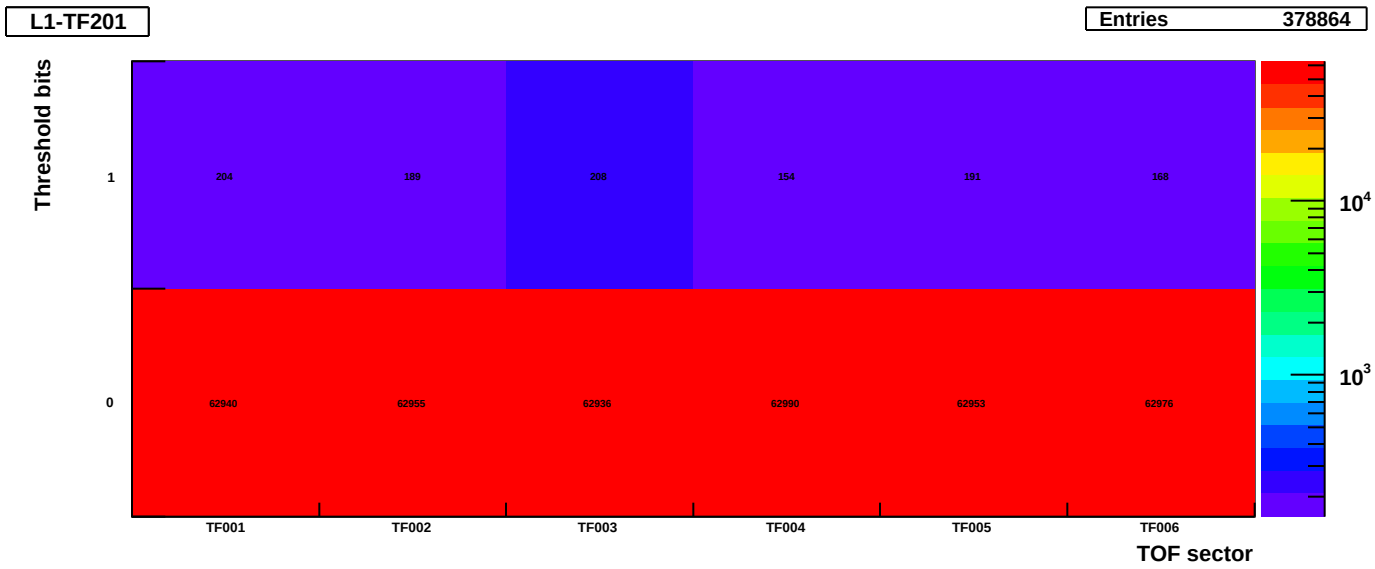

MIX-TF004
Entries 2652048
TOF MULT
TOF tray

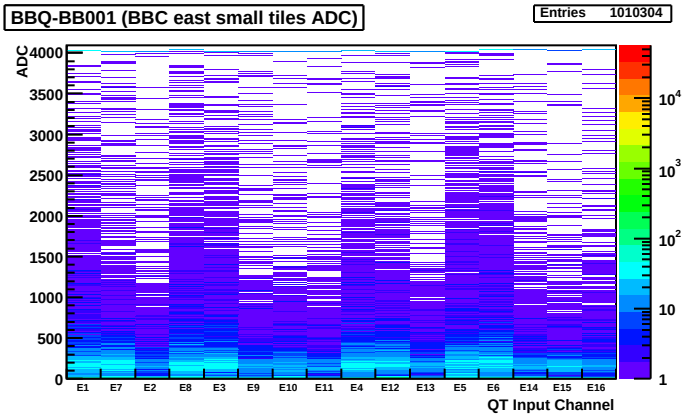

MIX-TF005
Entries 2652048
TOF MULT
TOF tray

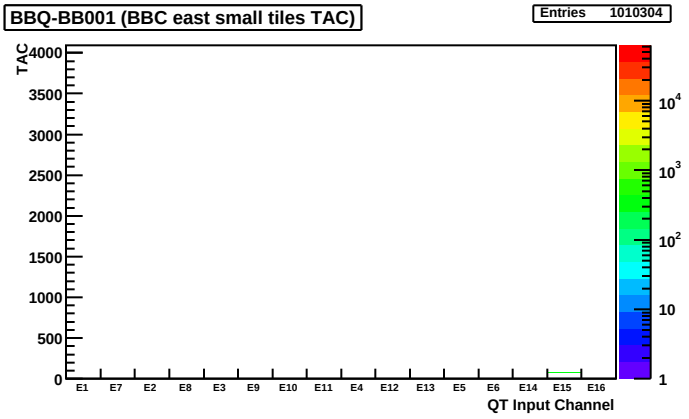

MIX-TF006
Entries 2652048
TOF MULT
TOF tray

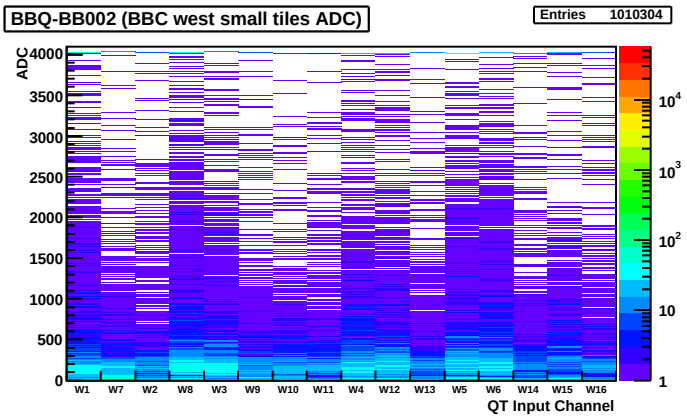

MIX-TF101
Entries 378864
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

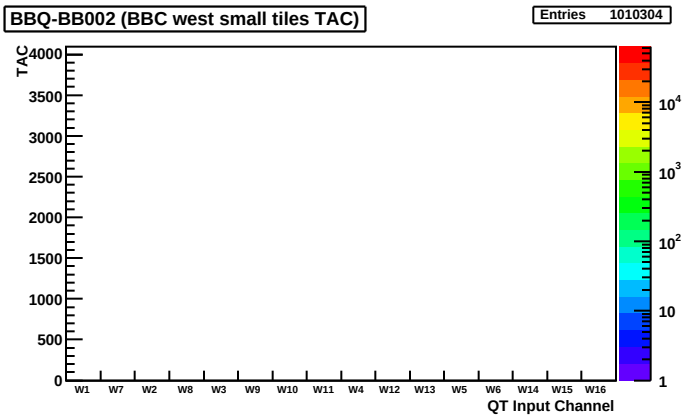

L1-TF201
Entries 63144
10^5
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

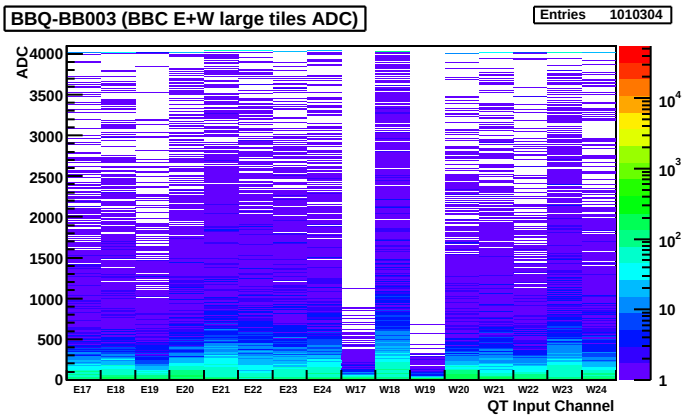

L1-TF201
Entries 378864
Threshold bits
1
0
204
189
208
154
191
168
62940
62955
62936
62990
62953
62976
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3

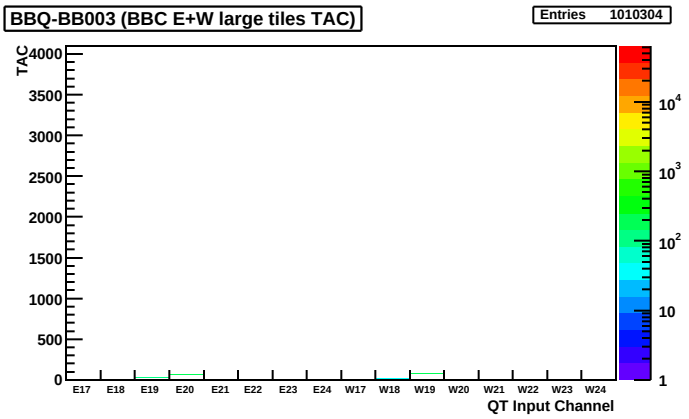

BBQ-BB001 (BBC east small tiles ADC)
Entries 1010304
ADC
QT Input Channel

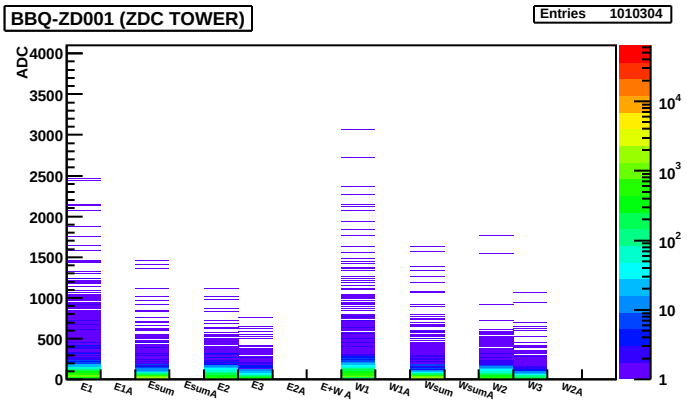

BBQ-BB001 (BBC east small tiles TAC)
Entries 1010304
TAC
QT Input Channel

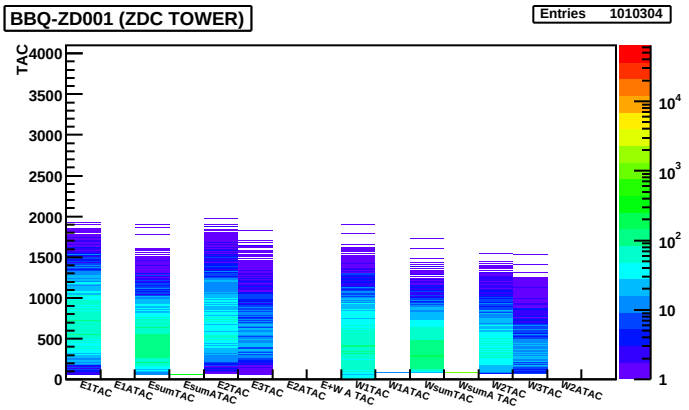

BBQ-BB002 (BBC west small tiles ADC)
Entries 1010304
ADC
QT Input Channel

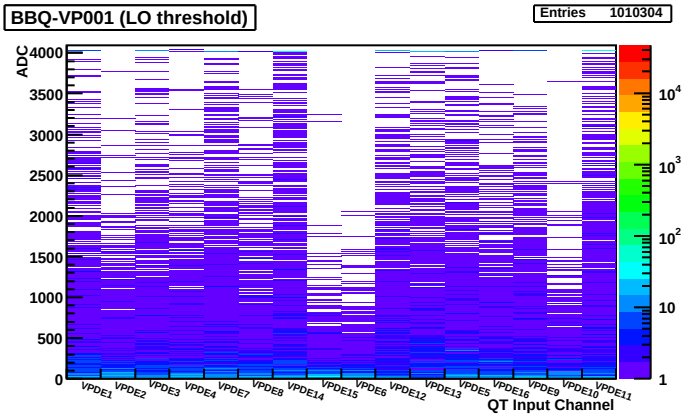

BBQ-BB002 (BBC west small tiles TAC)
Entries 1010304
TAC
QT Input Channel

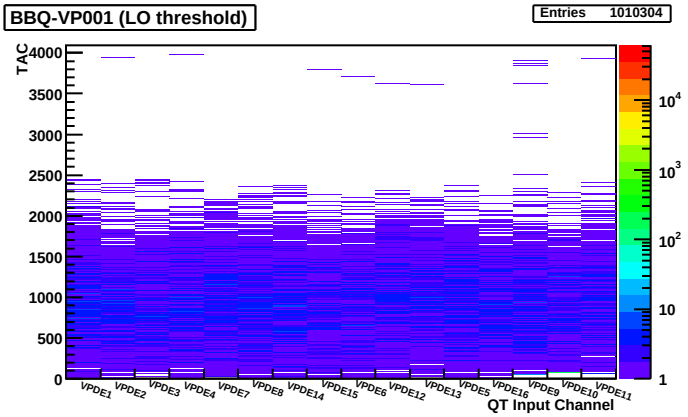

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1010304
ADC
QT Input Channel


BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1010304
TAC
QT Input Channel


BBQ-ZD001 (ZDC TOWER)
Entries 1010304
ADC


BBQ-ZD001 (ZDC TOWER)
Entries 1010304
TAC


BBQ-VP001 (LO threshold)
Entries 1010304
ADC
QT Input Channel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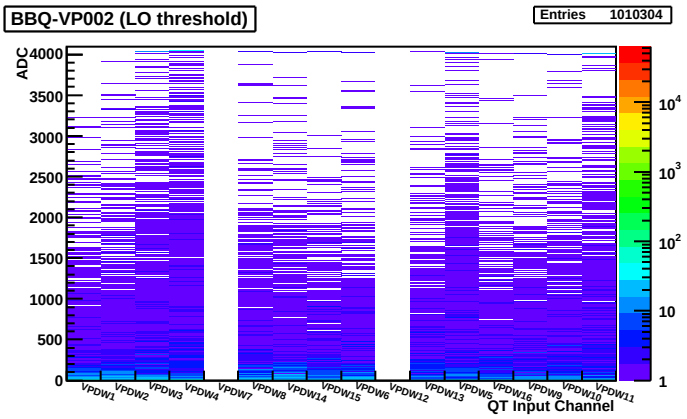
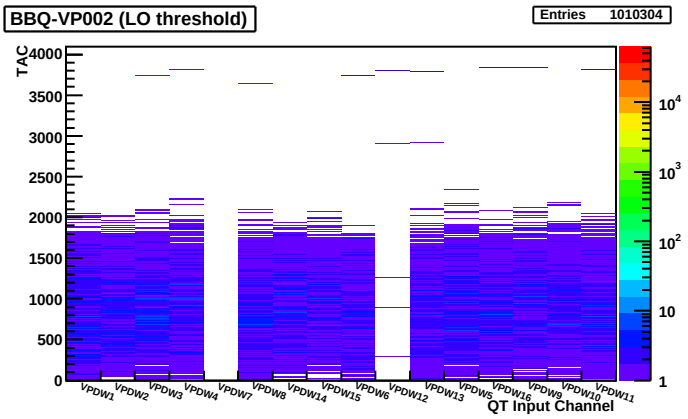
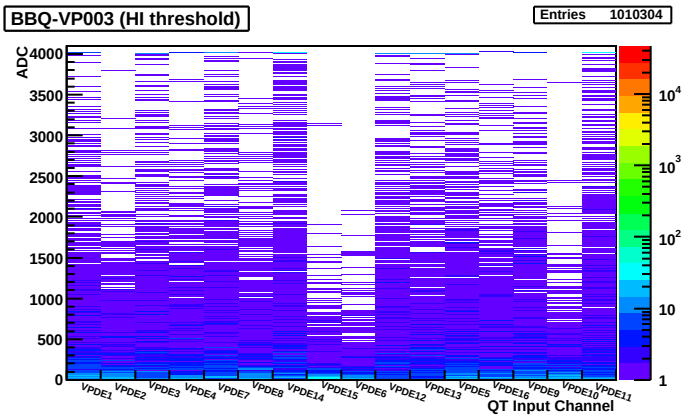
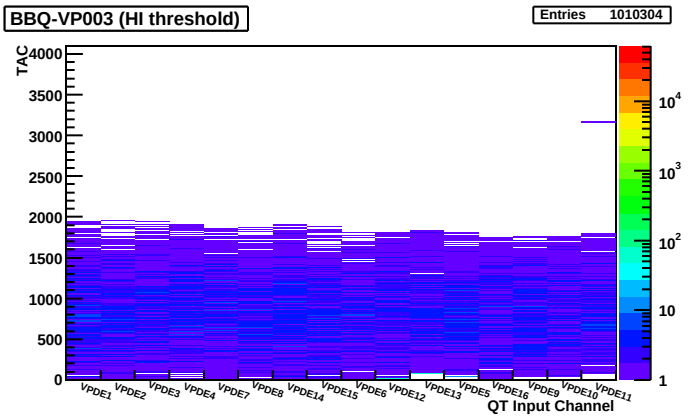
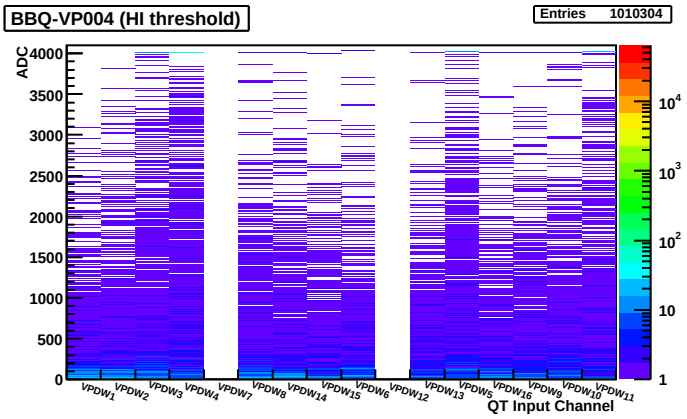
Entries 1010304
TAC
QT Input Channel

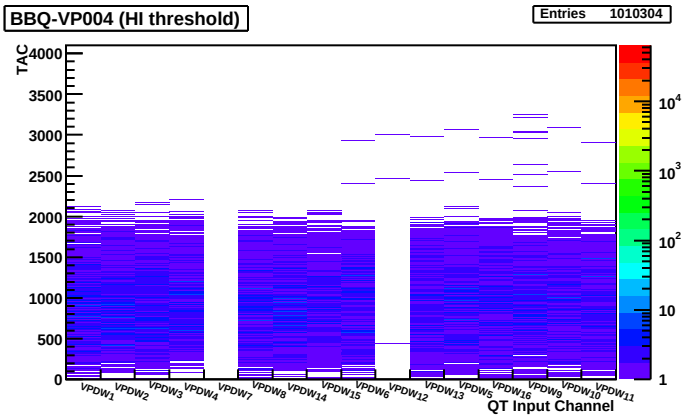

BBQ-VP002 (LO threshold)
Entries 1010304
ADC
QT Input Channel

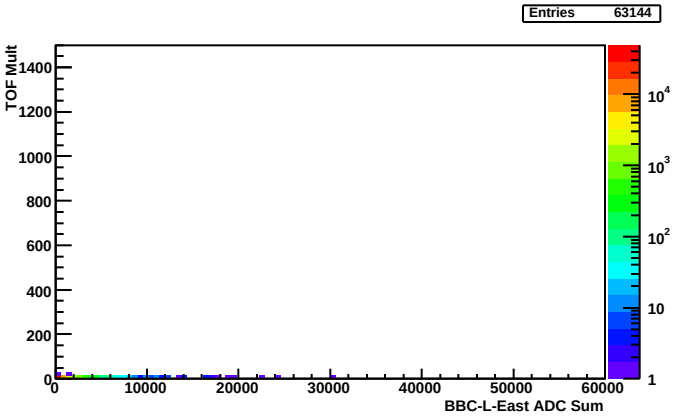

BBQ-VP002 (LO threshold)
Entries 1010304
TAC
QT Input Channel

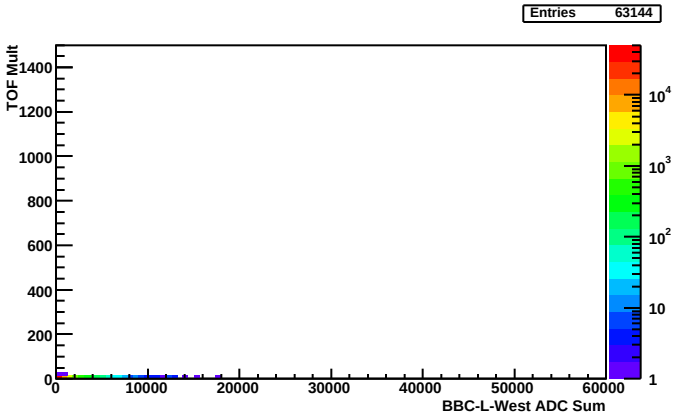

BBQ-VP003 (HI threshold)
Entries 1010304
ADC
QT Input Channel

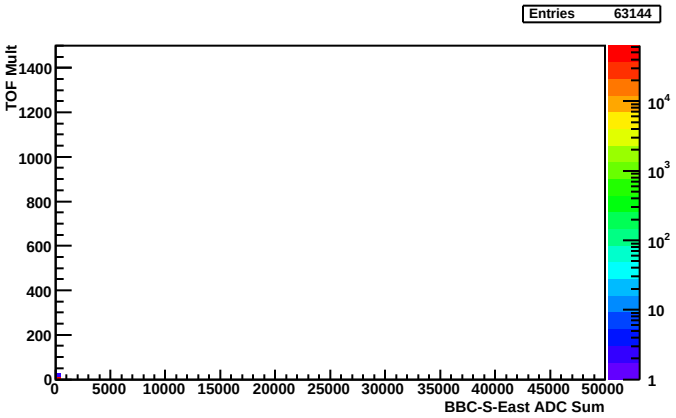

BBQ-VP003 (HI threshold)
Entries 1010304
TAC
QT Input Channel

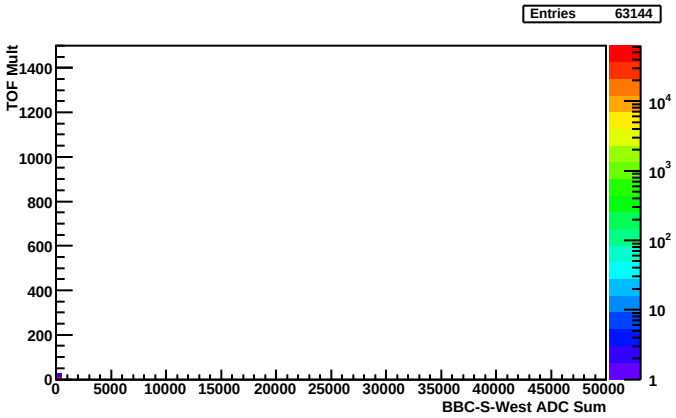

BBQ-VP004 (HI threshold)
Entries 1010304
ADC
QT Input Channel

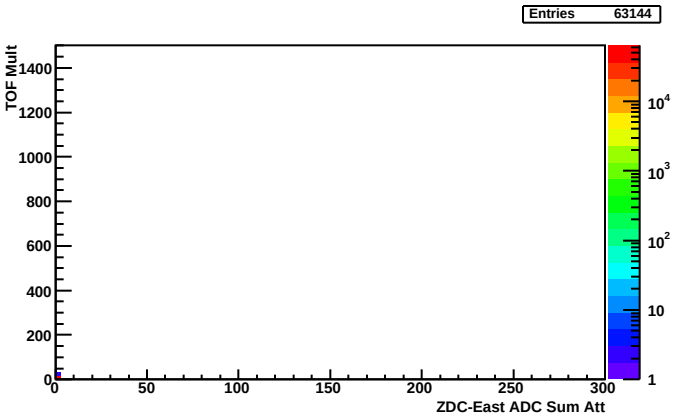

BBQ-VP004 (HI threshold)
Entries 1010304
TAC
QT Input Channel

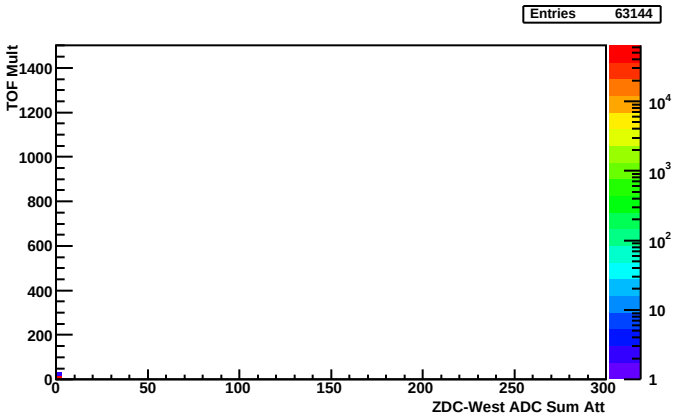

Entries 63144
TOF Mult
BBC-L-East ADC Sum

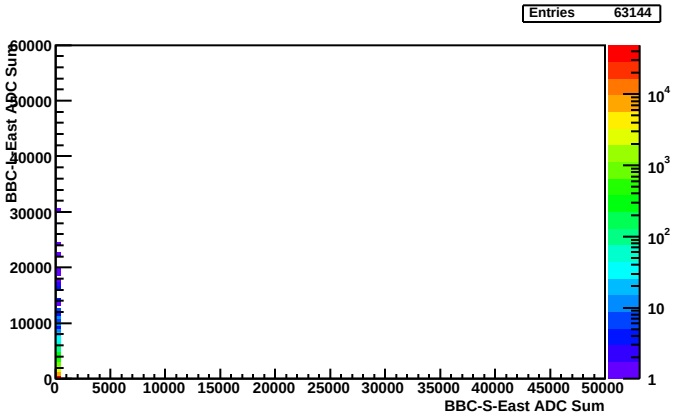

Entries 63144
TOF Mult
BBC-L-West ADC Sum

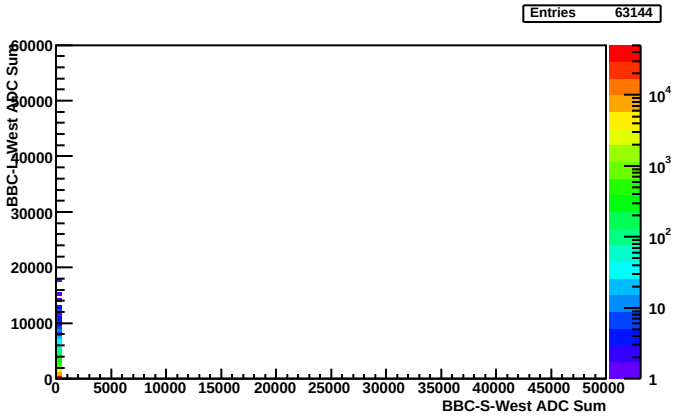

Entries 63144
TOF Mult
BBC-S-East ADC Sum

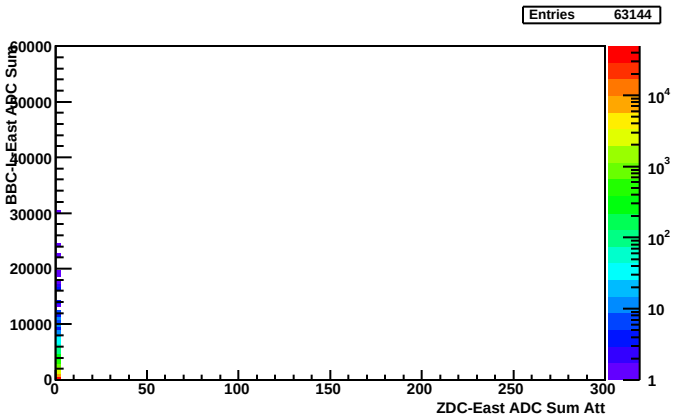

Entries 63144
TOF Mult
BBC-S-West ADC Sum

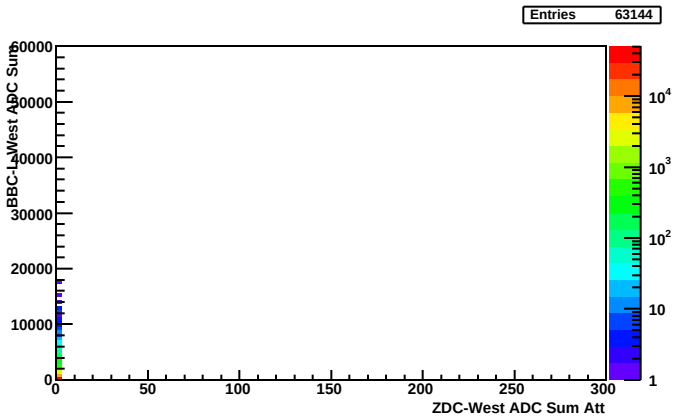

Entries 63144
TOF Mult
ZDC-East ADC Sum Att


Entries 63144
TOF Mult
ZDC-West ADC Sum Att


Entries 63144
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 63144
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 63144
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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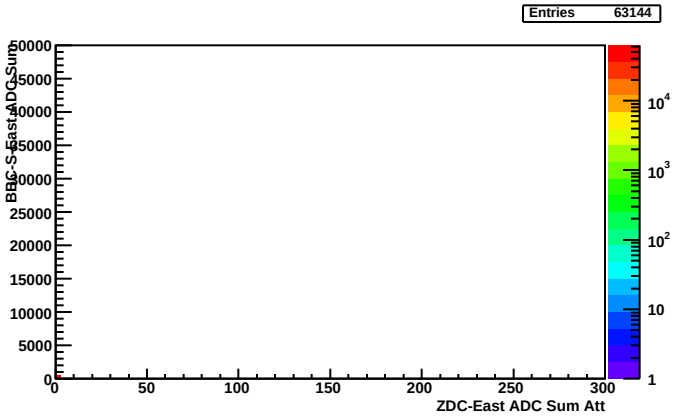
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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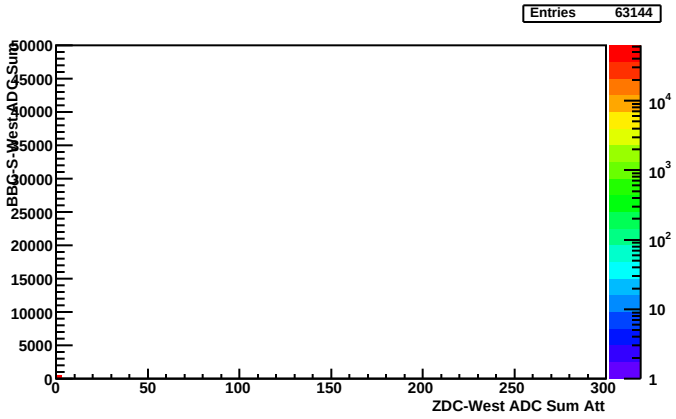
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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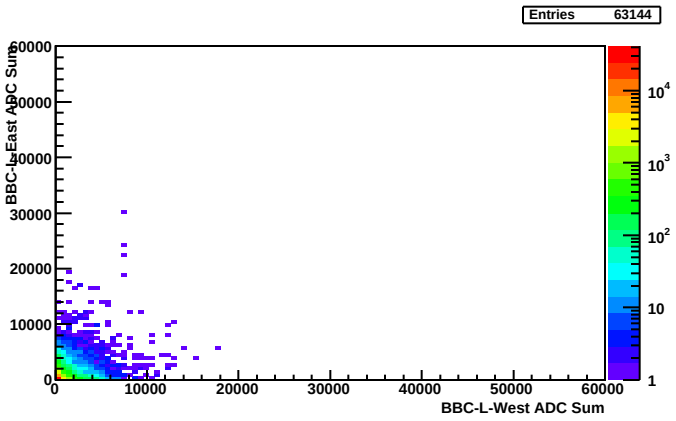
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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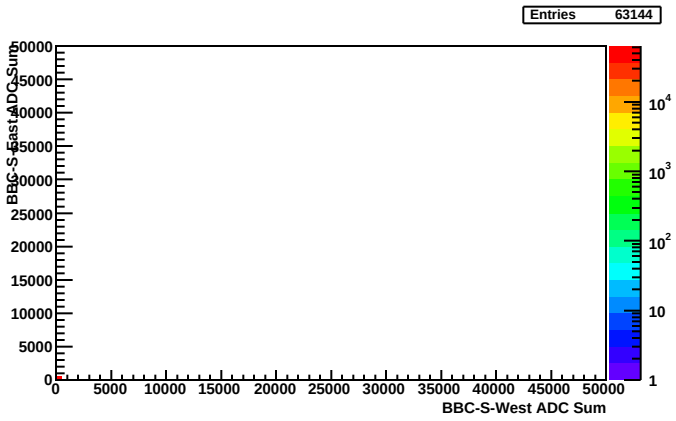
BBC-L-East ADC Sum
BBC-L-West ADC Sum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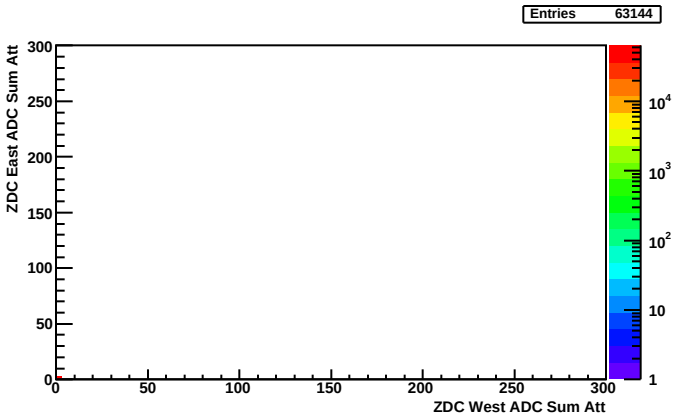
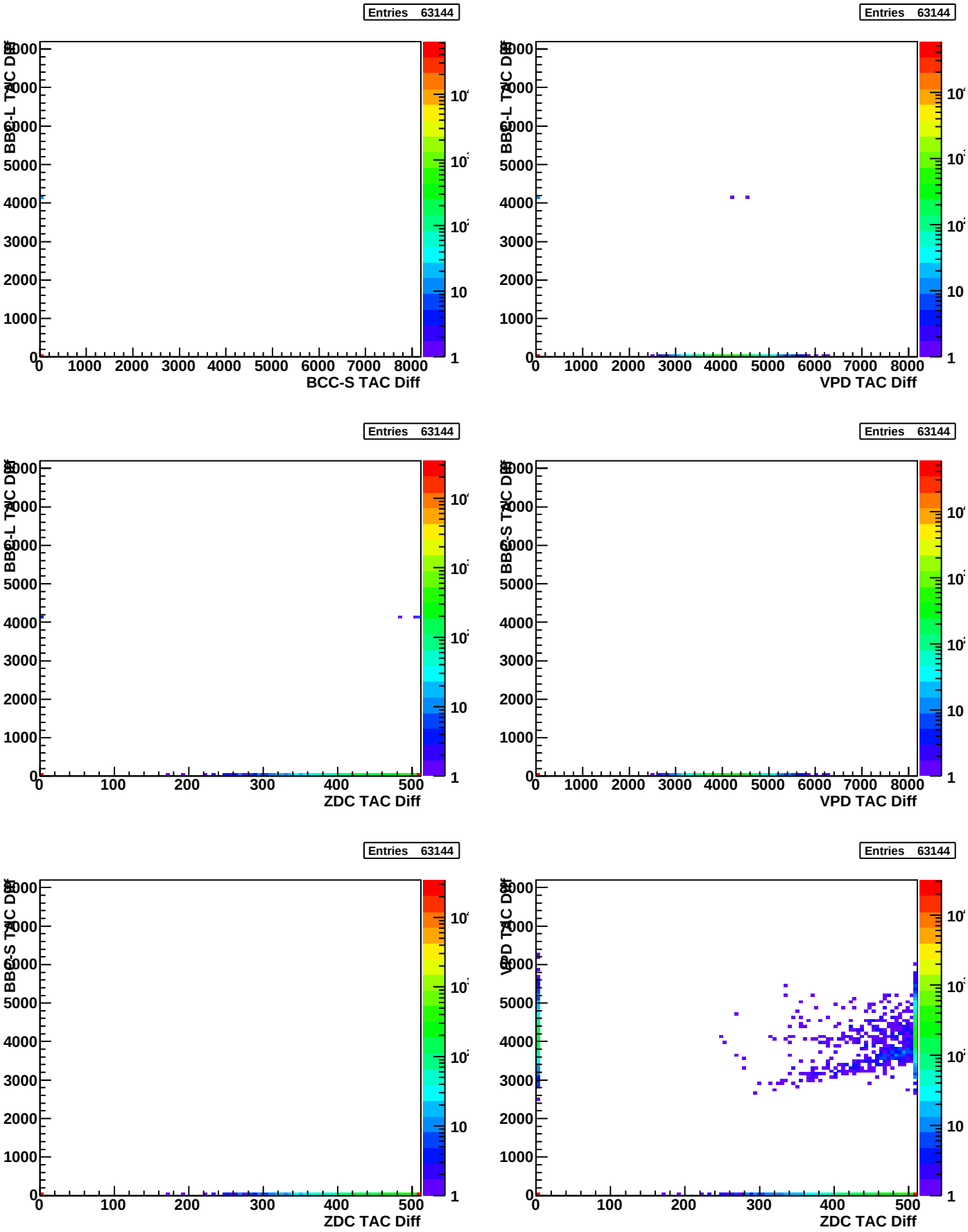
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 63144
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 63144
BBC-L TAC Diff
BCC-S TAC Diff
Entries 63144
BBC-L TAC Diff
VPD TAC Diff
Entries 63144
BBC-L TAC Diff
ZDC TAC Diff
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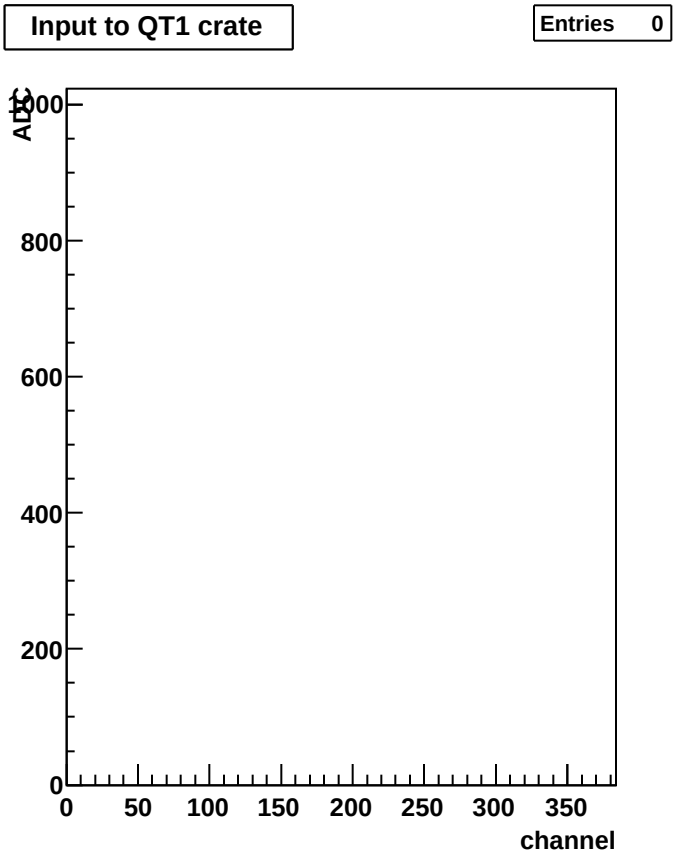
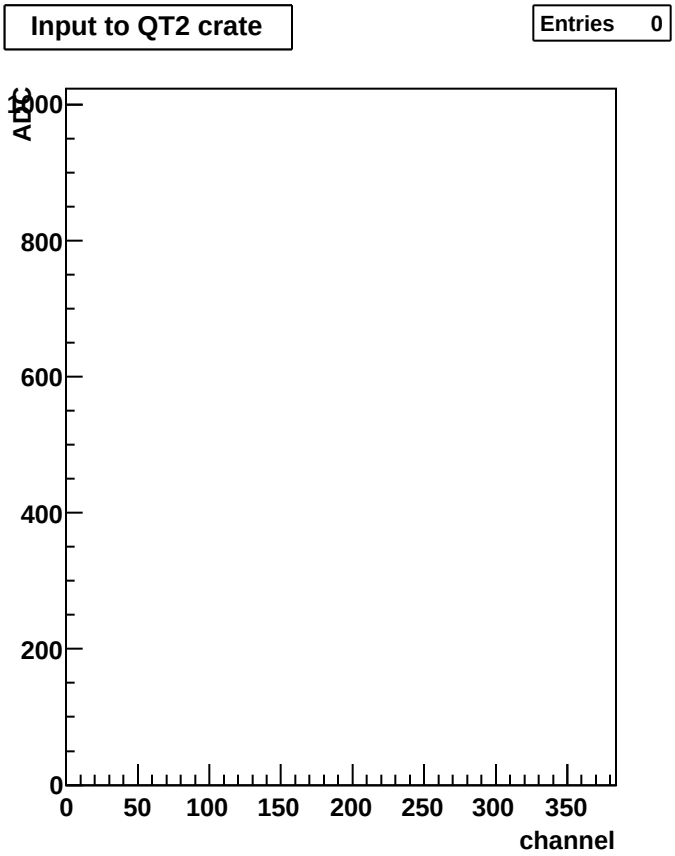
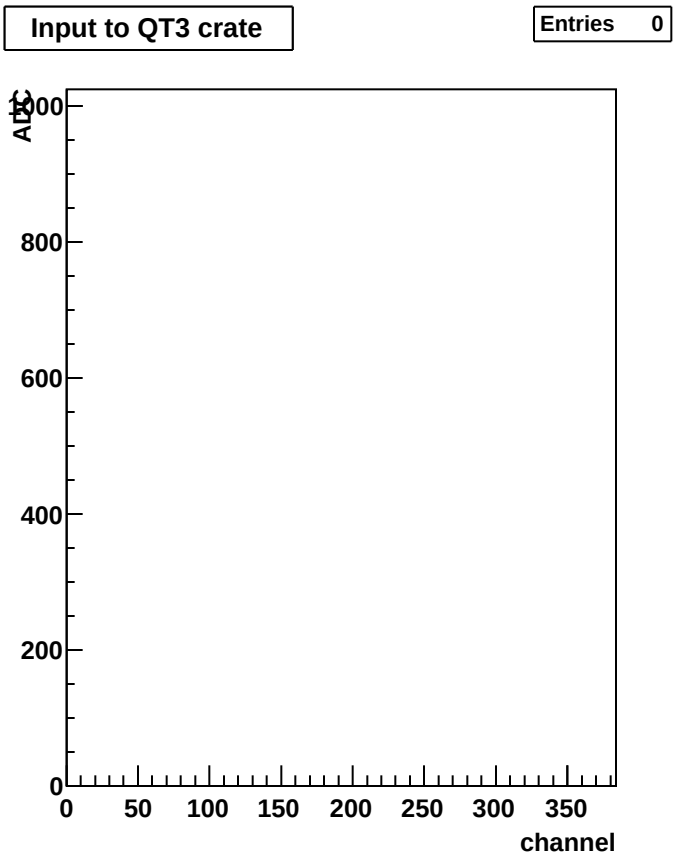
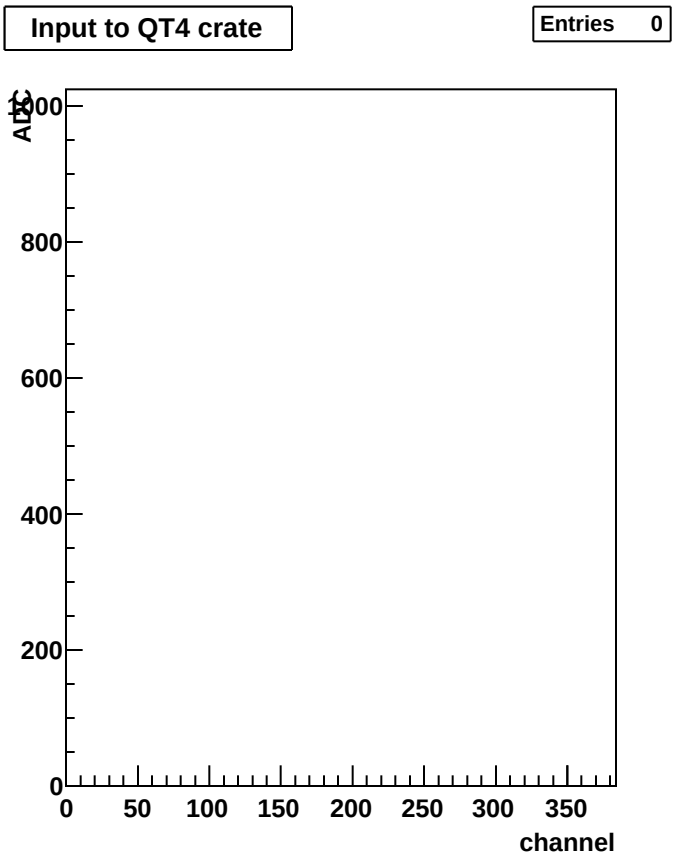
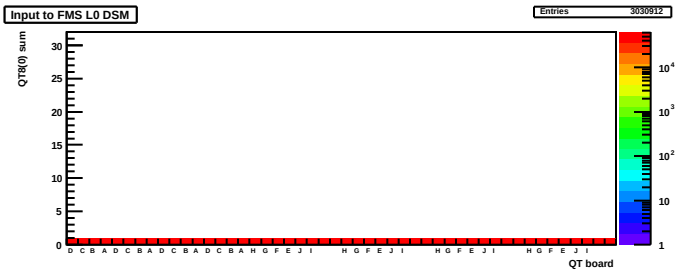
Entries 63144
VPD TAC Diff
ZDC TAC Diff

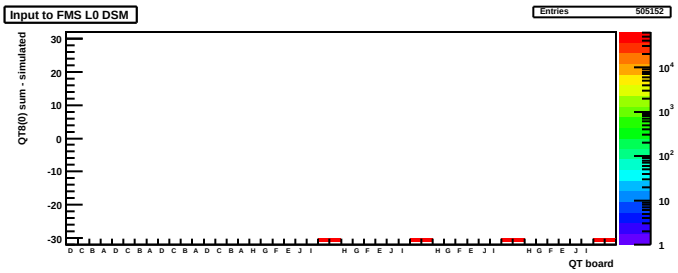

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

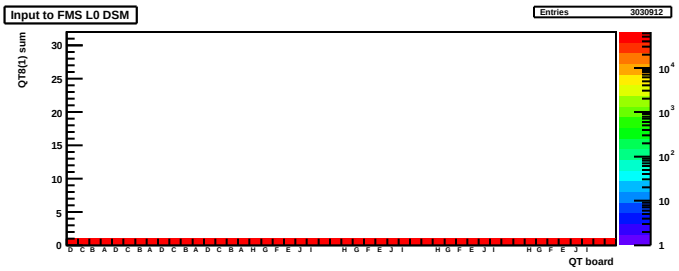

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

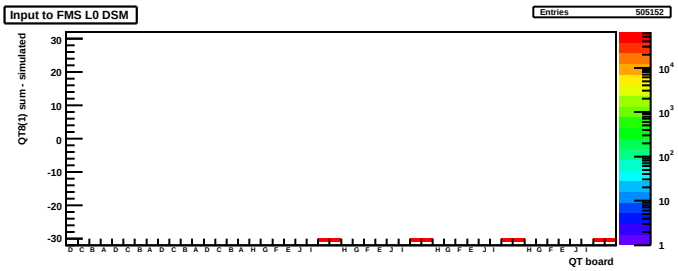

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

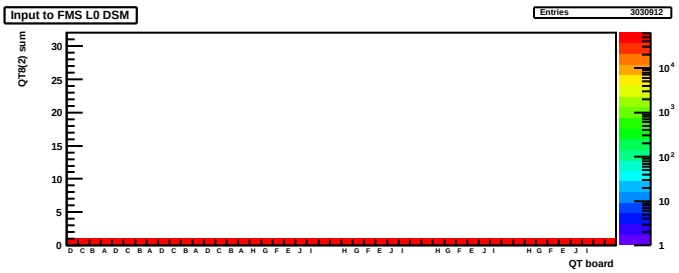

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

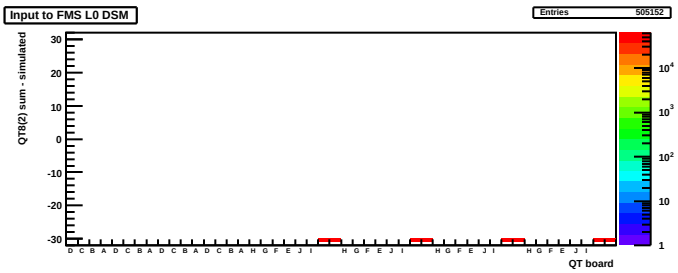

Input to FMS L0 DSM
Entries 3030912
QT8(0) sum
QT board

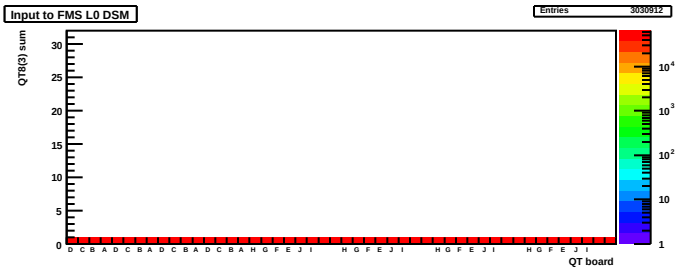

Input to FMS L0 DSM
Entries 505152
QT8(0) sum - simulated
QT board

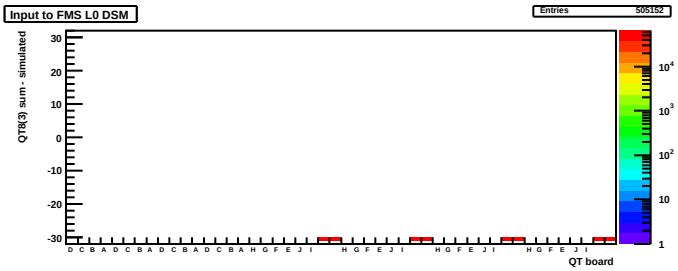

Input to FMS L0 DSM
Entries 3030912
QT8(1) sum
QT board

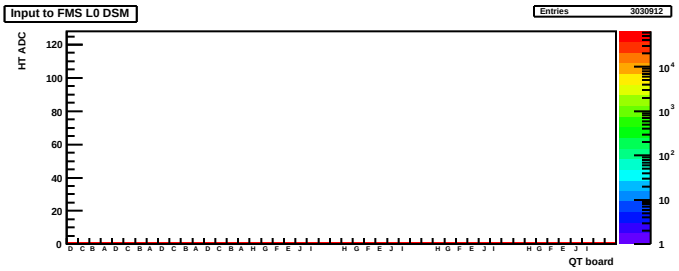

Input to FMS L0 DSM
Entries 505152
QT8(1) sum - simulated
QT board

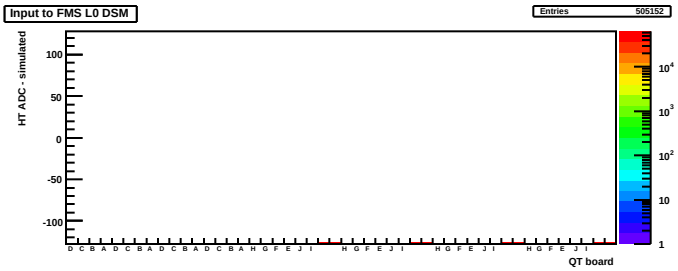

Input to FMS L0 DSM
Entries 3030912
QT8(2) sum
QT board

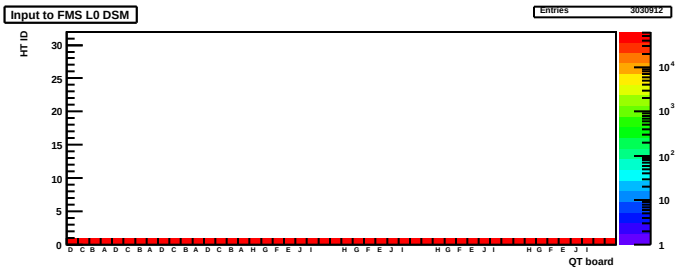

Input to FMS L0 DSM
Entries 505152
QT8(2) sum - simulated
QT board

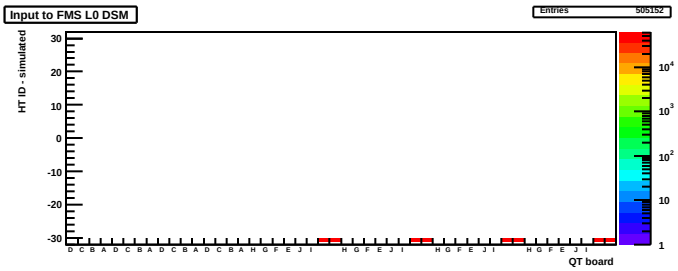

Input to FMS L0 DSM
Entries 3030912
QT8(3) sum
QT board

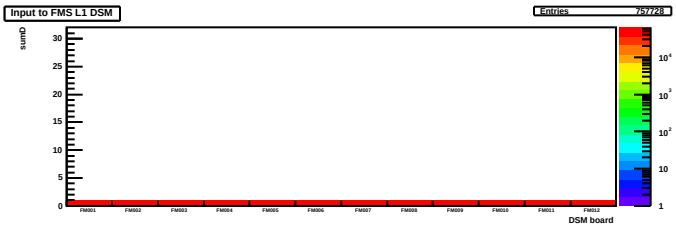

Input to FMS L0 DSM
Entries 505152
QT8(3) sum - simulated
QT board

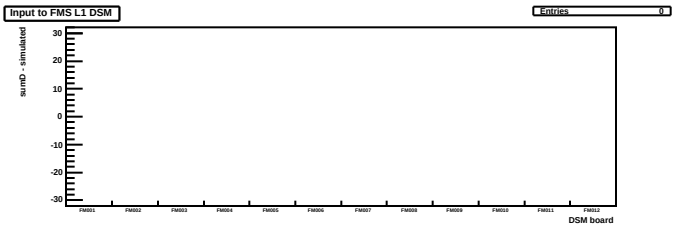

Input to FMS L0 DSM
Entries 3030912
HT ADC
QT board

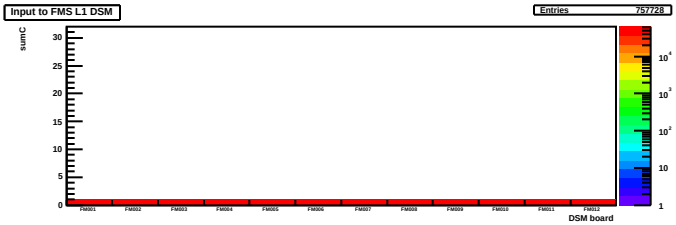

Input to FMS L0 DSM
Entries 505152
HT ADC - simulated
QT board

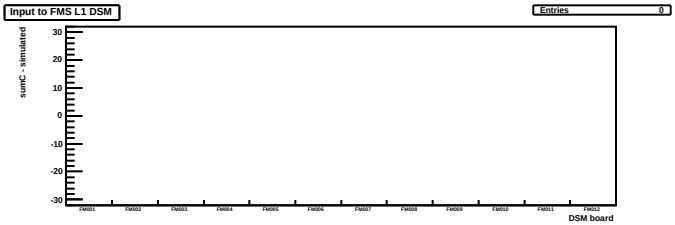

Input to FMS L0 DSM
Entries 3030912
HT ID
QT board

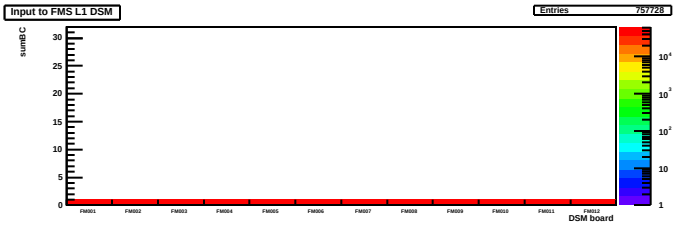

Input to FMS L0 DSM
Entries 505152
HT ID - simulated
QT board

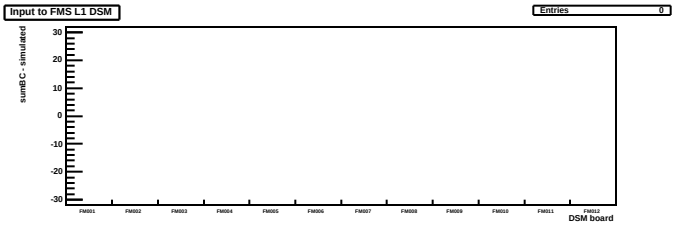

Input to FMS L1 DSM
Entries 757728
sumD
DSM board

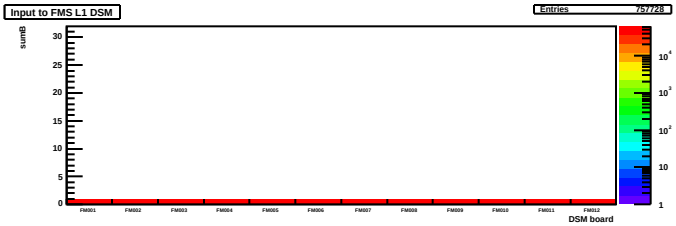

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

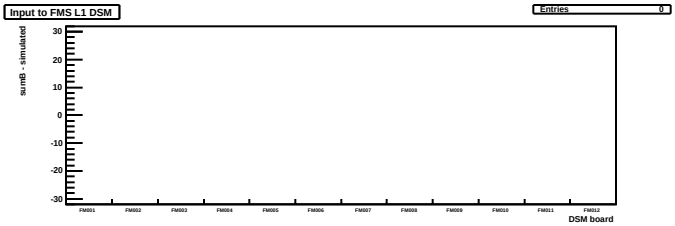

Input to FMS L1 DSM
Entries 757728
sumC
DSM board

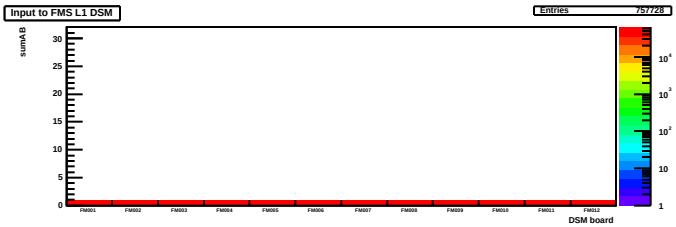

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

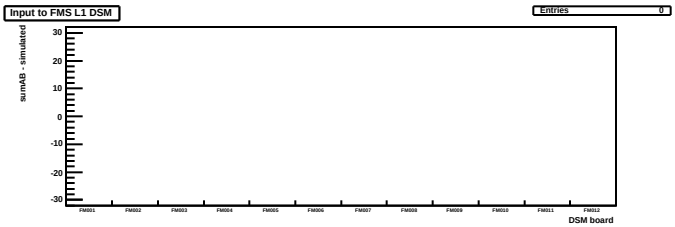

Input to FMS L1 DSM
Entries 757728
sumBC
DSM board

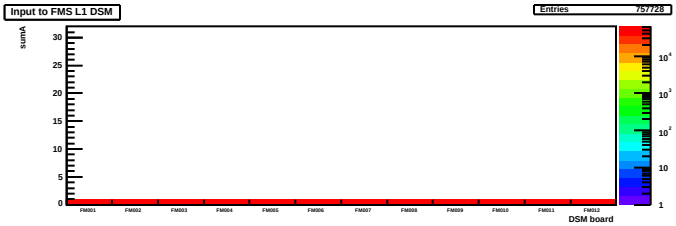

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

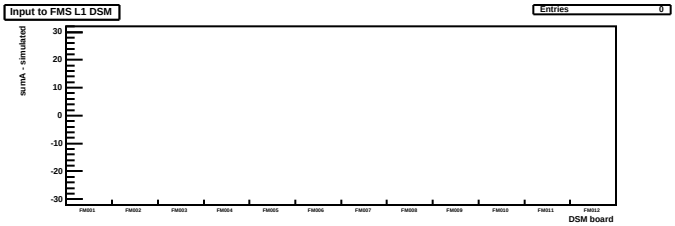

Input to FMS L1 DSM
Entries 757728
sumB
DSM board

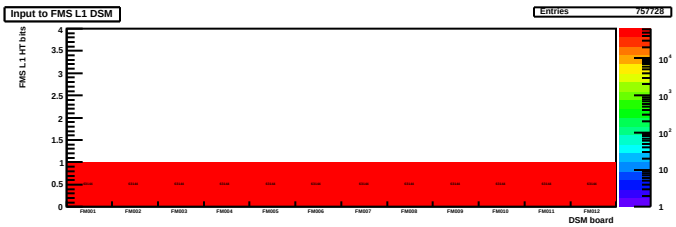

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

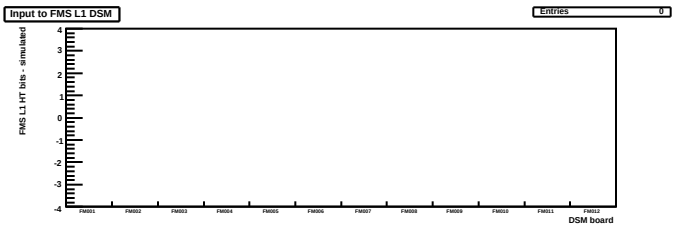

Input to FMS L1 DSM
Entries 757728
sumAB
DSM board

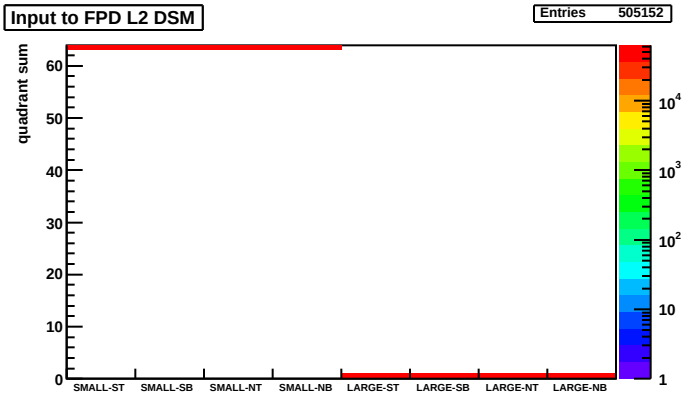

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

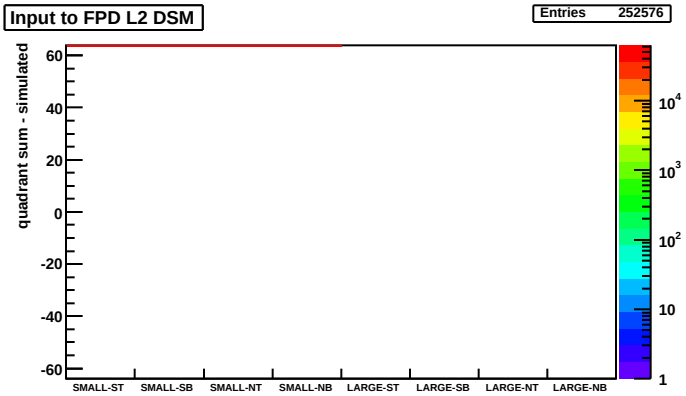

Input to FMS L1 DSM
Entries 757728
sumA
DSM board

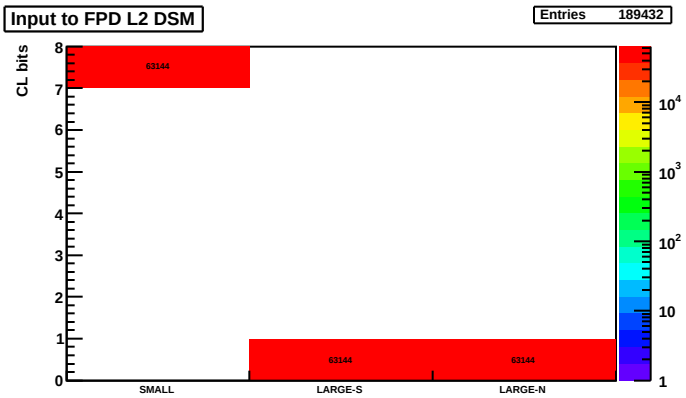

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

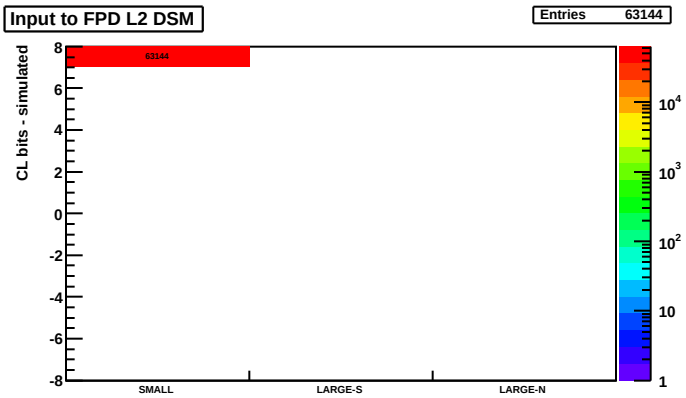

Input to FMS L1 DSM
Entries 757728
FMS L1 HT bits
DSM board

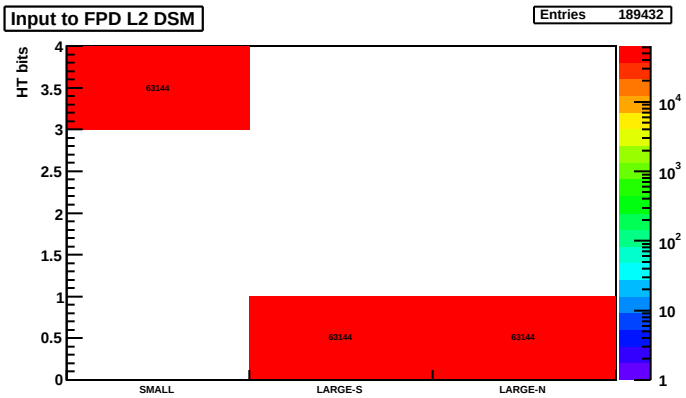

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

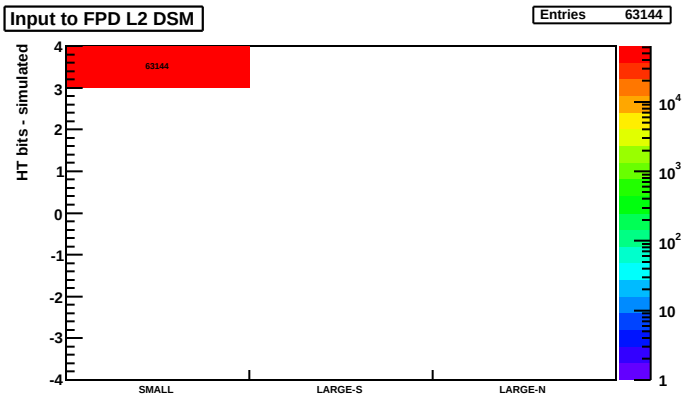

Input to FPD L2 DSM
Entries 505152
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

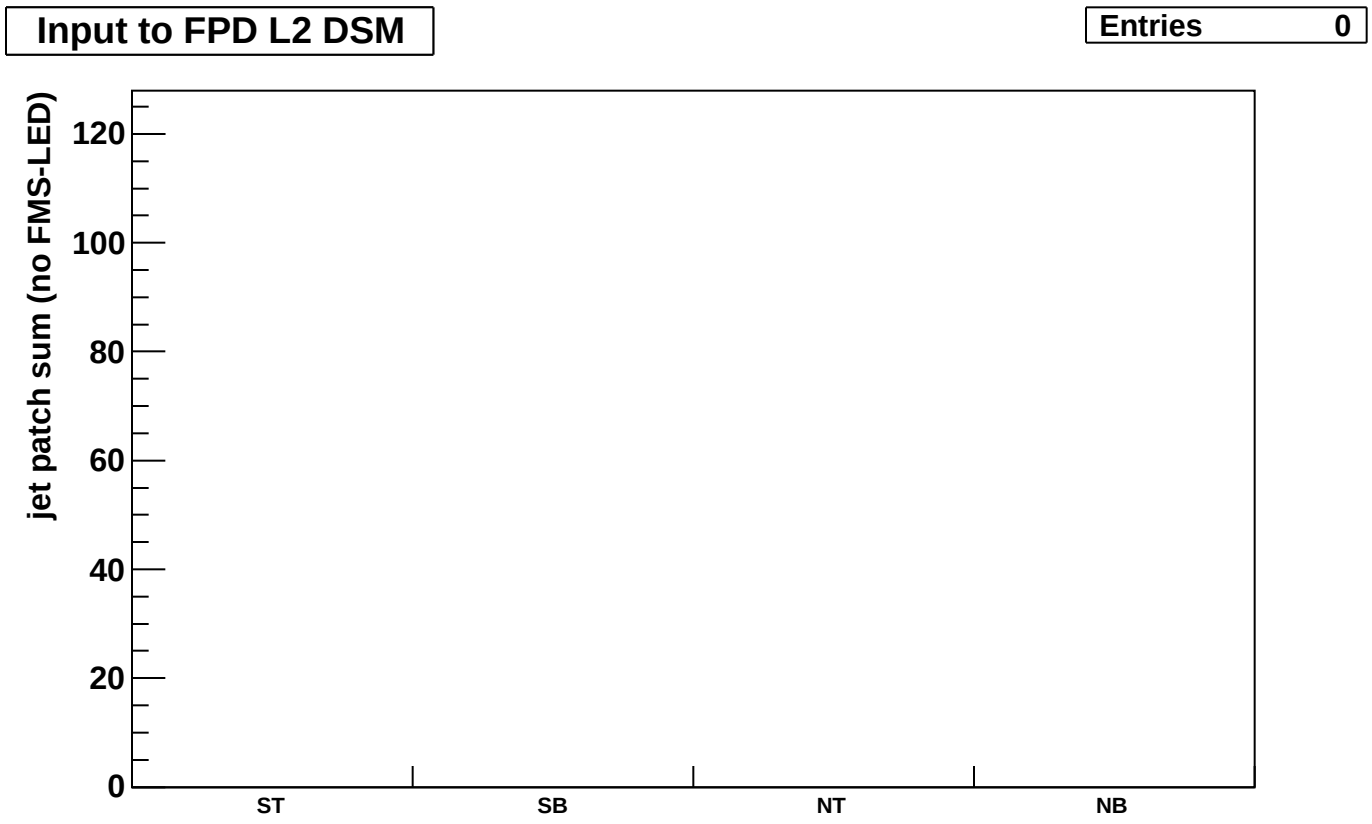

Input to FPD L2 DSM
Entries 252576
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

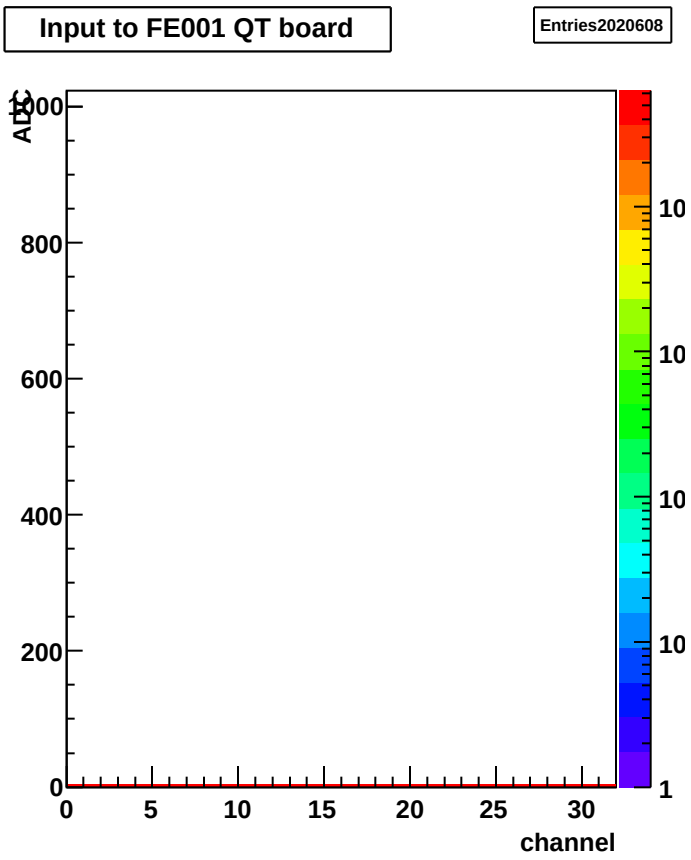

Input to FPD L2 DSM
Entries 189432
CL bits
63144
63144
63144
SMALL
LARGE-S
LARGE-N

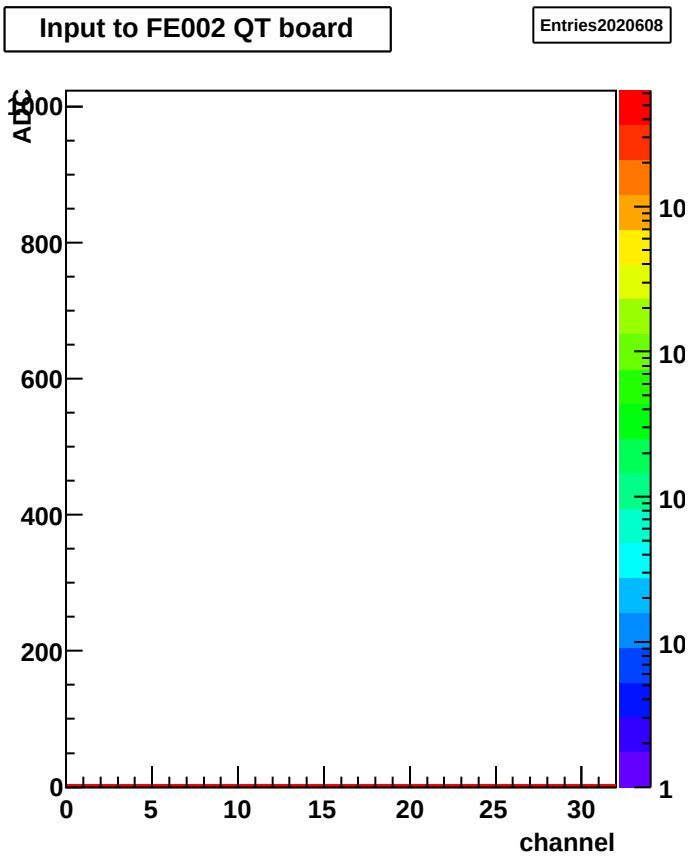

Input to FPD L2 DSM
Entries 63144
CL bits - simulated
63144
SMALL
LARGE-S
LARGE-N

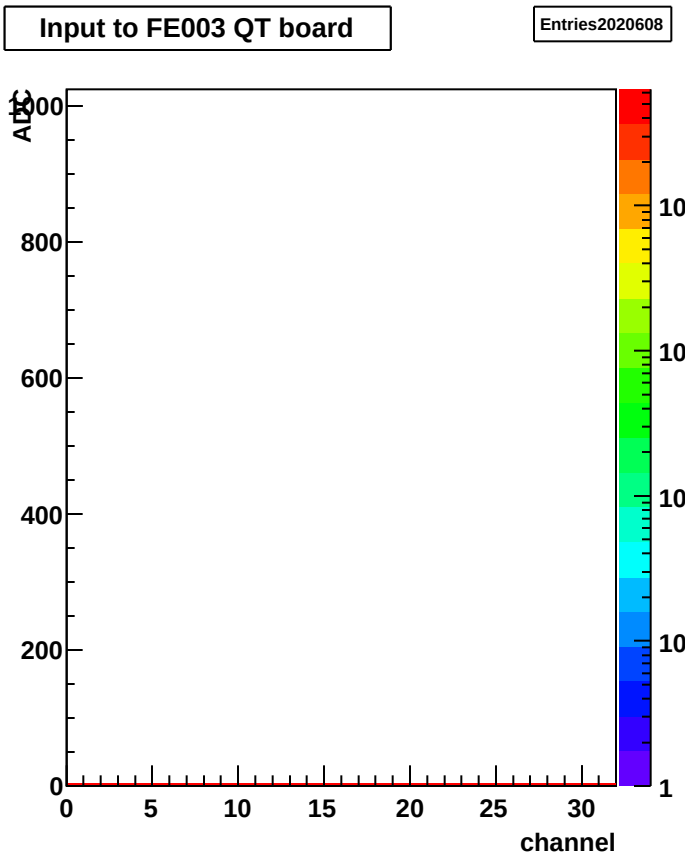

Input to FPD L2 DSM
Entries 189432
HT bits
63144
63144
63144
SMALL
LARGE-S
LARGE-N

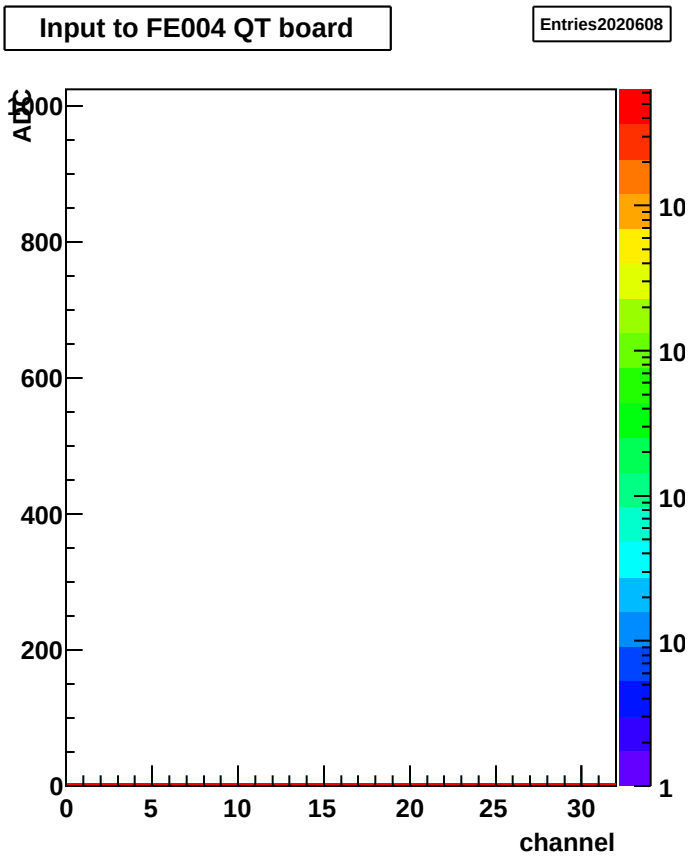

Input to FPD L2 DSM
Entries 63144
HT bits - simulated
63144
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 0
jet patch sum (no FMS-LED)
120
100
80
60
40
20
0
ST
SB
NT
NB

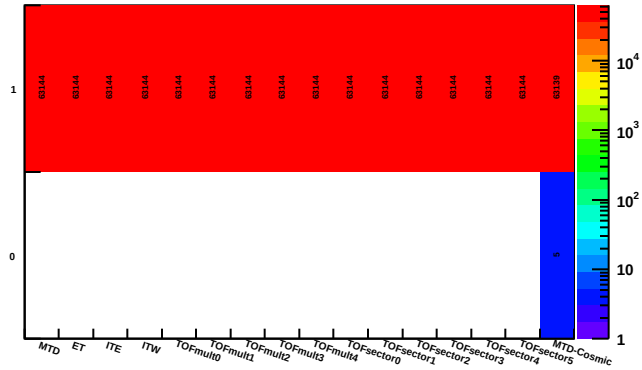

Input to FE001 QT board
Entries2020608
ADC
channel


Input to FE002 QT board
Entries2020608
ADC
channel

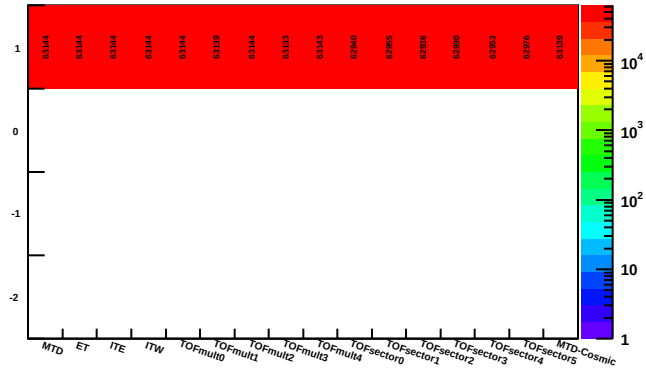

Input to FE003 QT board
Entries2020608
ADC
channel


Input to FE004 QT board
Entries2020608
ADC
channel

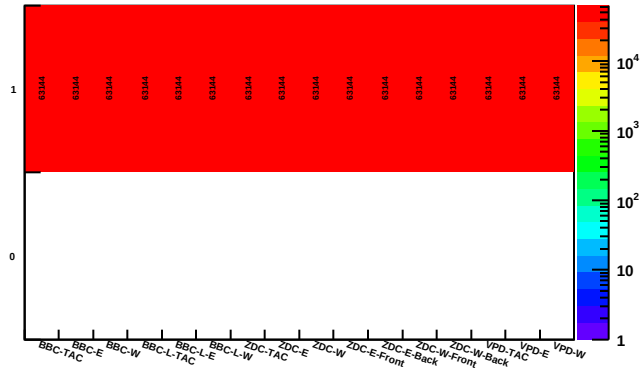
TF201 0-15 (ch0)



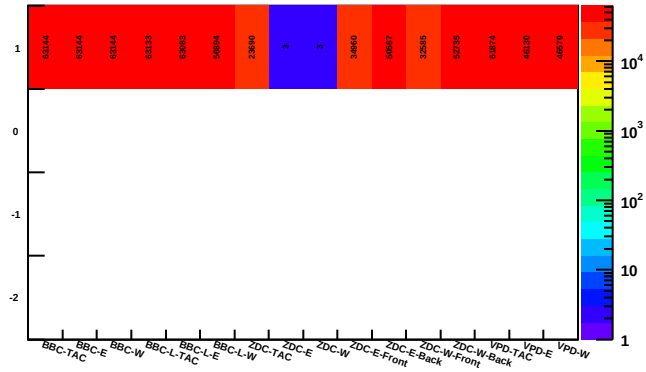
TF201 0-15 (ch0)



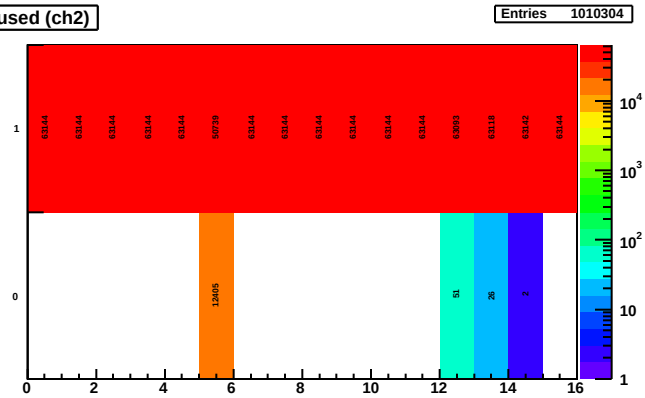
VT201 0-15 (ch1)

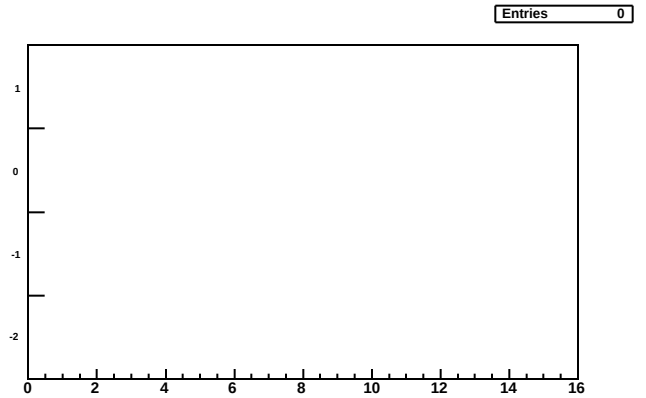


VT201 0-15 (ch1)

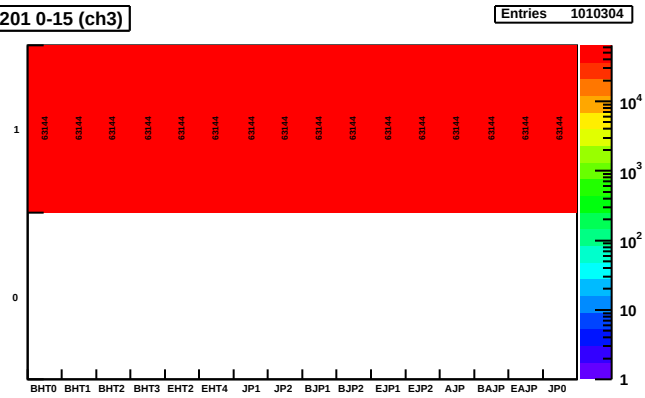


Unused (ch2)

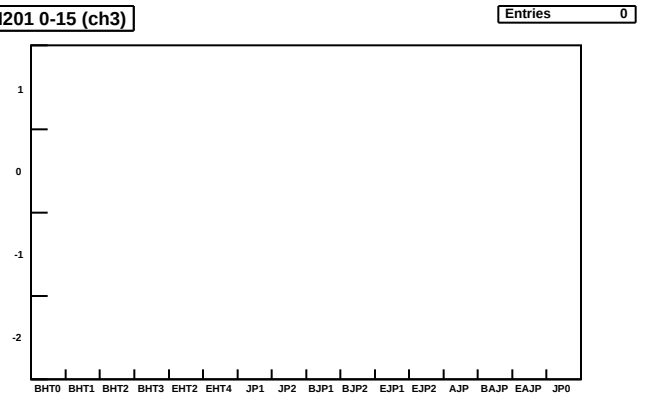




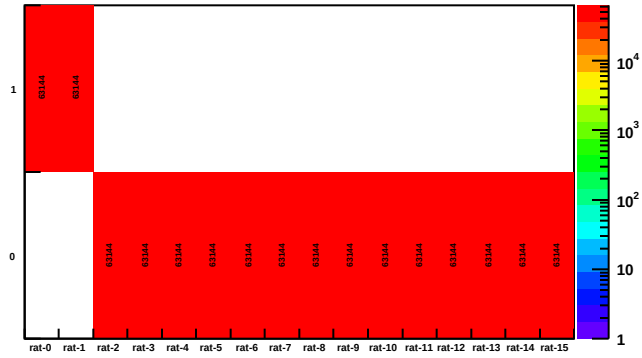
EM201 0-15 (ch3)

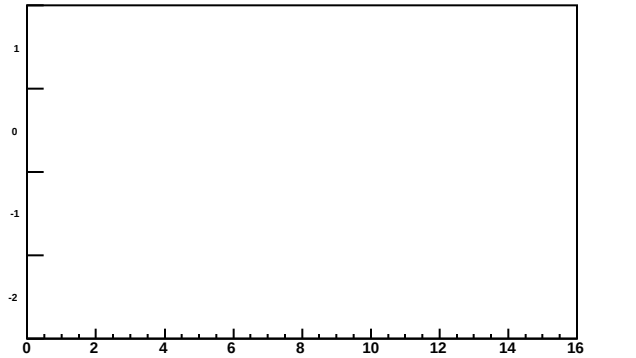


EM201 0-15 (ch3)

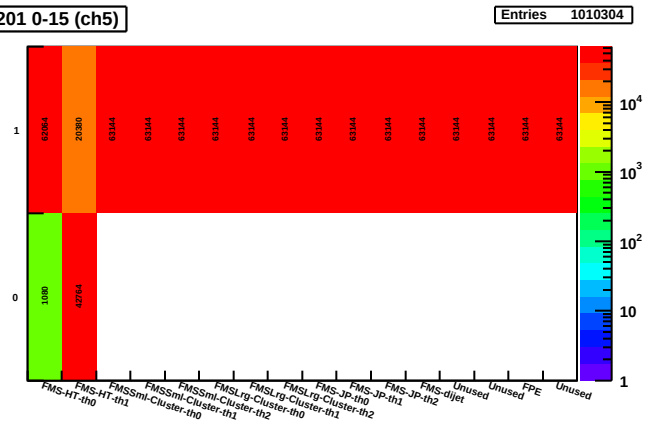


RAT board (ch4)

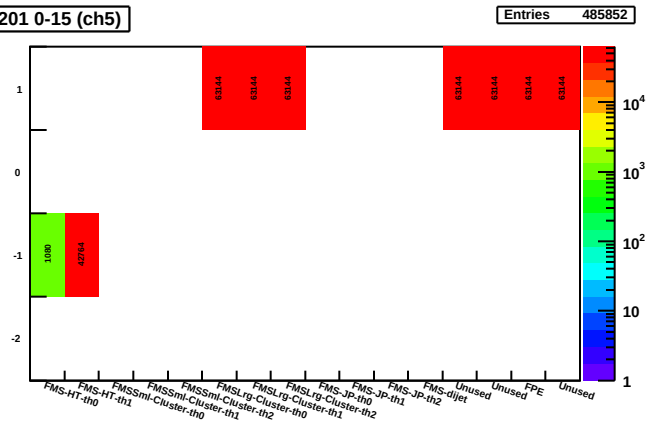




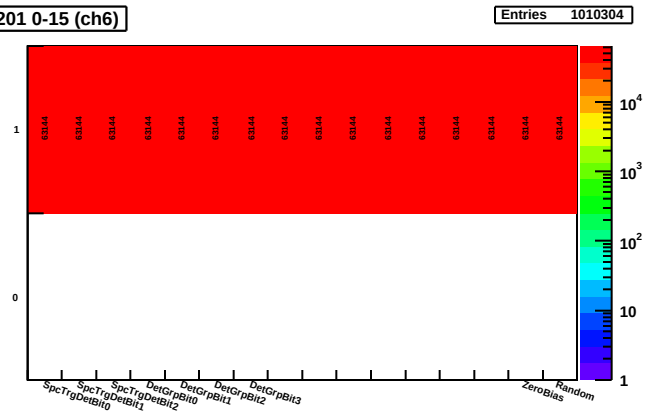
FP201 0-15 (ch5)

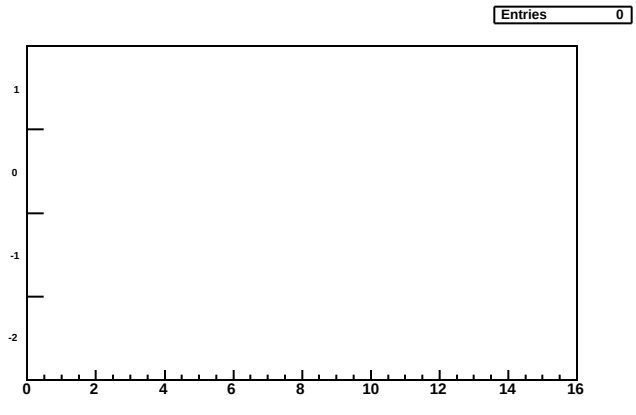
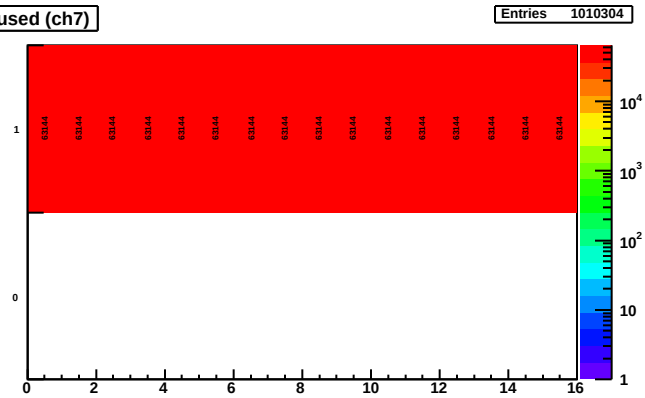
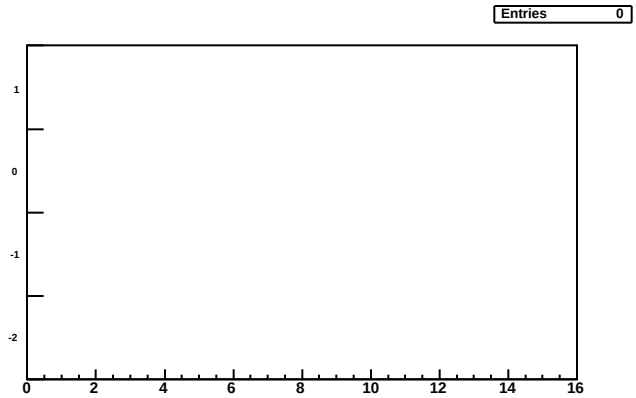


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)