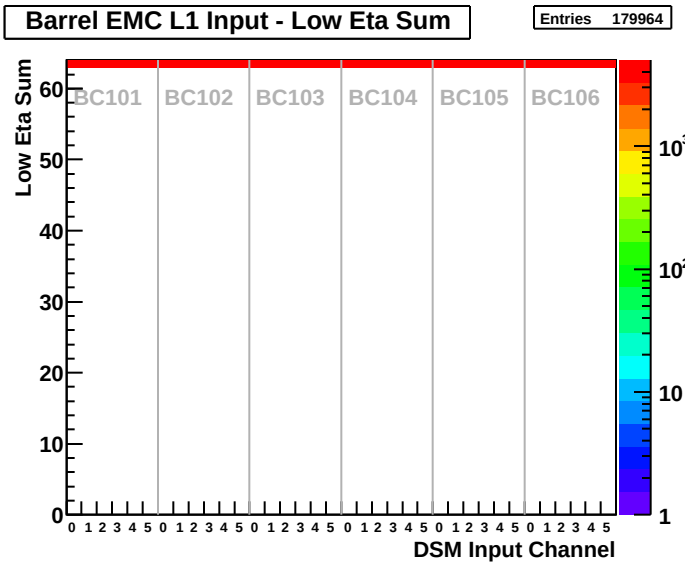
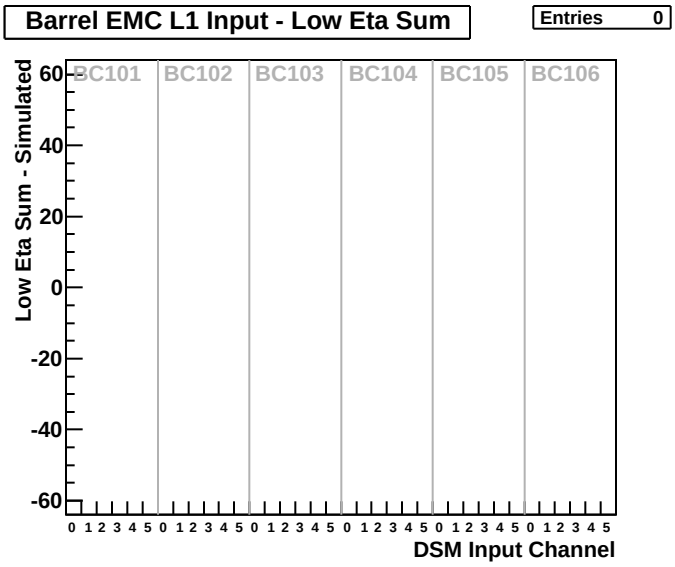
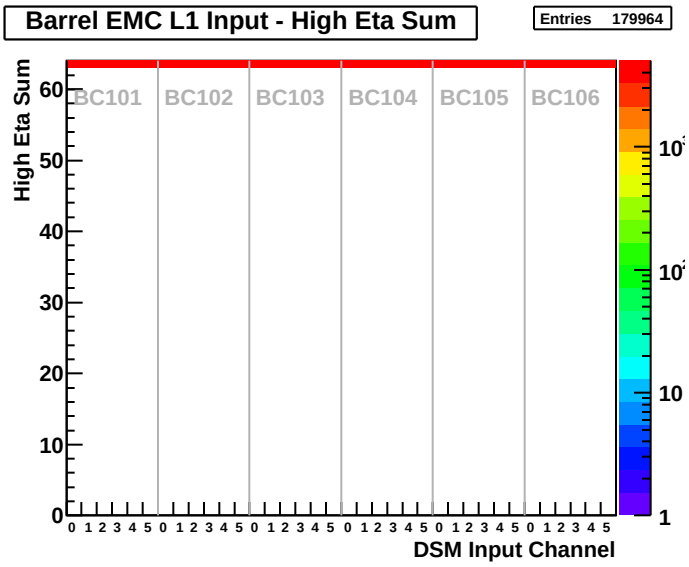
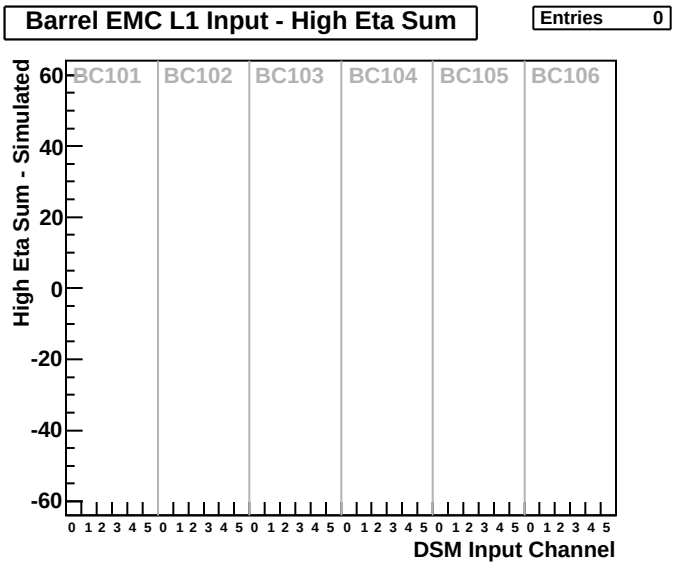
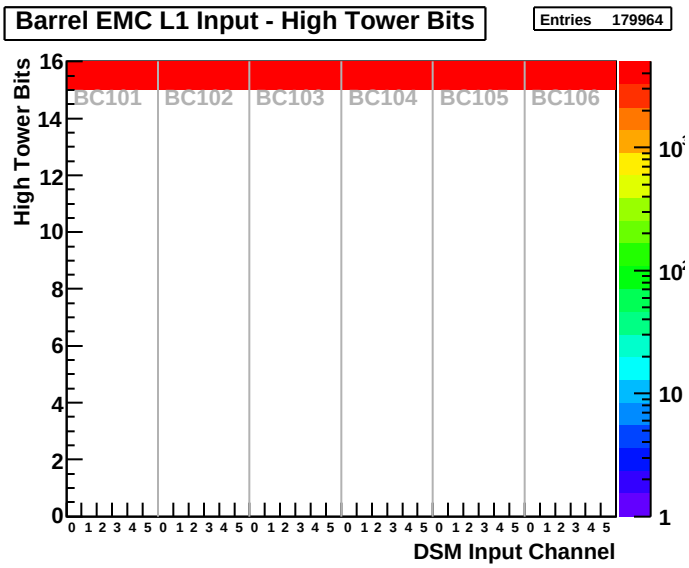
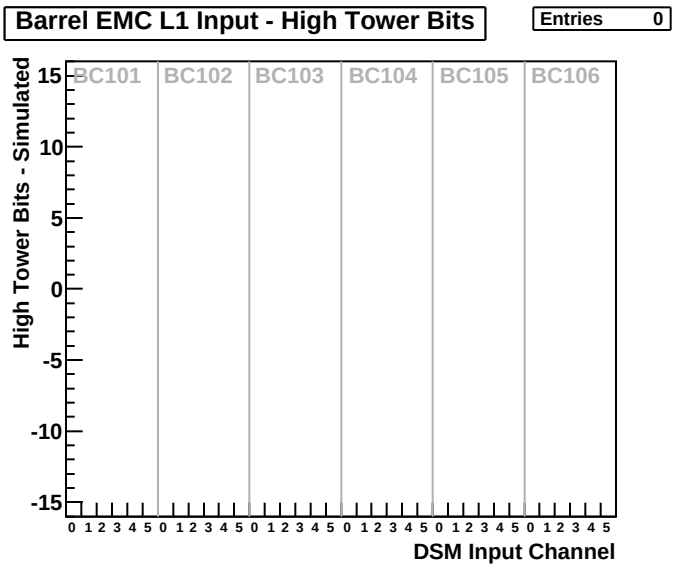

Barrel EMC L1 Input - Low Eta Sum
Entries 179964
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

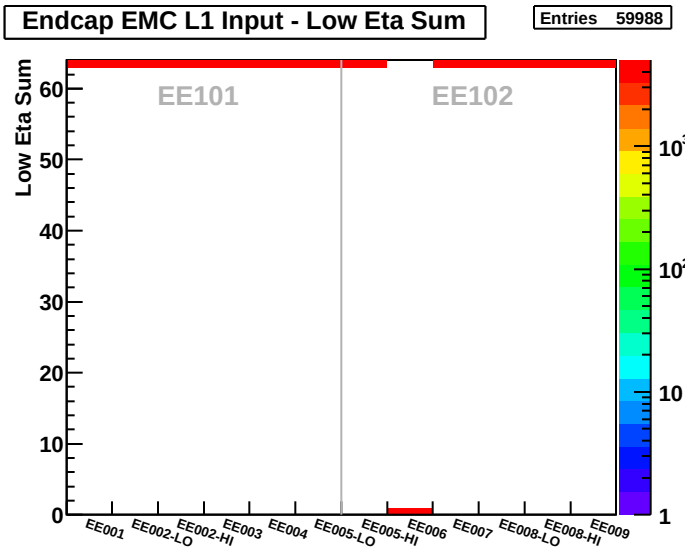

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

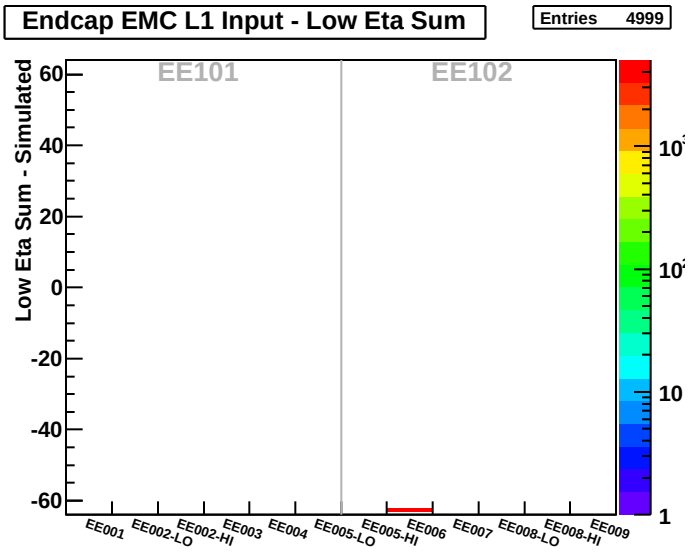

Barrel EMC L1 Input - High Eta Sum
Entries 179964
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

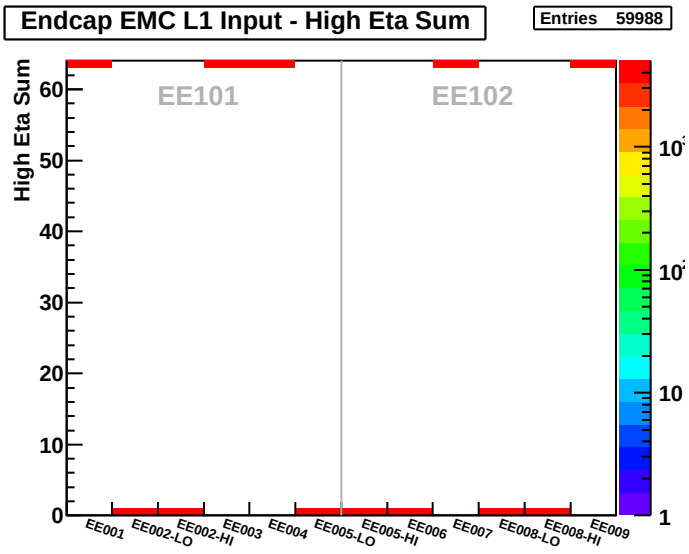

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

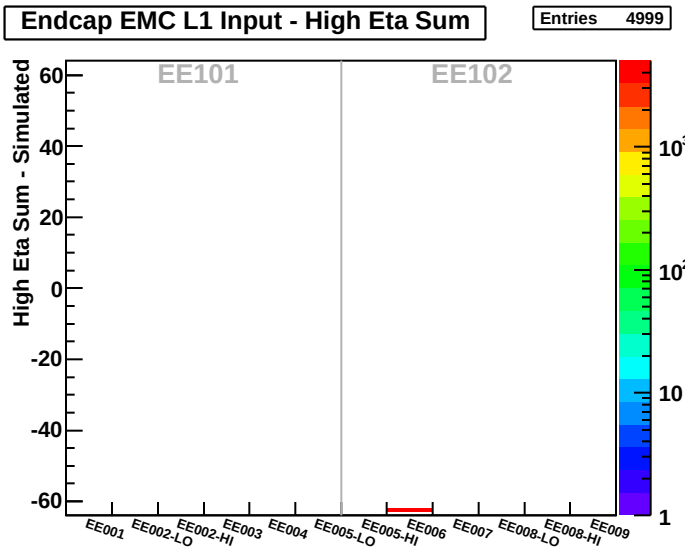

Barrel EMC L1 Input - High Tower Bits
Entries 179964
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

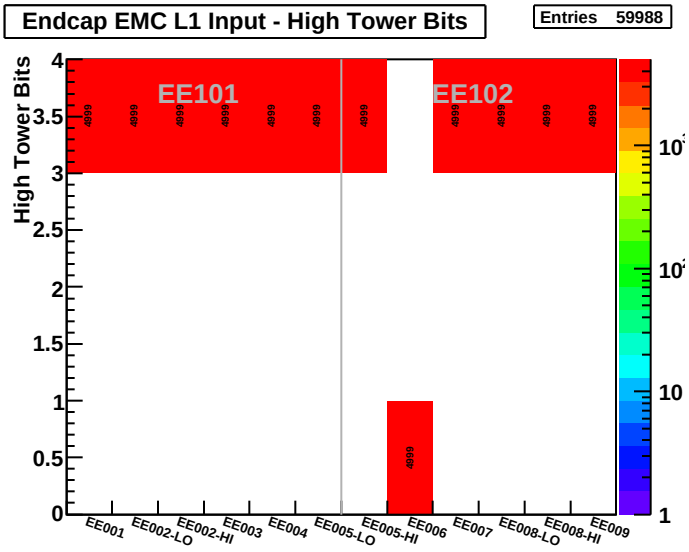

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

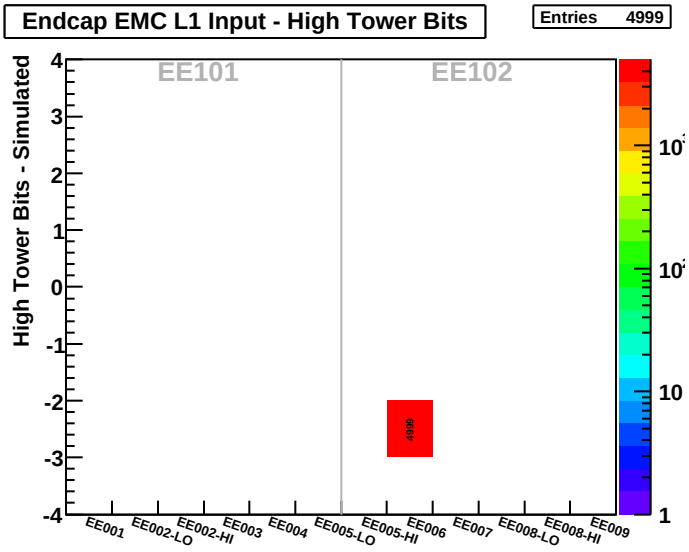

Endcap EMC L1 Input - Low Eta Sum
Entries 59988
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

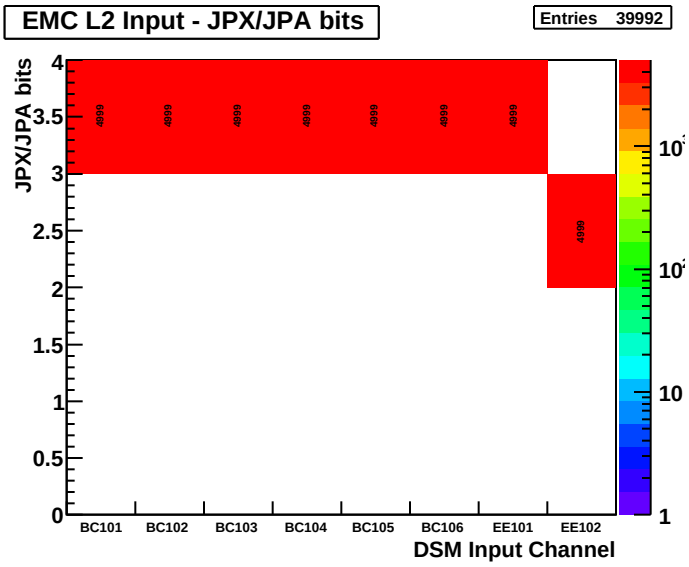

Endcap EMC L1 Input - Low Eta Sum
Entries 4999
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

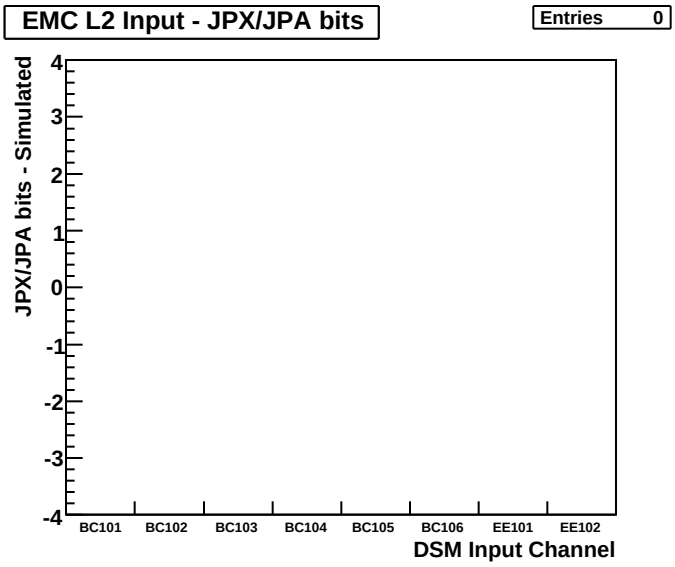

Endcap EMC L1 Input - High Eta Sum
Entries 59988
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

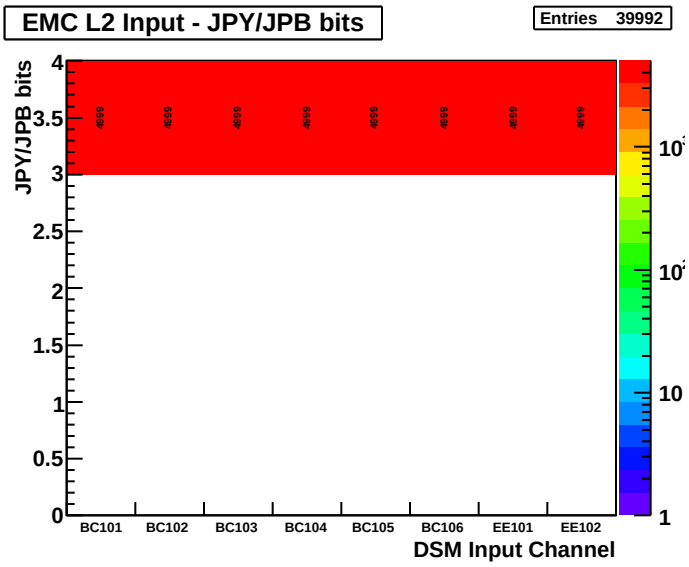

Endcap EMC L1 Input - High Eta Sum
Entries 4999
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

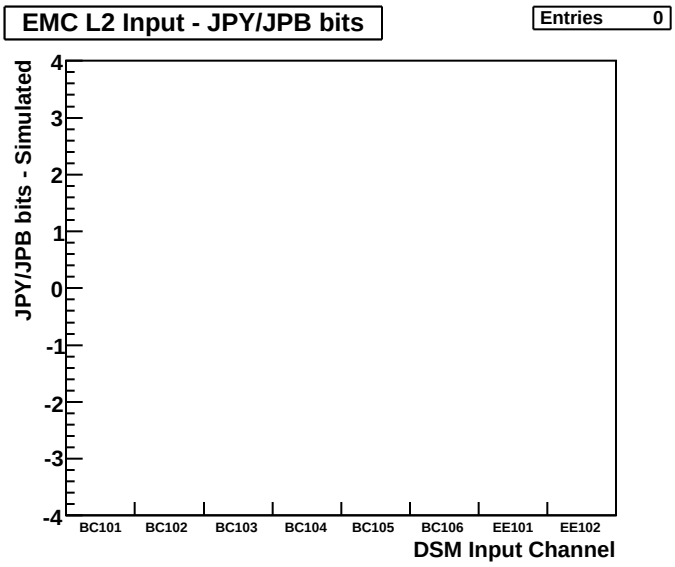

Endcap EMC L1 Input - High Tower Bits
Entries 59988
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

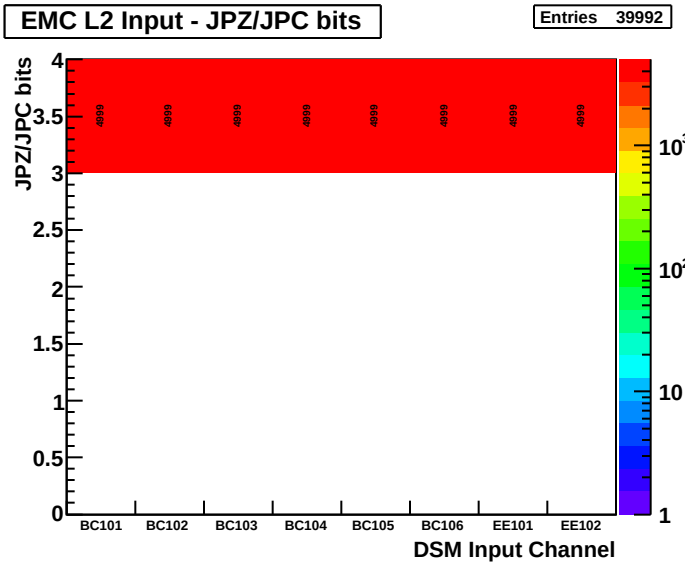

Endcap EMC L1 Input - High Tower Bits
Entries 4999
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

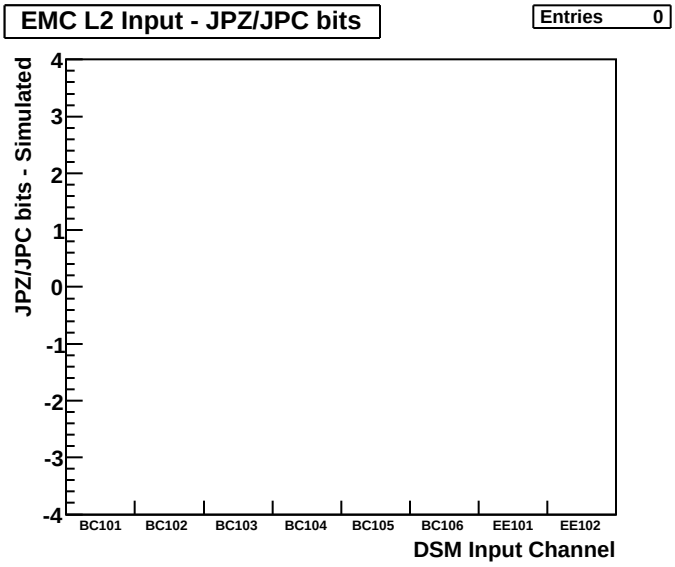

EMC L2 Input - JPX/JPA bits
Entries 39992
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4999

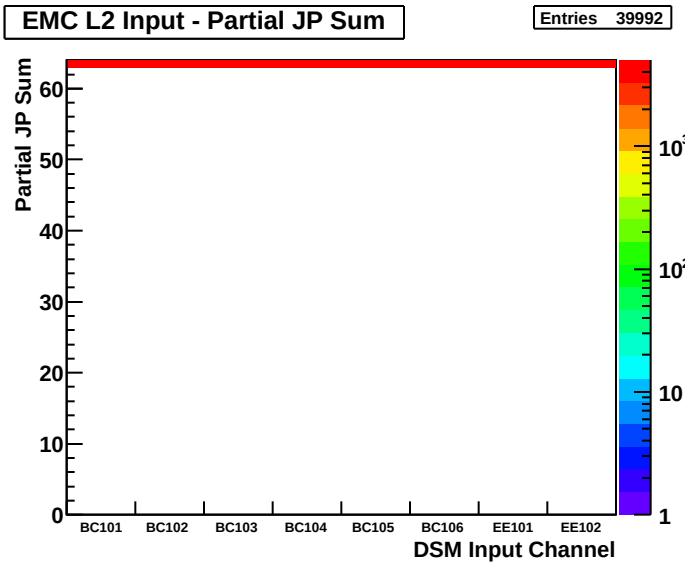

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

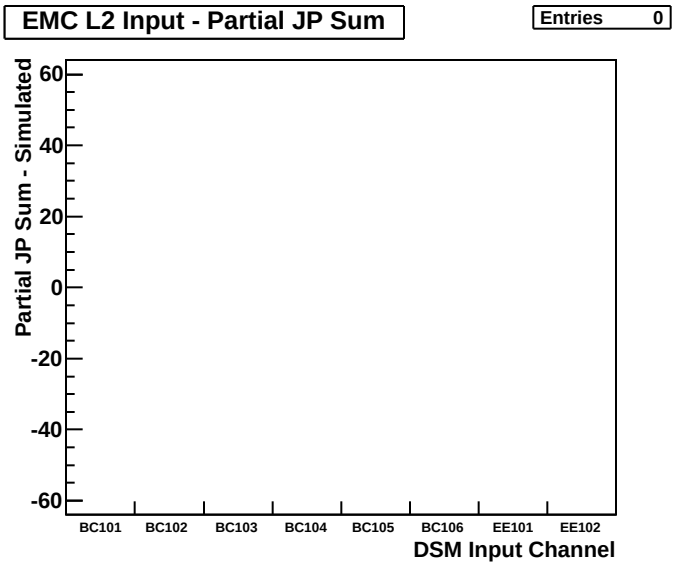

EMC L2 Input - JPY/JPB bits
Entries 39992
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4999

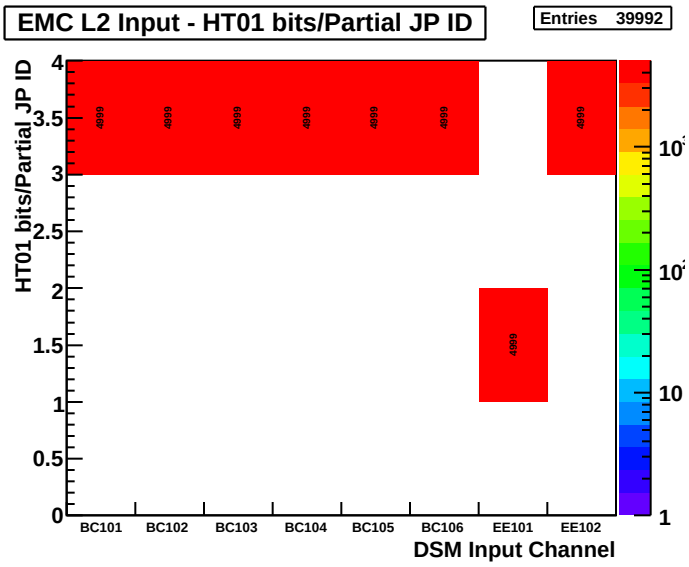

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

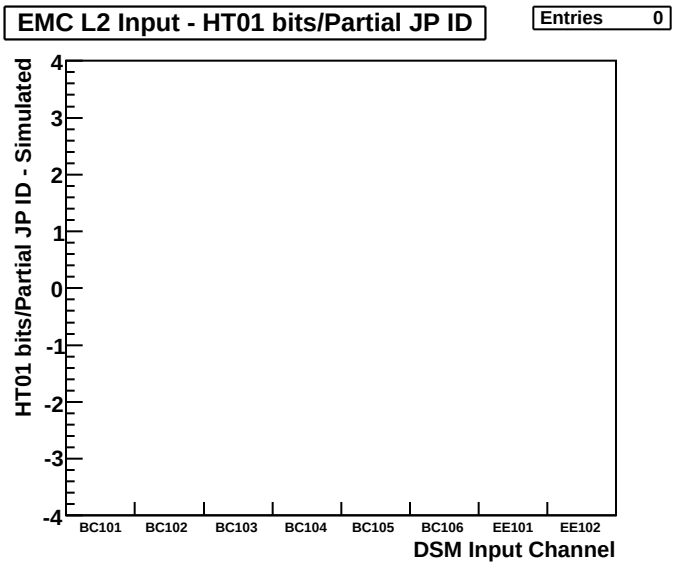

EMC L2 Input - JPZ/JPC bits
Entries 39992
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4999

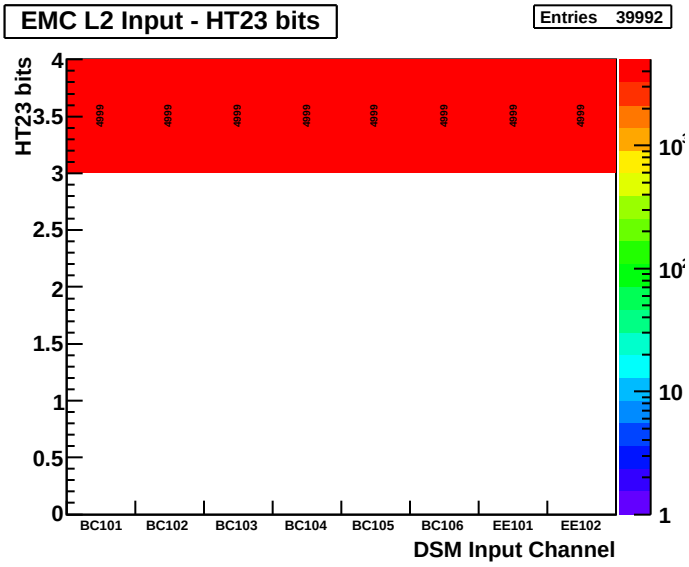

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

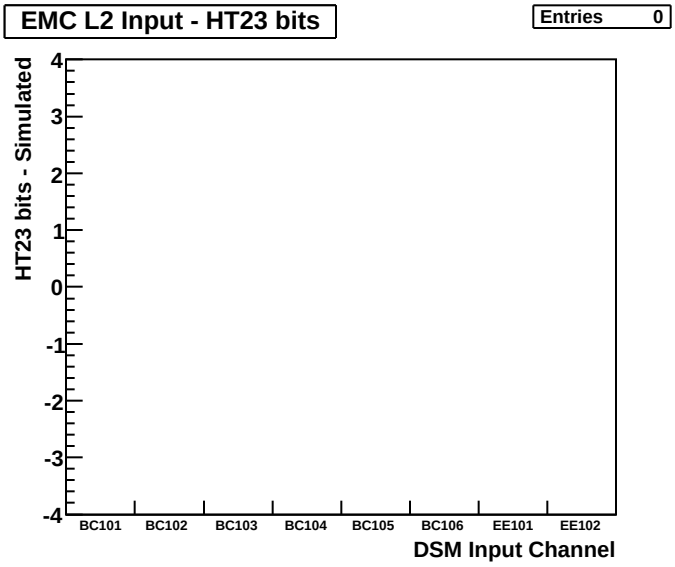

EMC L2 Input - Partial JP Sum
Entries 39992
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

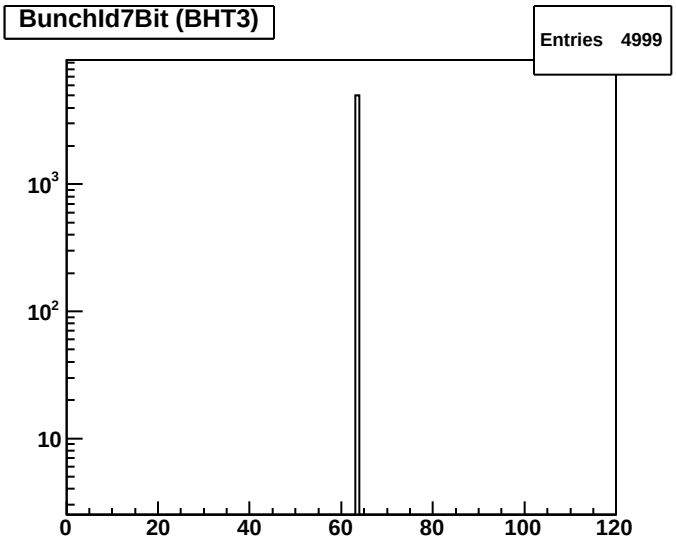

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

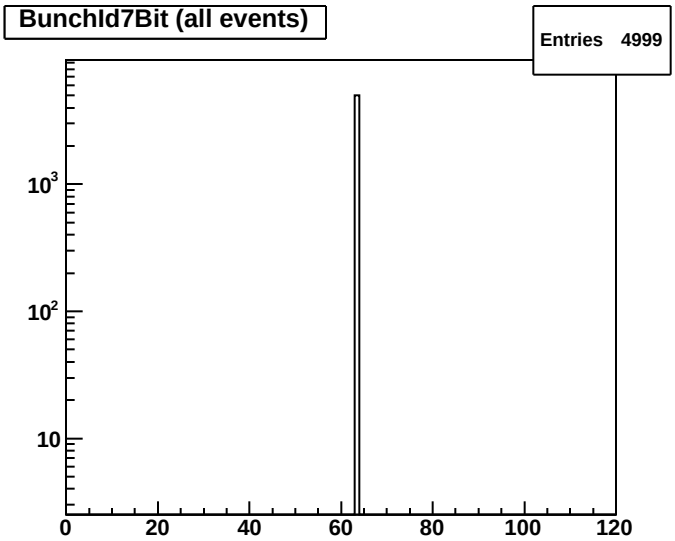

EMC L2 Input - HT01 bits/Partial JP ID
Entries 39992
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

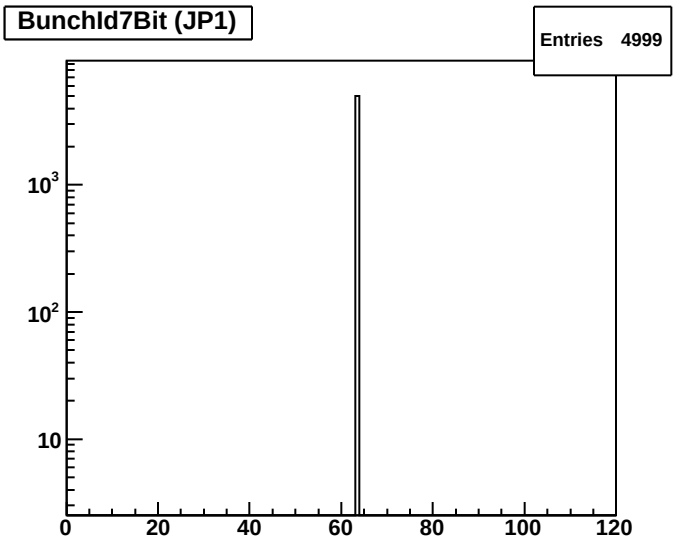

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

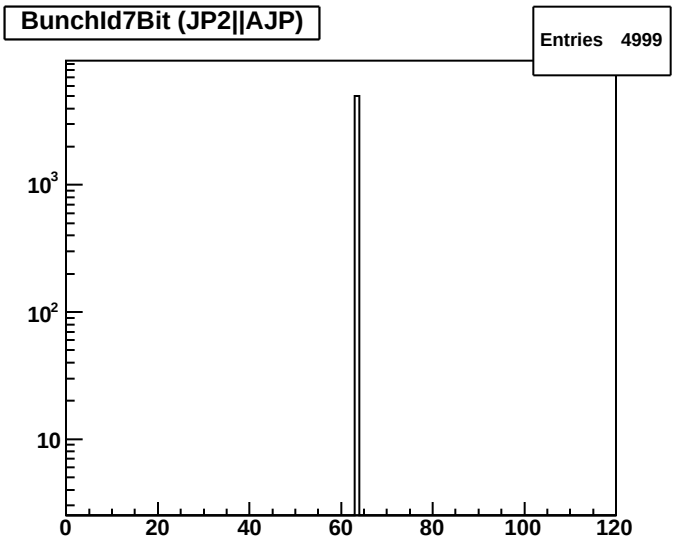

EMC L2 Input - HT23 bits
Entries 39992
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

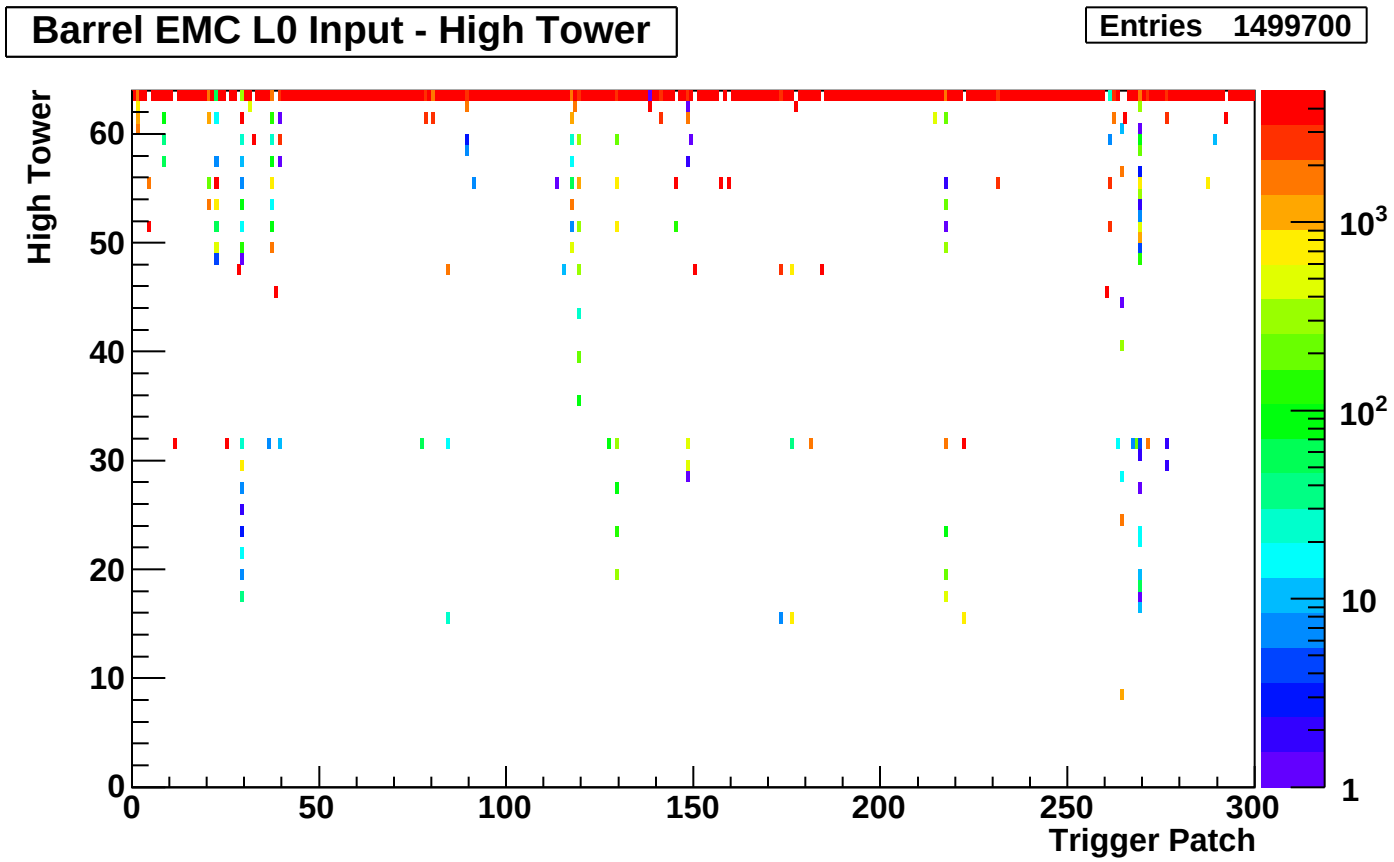

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

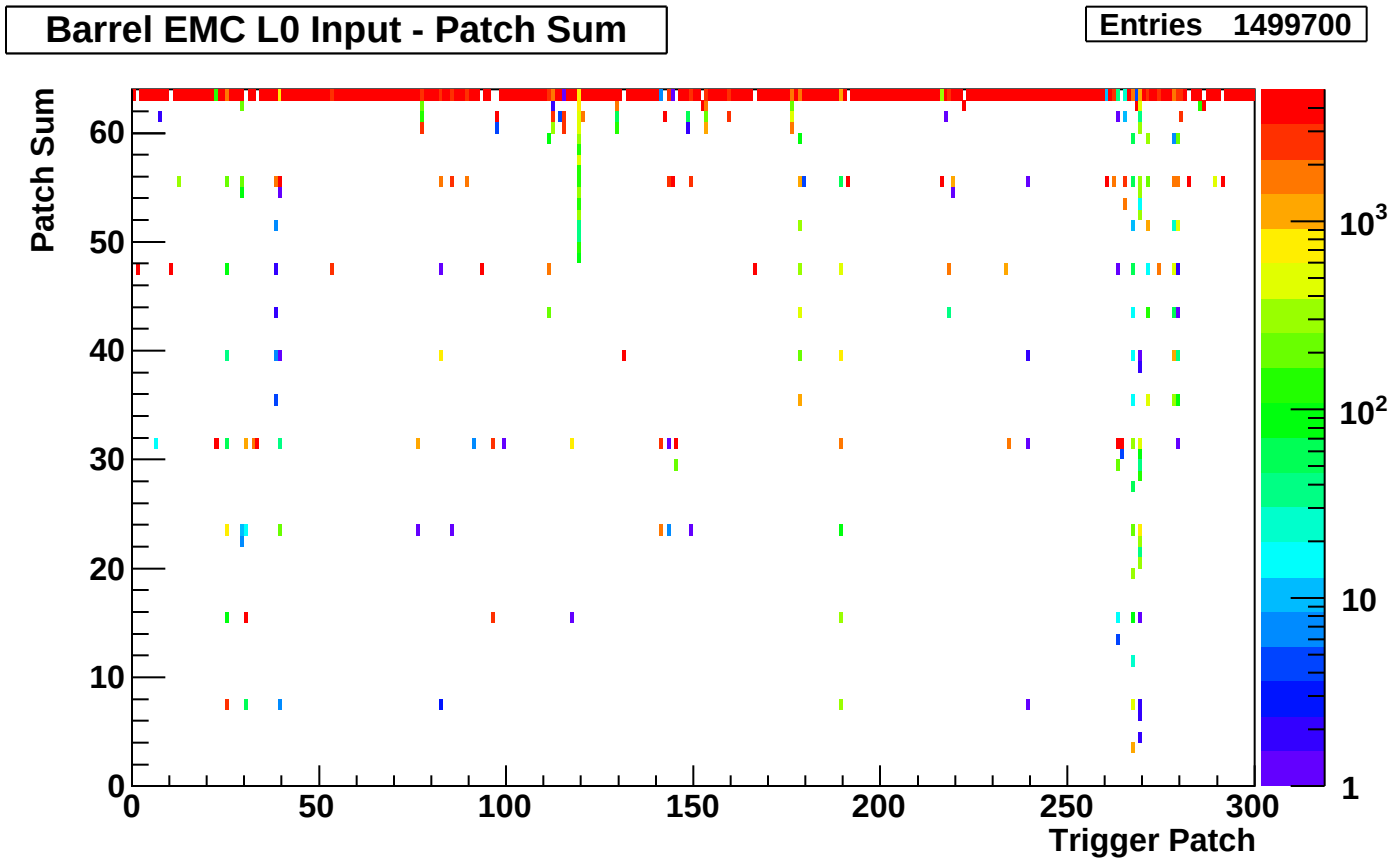

BunchId7Bit (BHT3)
Entries 4999
10^3
10^2
10
0
20
40
60
80
100
120

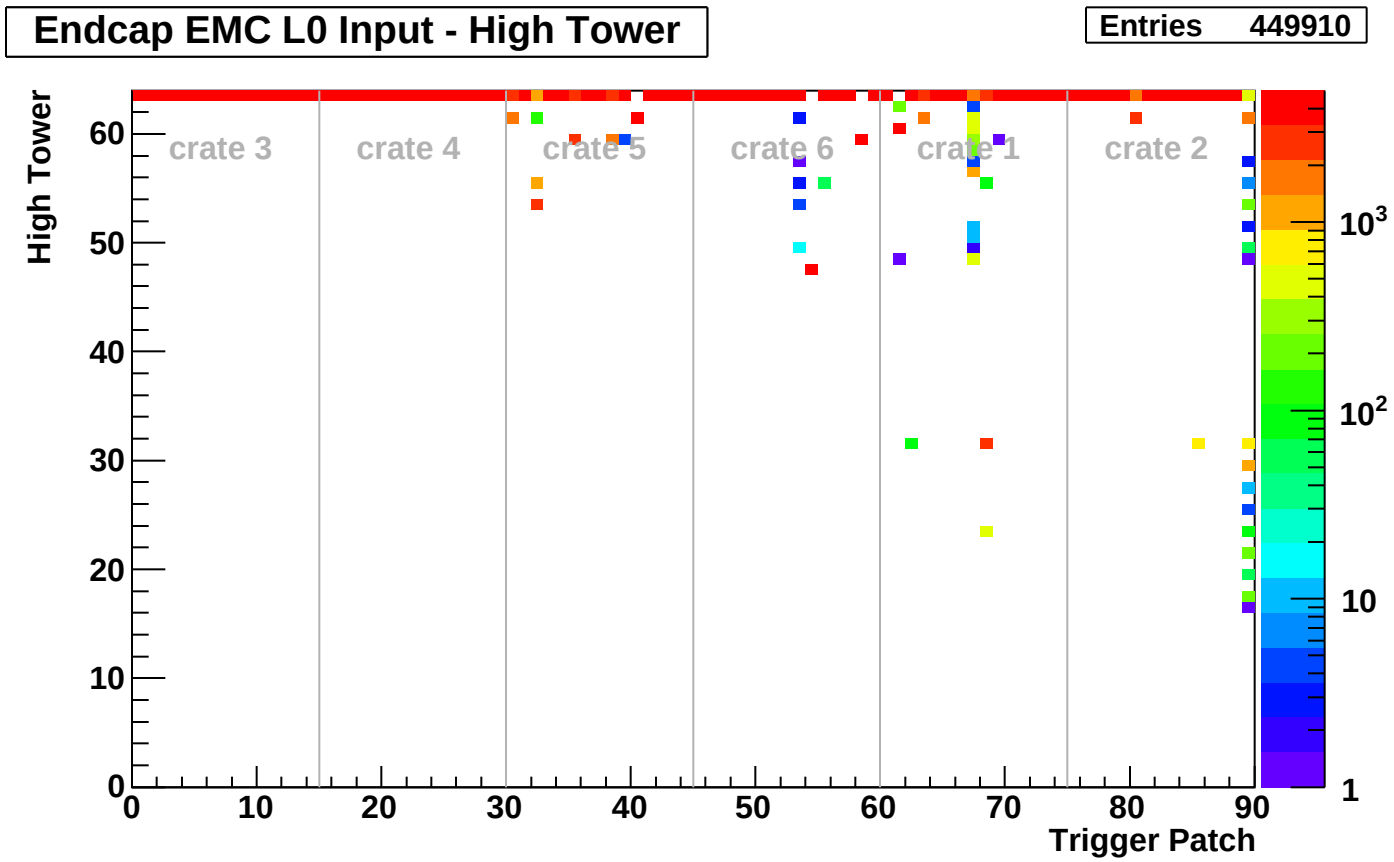

BunchId7Bit (all events)
Entries 4999
10^3
10^2
10
0
20
40
60
80
100
120

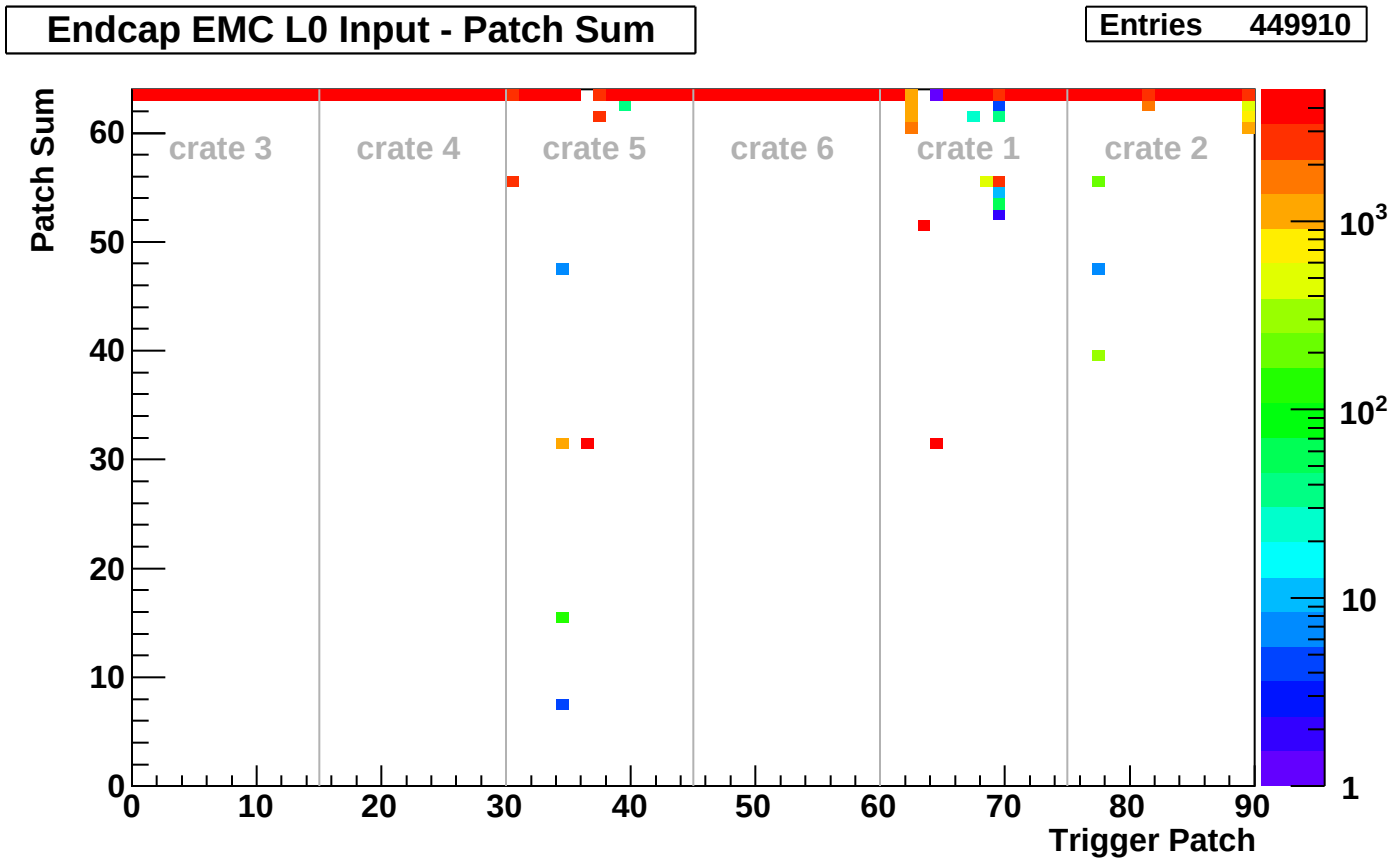

BunchId7Bit (JP1)
Entries 4999
10^3
10^2
10
0
20
40
60
80
100
120

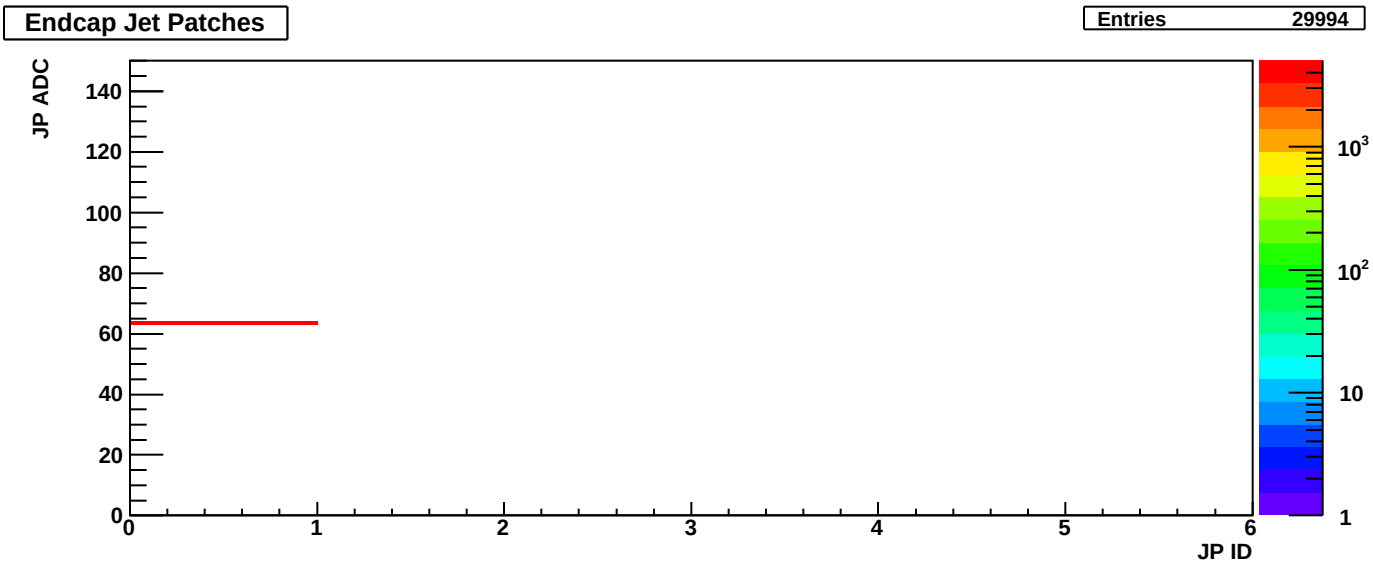

BunchId7Bit (JP2||AJP)
Entries 4999
10^3
10^2
10
0
20
40
60
80
100
120

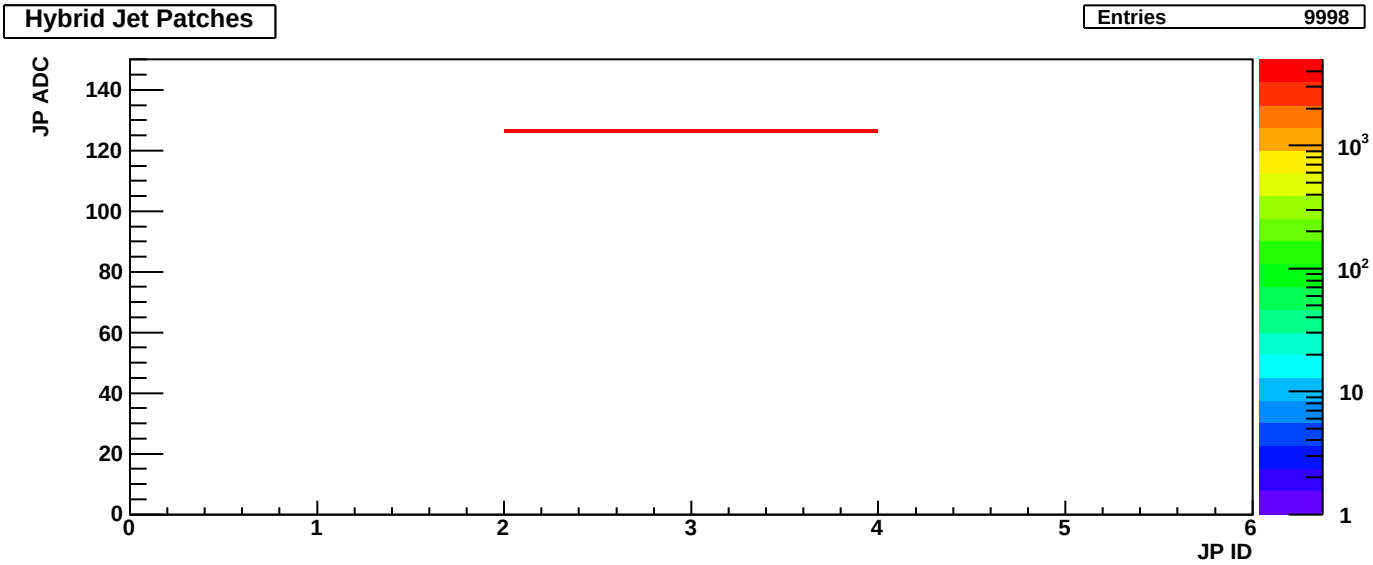

Barrel EMC L0 Input - High Tower
Entries 1499700
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

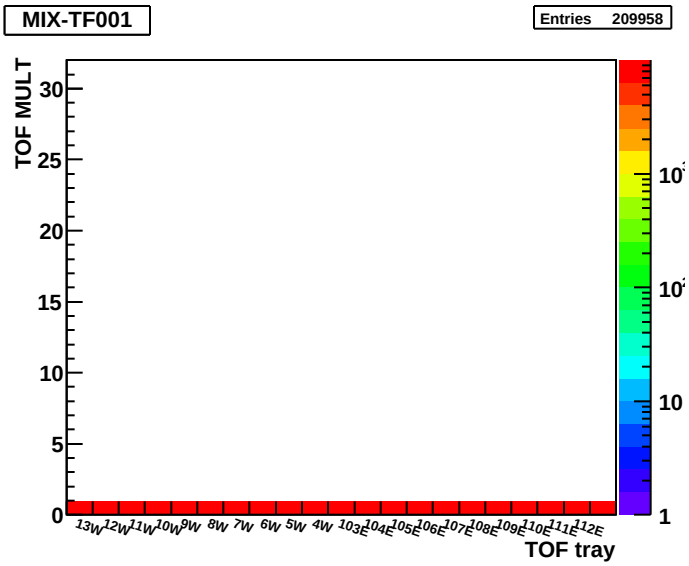

Barrel EMC L0 Input - Patch Sum
Entries 1499700
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

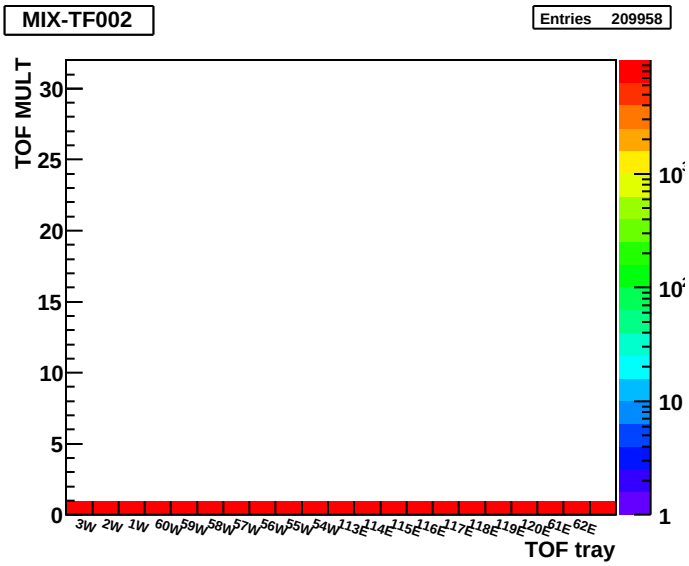

Endcap EMC L0 Input - High Tower
Entries 449910
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

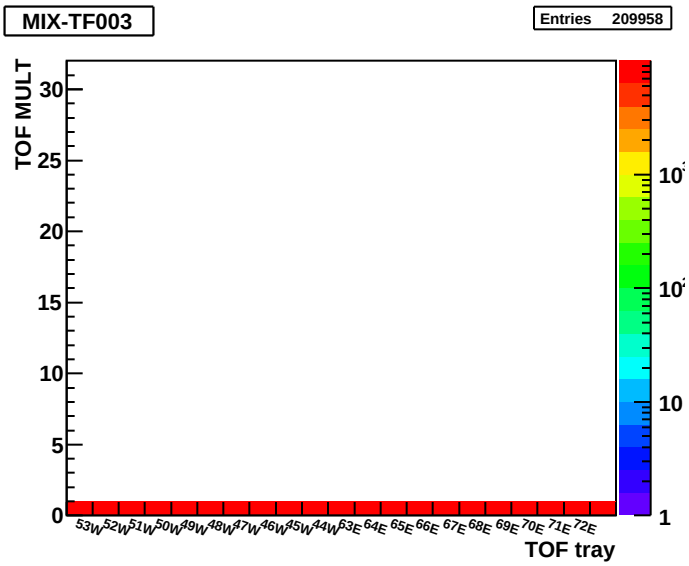

Endcap EMC L0 Input - Patch Sum
Entries 449910
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

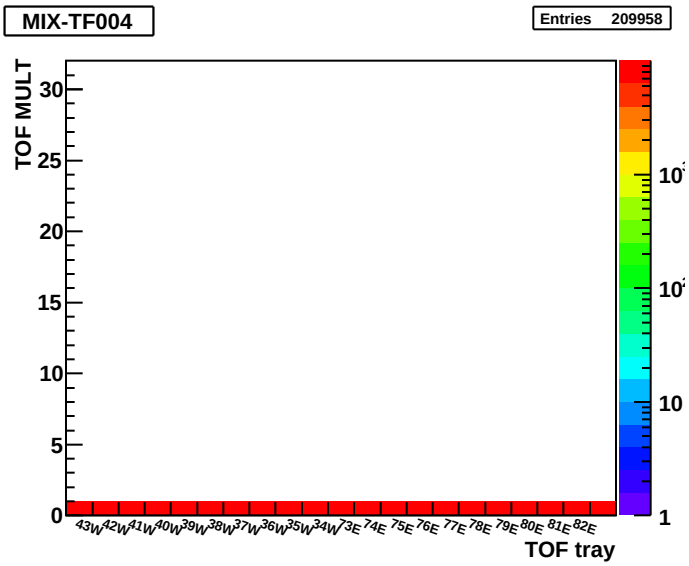

Endcap Jet Patches
Entries 29994
JP ADC
JP ID

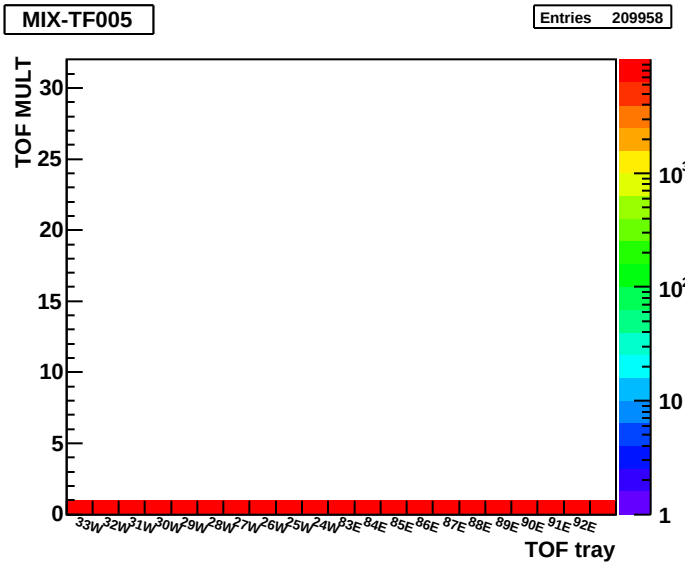

Hybrid Jet Patches
Entries 9998
JP ADC
JP ID

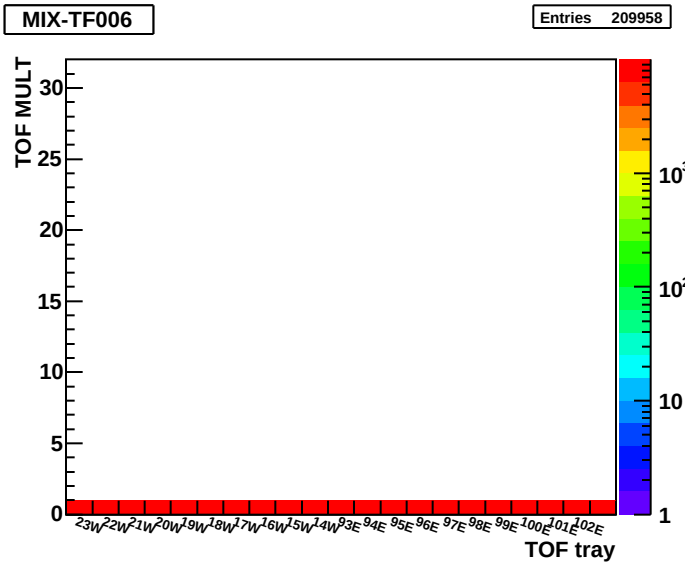

MIX-TF001
Entries 209958
TOF MULT
TOF tray

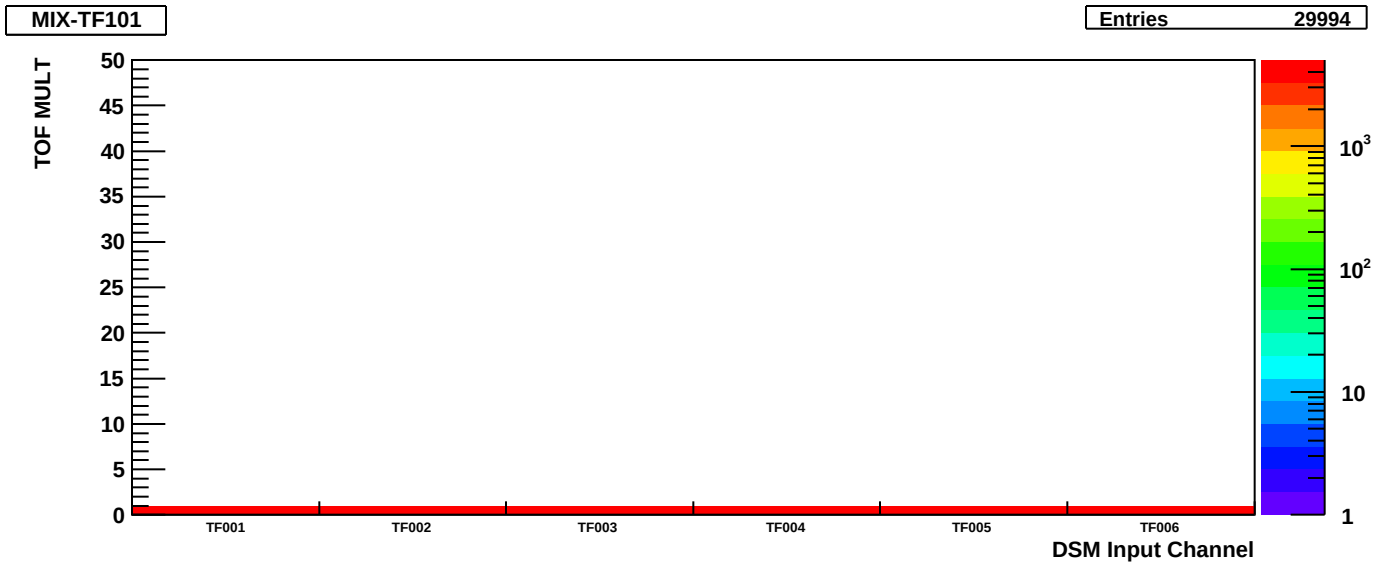

MIX-TF002
Entries 209958
TOF MULT
TOF tray

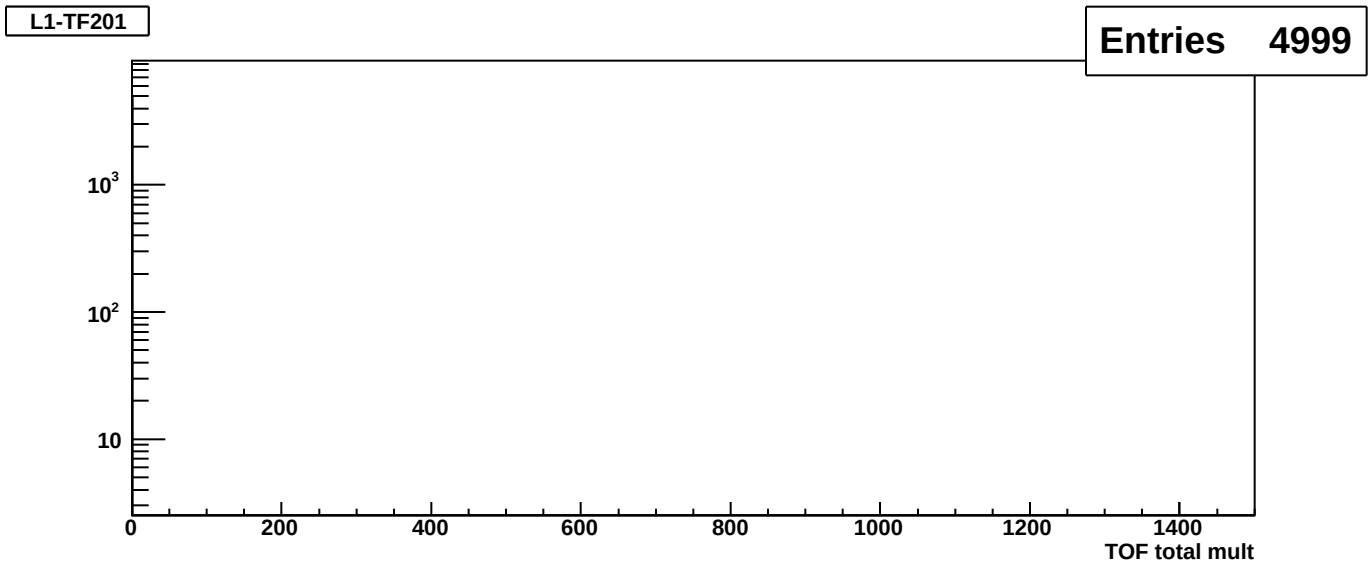

MIX-TF003
Entries 209958
TOF MULT
TOF tray

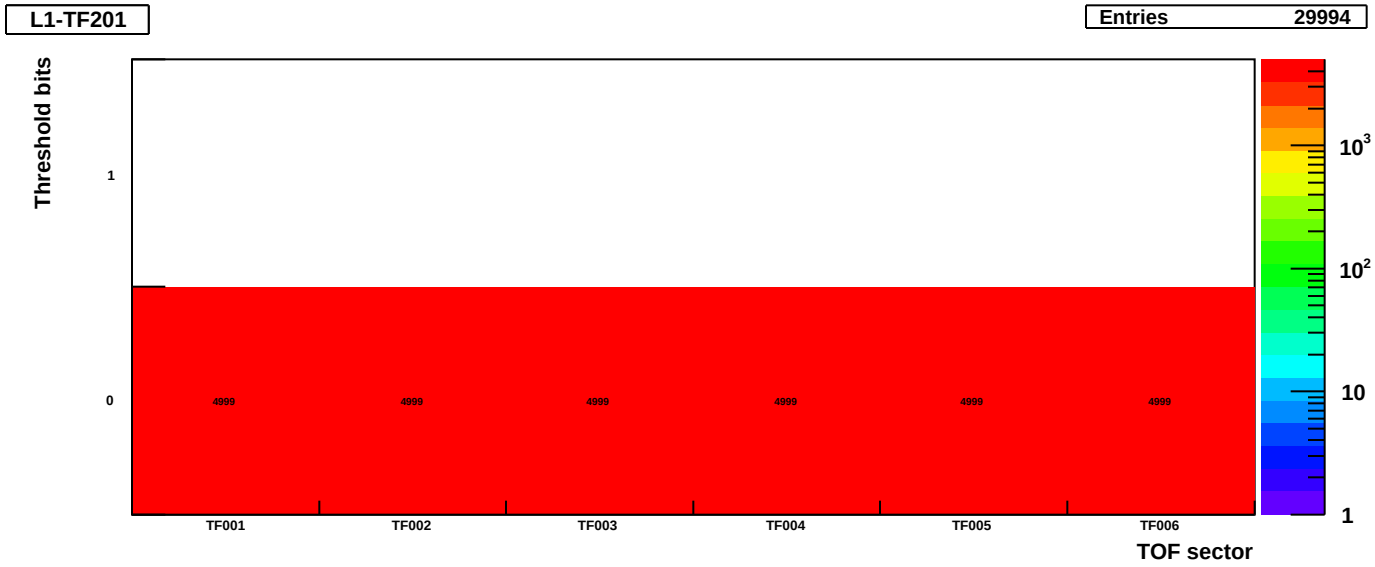

MIX-TF004
Entries 209958
TOF MULT
TOF tray

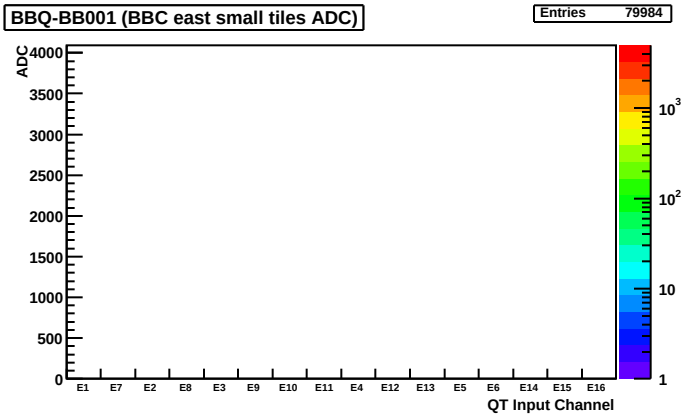

MIX-TF005
Entries 209958
TOF MULT
TOF tray

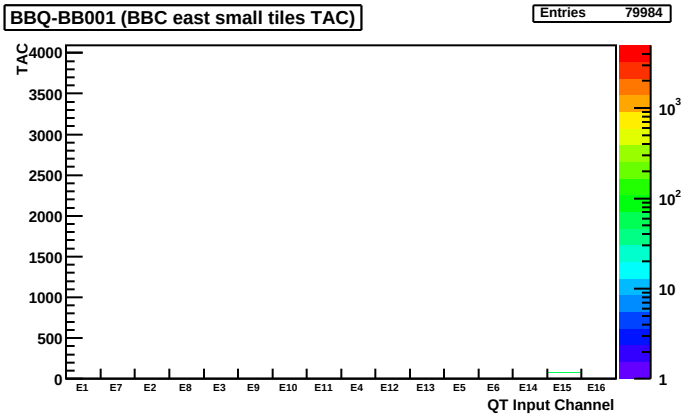

MIX-TF006
Entries 209958
TOF MULT
TOF tray

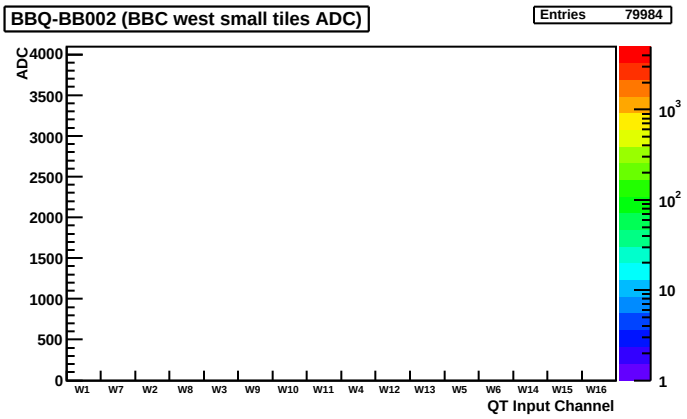

MIX-TF101
Entries 29994
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10
1

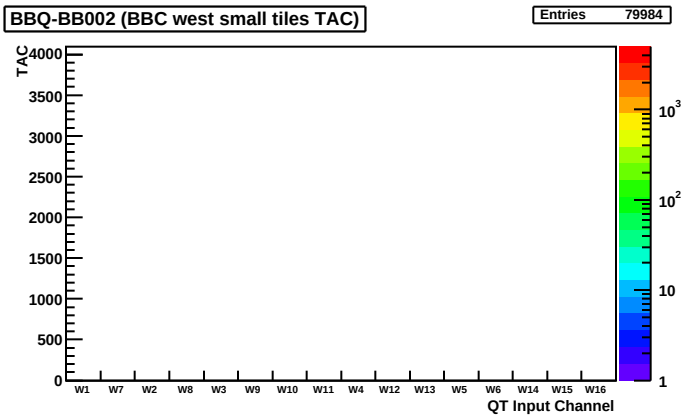

L1-TF201
Entries 4999
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

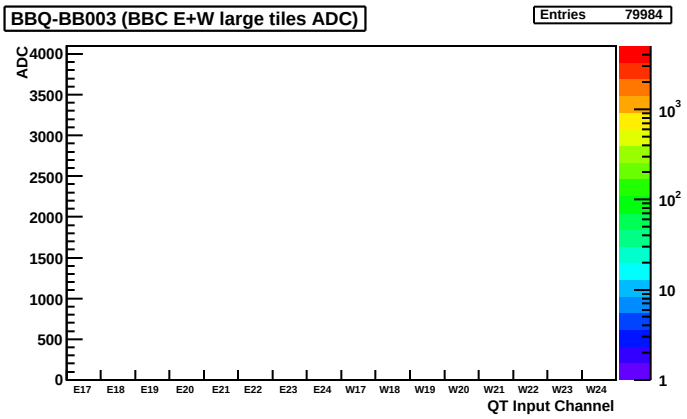

L1-TF201
Entries 29994
Threshold bits
1
0
4999
4999
4999
4999
4999
4999
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^3
10^2
10
1

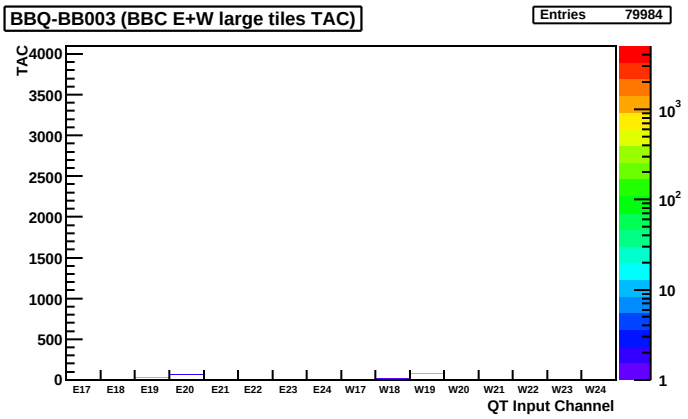

BBQ-BB001 (BBC east small tiles ADC)
Entries 79984
ADC
QT Input Channel

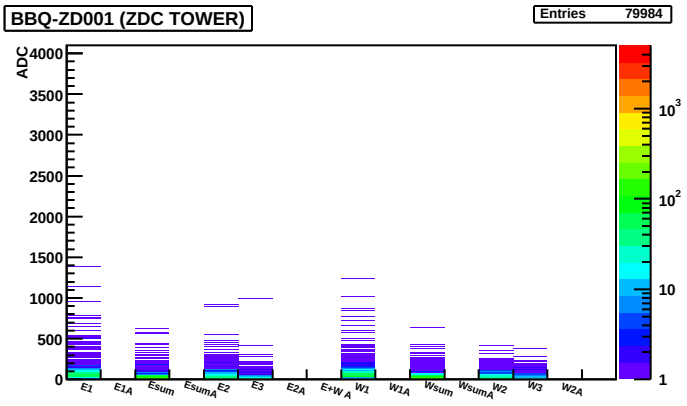

BBQ-BB001 (BBC east small tiles TAC)
Entries 79984
TAC
QT Input Channel

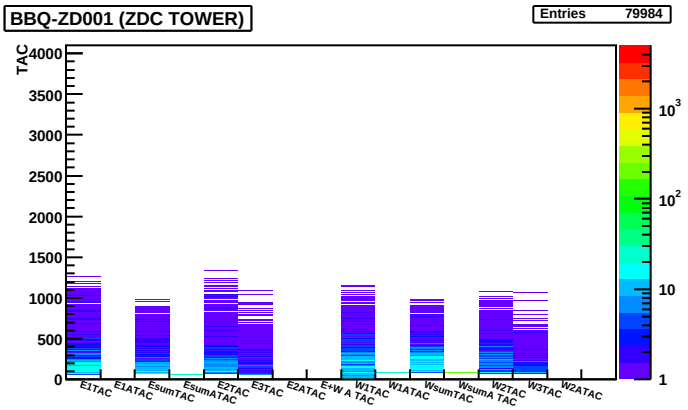

BBQ-BB002 (BBC west small tiles ADC)
Entries 79984
ADC
QT Input Channel

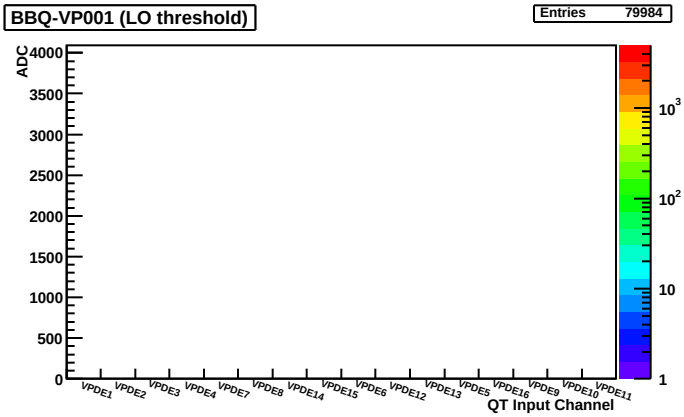

BBQ-BB002 (BBC west small tiles TAC)
Entries 79984
TAC
QT Input Channel

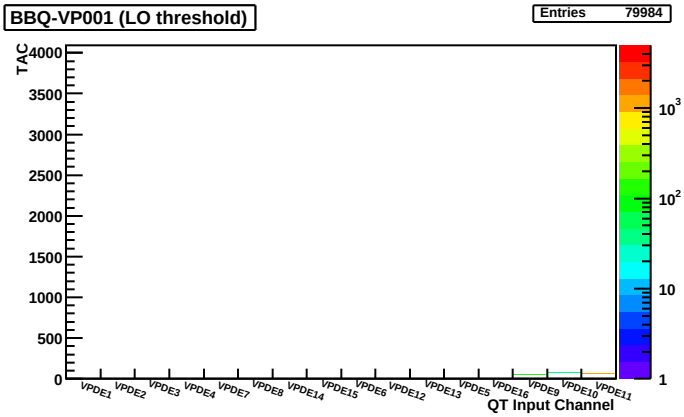

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 79984
ADC
QT Input Channel

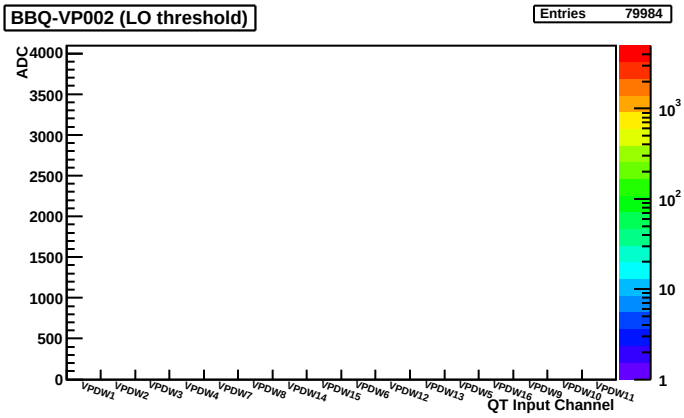

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 79984
TAC
QT Input Channel

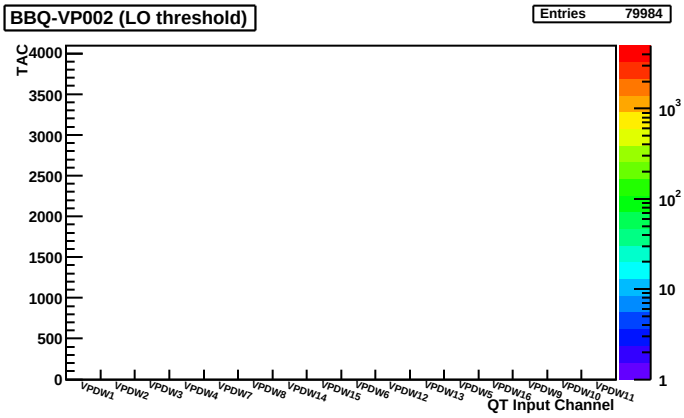

BBQ-ZD001 (ZDC TOWER)
Entries 79984
ADC

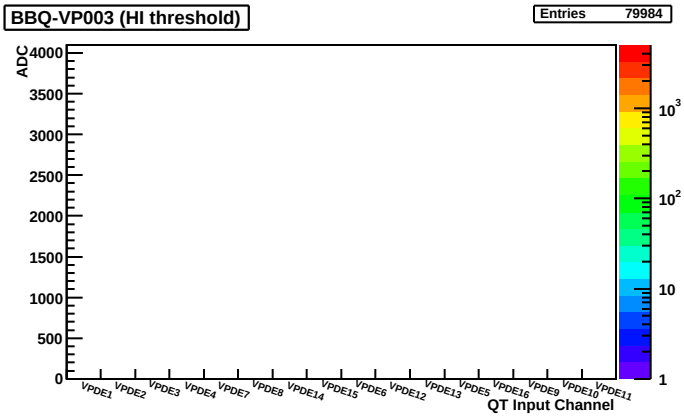

BBQ-ZD001 (ZDC TOWER)
Entries 79984
TAC

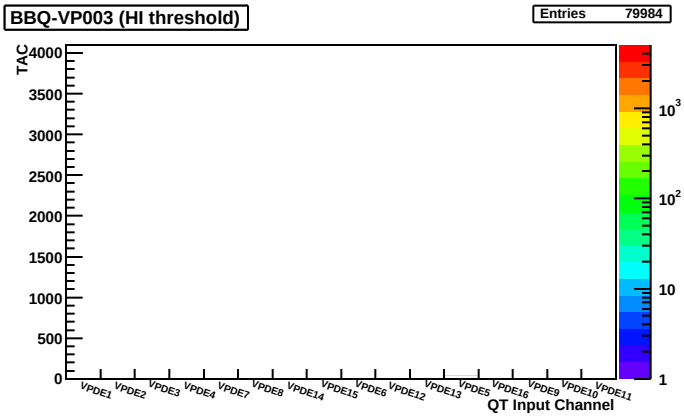

BBQ-VP001 (LO threshold)
Entries 79984
ADC
QT Input Channel

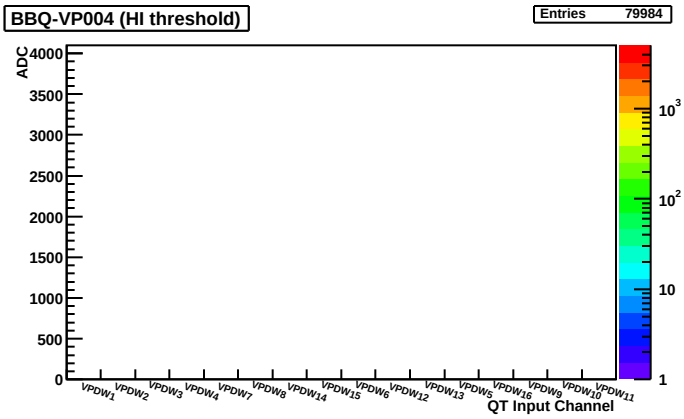

BBQ-VP001 (LO threshold)
Entries 79984
TAC
QT Input Channel

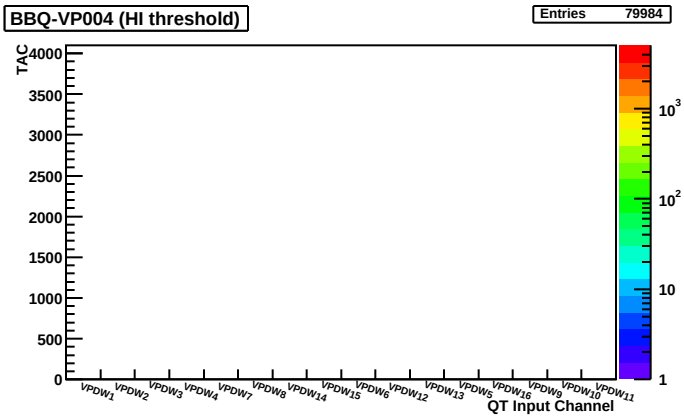

BBQ-VP002 (LO threshold)
Entries 79984
ADC
QT Input Channel

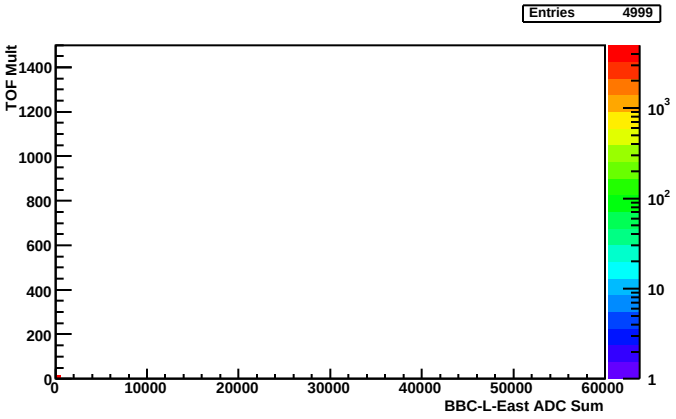

BBQ-VP002 (LO threshold)
Entries 79984
TAC
QT Input Channel

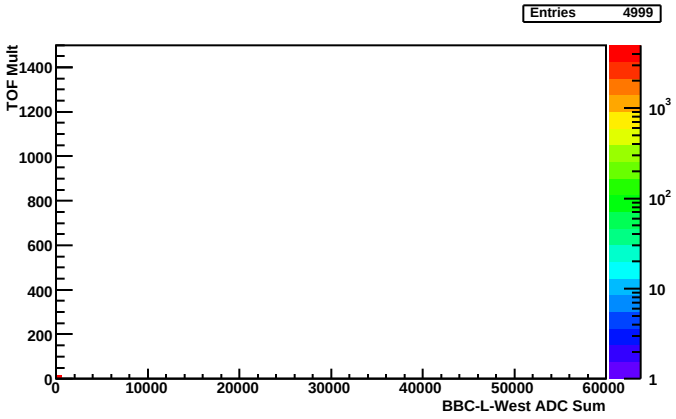

BBQ-VP003 (HI threshold)
Entries 79984
ADC
QT Input Channel

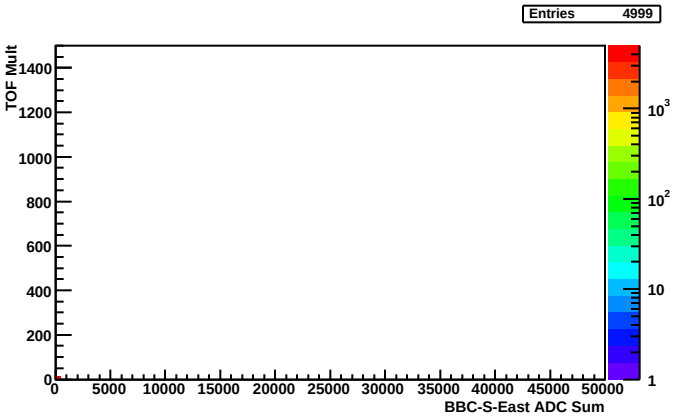

BBQ-VP003 (HI threshold)
Entries 79984
TAC
QT Input Channel

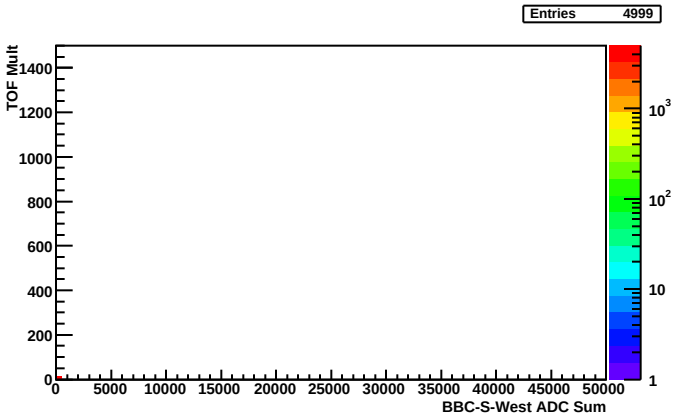

BBQ-VP004 (HI threshold)
Entries 79984
ADC
QT Input Channel

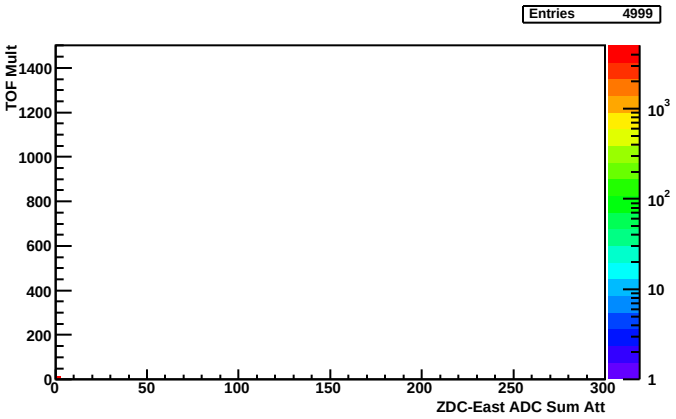

BBQ-VP004 (HI threshold)
Entries 79984
TAC
QT Input Channel

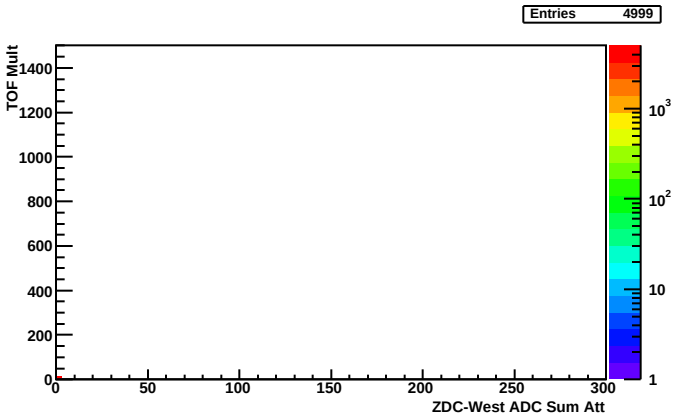

Entries 4999
TOF Mult
BBC-L-East ADC Sum

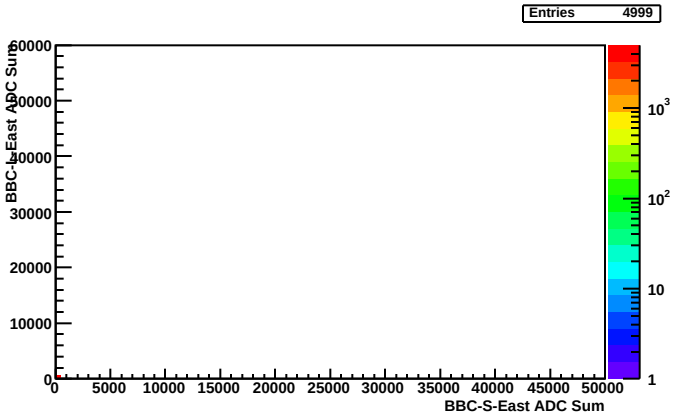

Entries 4999
TOF Mult
BBC-L-West ADC Sum

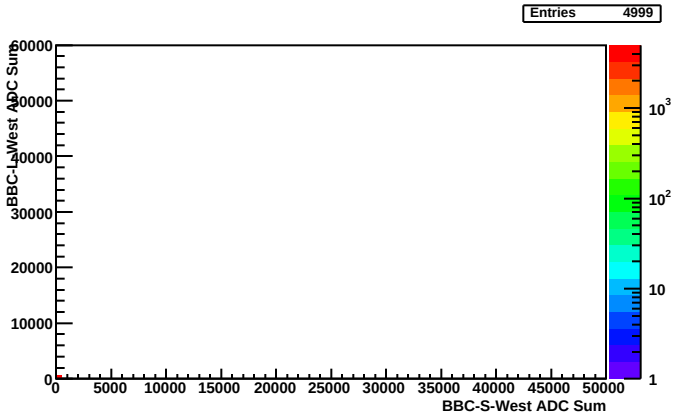

Entries 4999
TOF Mult
BBC-S-East ADC Sum

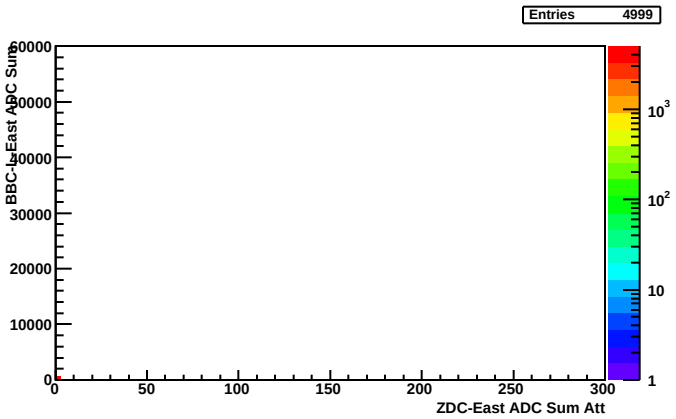

Entries 4999
TOF Mult
BBC-S-West ADC Sum

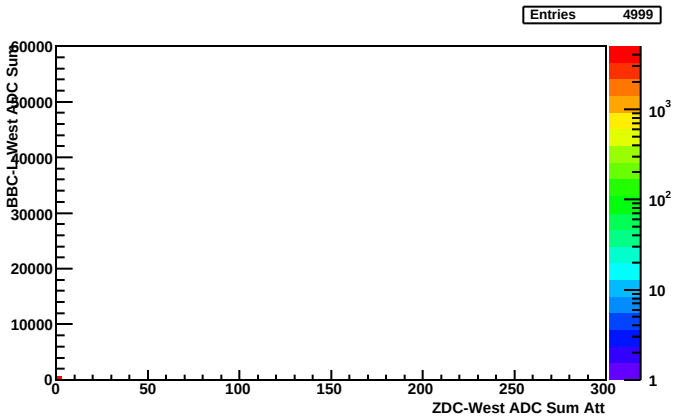

Entries 4999
TOF Mult
ZDC-East ADC Sum Att

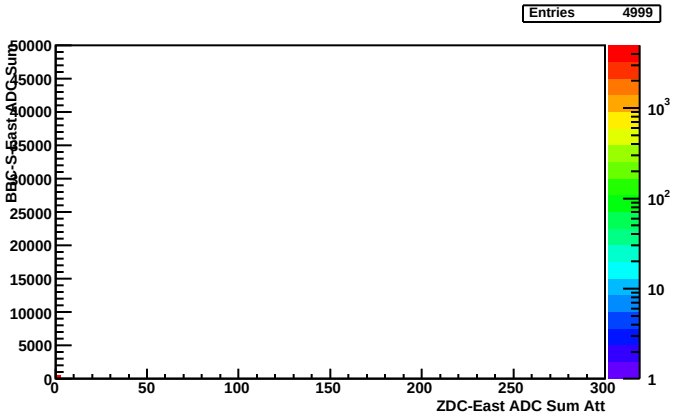

Entries 4999
TOF Mult
ZDC-West ADC Sum Att


Entries 4999
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 4999
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 4999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att


Entries 4999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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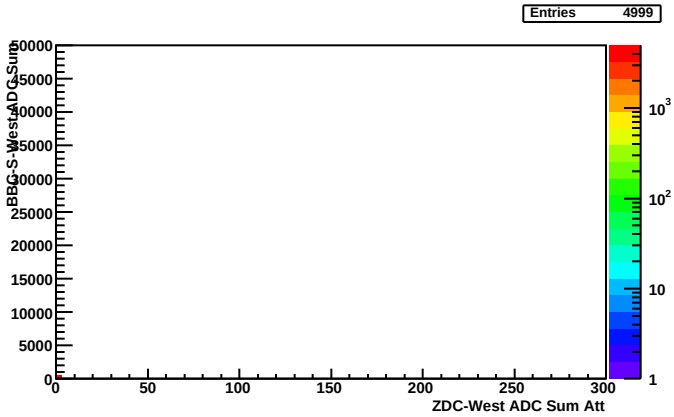
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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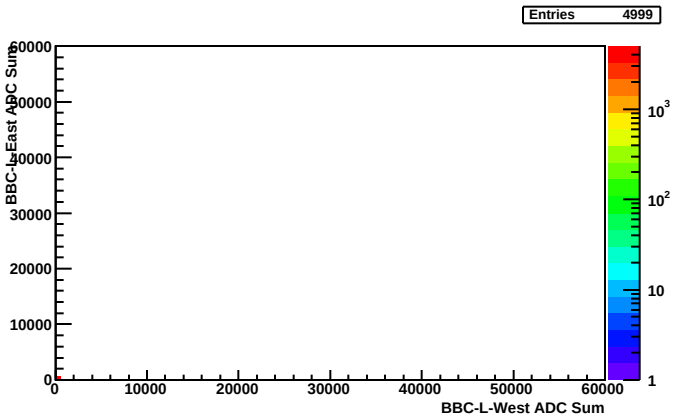
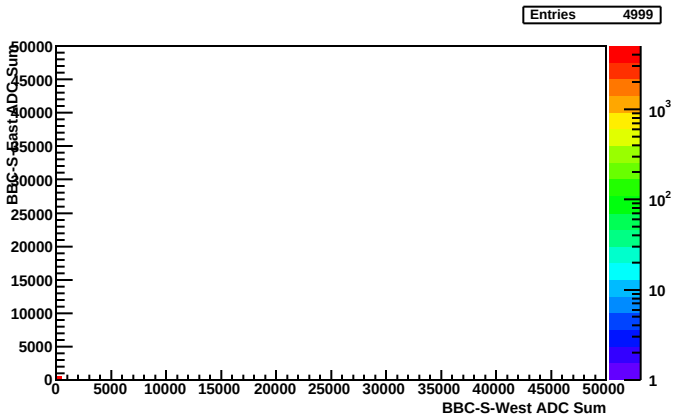
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 4999
BBC-L-East ADC Sum
BBC-L-West ADC Sum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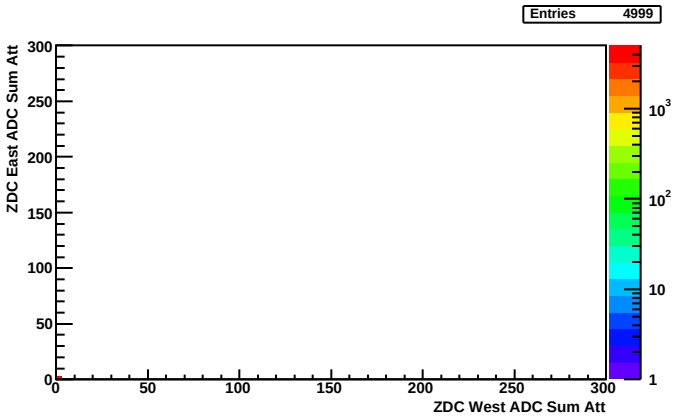
BBC-S-East ADC Sum
BBC-S-West ADC Sum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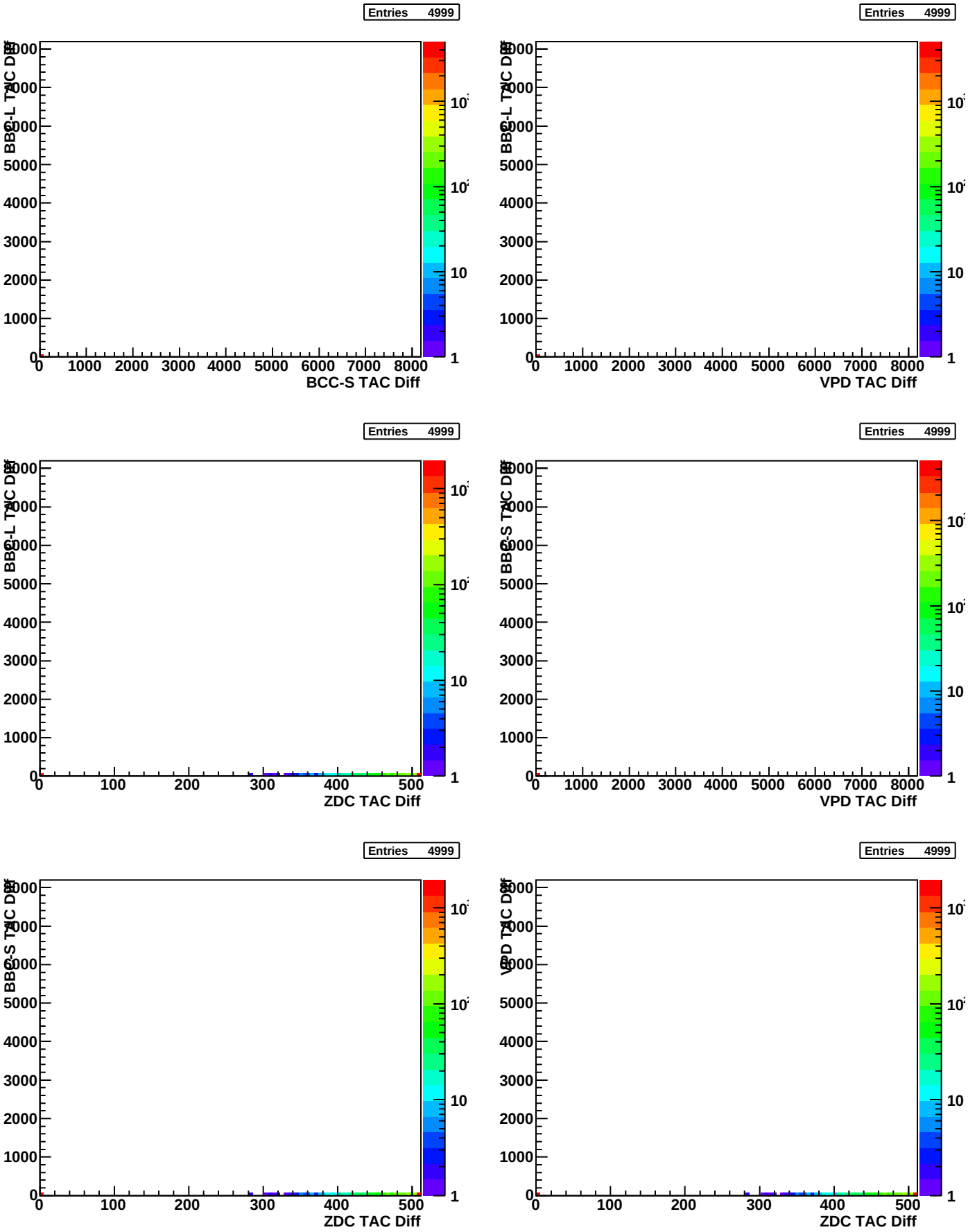
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 4999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 4999
BBC-L TAC Diff
VPD TAC Diff
Entries 4999
BBC-L TAC Diff
ZDC TAC Diff
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BBC-S TAC Diff
VPD TAC Diff
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BBC-S TAC Diff
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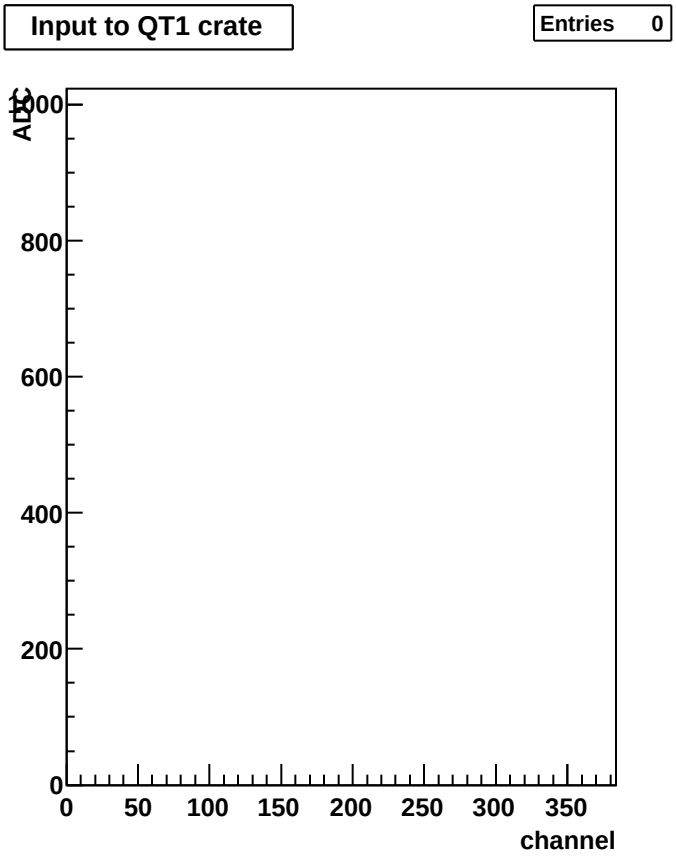
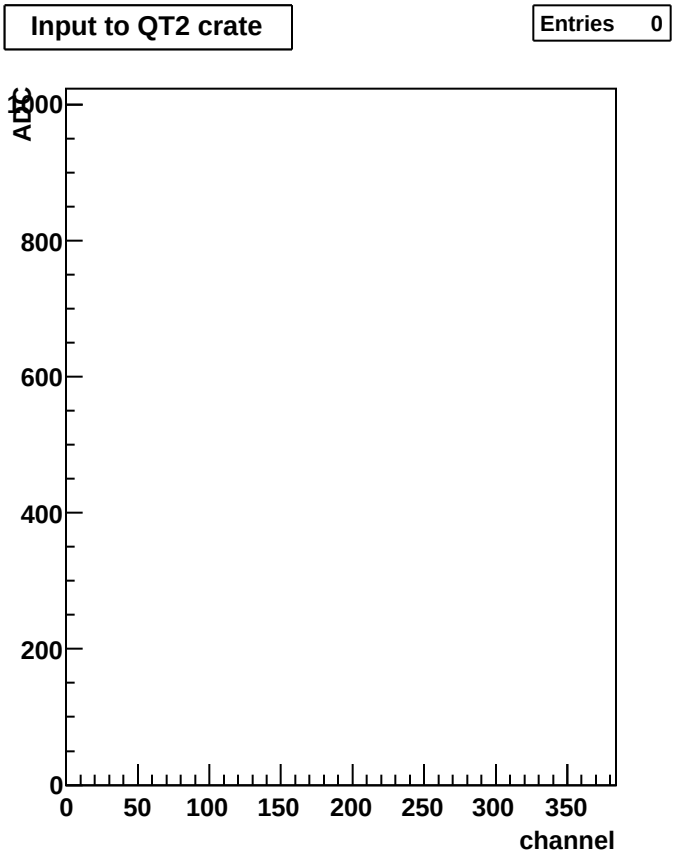
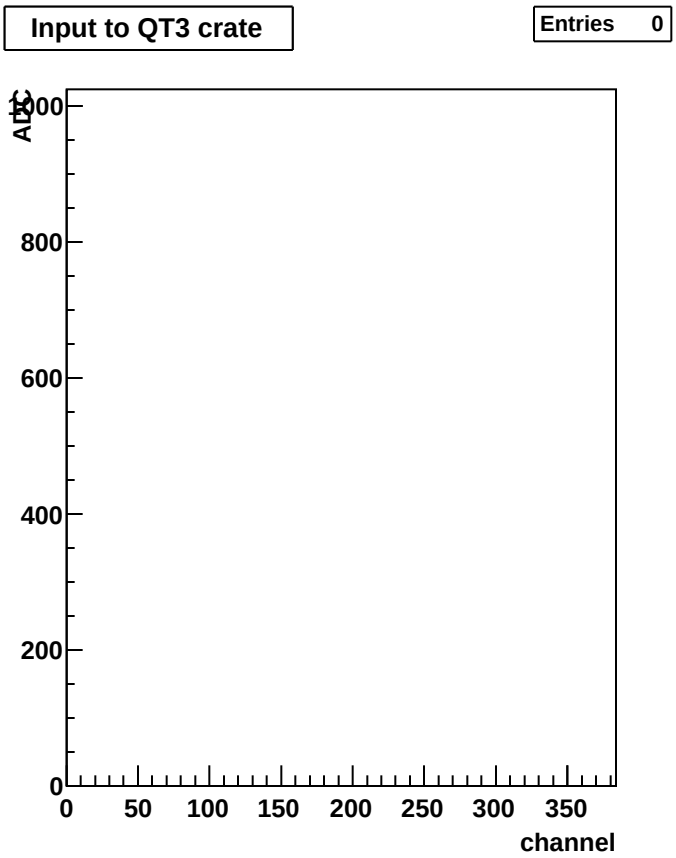
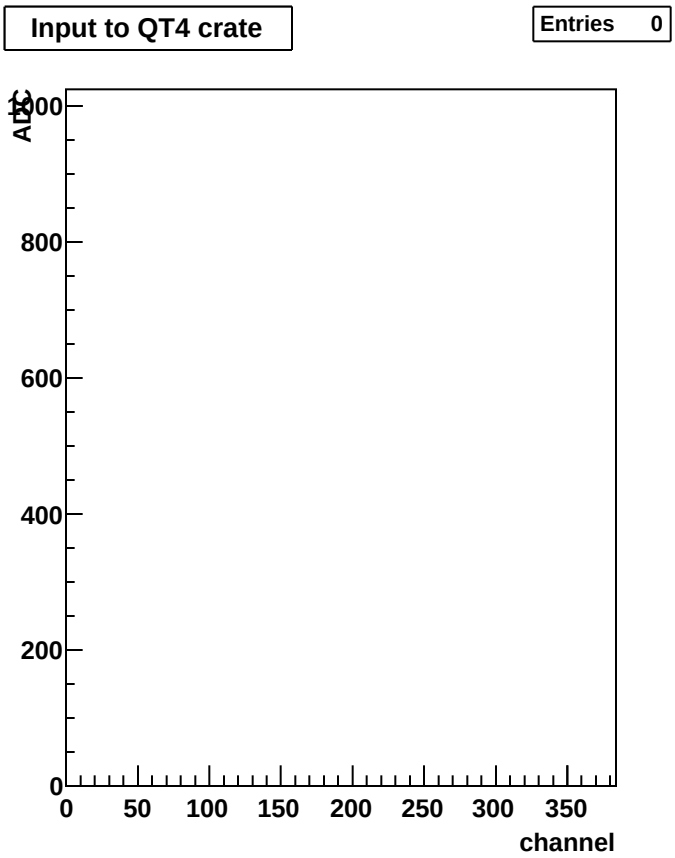
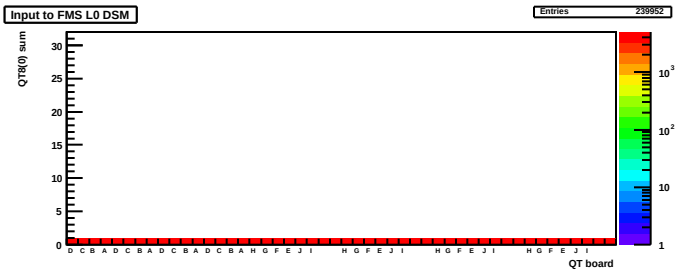
Entries 4999
VPD TAC Diff
ZDC TAC Diff

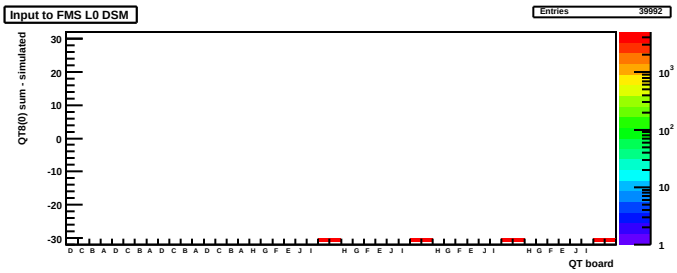

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

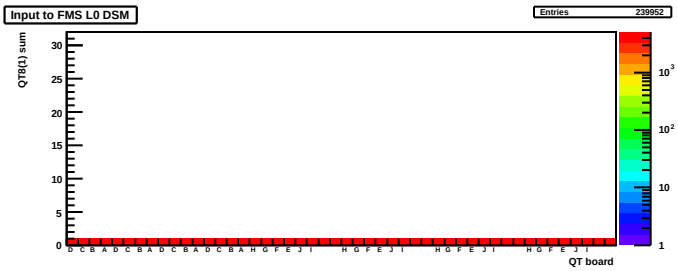

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

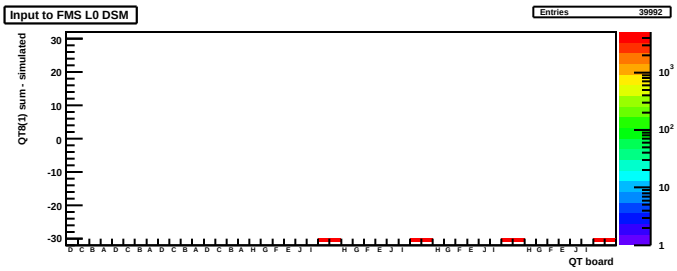

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

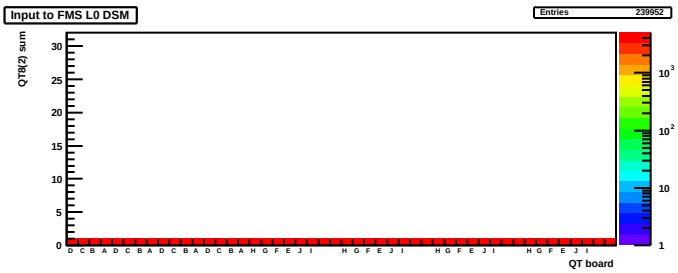

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

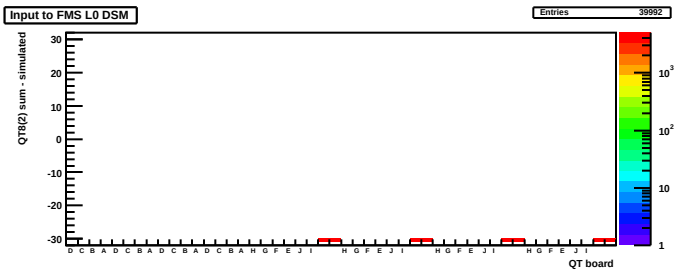

Input to FMS L0 DSM
Entries 239952
QT8(0) sum
QT board

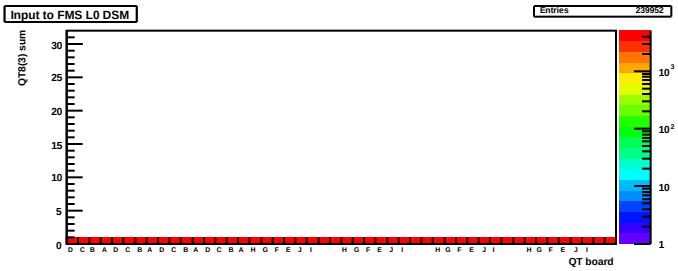

Input to FMS L0 DSM
Entries 39992
QT8(0) sum - simulated
QT board

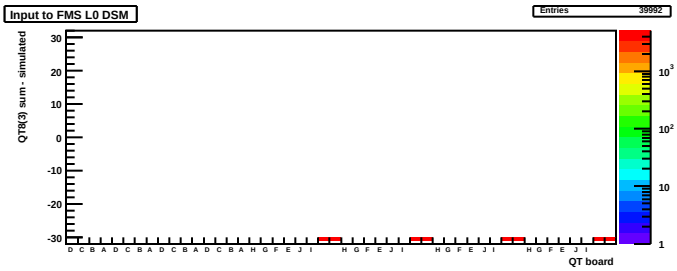

Input to FMS L0 DSM
Entries 239952
QT8(1) sum
QT board

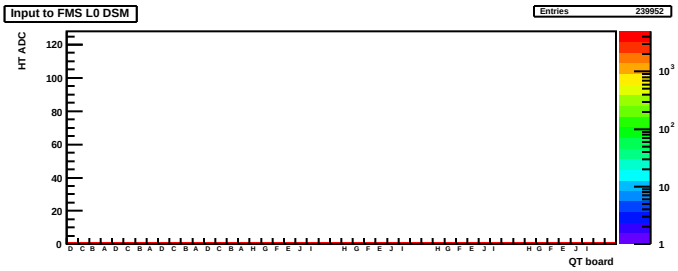

Input to FMS L0 DSM
Entries 39992
QT8(1) sum - simulated
QT board

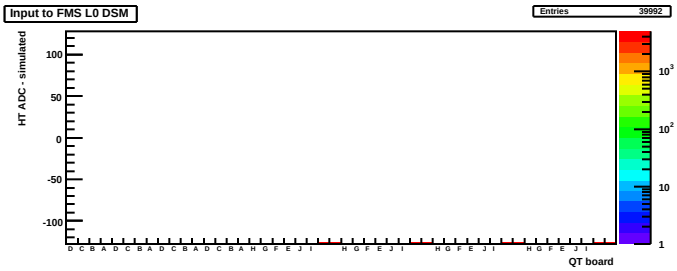

Input to FMS L0 DSM
Entries 239952
QT8(2) sum
QT board

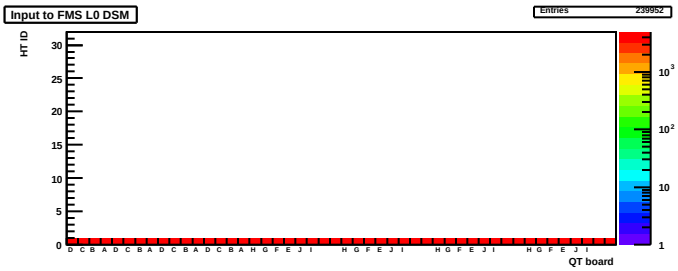

Input to FMS L0 DSM
Entries 39992
QT8(2) sum - simulated
QT board

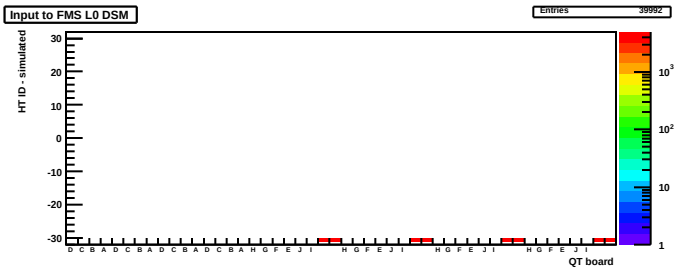

Input to FMS L0 DSM
Entries 239952
QT8(3) sum
QT board

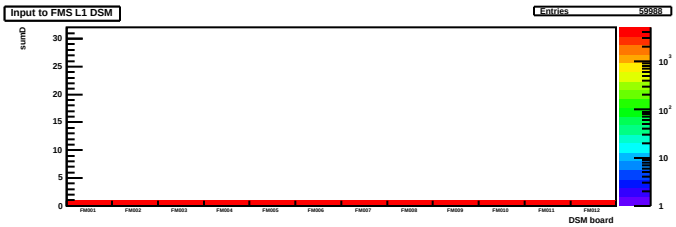

Input to FMS L0 DSM
Entries 39992
QT8(3) sum - simulated
QT board

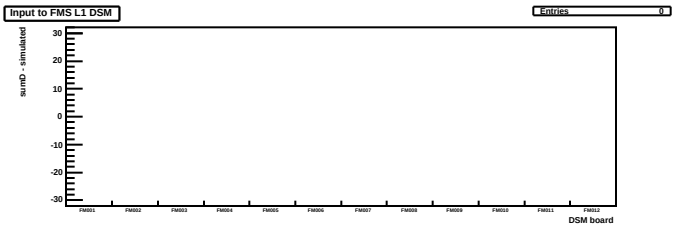

Input to FMS L0 DSM
Entries 239952
HT ADC
QT board

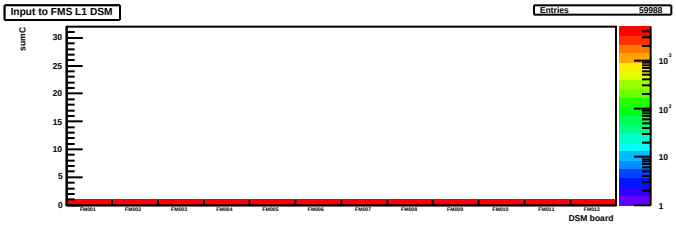

Input to FMS L0 DSM
Entries 39992
HT ADC - simulated
QT board

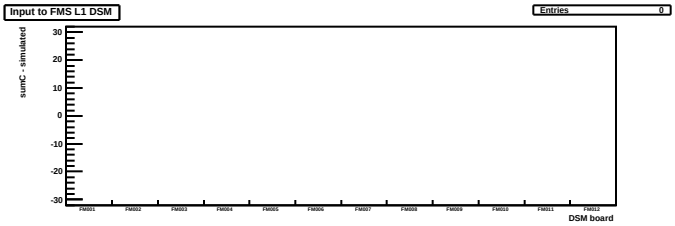

Input to FMS L0 DSM
Entries 239952
HT ID
QT board

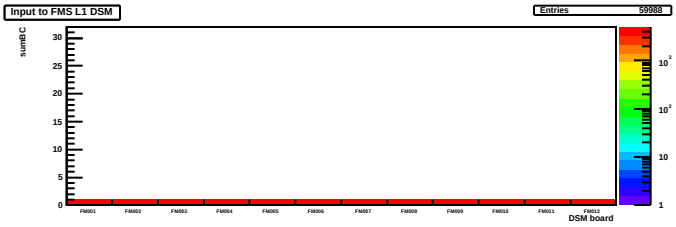

Input to FMS L0 DSM
Entries 39992
HT ID - simulated
QT board

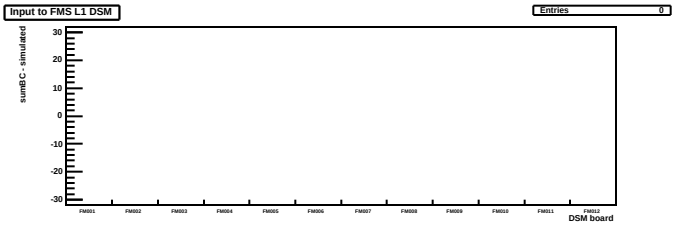

Input to FMS L1 DSM
Entries 59988
sumD
DSM board

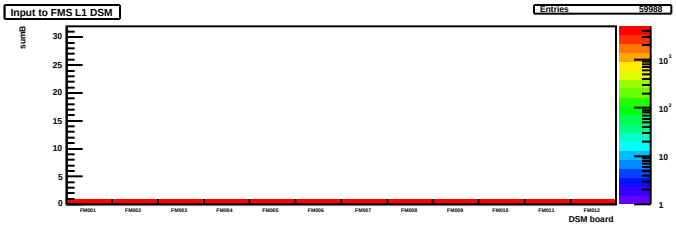

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

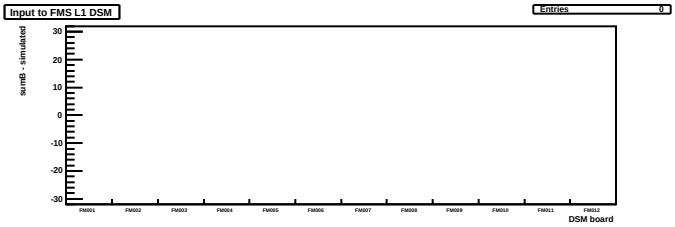

Input to FMS L1 DSM
Entries 59988
sumC
DSM board

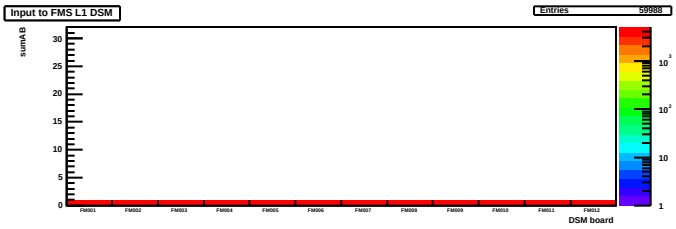

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

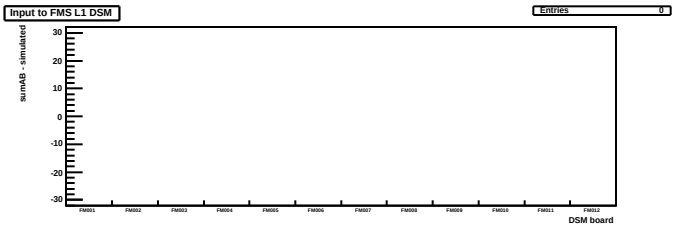

Input to FMS L1 DSM
Entries 59988
sumBC
DSM board

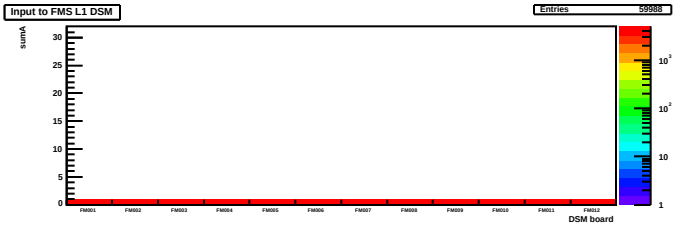

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

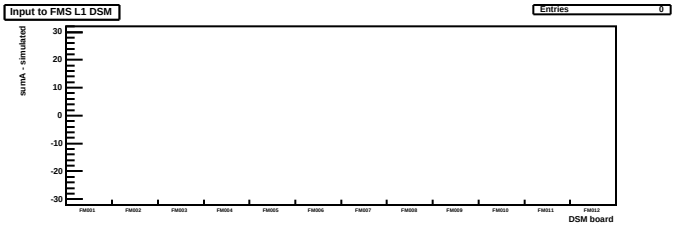

Input to FMS L1 DSM
Entries 59988
sumB
DSM board

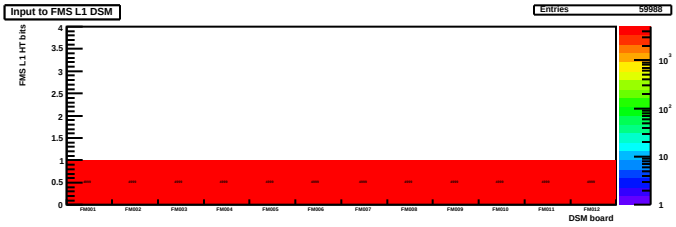

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

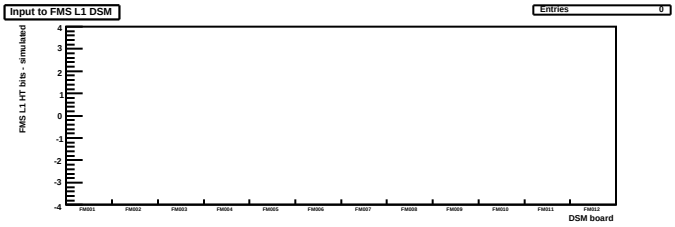

Input to FMS L1 DSM
Entries 59988
sumAB
DSM board

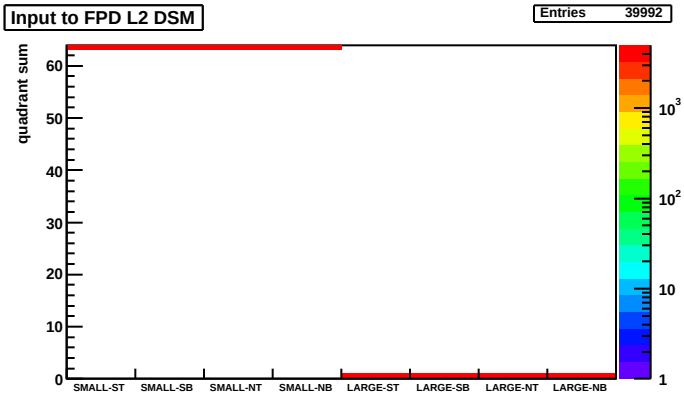

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

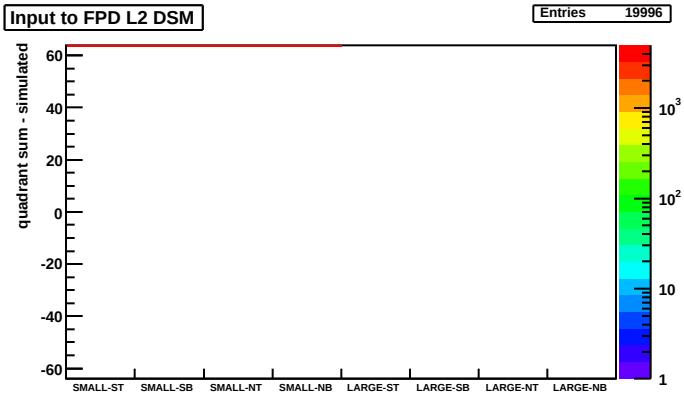

Input to FMS L1 DSM
Entries 59988
sumA
DSM board

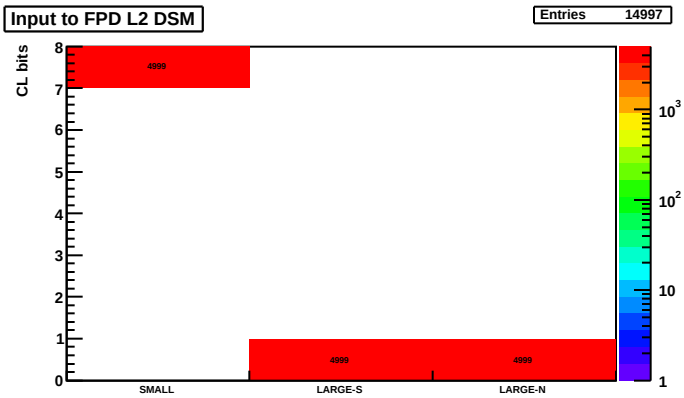

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

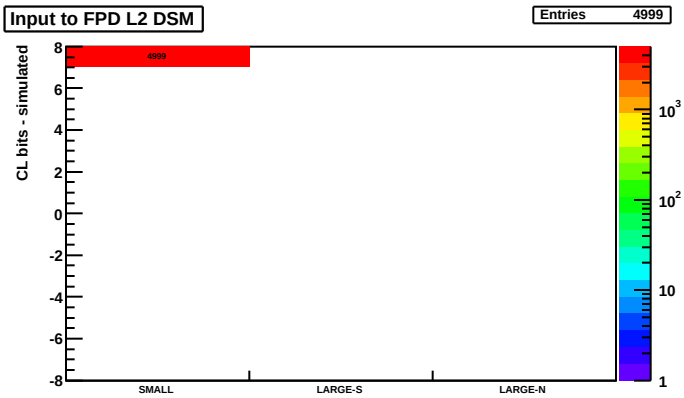

Input to FMS L1 DSM
Entries 59988
FMS L1 HT bits
DSM board

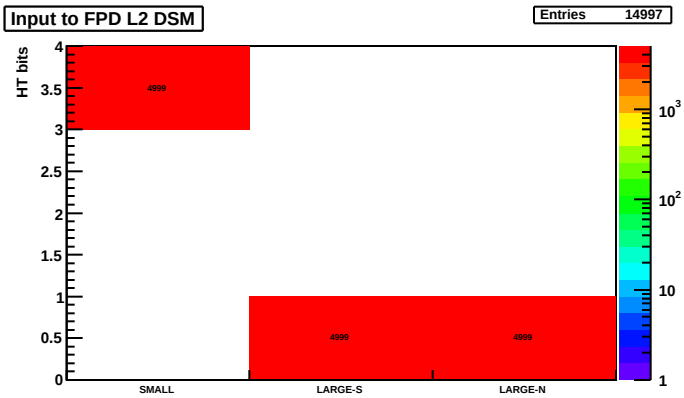

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

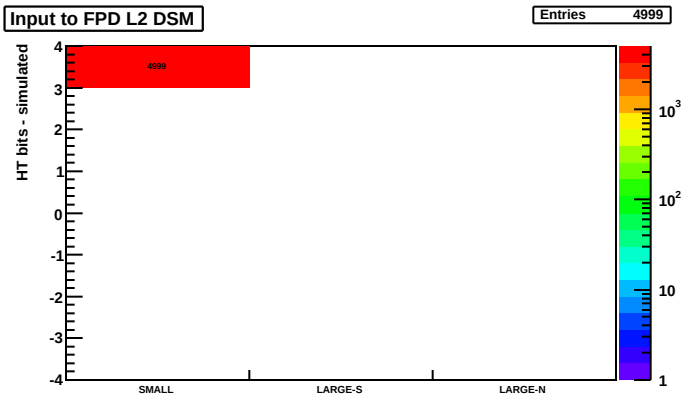

Input to FPD L2 DSM
Entries 39992
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

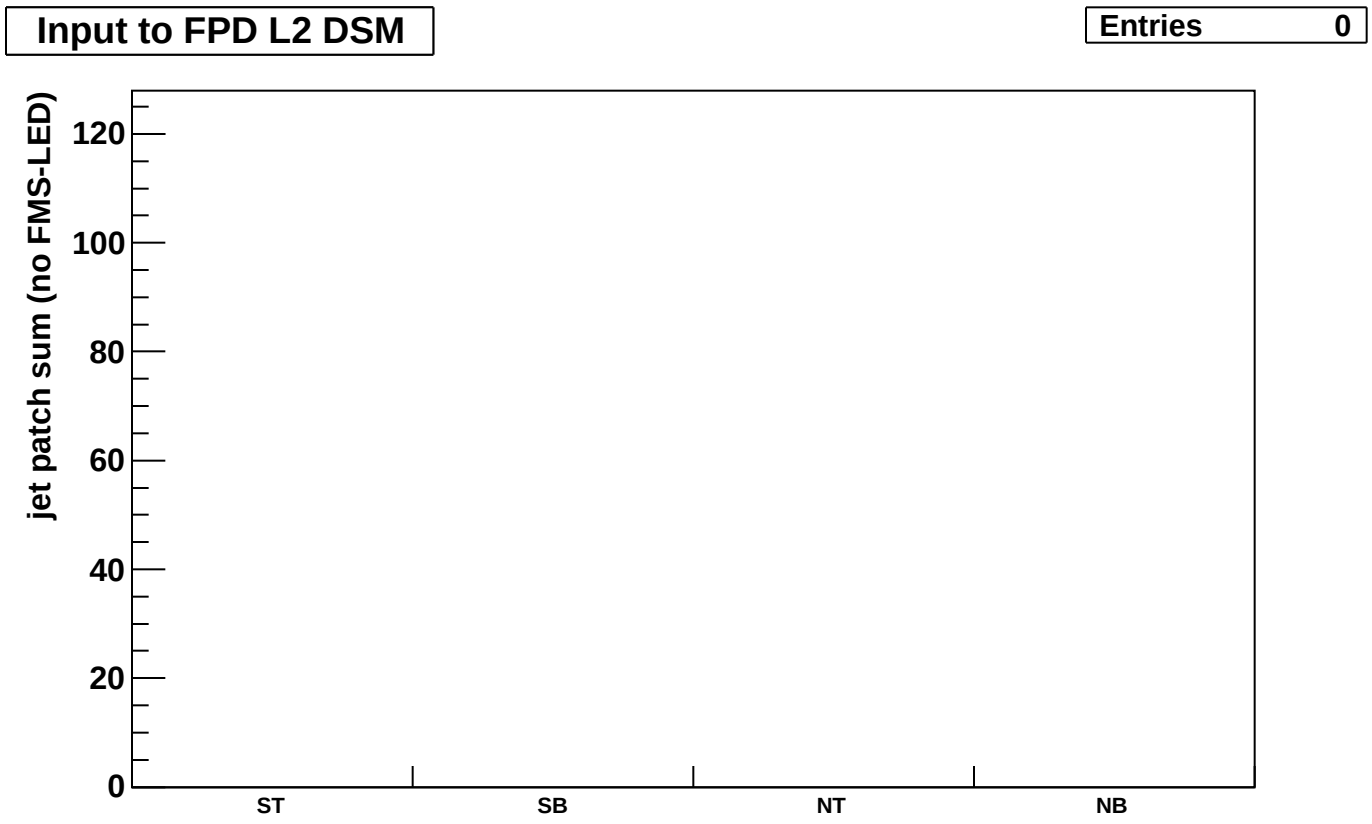

Input to FPD L2 DSM
Entries 19996
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

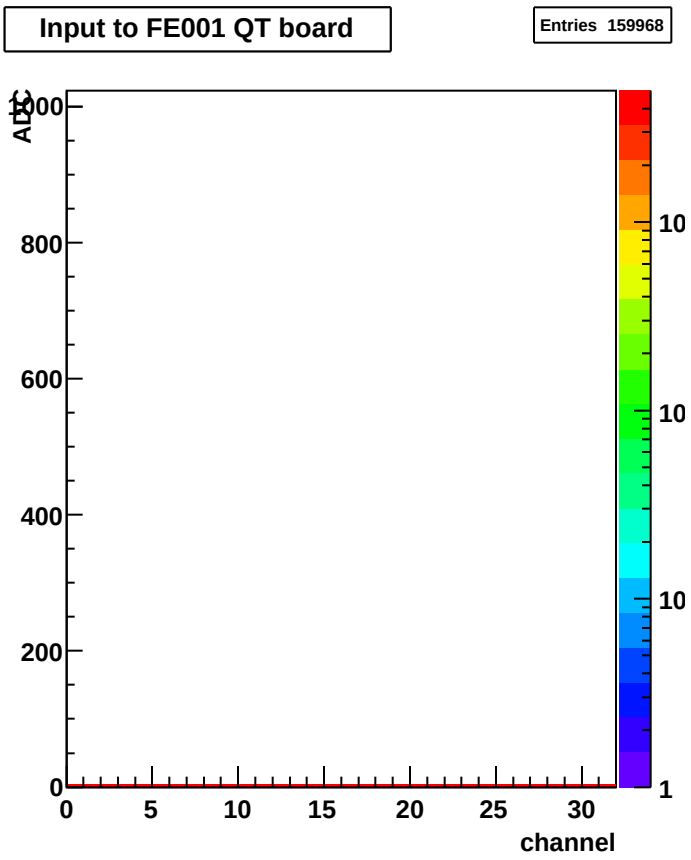

Input to FPD L2 DSM
Entries 14997
CL bits
4999
4999
4999
SMALL
LARGE-S
LARGE-N

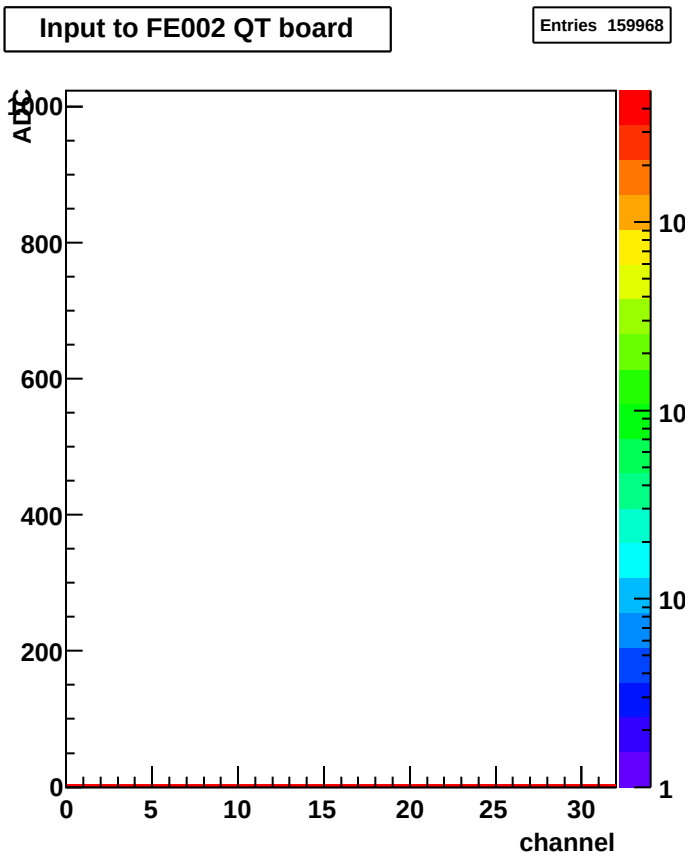

Input to FPD L2 DSM
Entries 4999
CL bits - simulated
4999
SMALL
LARGE-S
LARGE-N

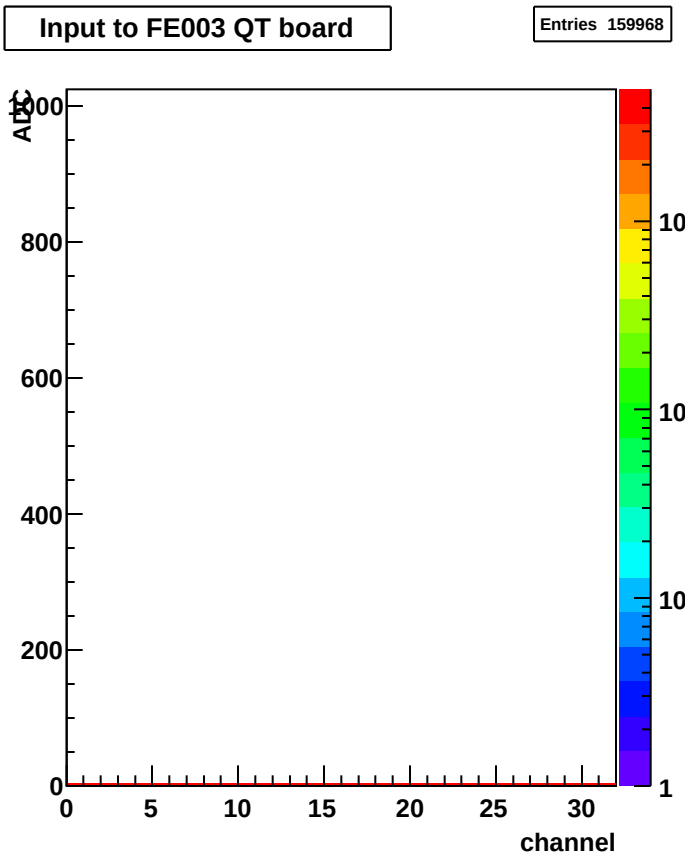

Input to FPD L2 DSM
Entries 14997
HT bits
4999
4999
4999
SMALL
LARGE-S
LARGE-N

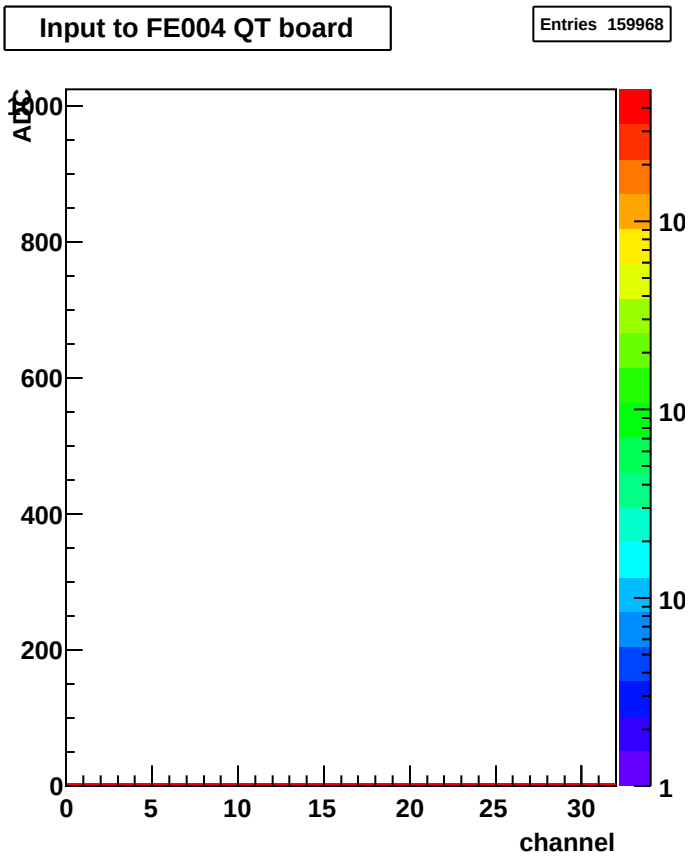

Input to FPD L2 DSM
Entries 4999
HT bits - simulated
4999
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 0
jet patch sum (no FMS-LED)
120
100
80
60
40
20
0
ST
SB
NT
NB

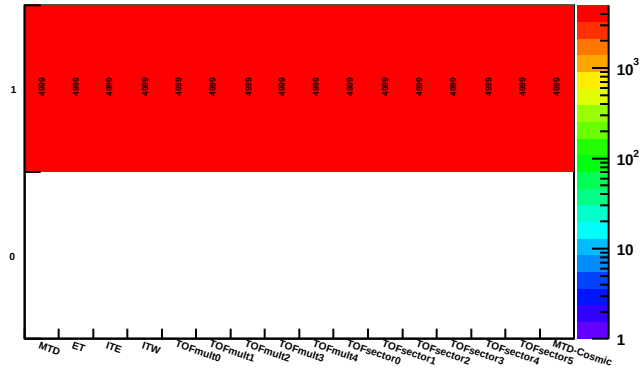

Input to FE001 QT board
Entries 159968
ADC
channel


Input to FE002 QT board
Entries 159968
ADC
channel

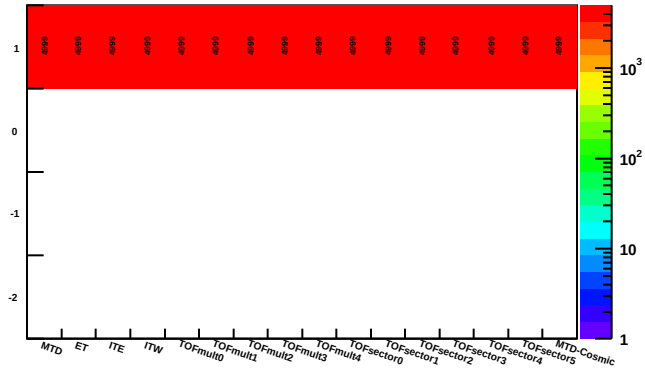

Input to FE003 QT board
Entries 159968
ADC
channel


Input to FE004 QT board
Entries 159968
ADC
channel

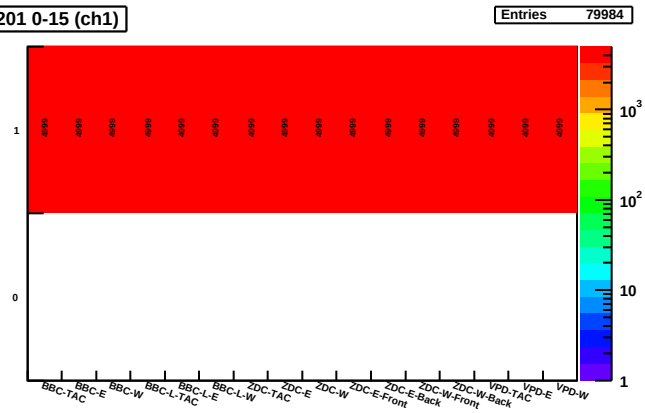
TF201 0-15 (ch0)



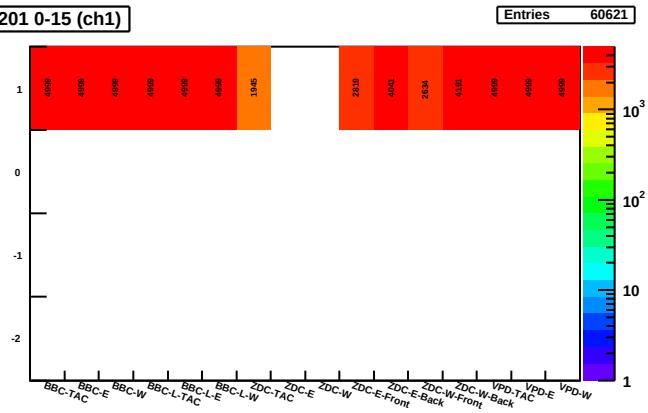
TF201 0-15 (ch0)



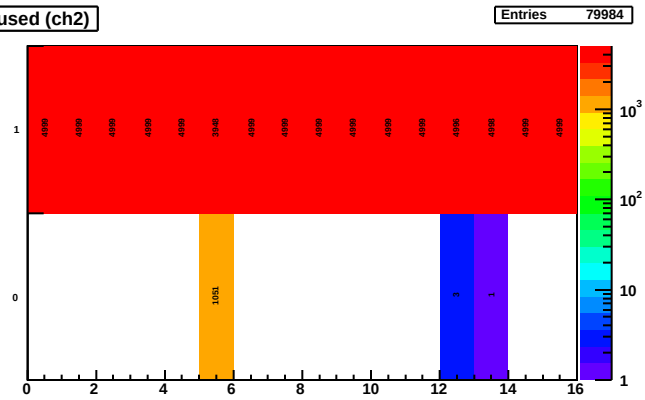
VT201 0-15 (ch1)

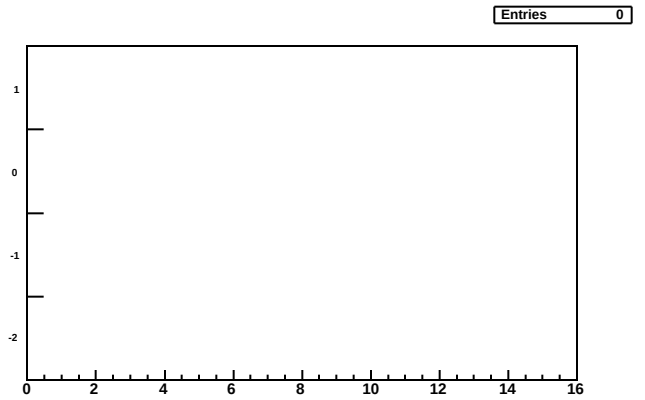


VT201 0-15 (ch1)

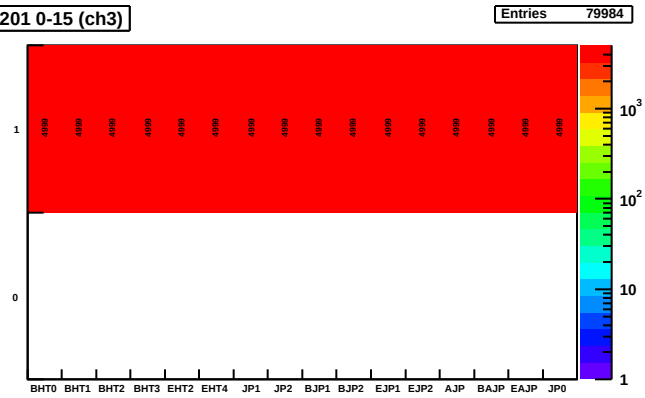


Unused (ch2)

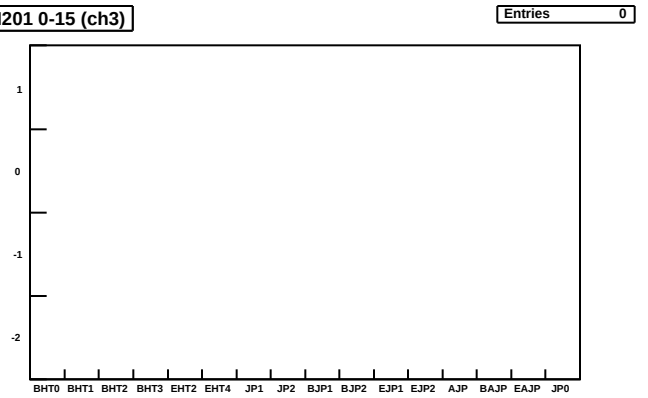




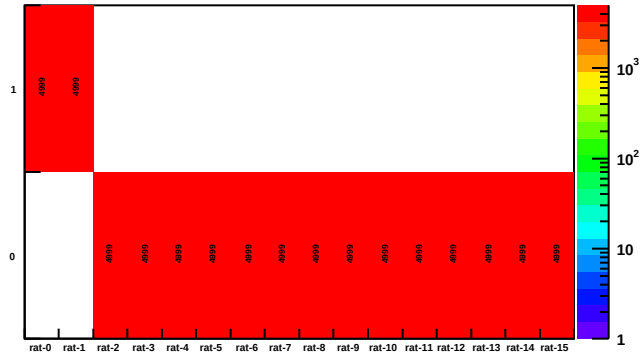
EM201 0-15 (ch3)

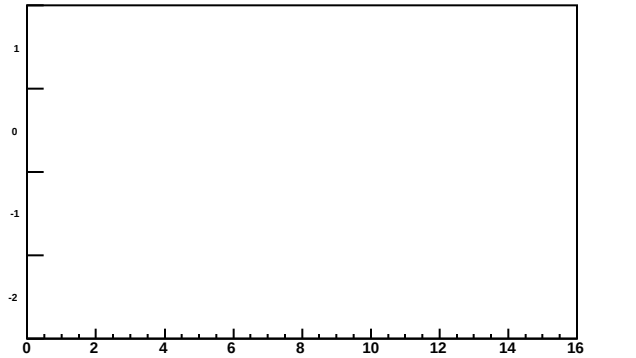


EM201 0-15 (ch3)

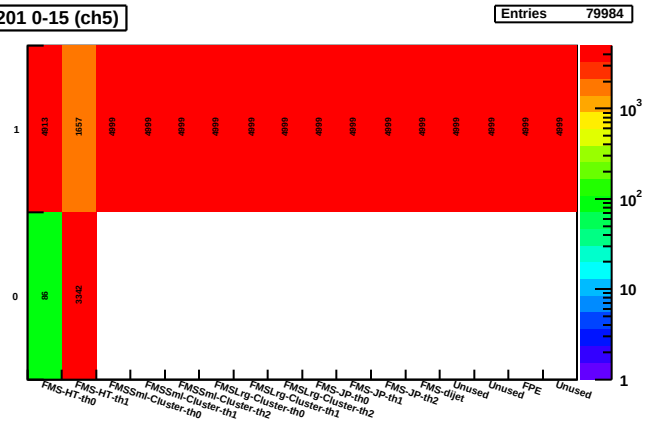


RAT board (ch4)

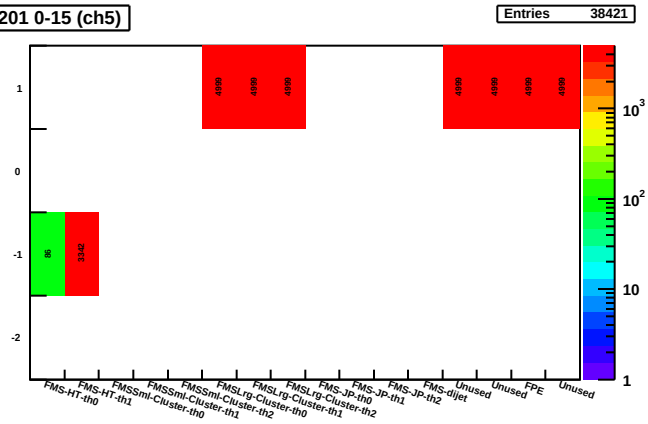




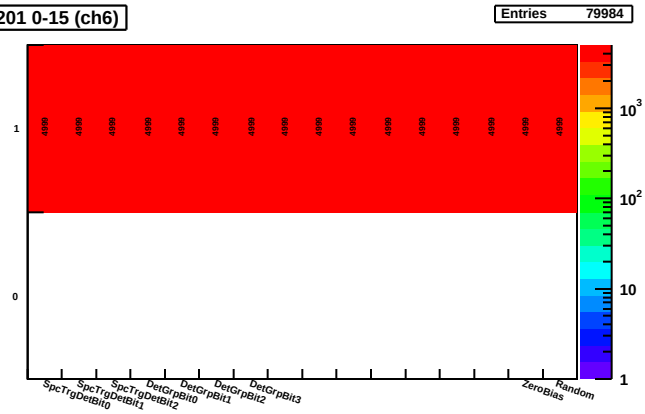
FP201 0-15 (ch5)

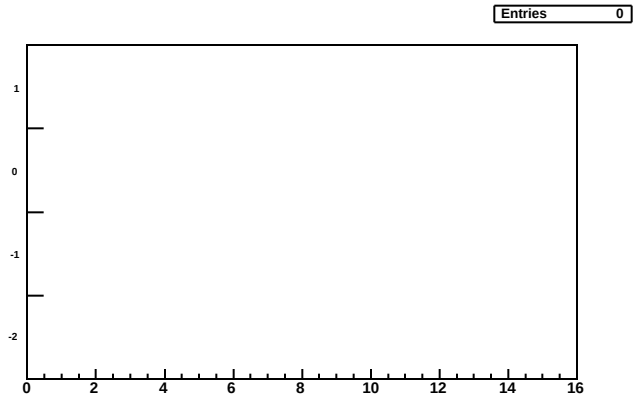
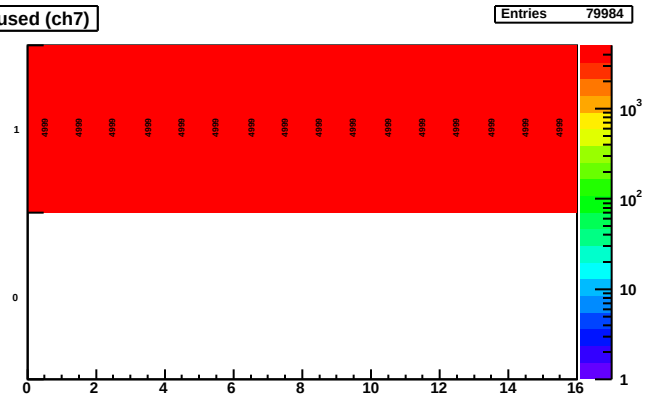
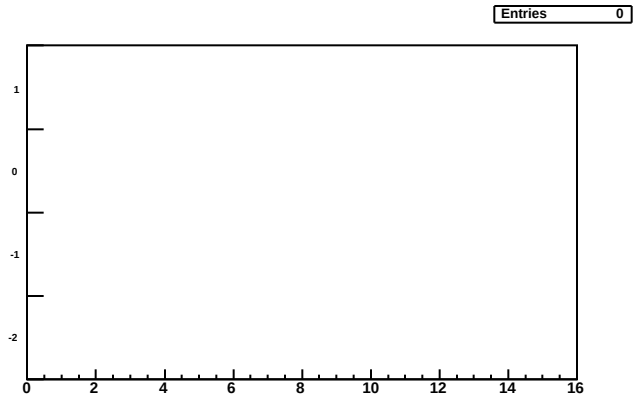


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)