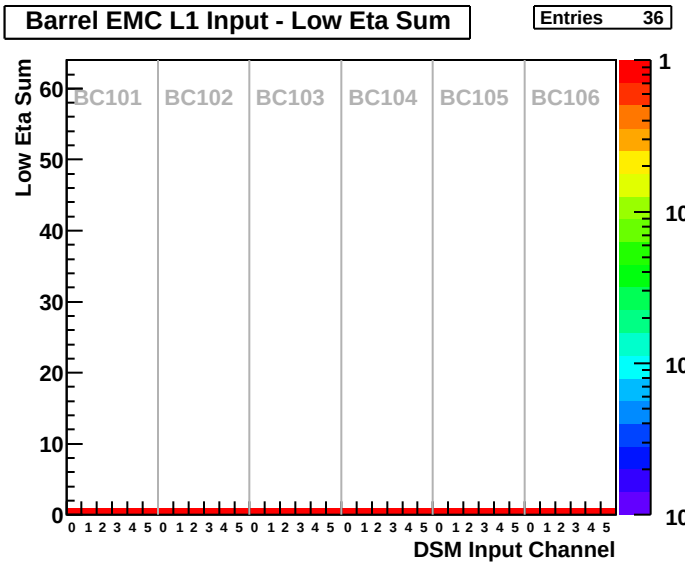
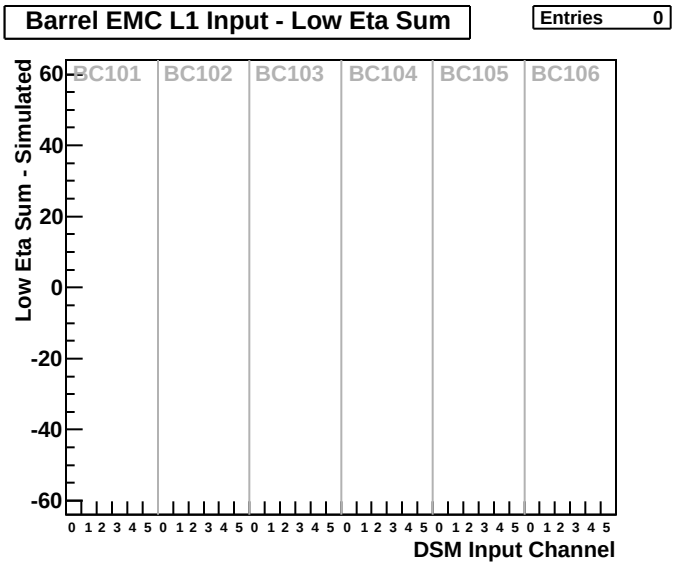
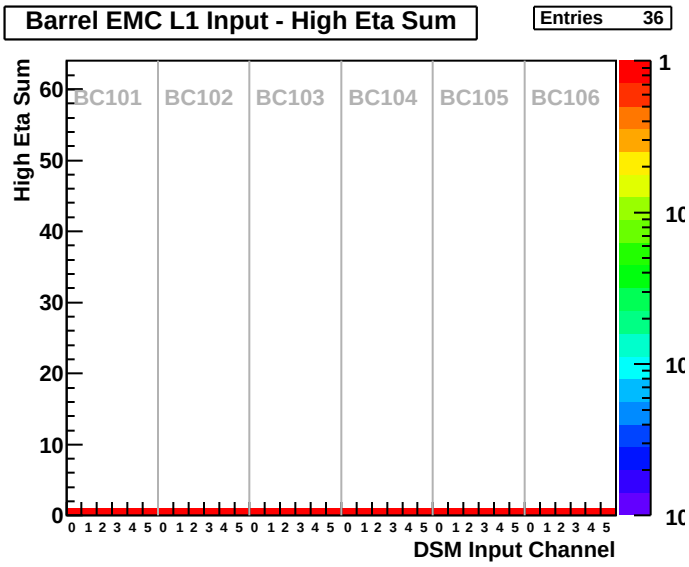
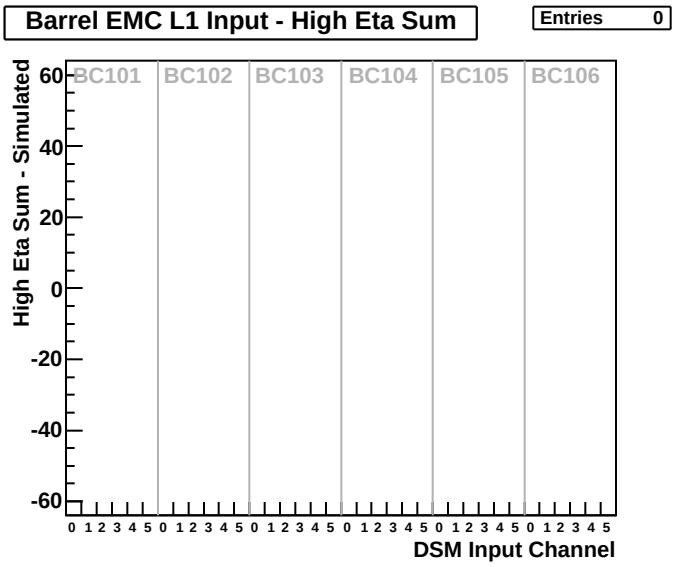
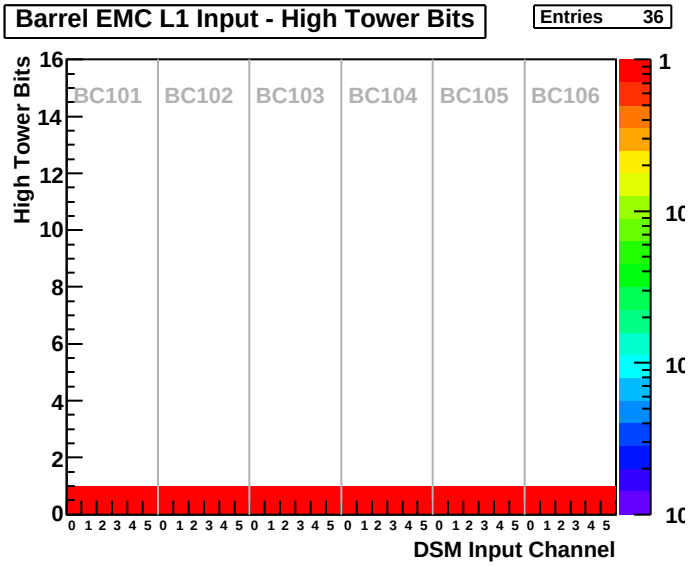
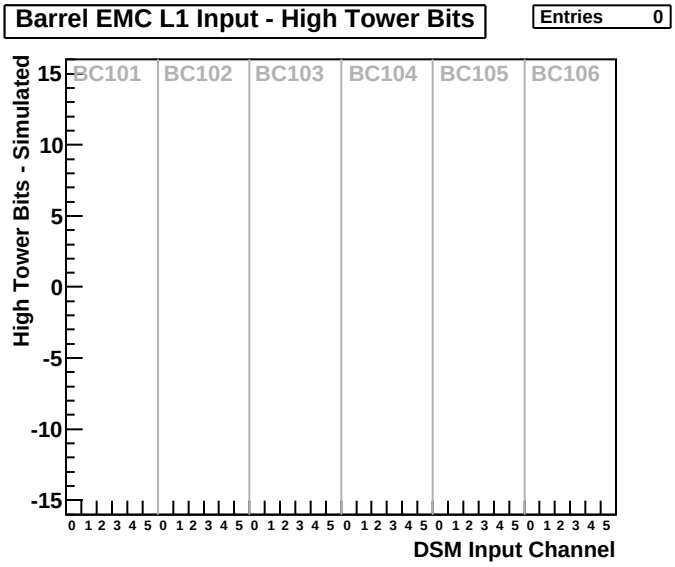

Barrel EMC L1 Input - Low Eta Sum
Entries 36
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

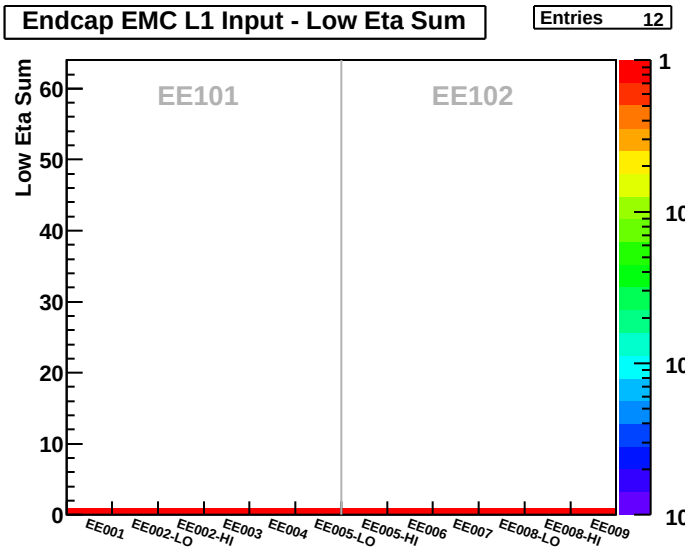

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

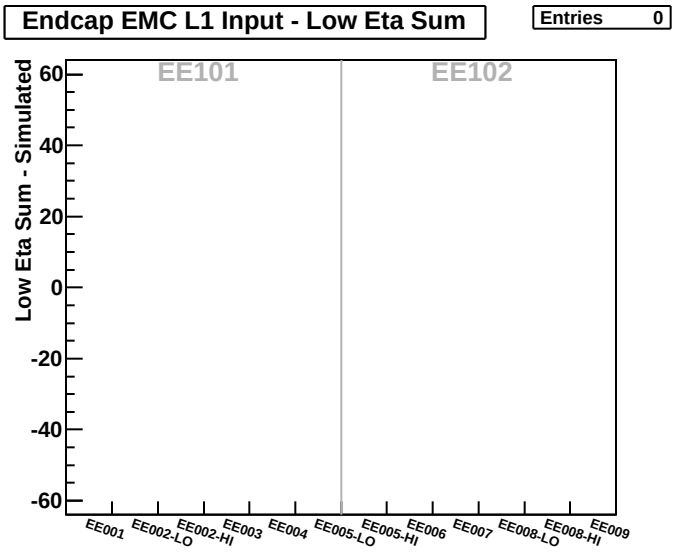

Barrel EMC L1 Input - High Eta Sum
Entries 36
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

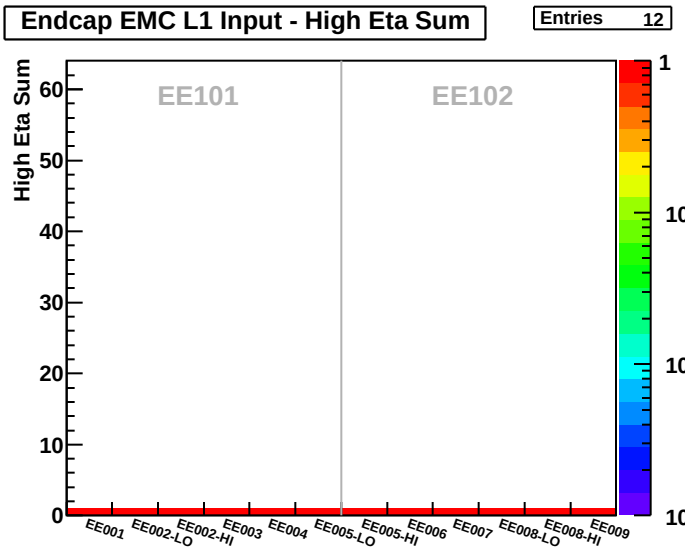

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

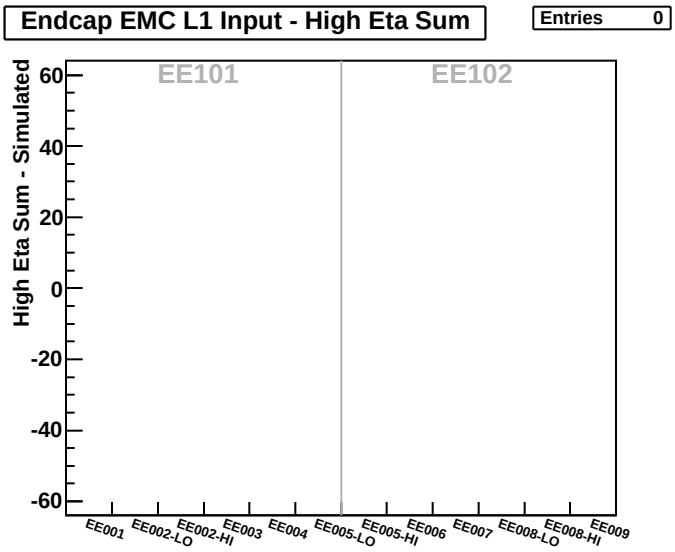

Barrel EMC L1 Input - High Tower Bits
Entries 36
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

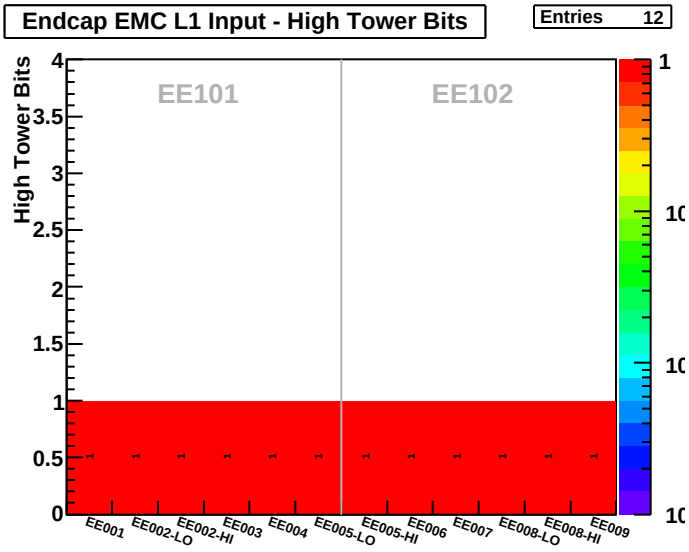

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

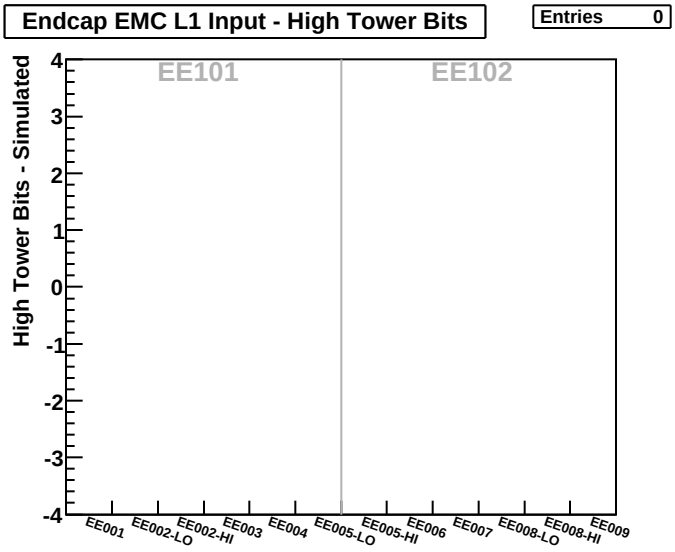

Endcap EMC L1 Input - Low Eta Sum
Entries 12
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

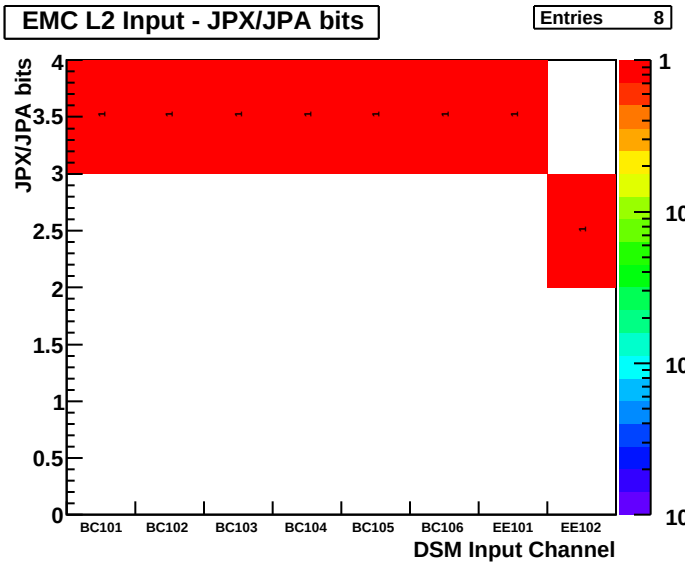

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

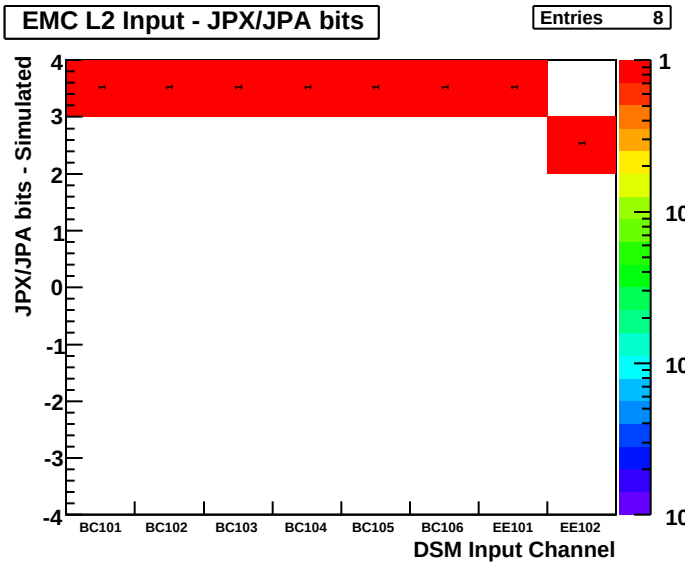

Endcap EMC L1 Input - High Eta Sum
Entries 12
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

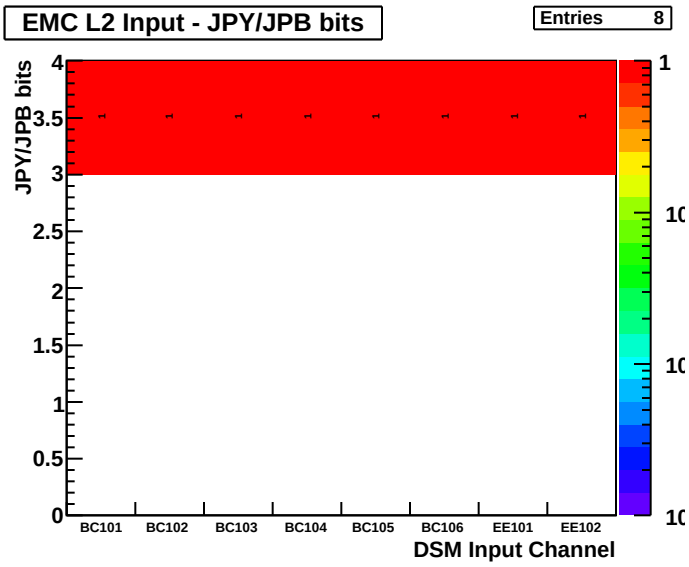

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

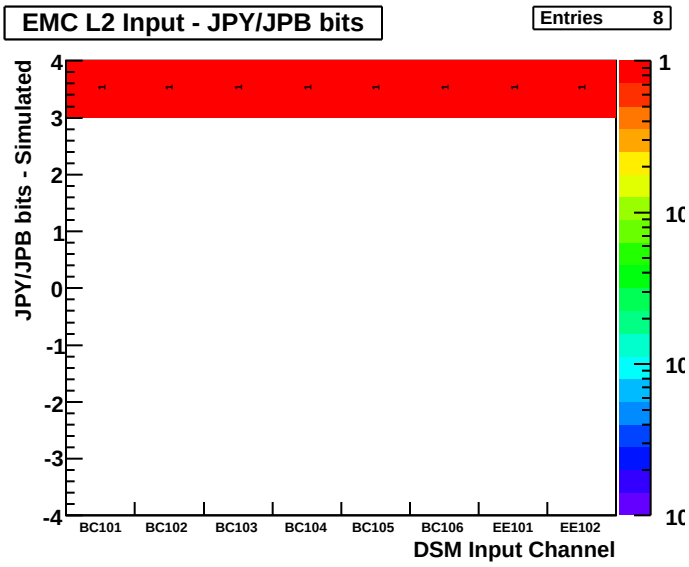

Endcap EMC L1 Input - High Tower Bits
Entries 12
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

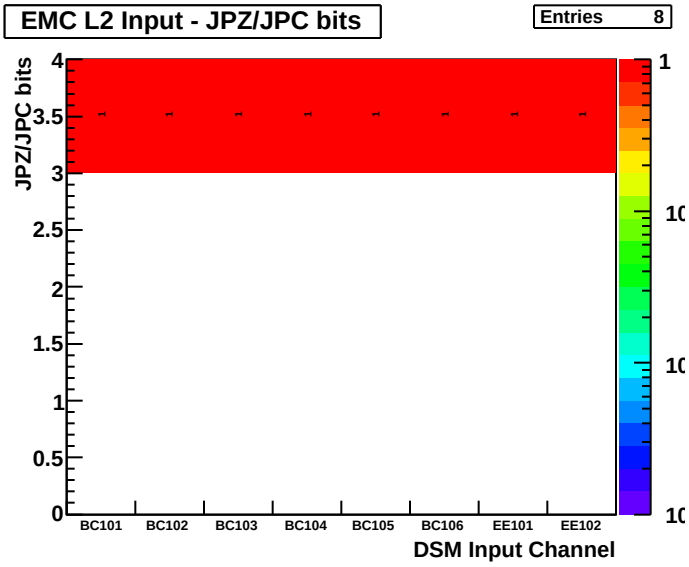

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

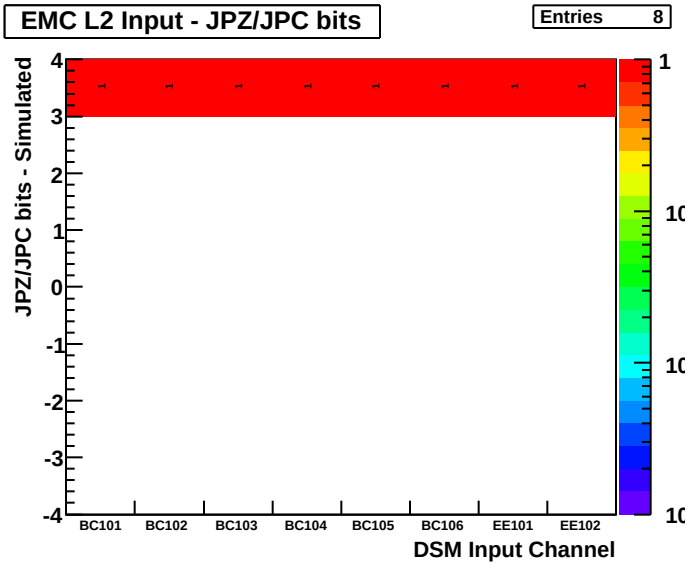

EMC L2 Input - JPX/JPA bits
Entries 8
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

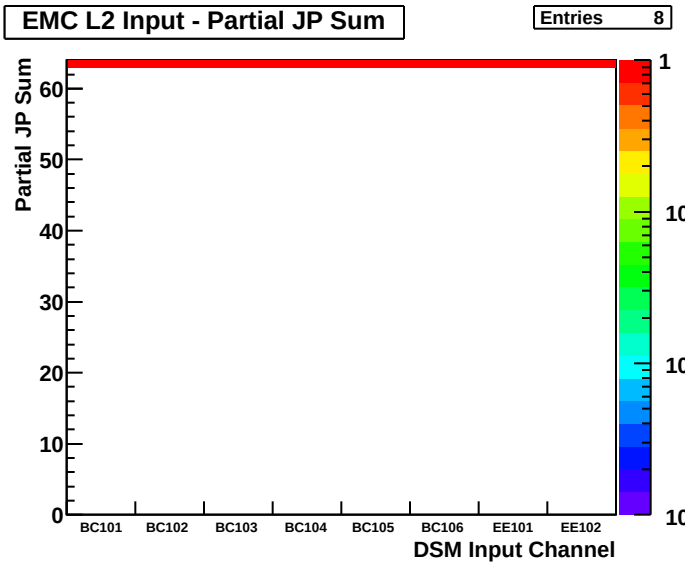

EMC L2 Input - JPX/JPA bits
Entries 8
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

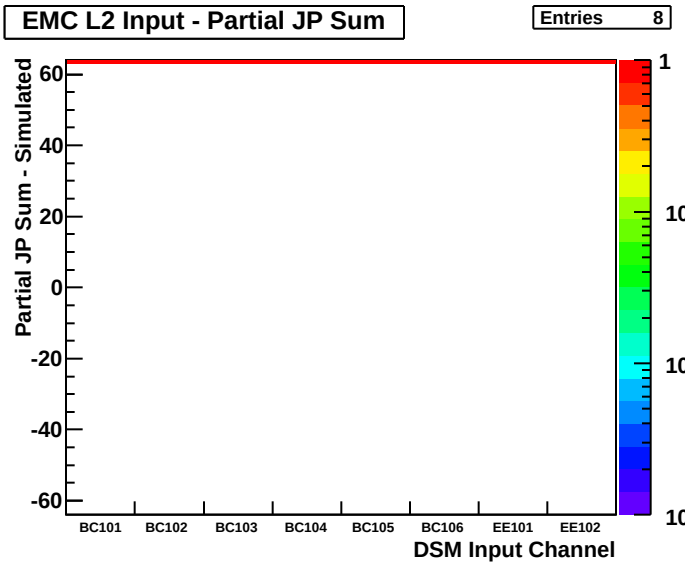

EMC L2 Input - JPY/JPB bits
Entries 8
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

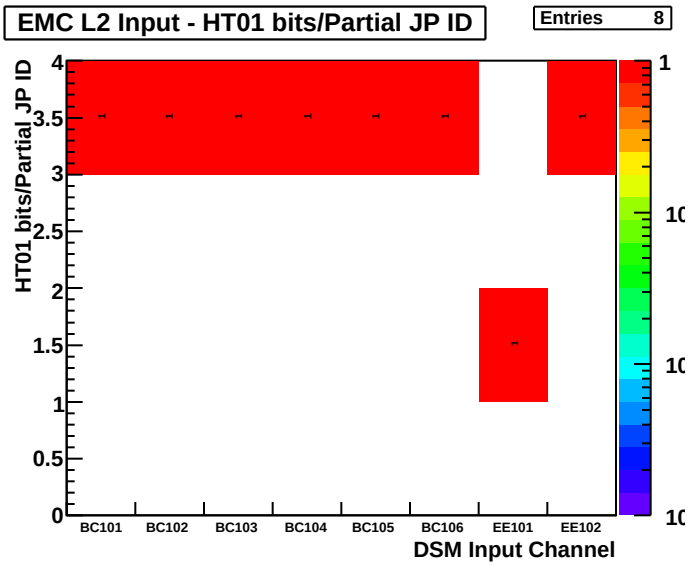

EMC L2 Input - JPY/JPB bits
Entries 8
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

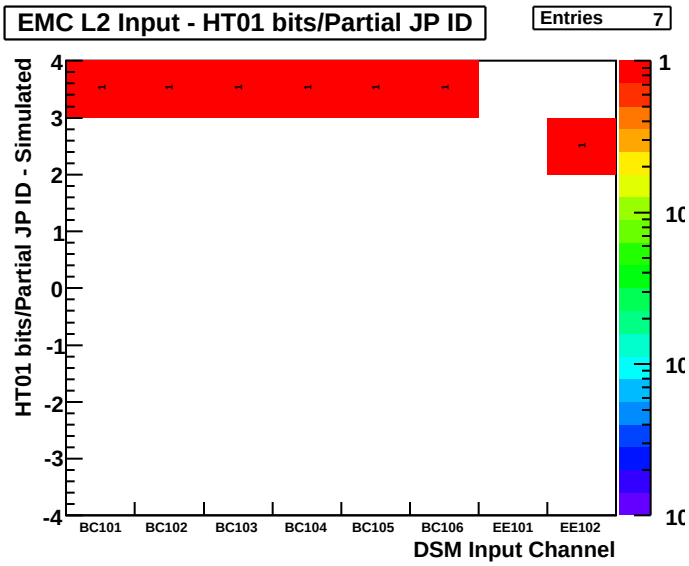

EMC L2 Input - JPZ/JPC bits
Entries 8
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

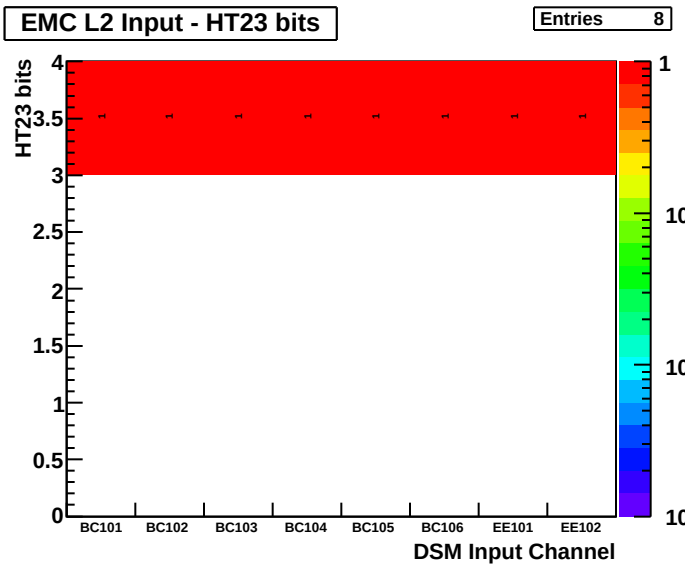

EMC L2 Input - JPZ/JPC bits
Entries 8
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

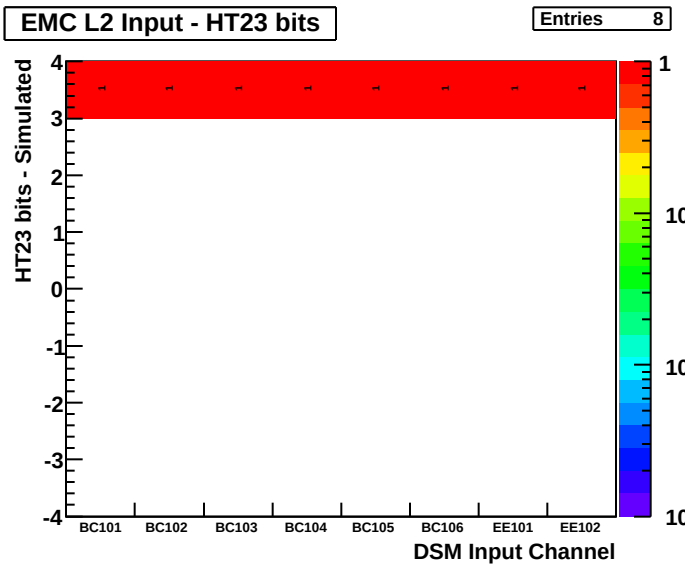

EMC L2 Input - Partial JP Sum
Entries 8
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

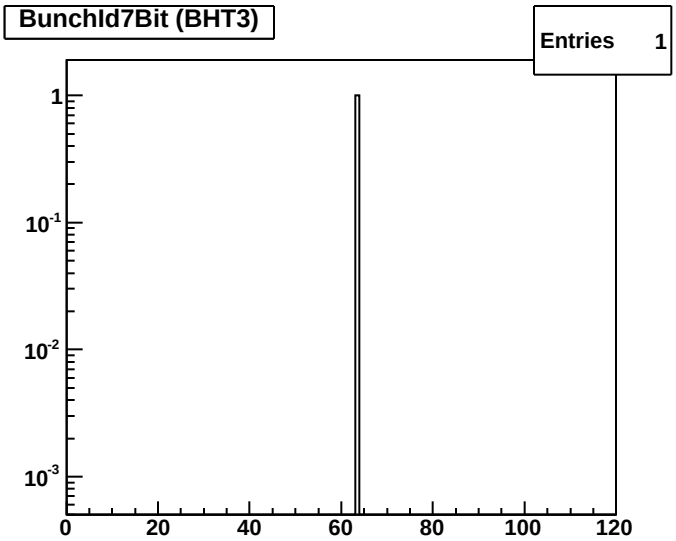

EMC L2 Input - Partial JP Sum
Entries 8
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

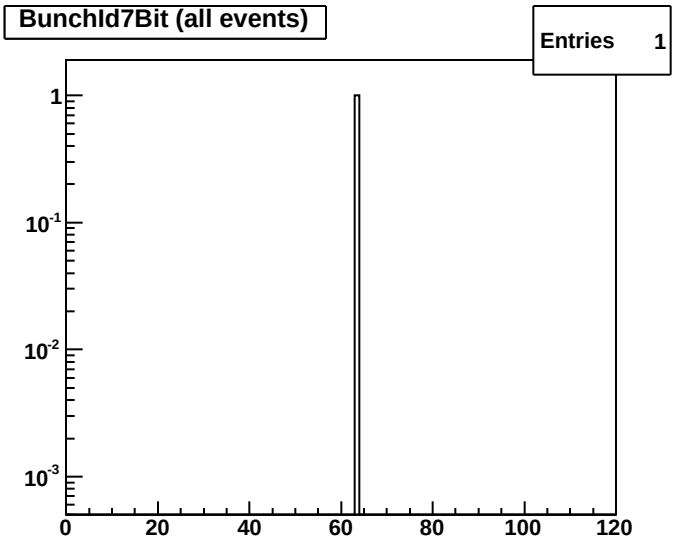

EMC L2 Input - HT01 bits/Partial JP ID
Entries 8
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

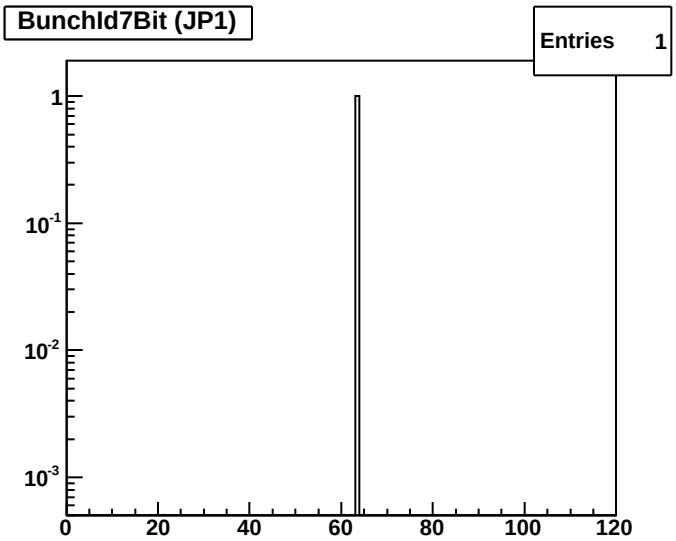

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

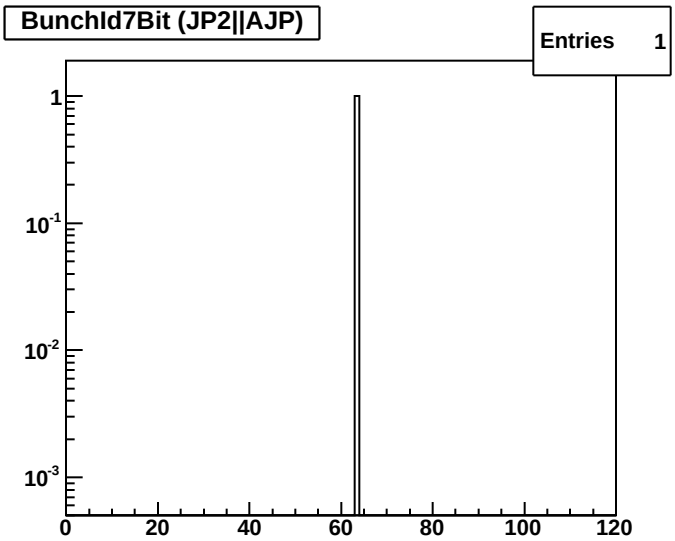

EMC L2 Input - HT23 bits
Entries 8
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

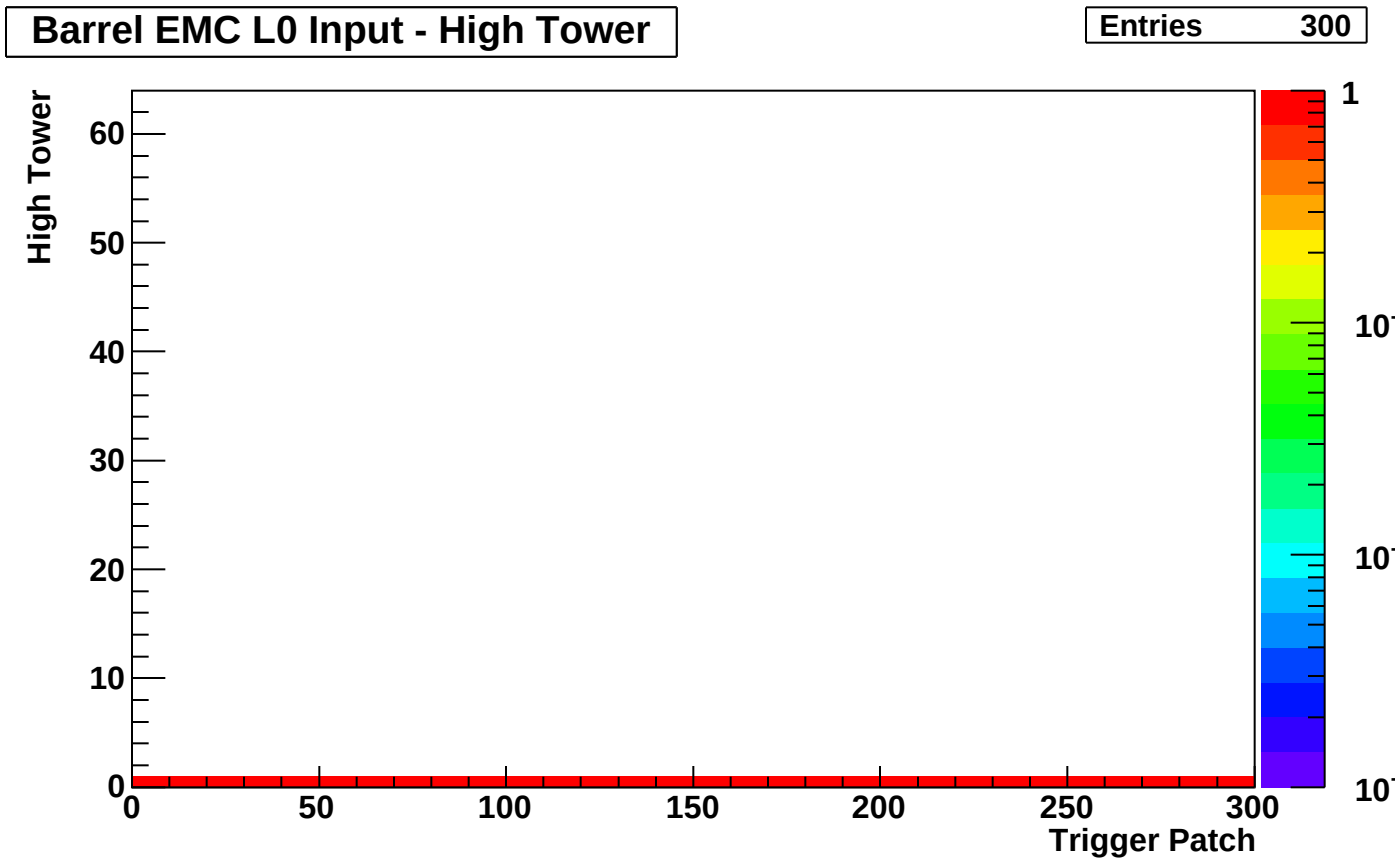

EMC L2 Input - HT23 bits
Entries 8
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

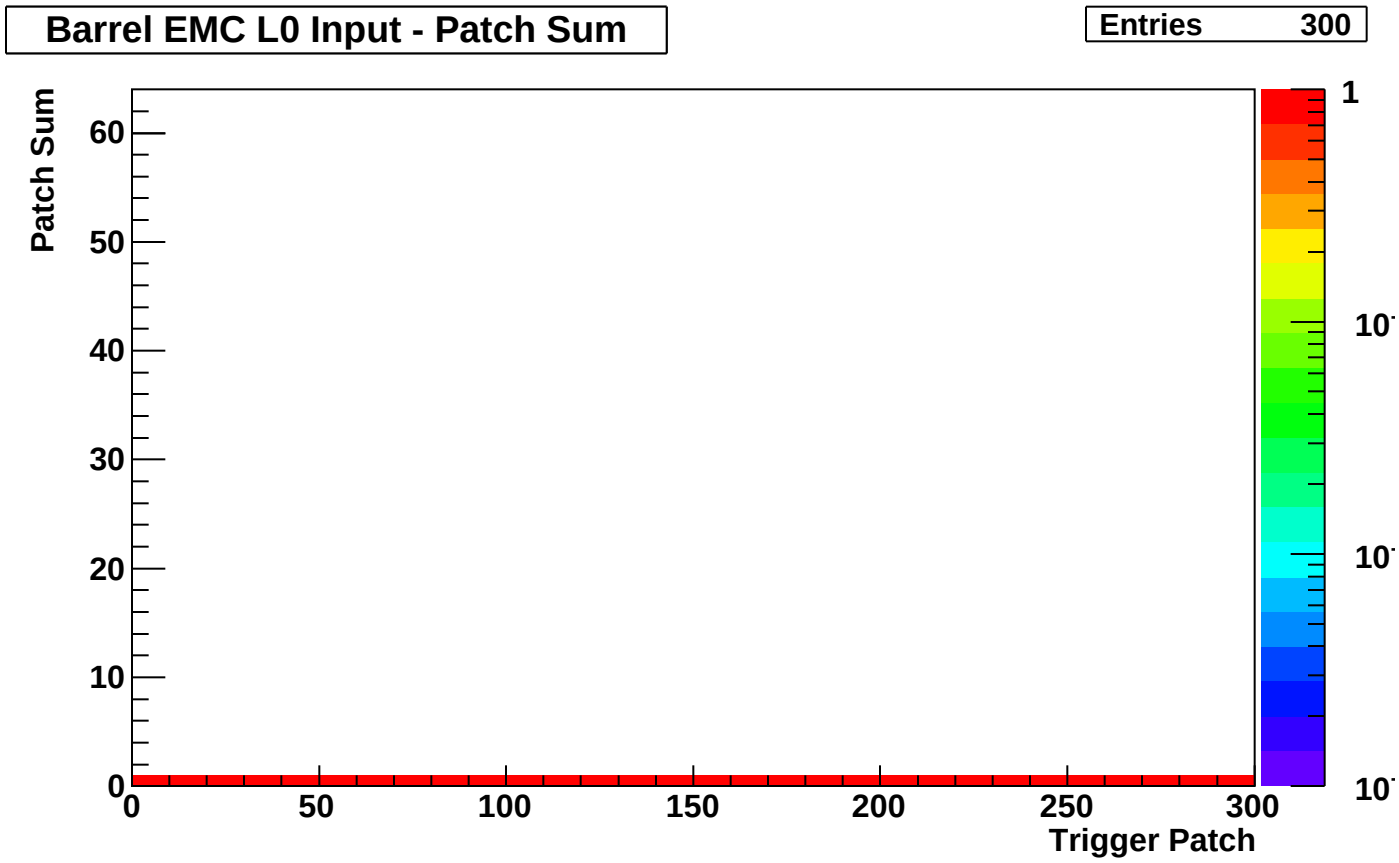

BunchId7Bit (BHT3)
Entries 1
1
10-1
10-2
10-3
0
20
40
60
80
100
120

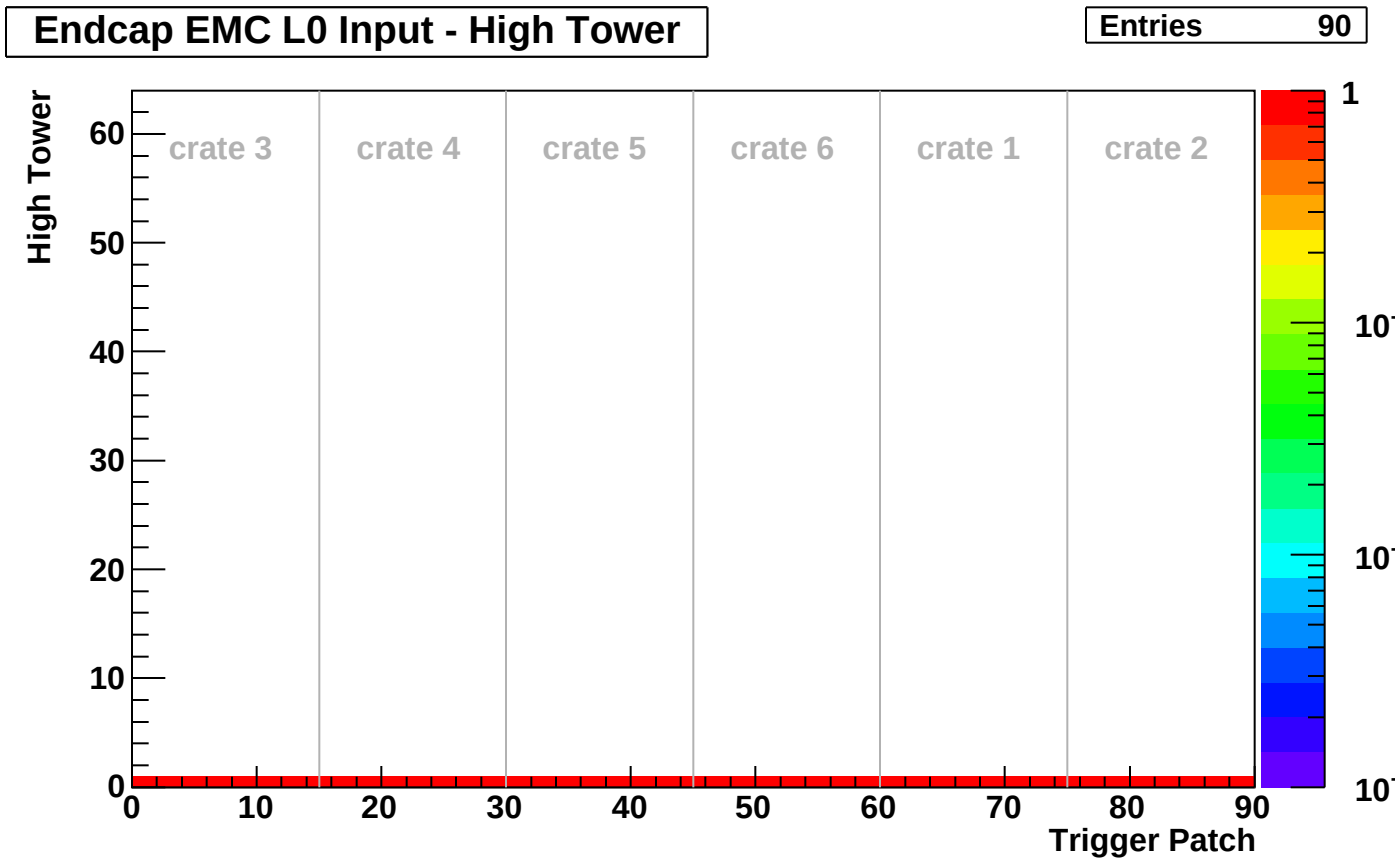

BunchId7Bit (all events)
Entries 1
1
10-1
10-2
10-3
0
20
40
60
80
100
120

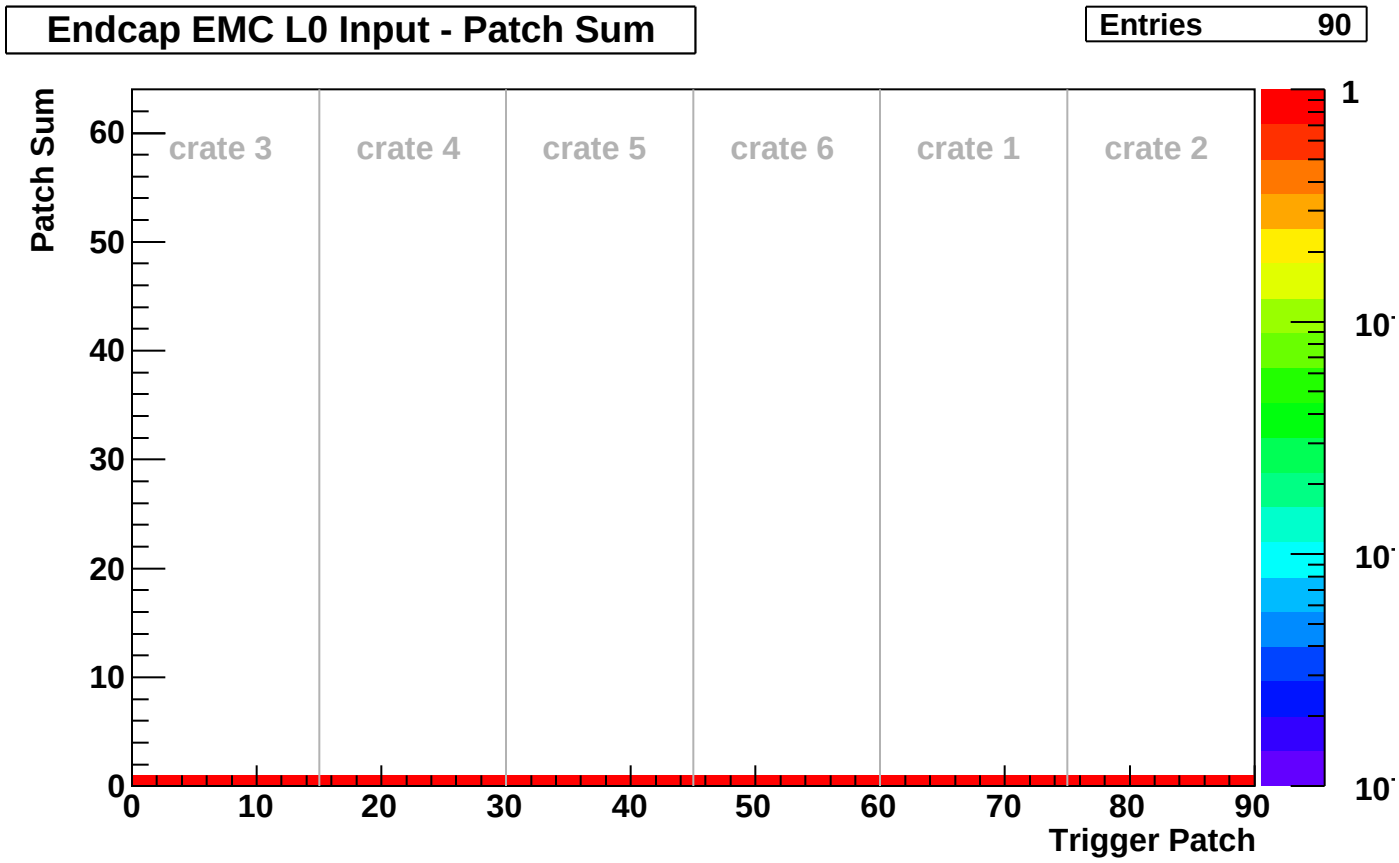

BunchId7Bit (JP1)
Entries 1
1
10-1
10-2
10-3
0
20
40
60
80
100
120

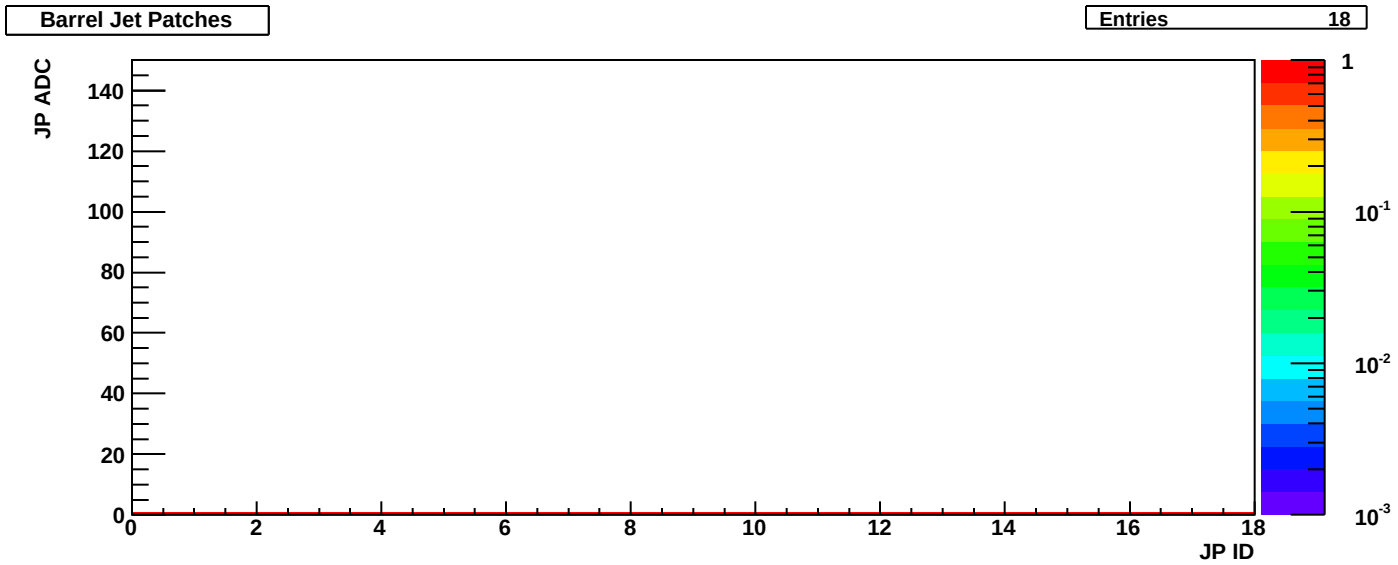

BunchId7Bit (JP2||AJP)
Entries 1
1
10-1
10-2
10-3
0
20
40
60
80
100
120

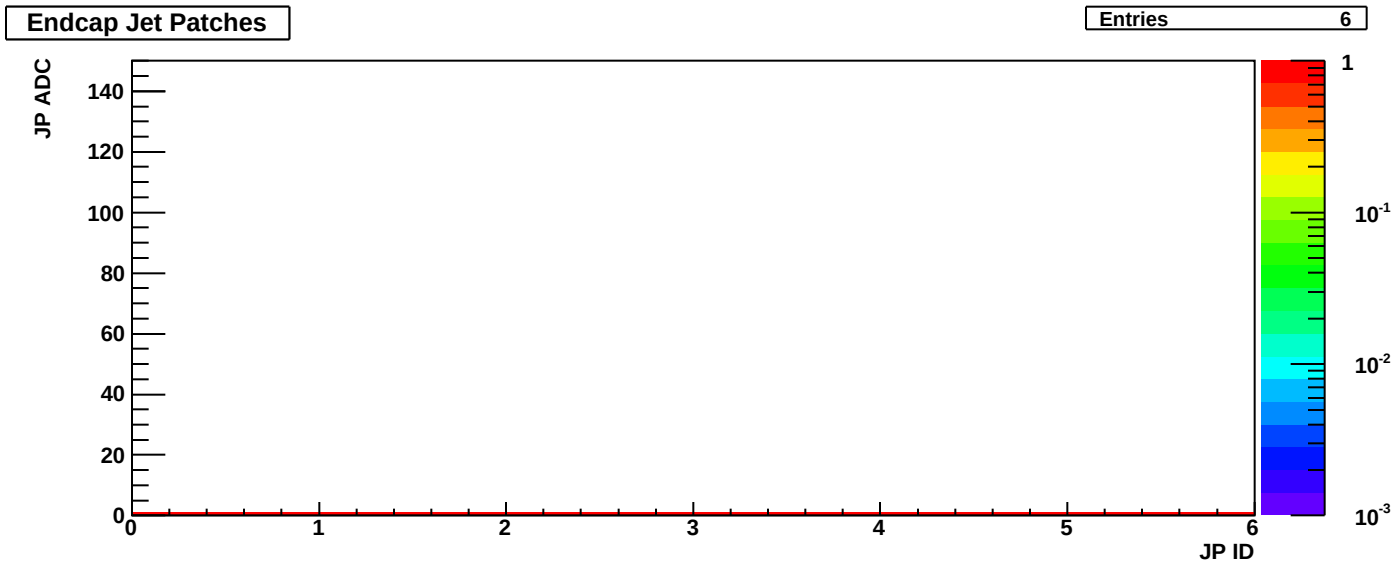

Barrel EMC L0 Input - High Tower
Entries 300
High Tower
0
10
20
30
40
50
60
0
50
100
150
200
250
300
Trigger Patch
1

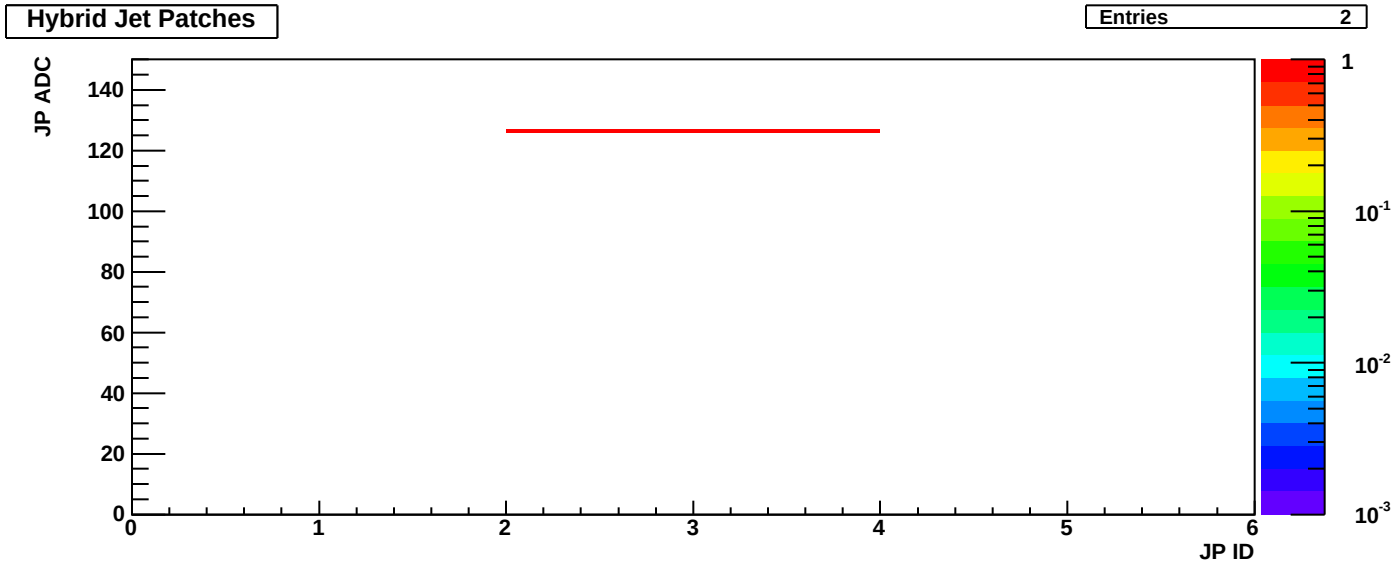

Barrel EMC L0 Input - Patch Sum
Entries 300
Patch Sum
0
10
20
30
40
50
60
0
50
100
150
200
250
300
Trigger Patch
1

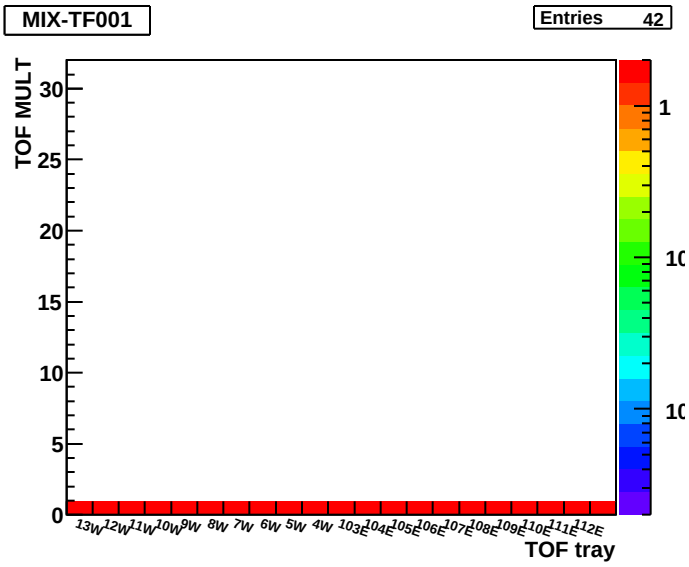

Endcap EMC L0 Input - High Tower
Entries 90
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

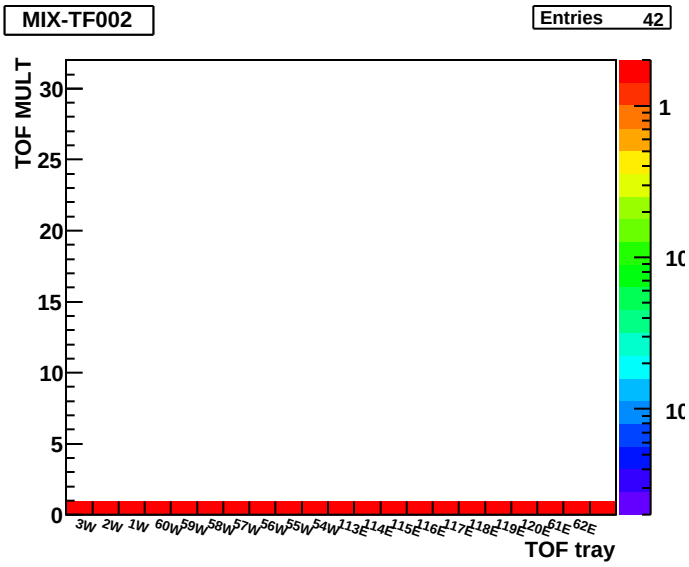

Endcap EMC L0 Input - Patch Sum
Entries 90
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

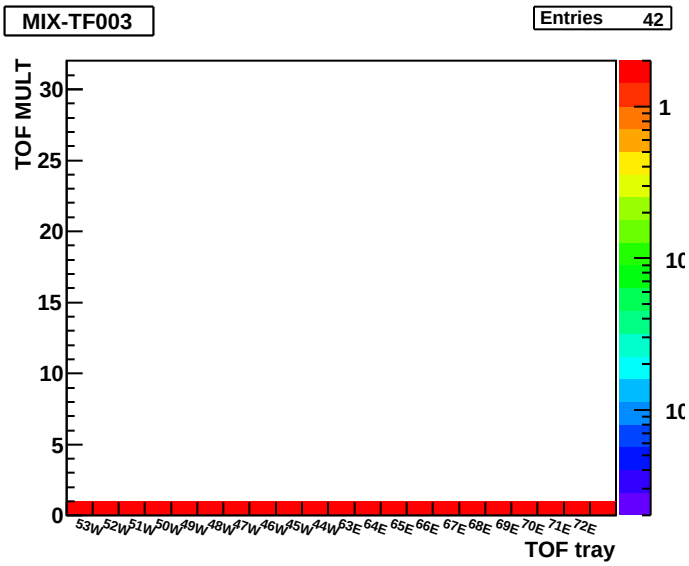

Barrel Jet Patches
Entries 18
JP ADC
JP ID

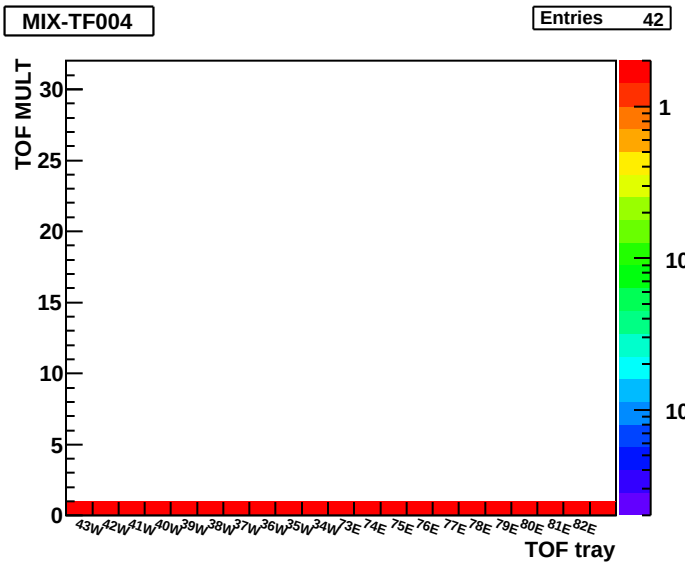

Endcap Jet Patches
Entries 6
JP ADC
JP ID

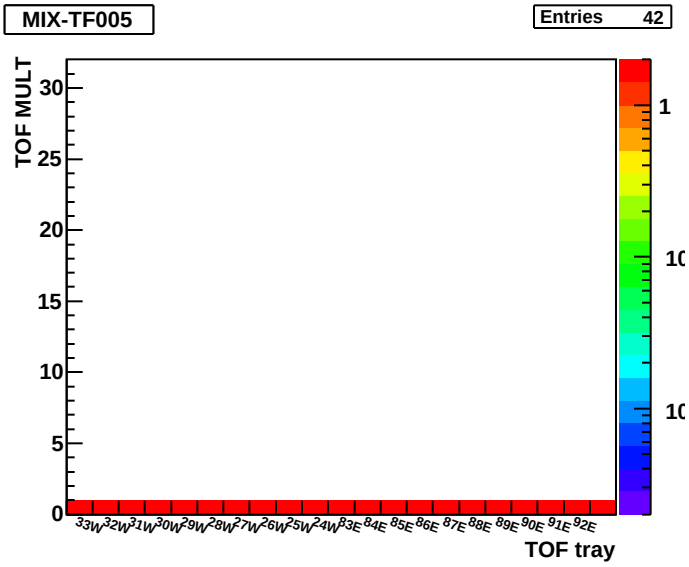

Hybrid Jet Patches
Entries 2
JP ADC
JP ID

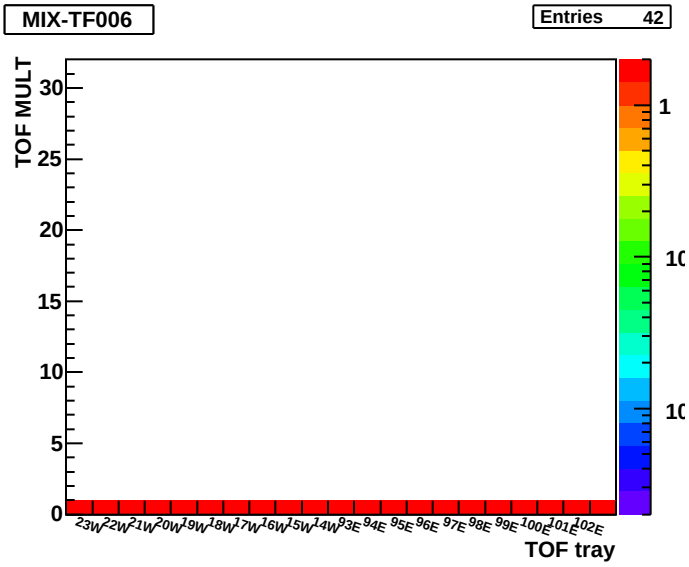

MIX-TF001
Entries 42
TOF MULT
TOF tray

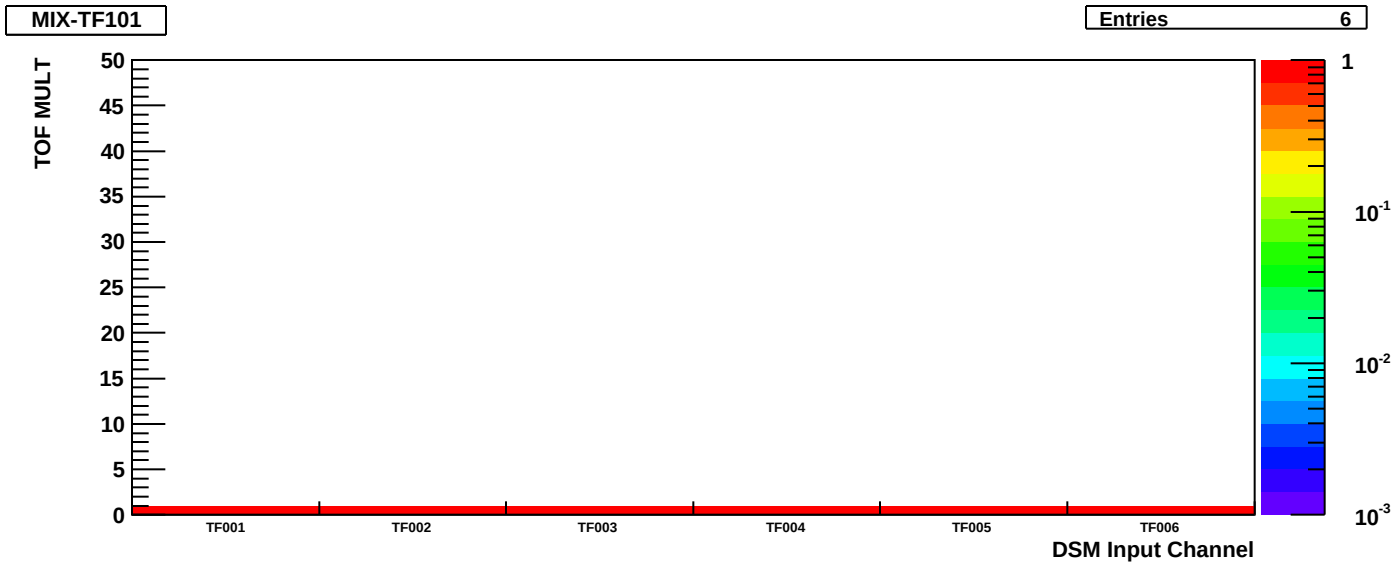

MIX-TF002
Entries 42
TOF MULT
TOF tray

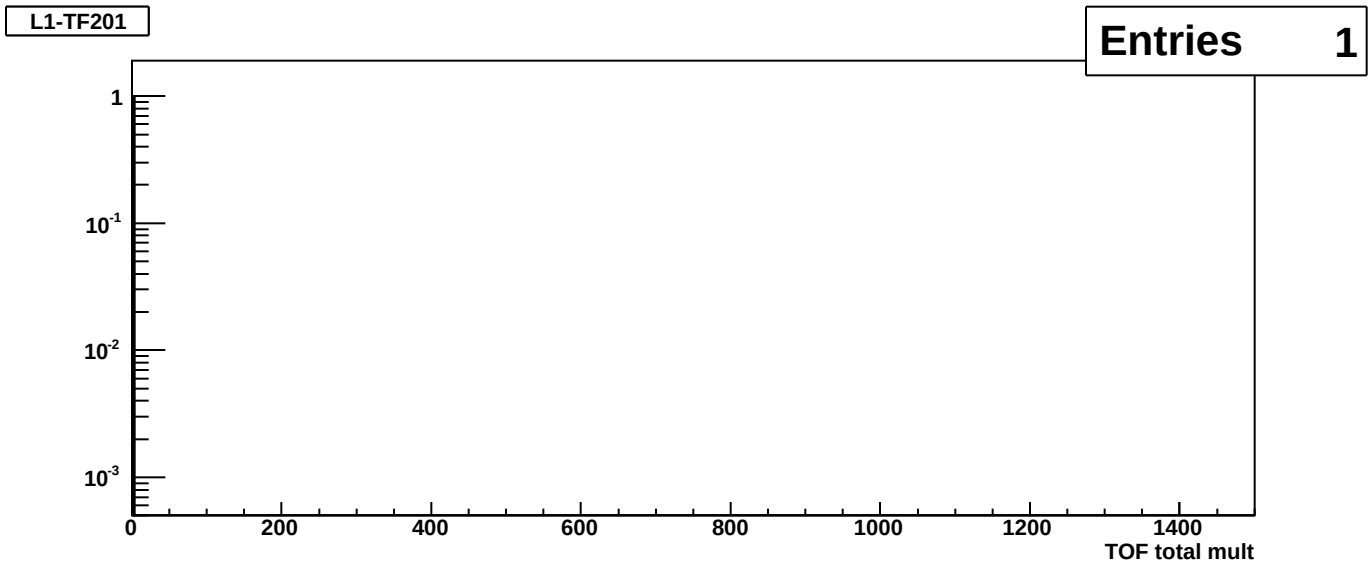

MIX-TF003
Entries 42
TOF MULT
TOF tray

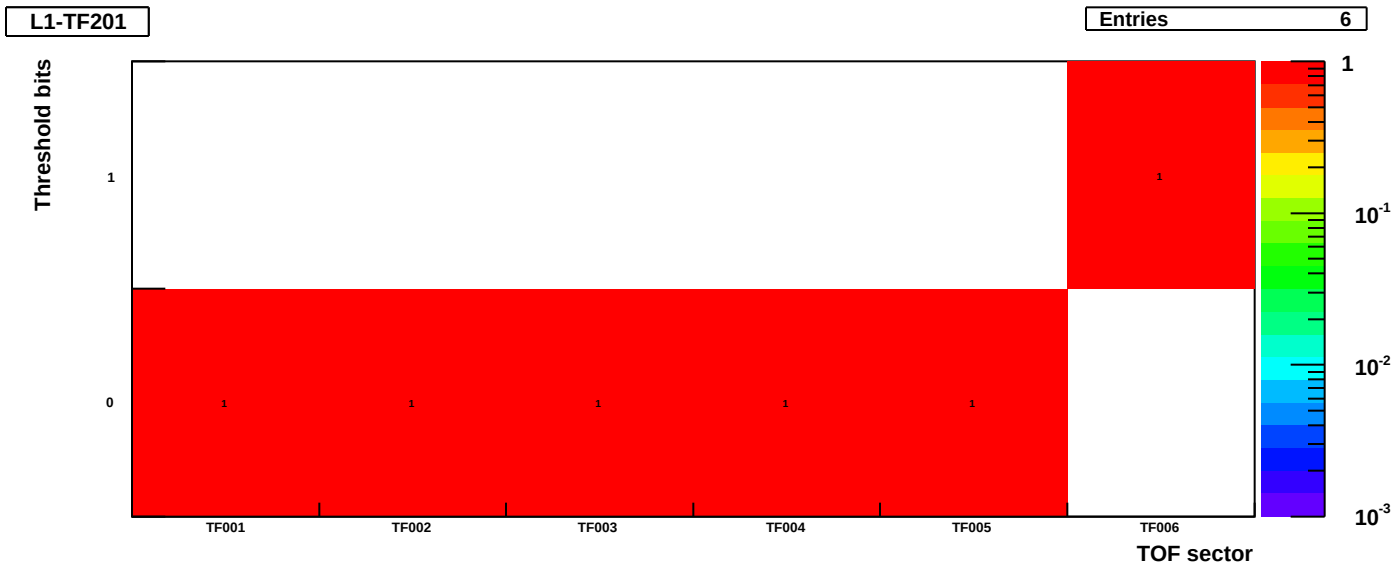

MIX-TF004
Entries 42
TOF MULT
TOF tray

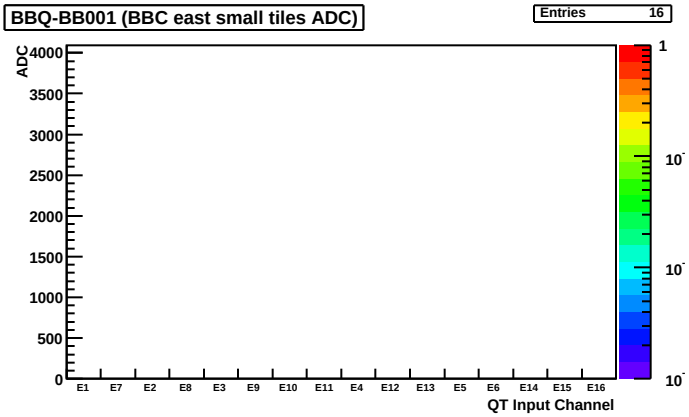

MIX-TF005
Entries 42
TOF MULT
TOF tray

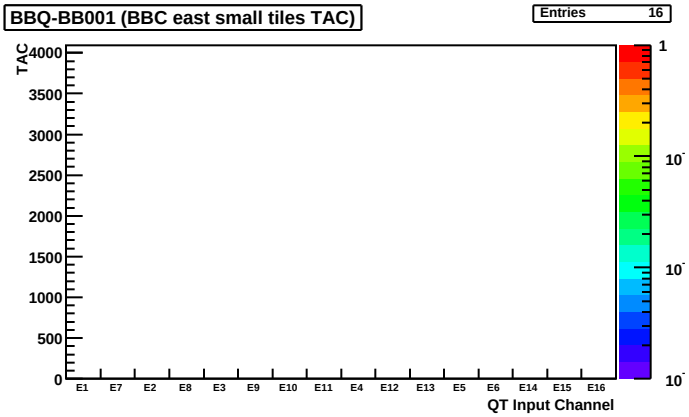

MIX-TF006
Entries 42
TOF MULT
TOF tray

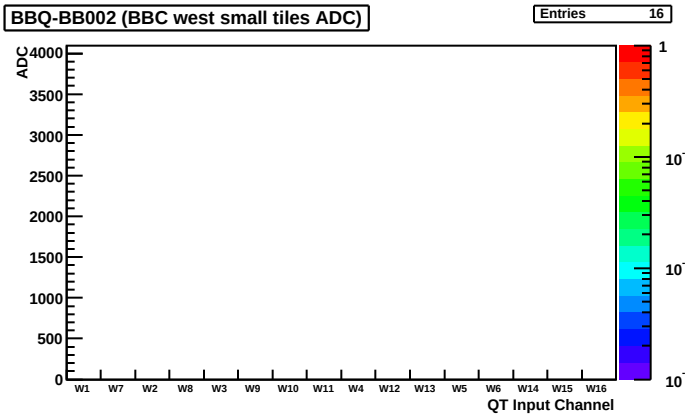

MIX-TF101
Entries 6
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
1
10^{-1}
10^{-2}
10^{-3}

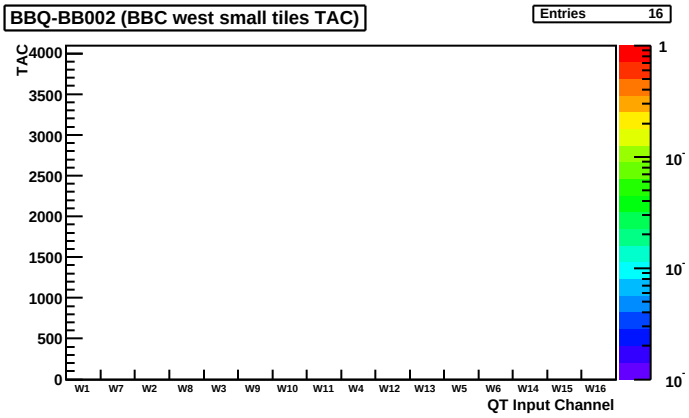

L1-TF201
Entries 1
1
10^{-1}
10^{-2}
10^{-3}
0
200
400
600
800
1000
1200
1400
TOF total mult

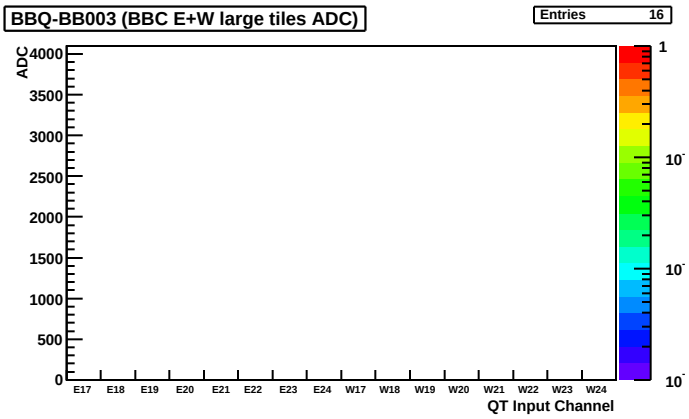

L1-TF201
Entries 6
Threshold bits
1
0
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
1
10^{-1}
10^{-2}
10^{-3}

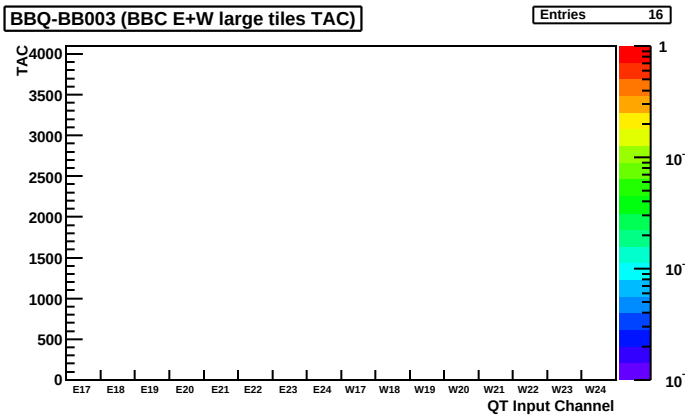

BBQ-BB001 (BBC east small tiles ADC)
Entries 16
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

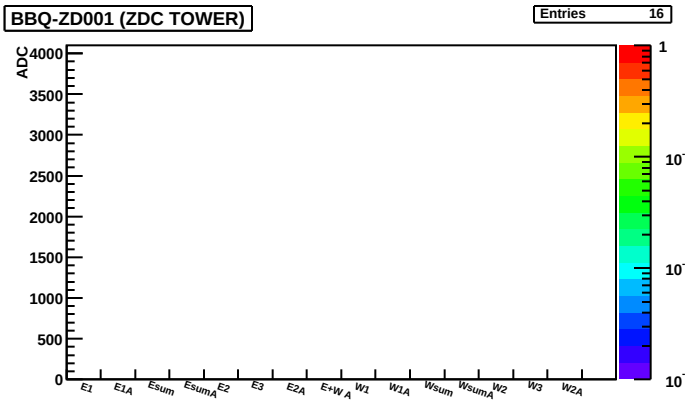

BBQ-BB001 (BBC east small tiles TAC)
Entries 16
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

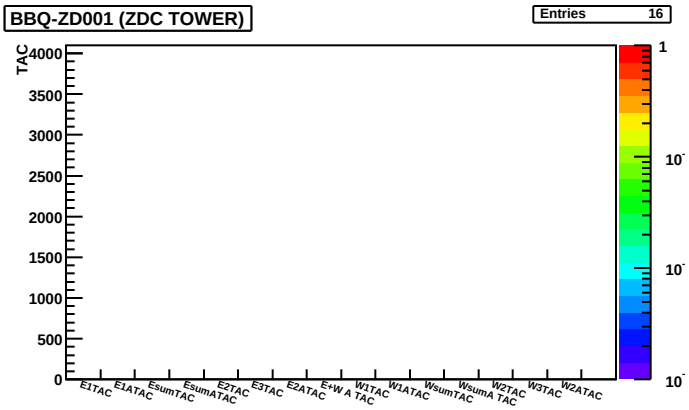

BBQ-BB002 (BBC west small tiles ADC)
Entries 16
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

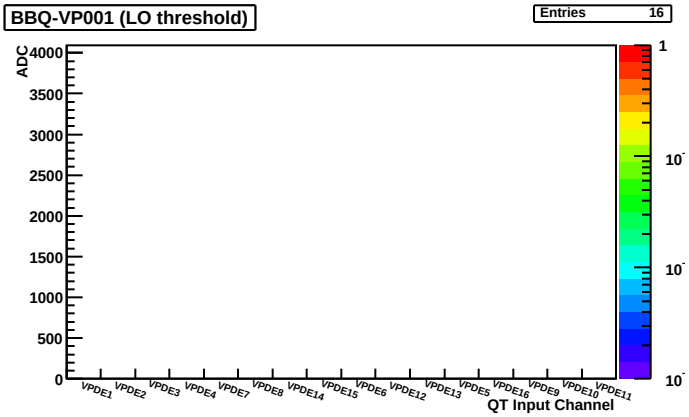

BBQ-BB002 (BBC west small tiles TAC)
Entries 16
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

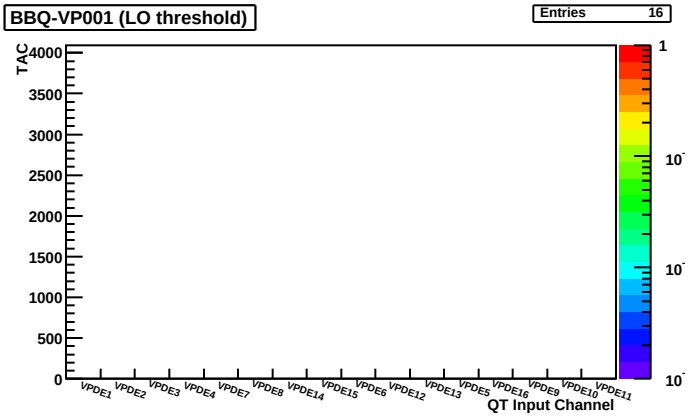

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 16
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

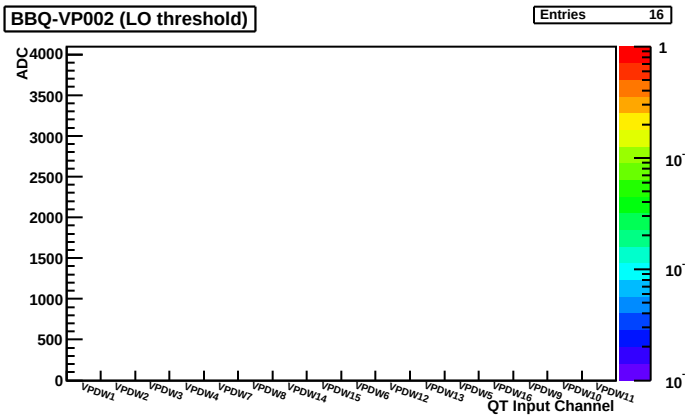

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 16
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

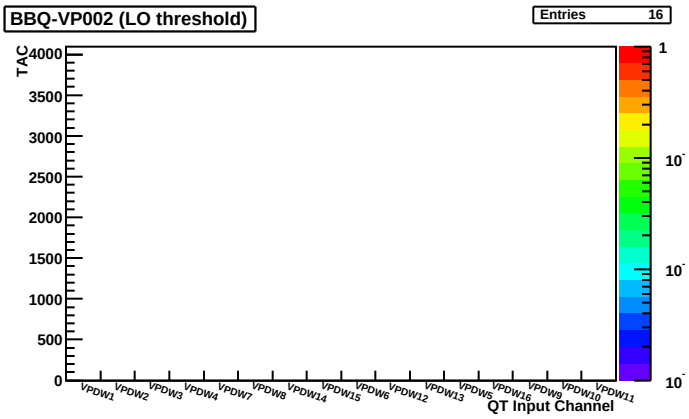

BBQ-ZD001 (ZDC TOWER)
Entries 16
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

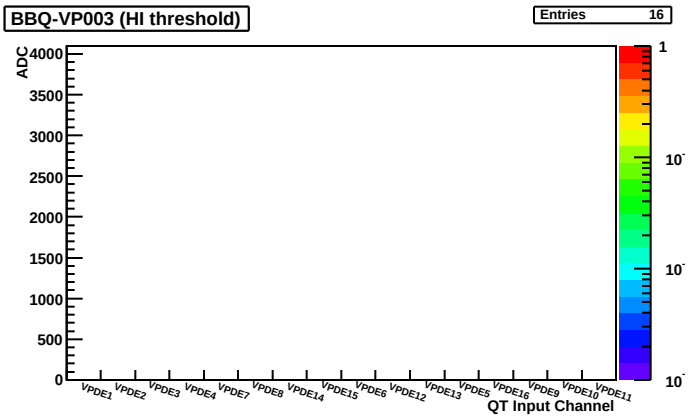

BBQ-ZD001 (ZDC TOWER)
Entries 16
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

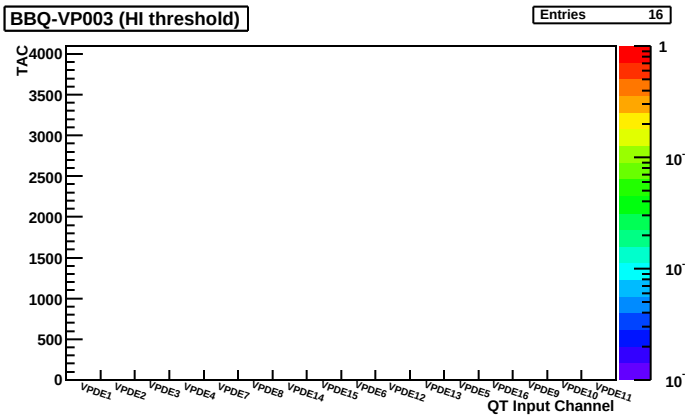

BBQ-VP001 (LO threshold)
Entries 16
ADC
QT Input Channel

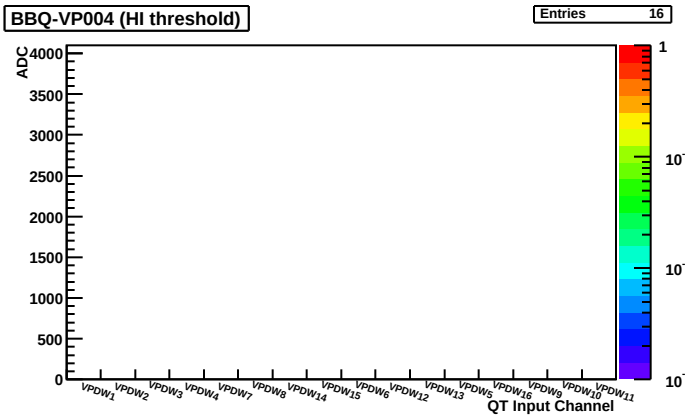

BBQ-VP001 (LO threshold)
Entries 16
TAC
QT Input Channel

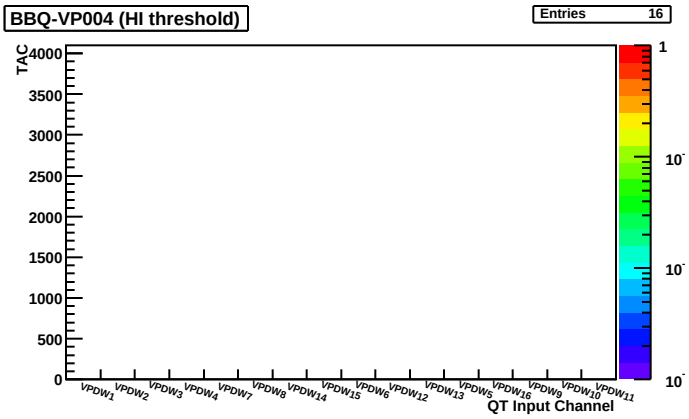

BBQ-VP002 (LO threshold)
Entries 16
ADC
QT Input Channel

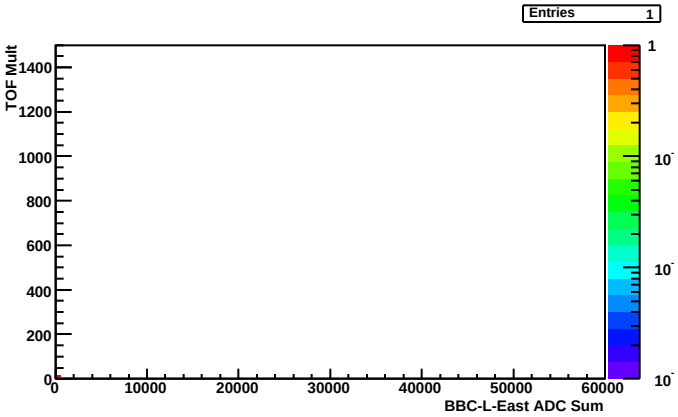

BBQ-VP002 (LO threshold)
Entries 16
TAC
QT Input Channel

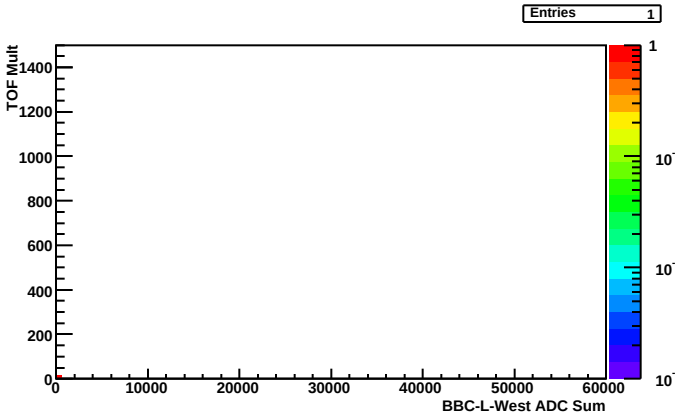

BBQ-VP003 (HI threshold)
Entries 16
ADC
QT Input Channel

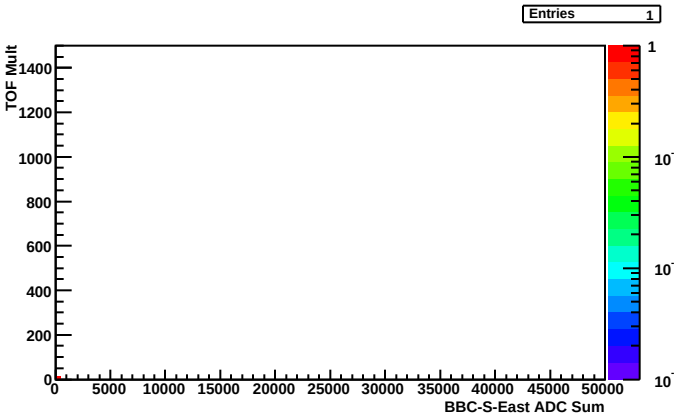

BBQ-VP003 (HI threshold)
Entries 16
TAC
QT Input Channel

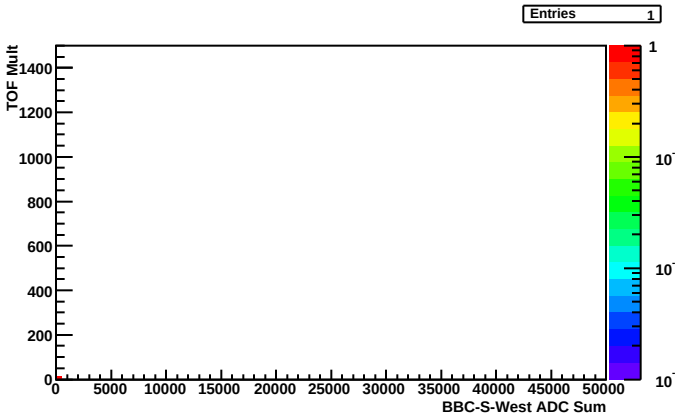

BBQ-VP004 (HI threshold)
Entries 16
ADC
QT Input Channel

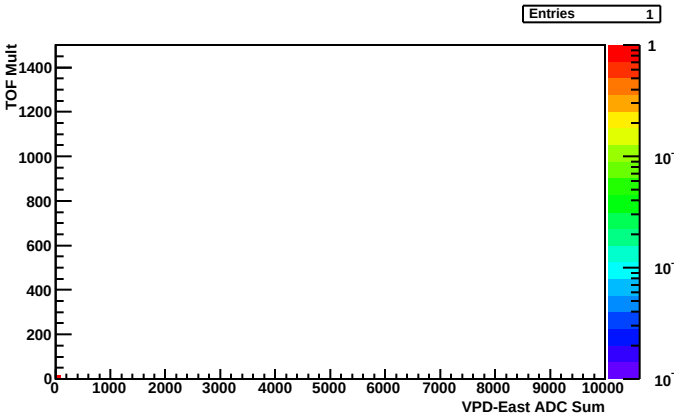

BBQ-VP004 (HI threshold)
Entries 16
TAC
QT Input Channel

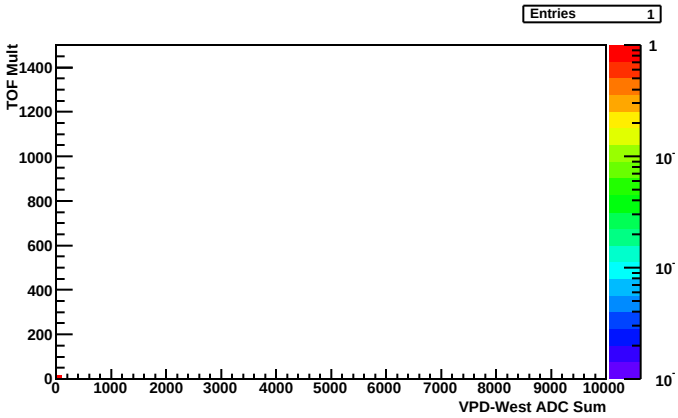

Entries 1
TOF Mult
BBC-L-East ADC Sum

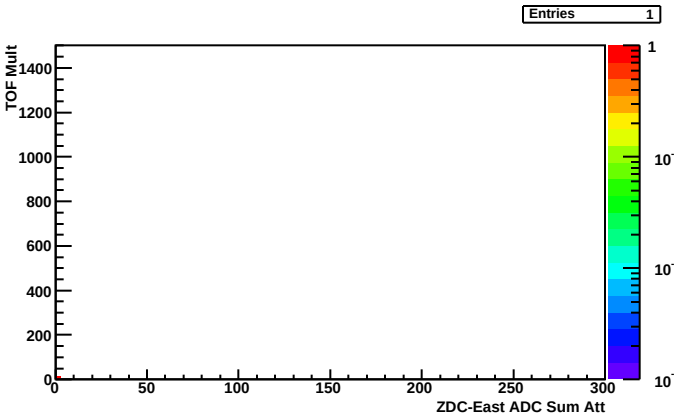

Entries 1
TOF Mult
BBC-L-West ADC Sum

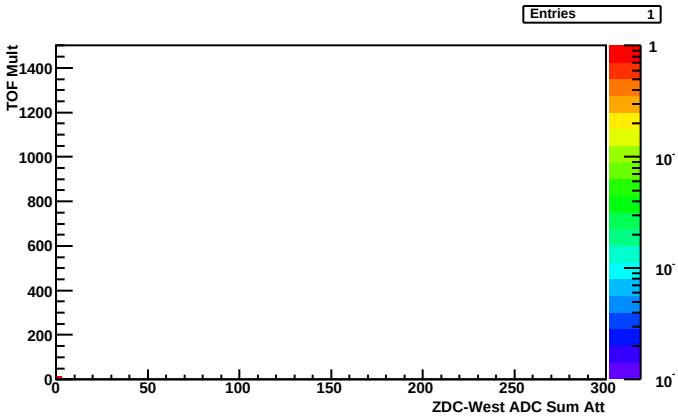

Entries 1
TOF Mult
BBC-S-East ADC Sum

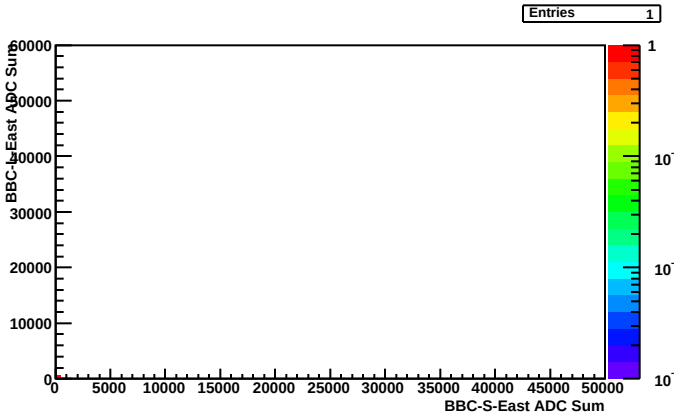

Entries 1
TOF Mult
BBC-S-West ADC Sum

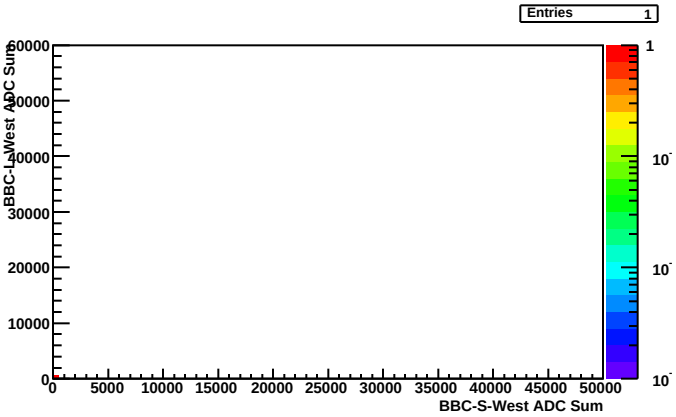

Entries 1
TOF Mult
VPD-East ADC Sum

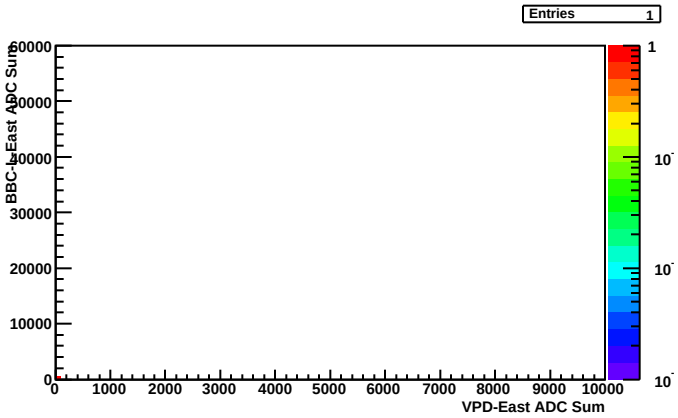

Entries 1
TOF Mult
VPD-West ADC Sum

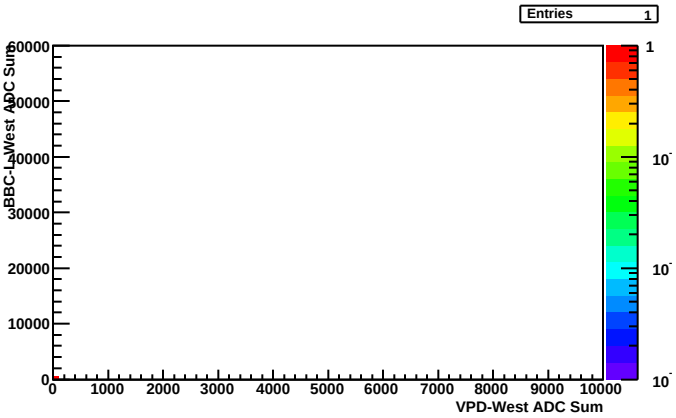

Entries 1
TOF Mult
ZDC-East ADC Sum Att

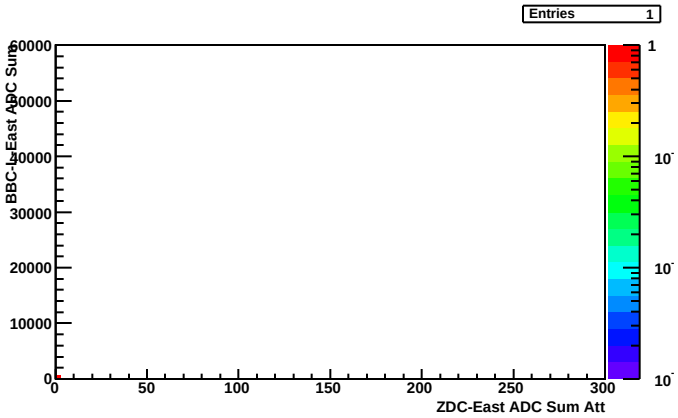

Entries 1
TOF Mult
ZDC-West ADC Sum Att

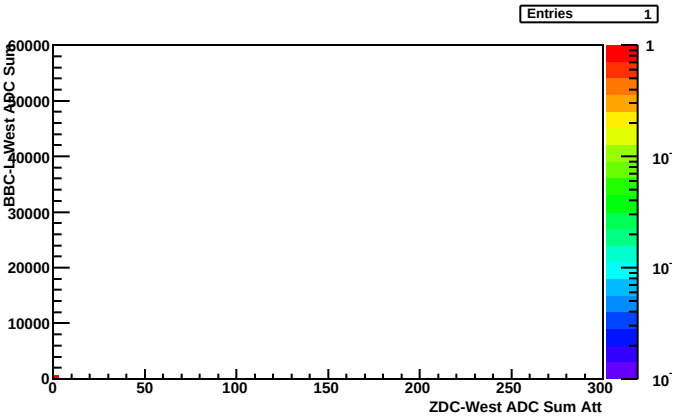

Entries 1
BBC-L-East ADC Sum
BBC-S-East ADC Sum

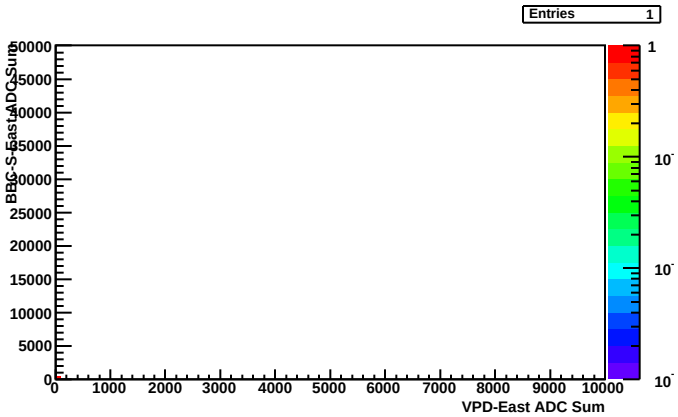

Entries 1
BBC-L-West ADC Sum
BBC-S-West ADC Sum

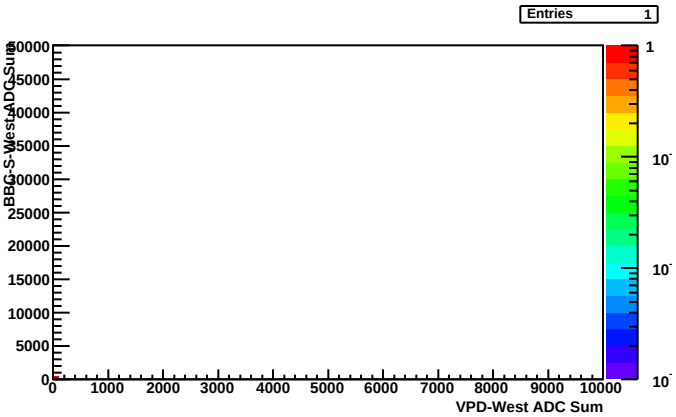

Entries 1
BBC-L-East ADC Sum
VPD-East ADC Sum

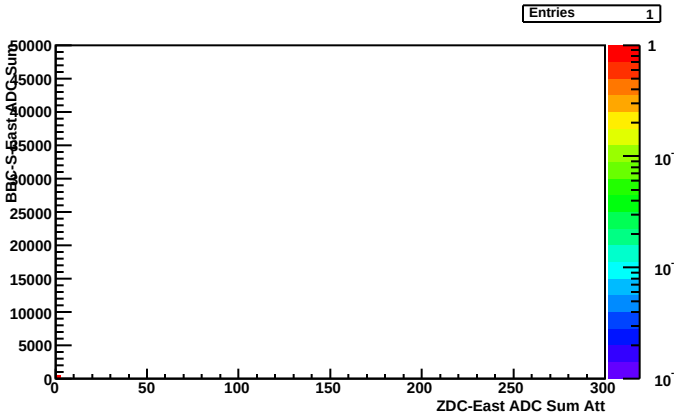

Entries 1
BBC-L-West ADC Sum
VPD-West ADC Sum

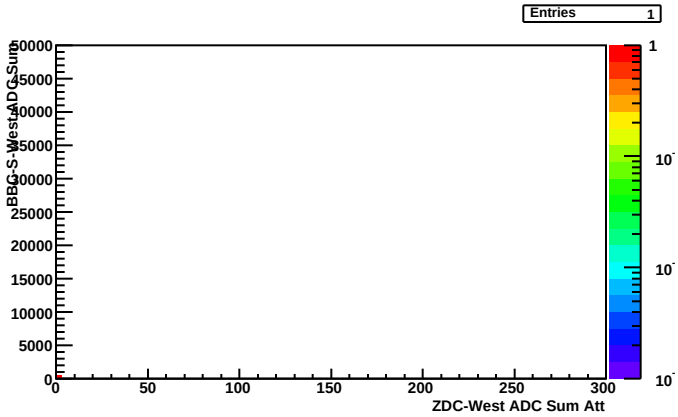

Entries 1
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

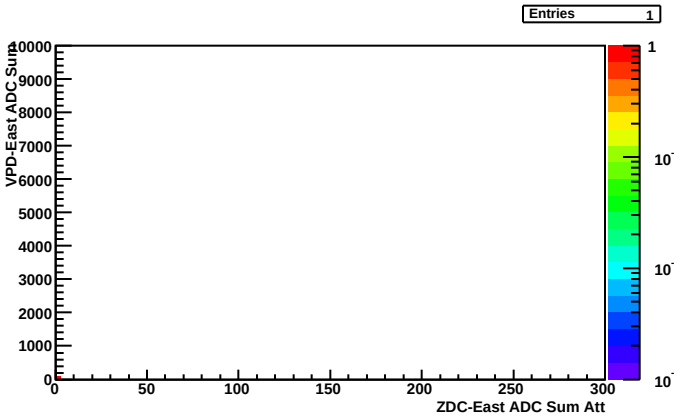

Entries 1
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

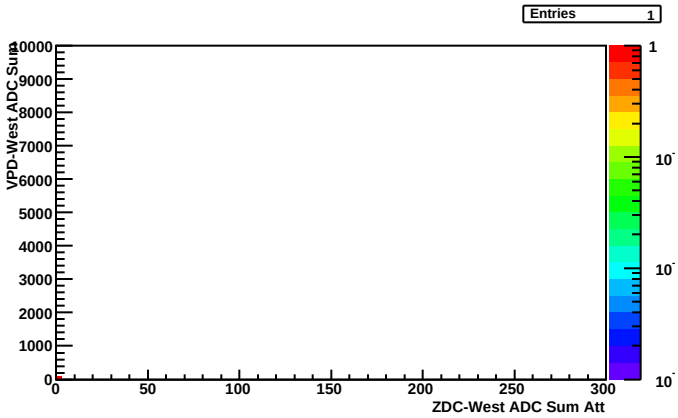

Entries 1
BBC-S-East ADC Sum
VPD-East ADC Sum

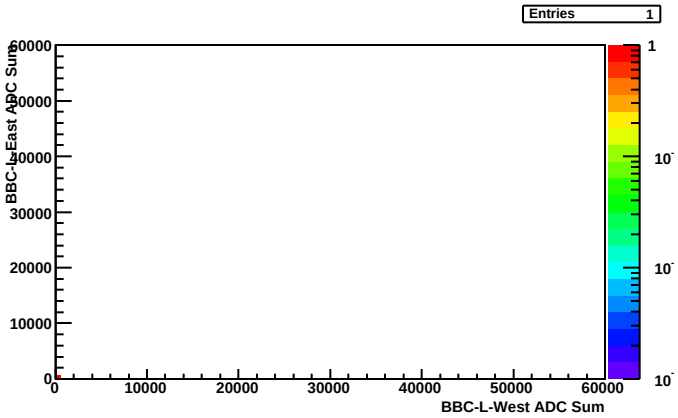

Entries 1
BBC-S-West ADC Sum
VPD-West ADC Sum

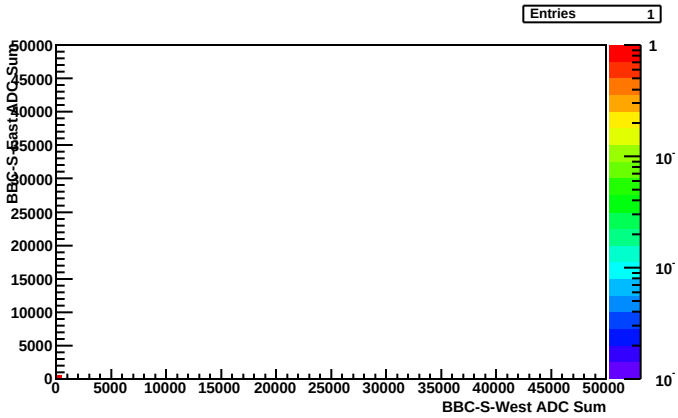

Entries 1
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

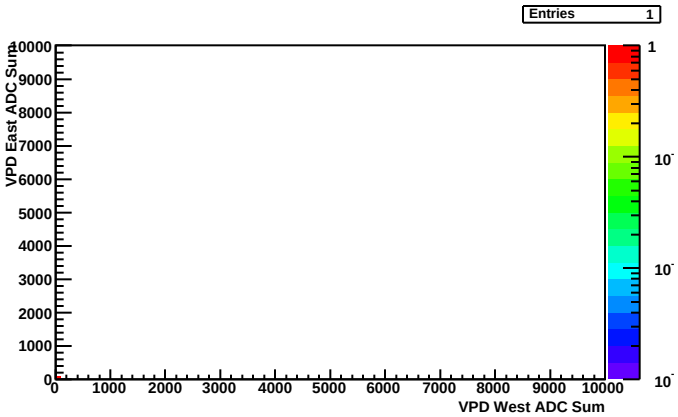

Entries 1
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

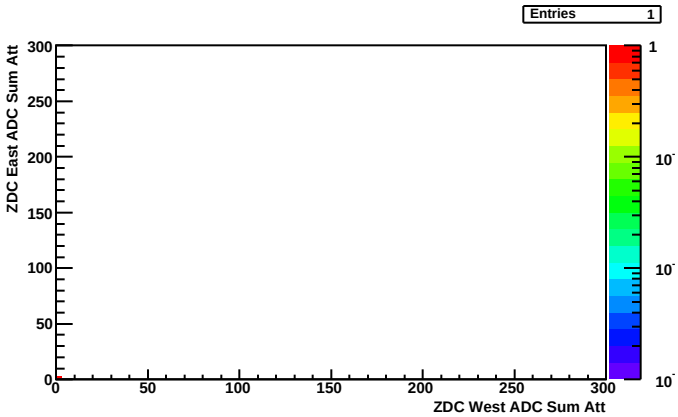

Entries 1
VPD-East ADC Sum
ZDC-East ADC Sum Att

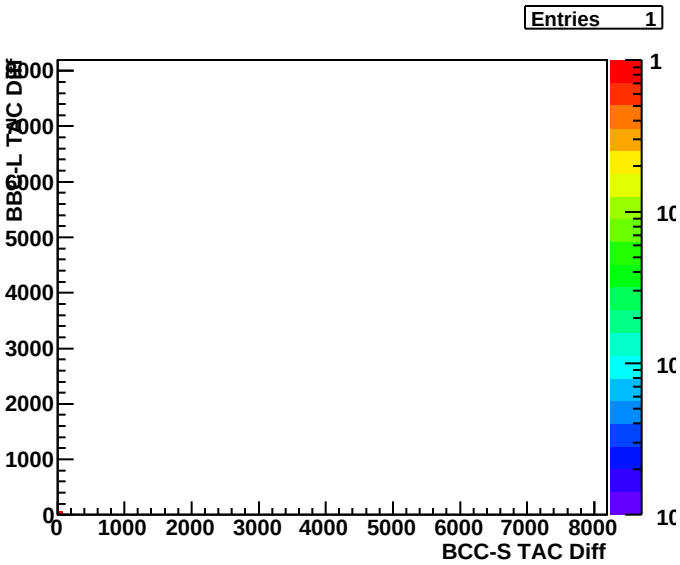

Entries 1
VPD-West ADC Sum
ZDC-West ADC Sum Att

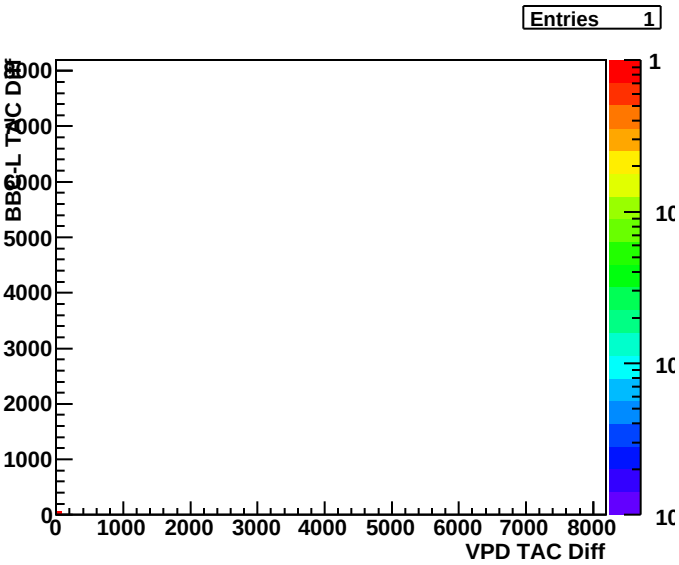

Entries 1
BBC-L-East ADC Sum
BBC-L-West ADC Sum

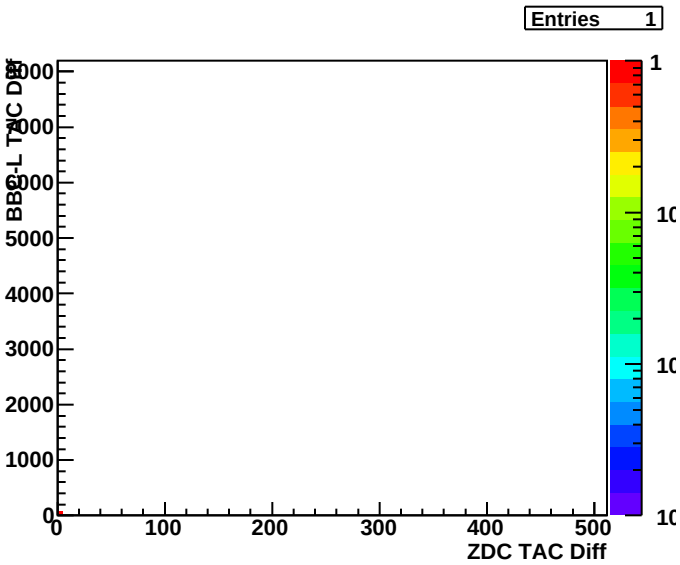

Entries 1
BBC-S-East ADC Sum
BBC-S-West ADC Sum

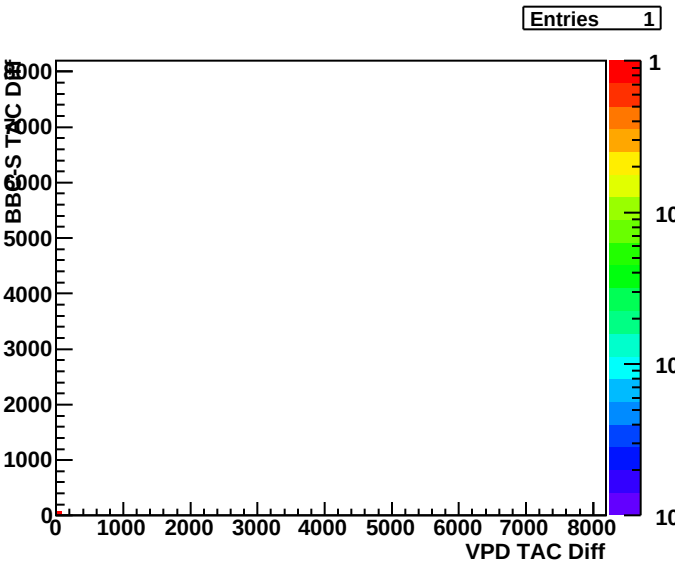

Entries 1
VPD East ADC Sum
VPD West ADC Sum

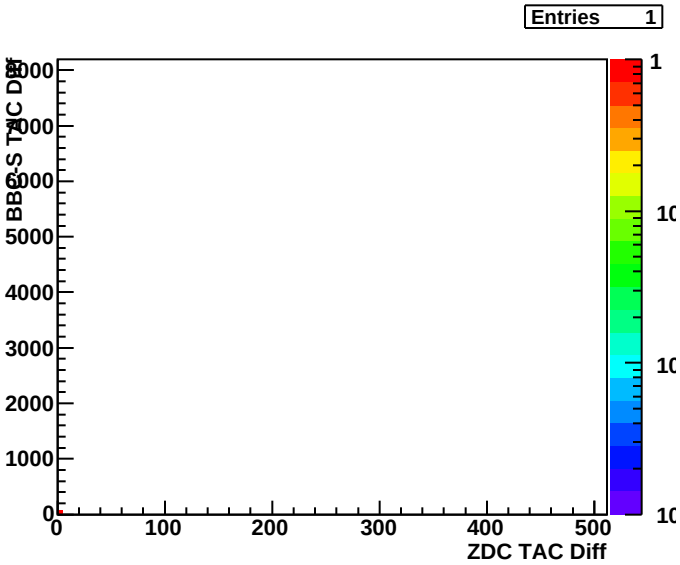

Entries 1
ZDC East ADC Sum Att
ZDC West ADC Sum Att

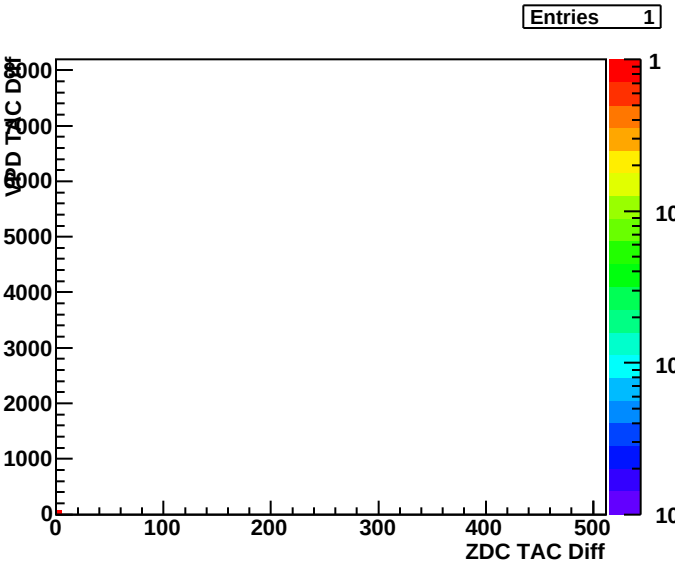

Entries 1
BBC-L TAC Diff
BCC-S TAC Diff

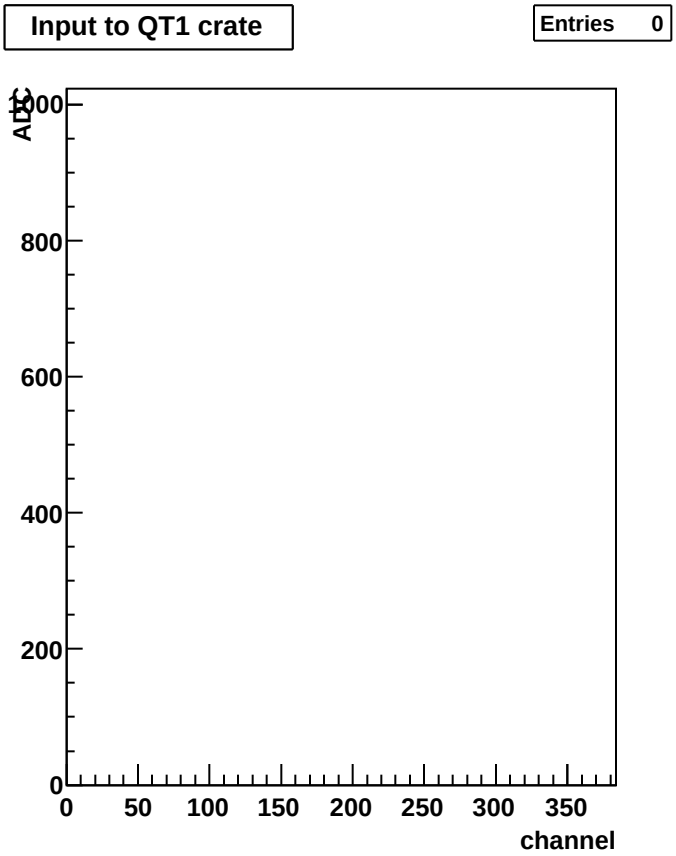

Entries 1
BBC-L TAC Diff
VPD TAC Diff

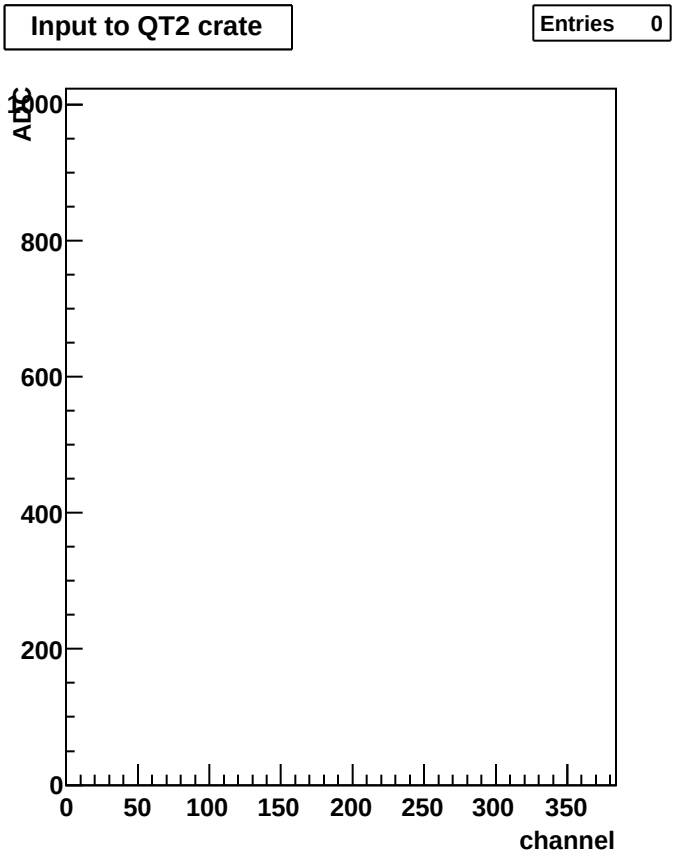

Entries 1
BBC-L TAC Diff
ZDC TAC Diff

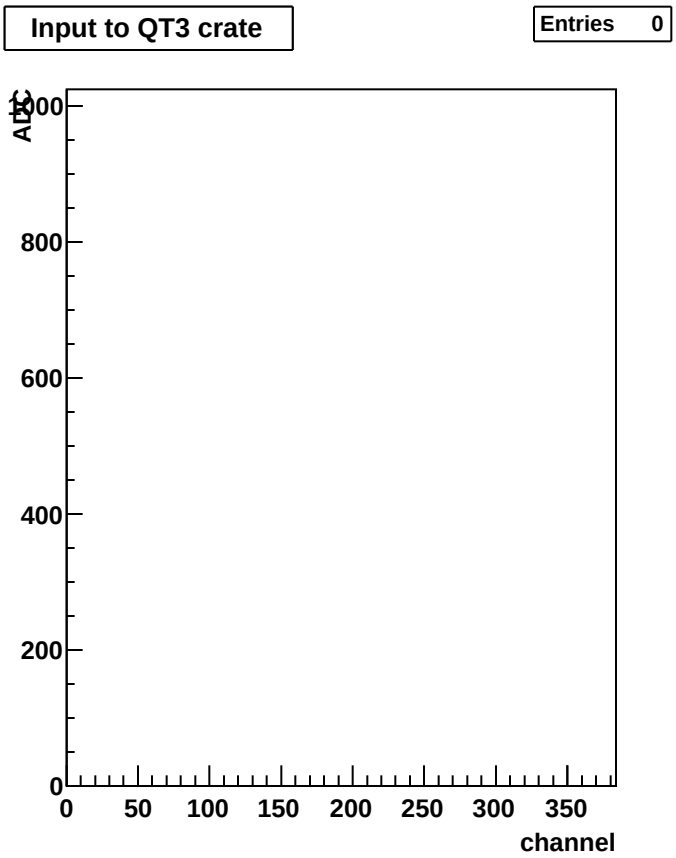

Entries 1
BBC-S TAC Diff
VPD TAC Diff

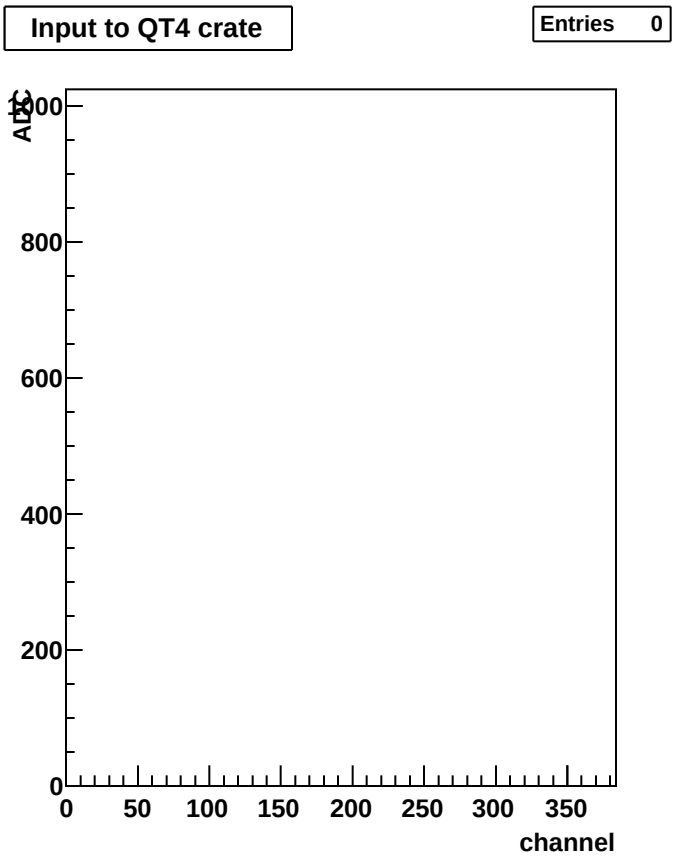

Entries 1
BBC-S TAC Diff
ZDC TAC Diff

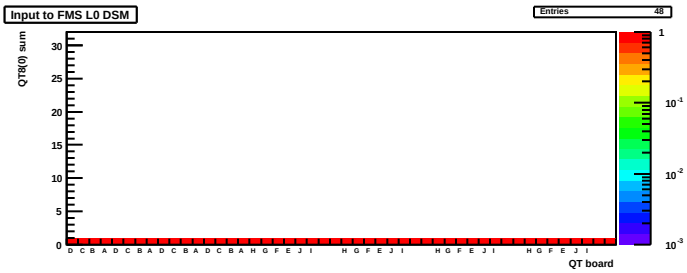

Entries 1
VPD TAC Diff
ZDC TAC Diff

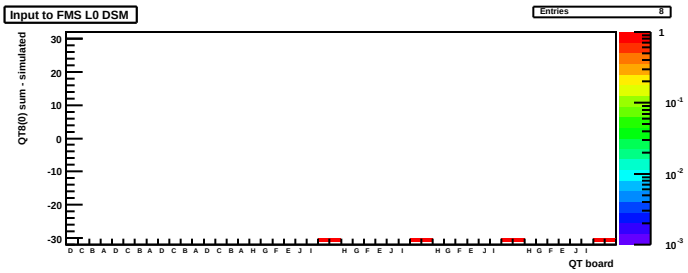

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

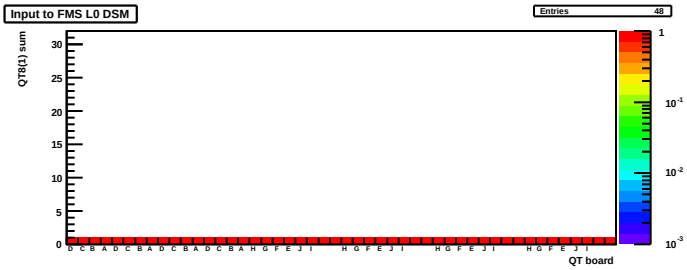

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

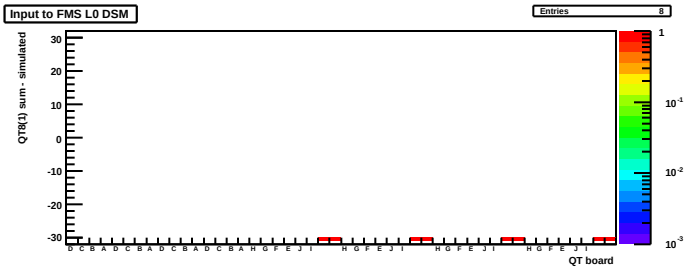

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

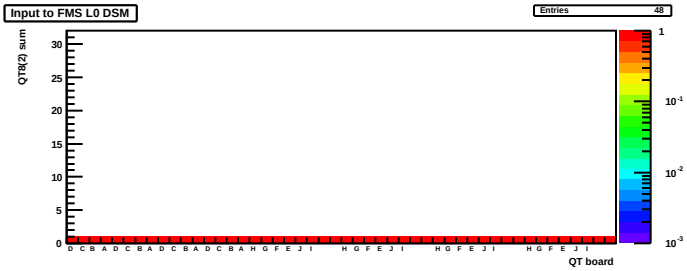

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

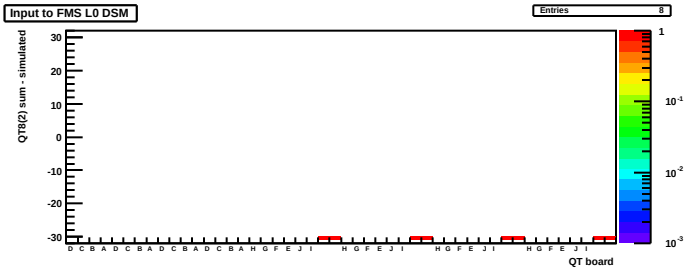

Input to FMS L0 DSM
Entries 48
QT8(0) sum
QT board

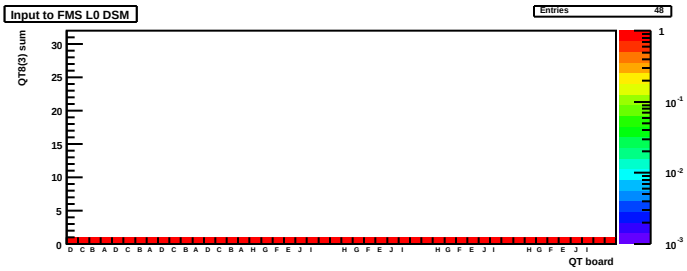

Input to FMS L0 DSM
Entries 8
QT8(0) sum - simulated
QT board

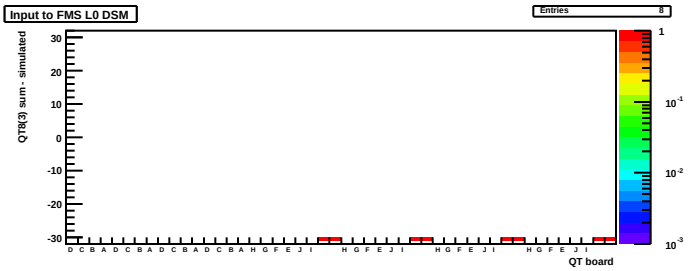

Input to FMS L0 DSM
Entries 48
QT8(1) sum
QT board

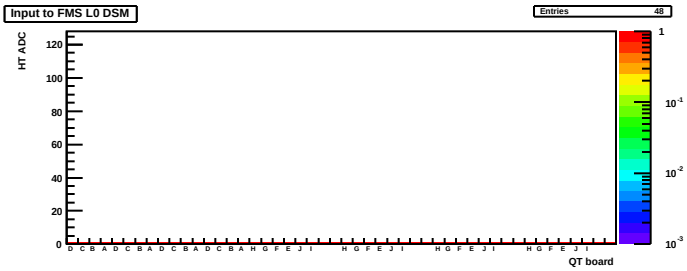

Input to FMS L0 DSM
Entries 8
QT8(1) sum - simulated
QT board

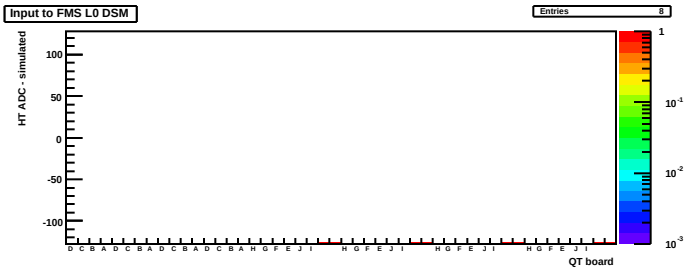

Input to FMS L0 DSM
Entries 48
QT8(2) sum
QT board

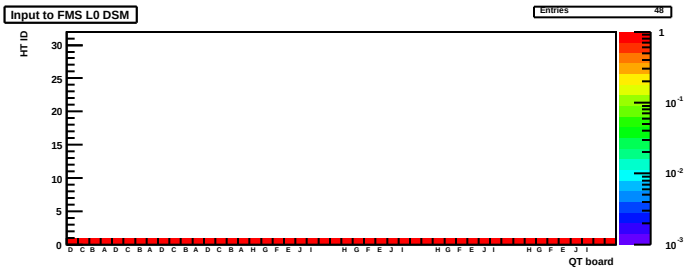

Input to FMS L0 DSM
Entries 8
QT8(2) sum - simulated
QT board

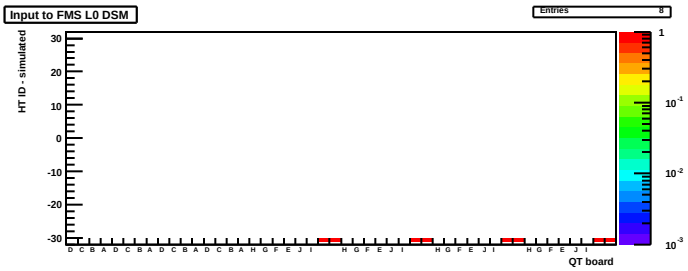

Input to FMS L0 DSM
Entries 48
QT8(3) sum
QT board

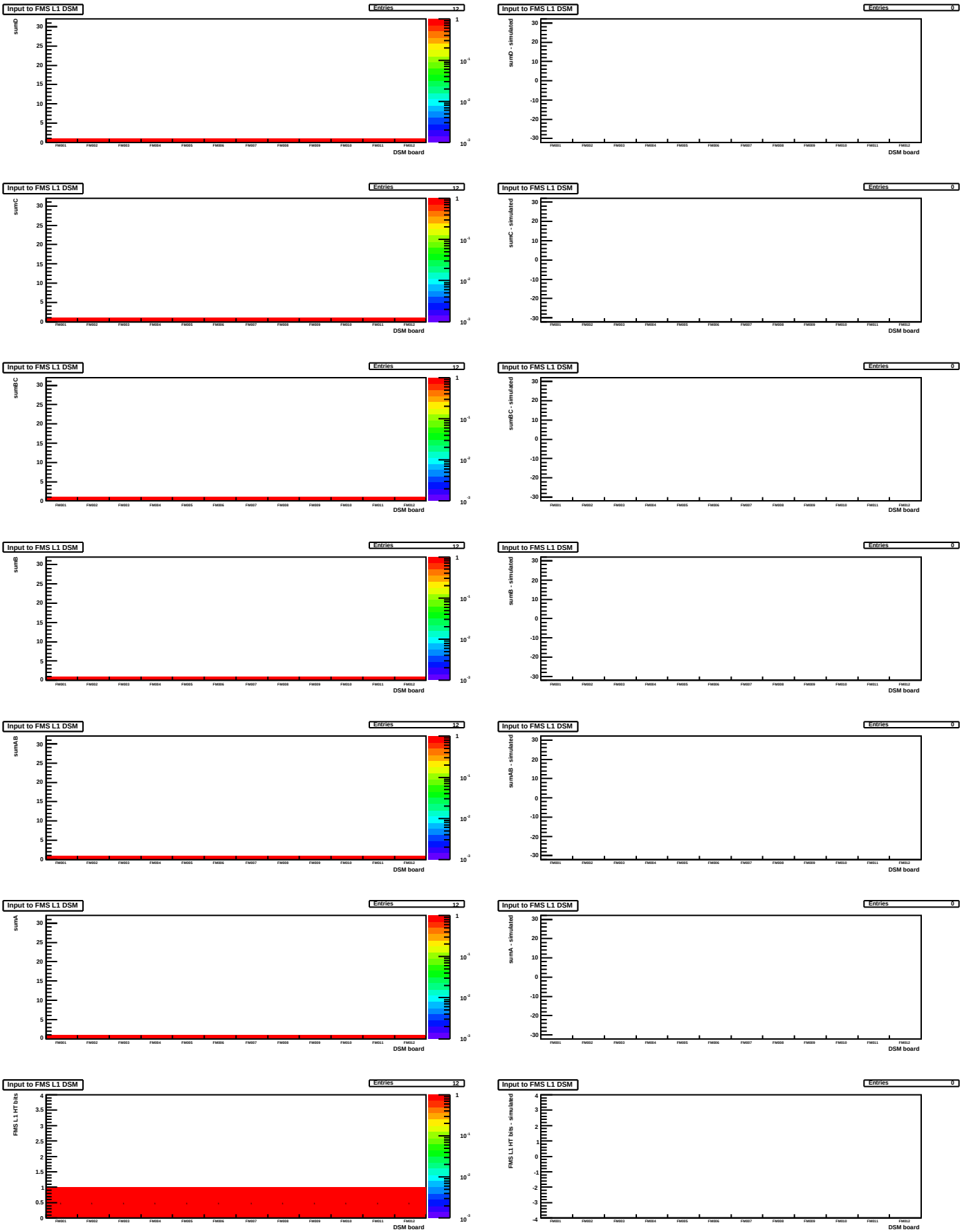

Input to FMS L0 DSM
Entries 8
QT8(3) sum - simulated
QT board

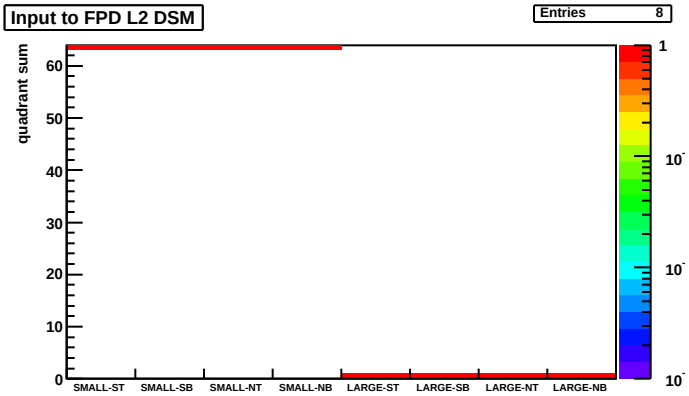

Input to FMS L0 DSM
Entries 48
HT ADC
QT board

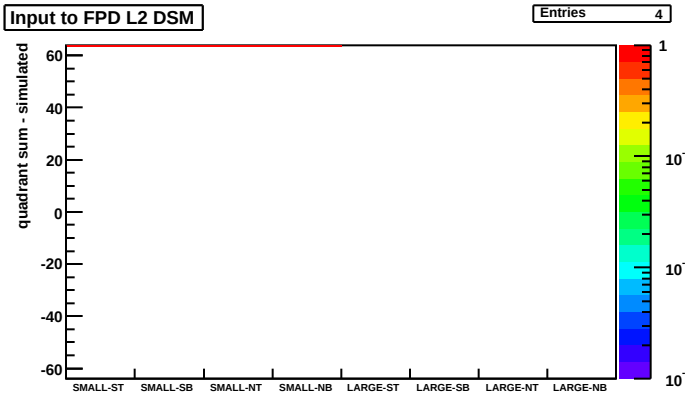

Input to FMS L0 DSM
Entries 8
HT ADC - simulated
QT board

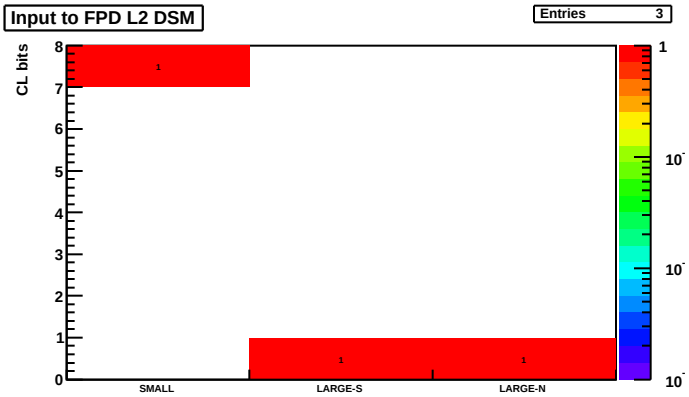

Input to FMS L0 DSM
Entries 48
HT ID
QT board

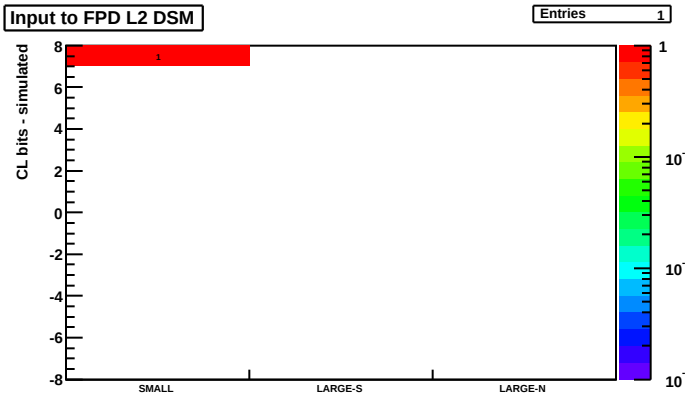

Input to FMS L0 DSM
Entries 8
HT ID - simulated
QT board

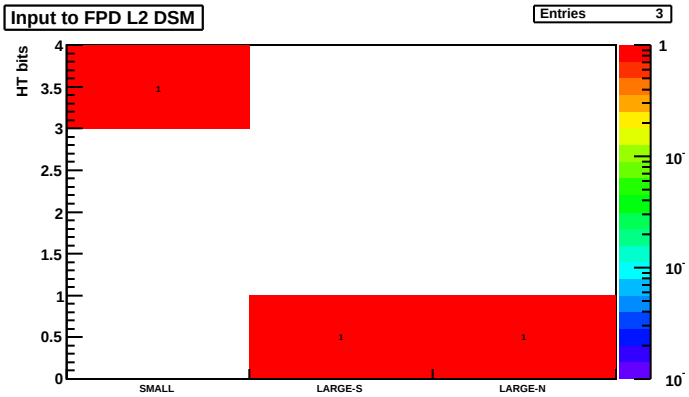

Input to FMS L1 DSM
Entries 12
sumD
sumC
sumBC
sumB
sumAB
sumA
FMS L1 HT bits
Entries 0
sumD - simulated
sumC - simulated
sumBC - simulated
sumB - simulated
sumAB - simulated
sumA - simulated
FMS L1 HT bits - simulated
FM001 FM002 FM003 FM004 FM005 FM006 FM007 FM008 FM009 FM010 FM011 FM012
DSM board

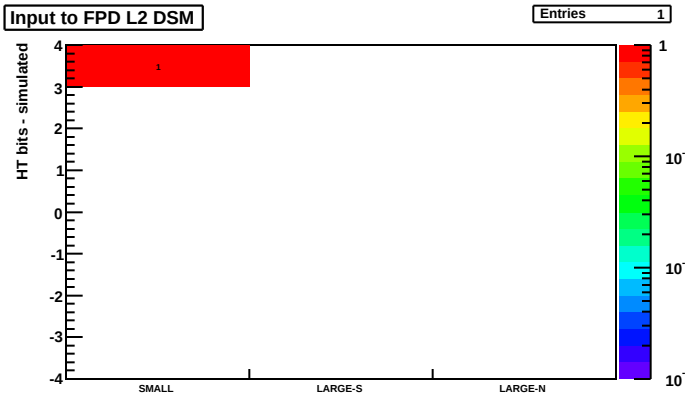

Input to FPD L2 DSM
Entries 8
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

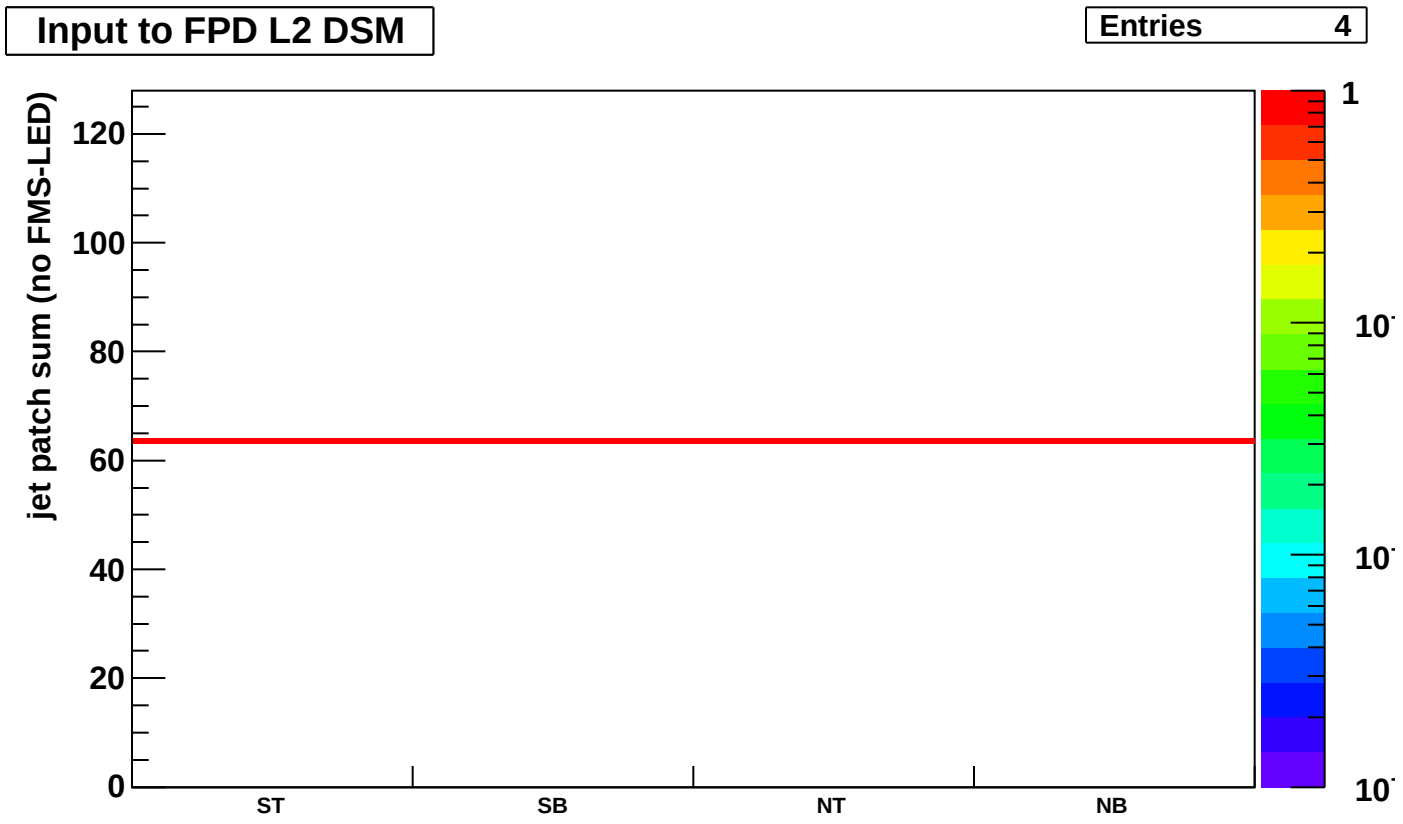

Input to FPD L2 DSM
Entries 4
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

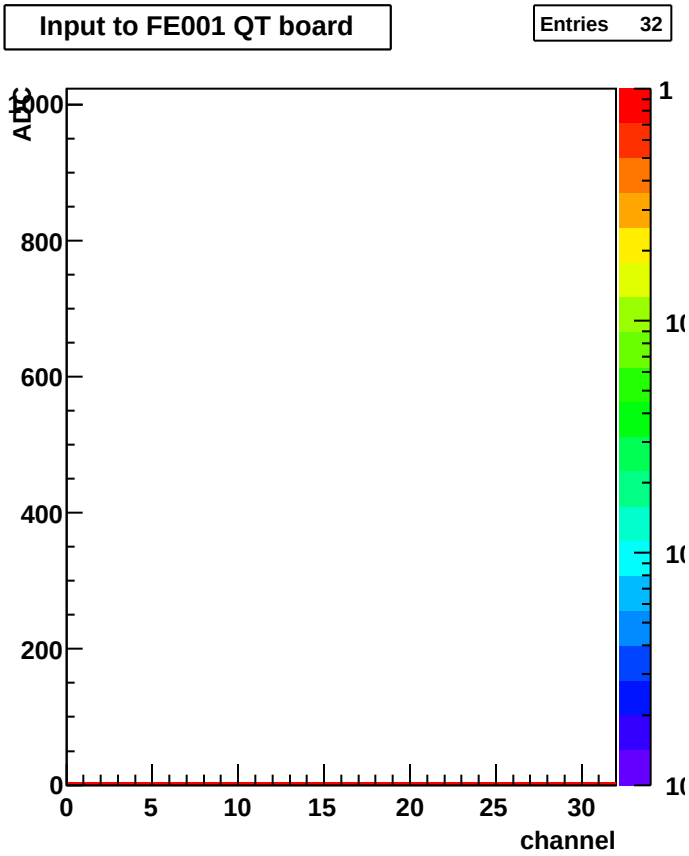

Input to FPD L2 DSM
Entries 3
CL bits
SMALL
LARGE-S
LARGE-N

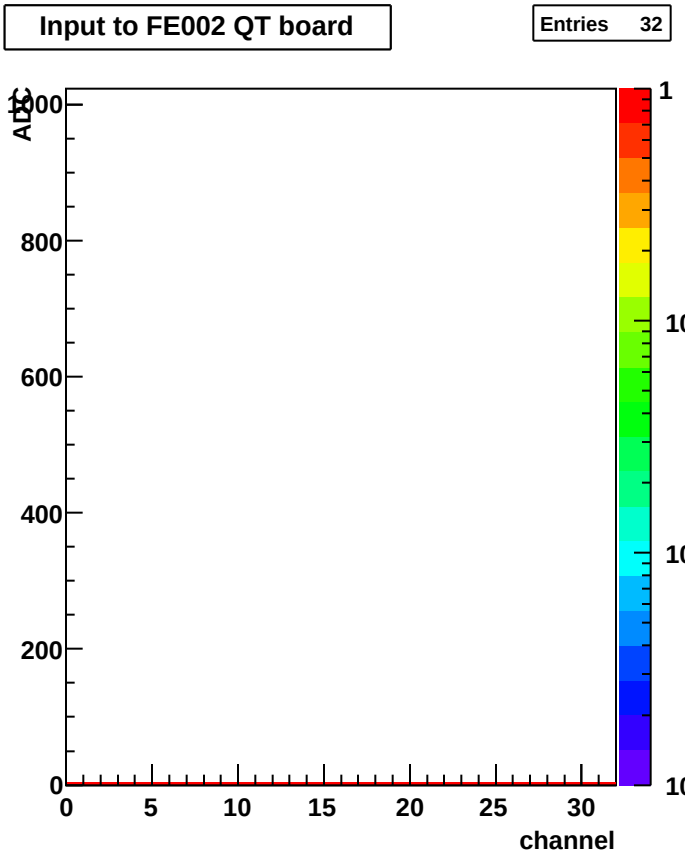

Input to FPD L2 DSM
Entries 1
CL bits - simulated
SMALL
LARGE-S
LARGE-N

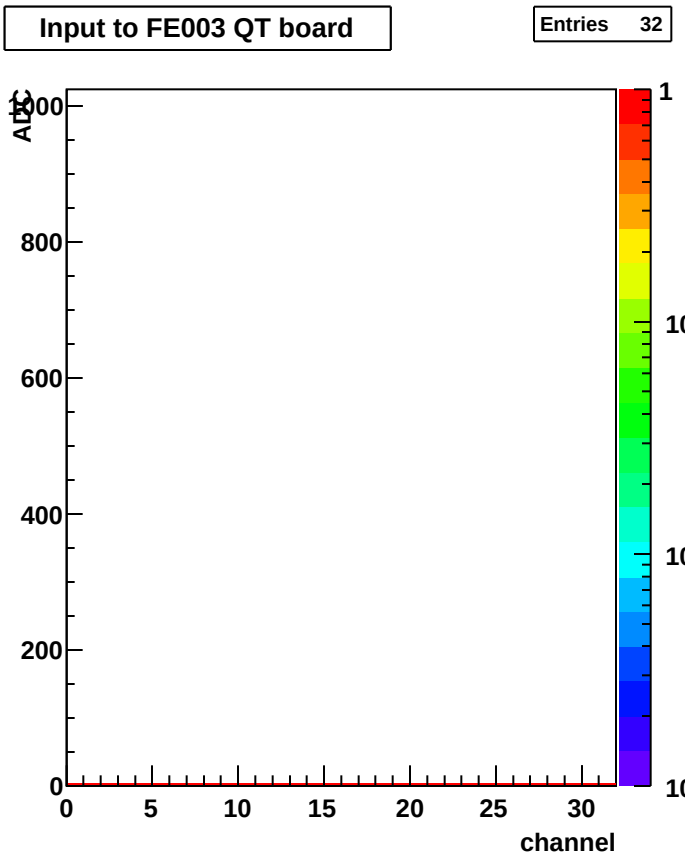

Input to FPD L2 DSM
Entries 3
HT bits
SMALL
LARGE-S
LARGE-N

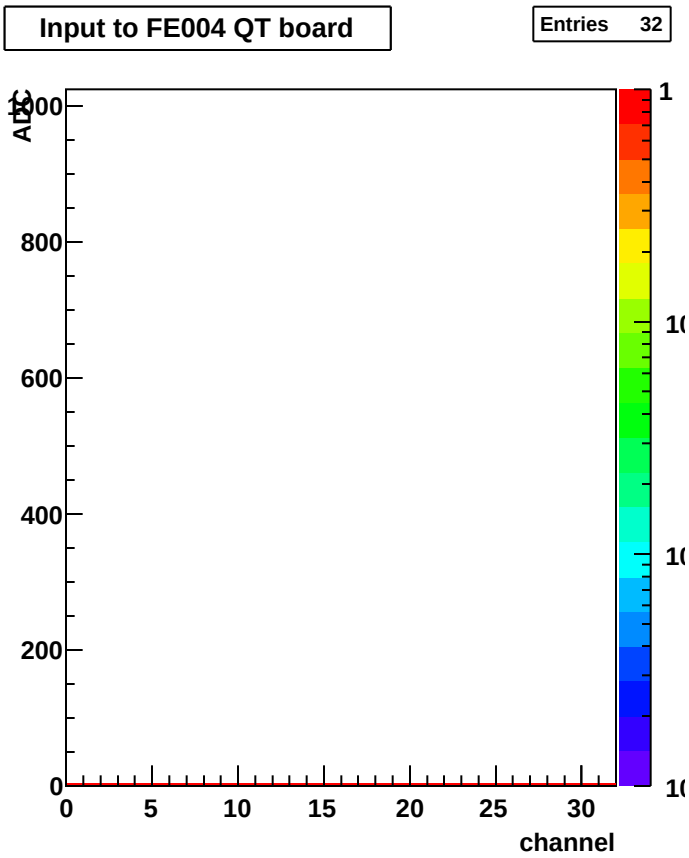

Input to FPD L2 DSM
Entries 1
HT bits - simulated
SMALL
LARGE-S
LARGE-N

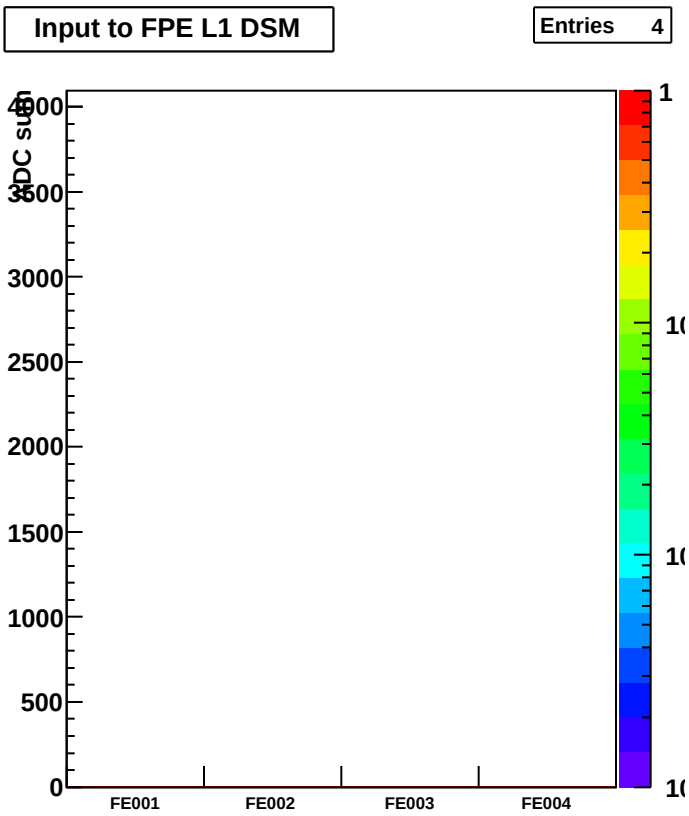

Input to FPD L2 DSM
Entries 4
jet patch sum (no FMS-LED)
120
100
80
60
40
20
0
ST
SB
NT
NB
1

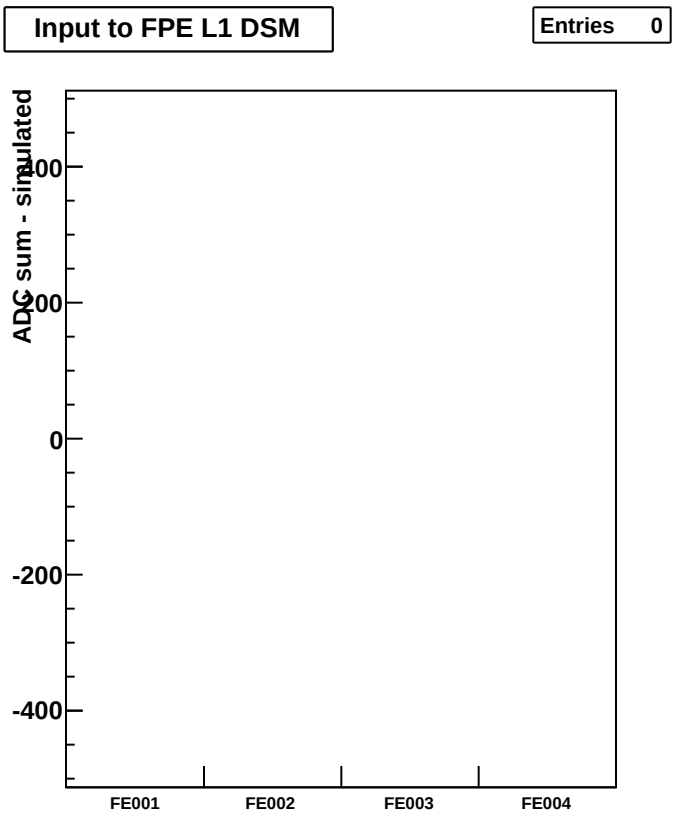

Input to FE001 QT board
Entries 32
ADC
channel

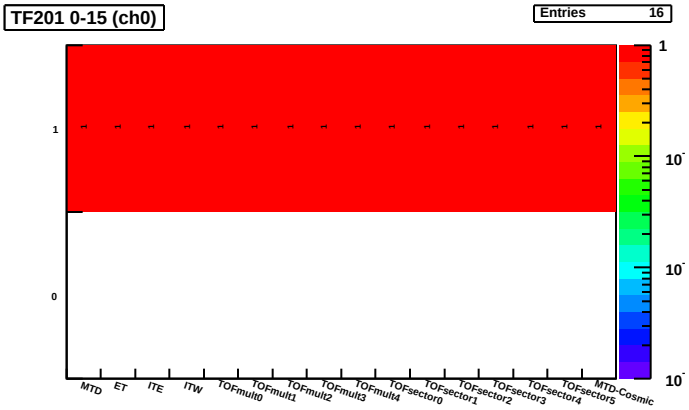

Input to FE002 QT board
Entries 32
ADC
channel

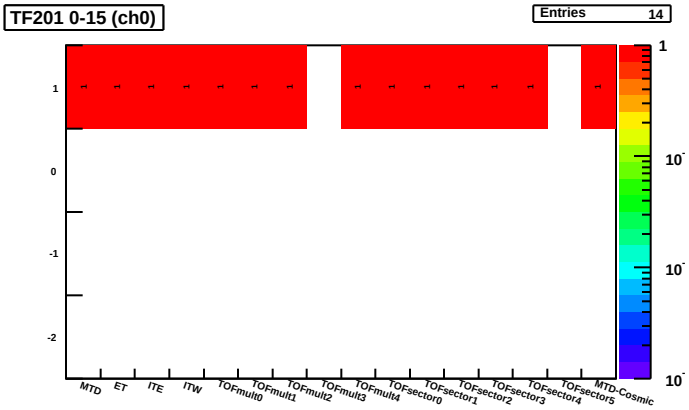

Input to FE003 QT board
Entries 32
ADC
channel

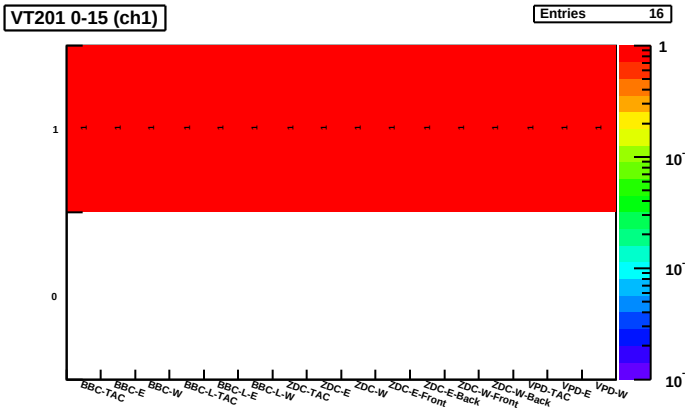

Input to FE004 QT board
Entries 32
ADC
channel

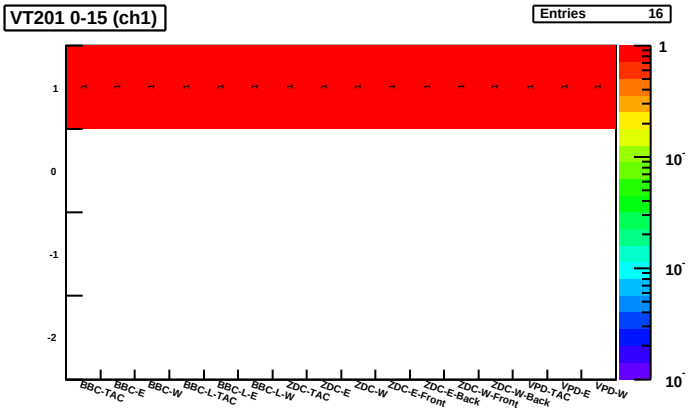

Input to FPE L1 DSM
Entries 4
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004

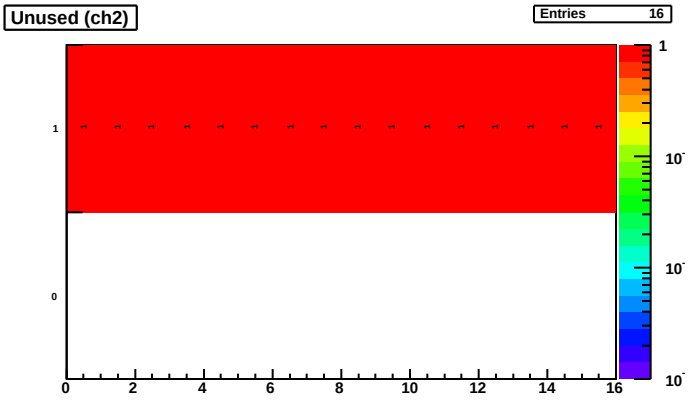

Input to FPE L1 DSM
Entries 0
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004

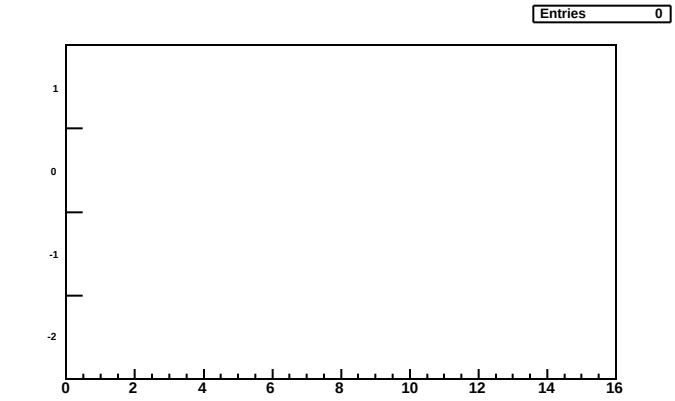

TF201 0-15 (ch0)
Entries 16
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

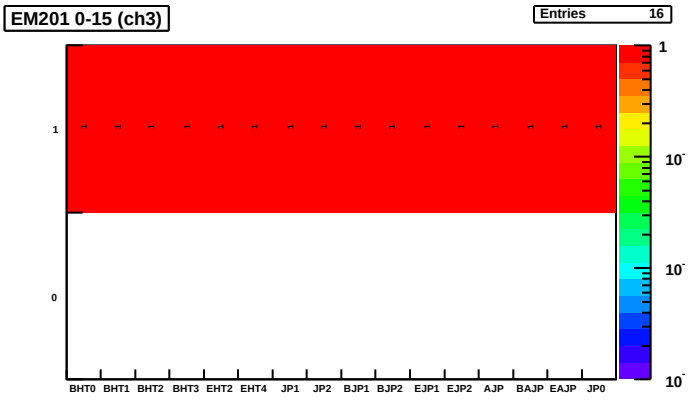

TF201 0-15 (ch0)
Entries 14
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

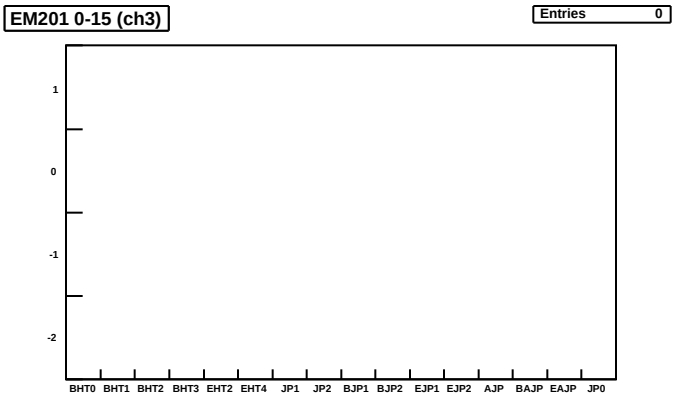

VT201 0-15 (ch1)
Entries 16
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

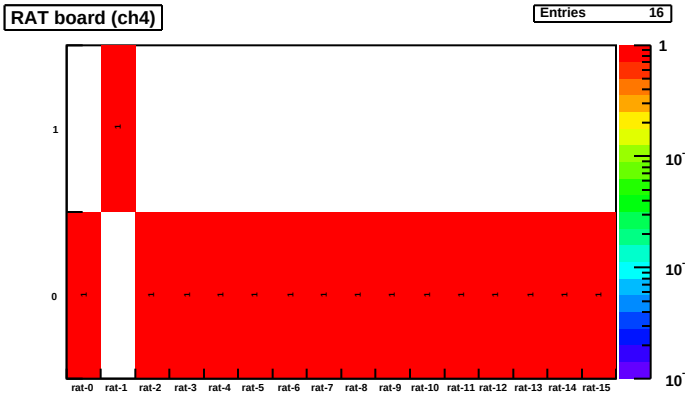

VT201 0-15 (ch1)
Entries 16
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

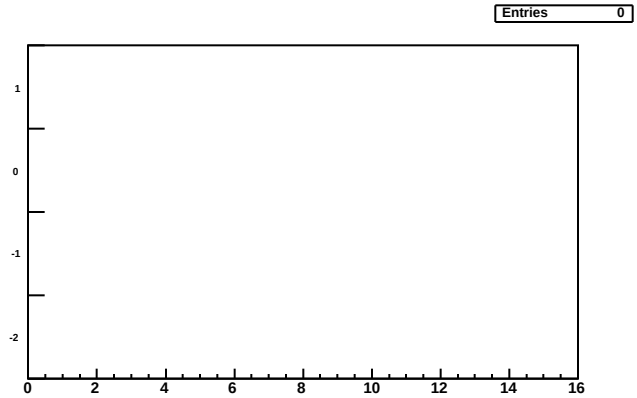

Unused (ch2)
Entries 16

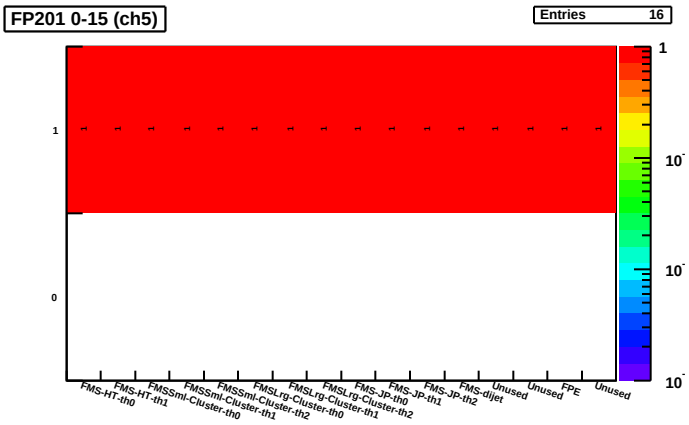

Entries 0

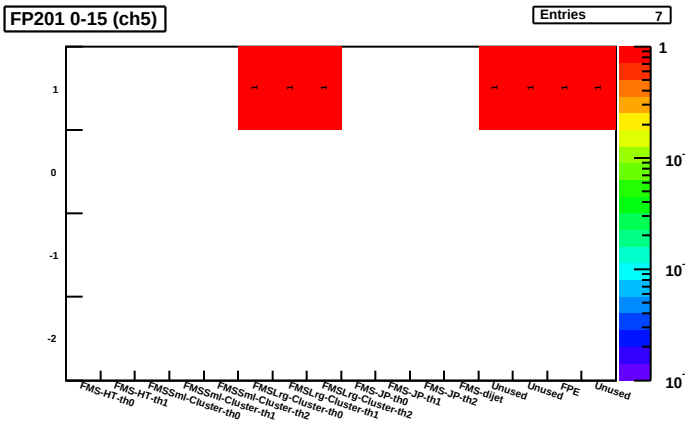

EM201 0-15 (ch3)
Entries 16
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

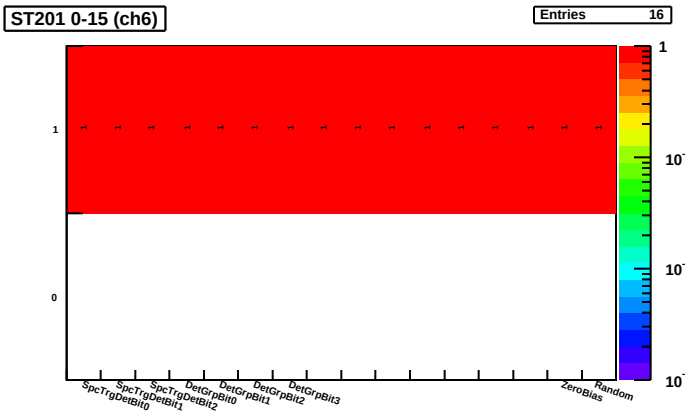

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

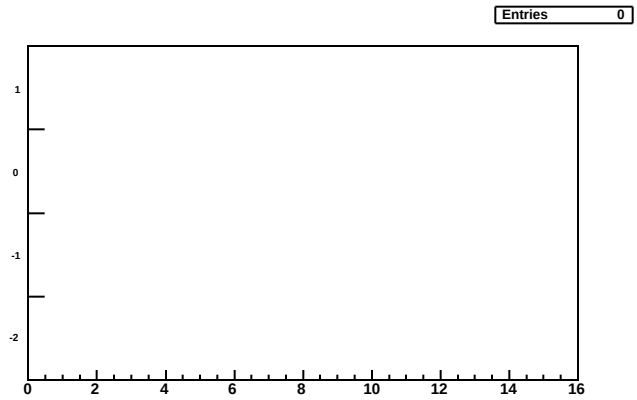

RAT board (ch4)
Entries 16
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

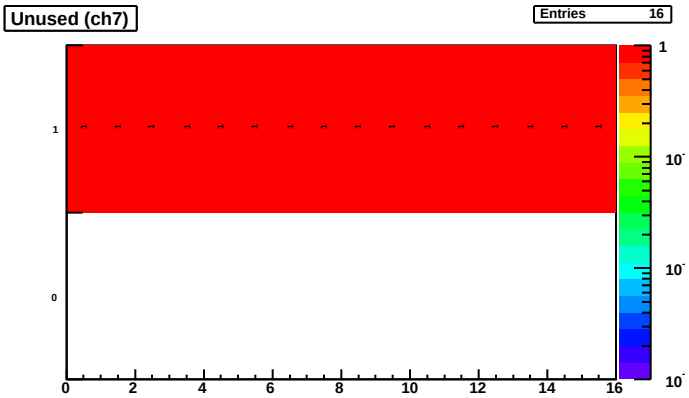

Entries 0

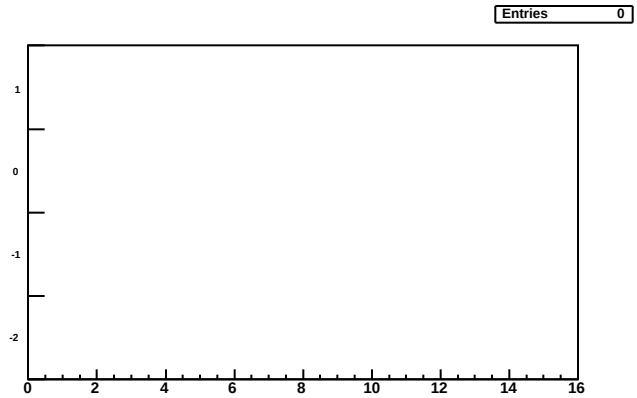

FP201 0-15 (ch5)
Entries 16
FMS-HT-th0 FMS-HT-th1 FMSSml-Cluster-th0 FMSSml-Cluster-th1 FMSSml-Cluster-th2 FMSLrg-Cluster-th0 FMSLrg-Cluster-th1 FMSLrg-Cluster-th2 FMS-JP-th0 FMS-JP-th1 FMS-JP-th2 FMS-diJet Unused Unused FPE Unused


FP201 0-15 (ch5)
Entries 7
FMS-HT-th0 FMS-HT-th1 FMSSml-Cluster-th0 FMSSml-Cluster-th1 FMSSml-Cluster-th2 FMSLrg-Cluster-th0 FMSLrg-Cluster-th1 FMSLrg-Cluster-th2 FMS-JP-th0 FMS-JP-th1 FMS-JP-th2 FMS-diJet Unused Unused FPE Unused


ST201 0-15 (ch6)
Entries 16
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random


Entries 0


Unused (ch7)
Entries 16


Entries 0