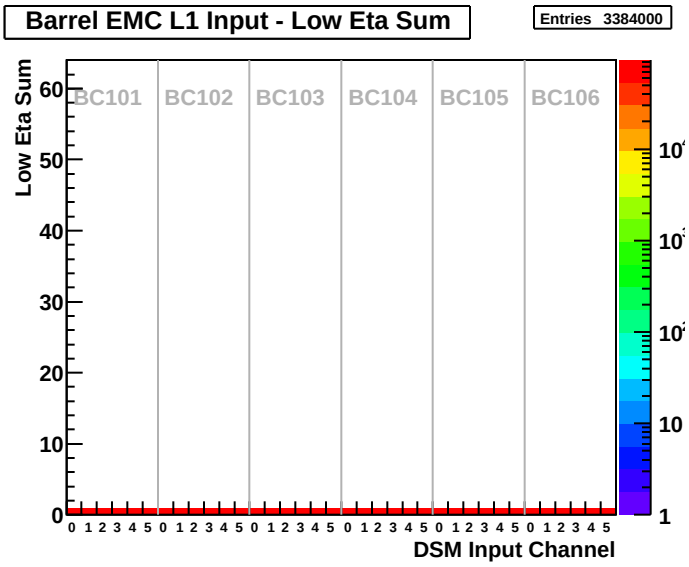
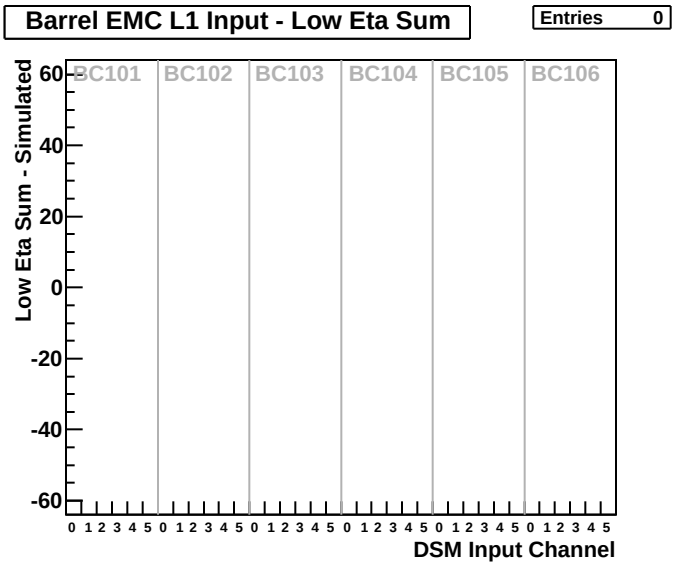
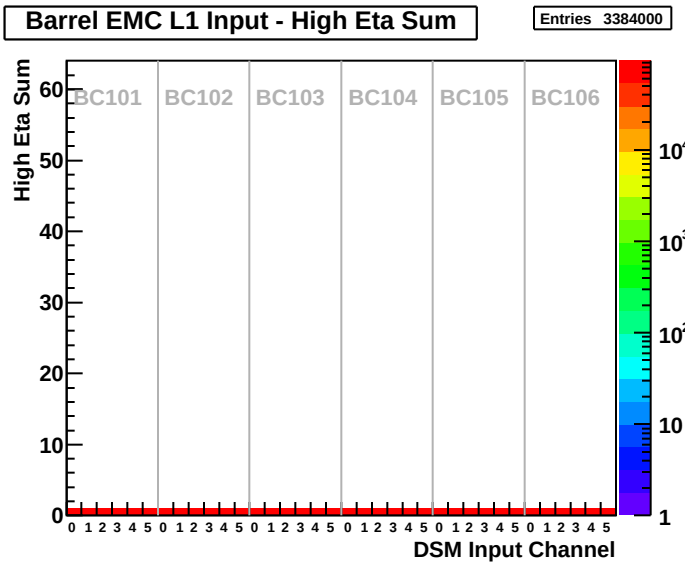
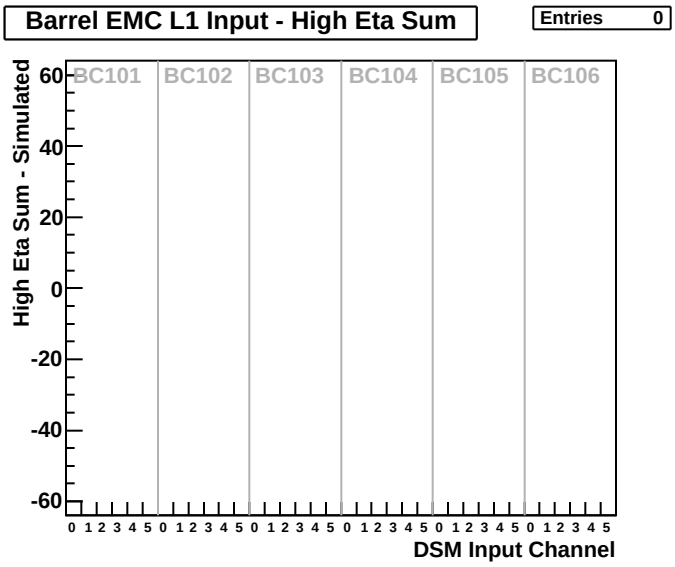
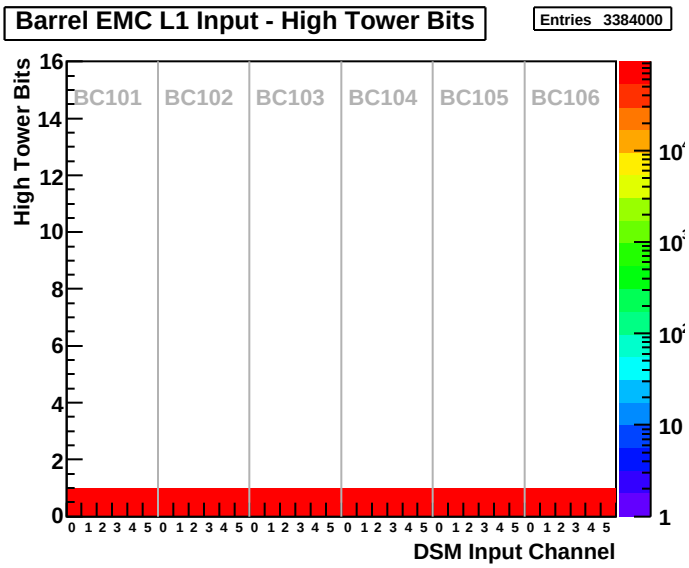
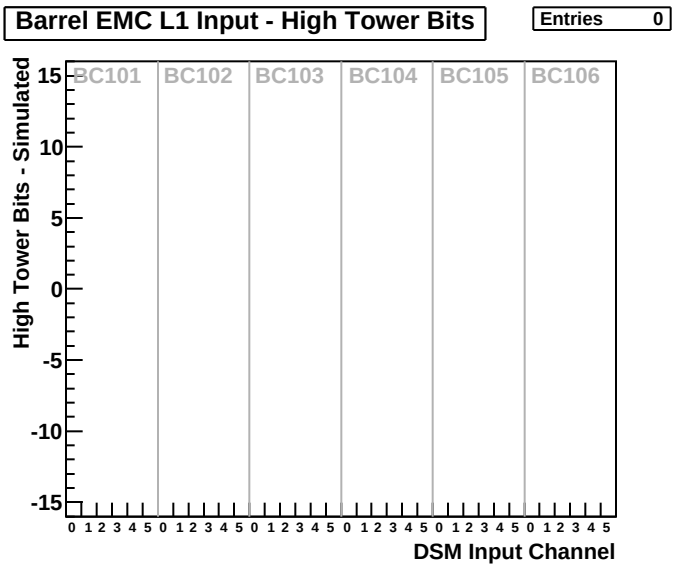

Barrel EMC L1 Input - Low Eta Sum
Entries 3384000
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

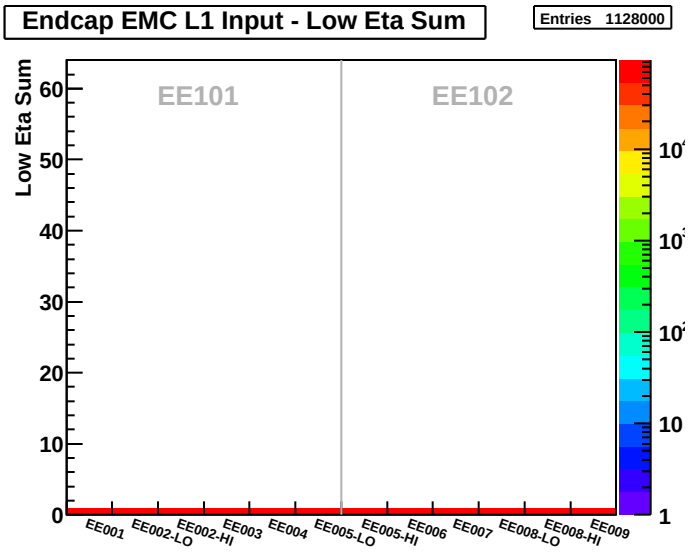

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

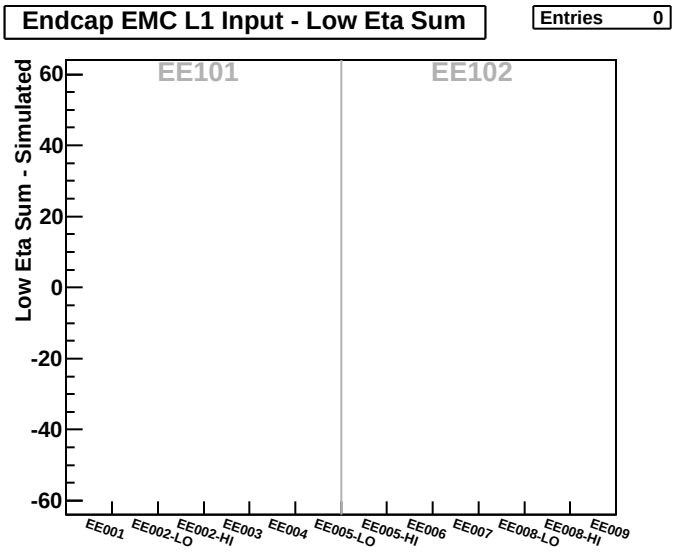

Barrel EMC L1 Input - High Eta Sum
Entries 3384000
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

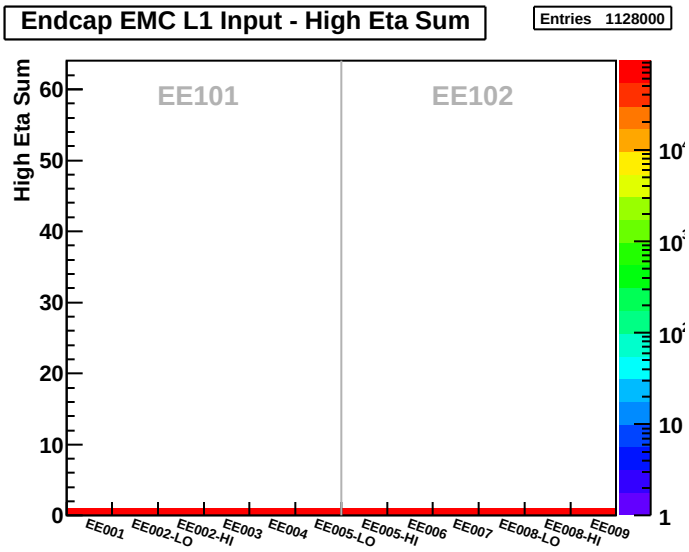

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

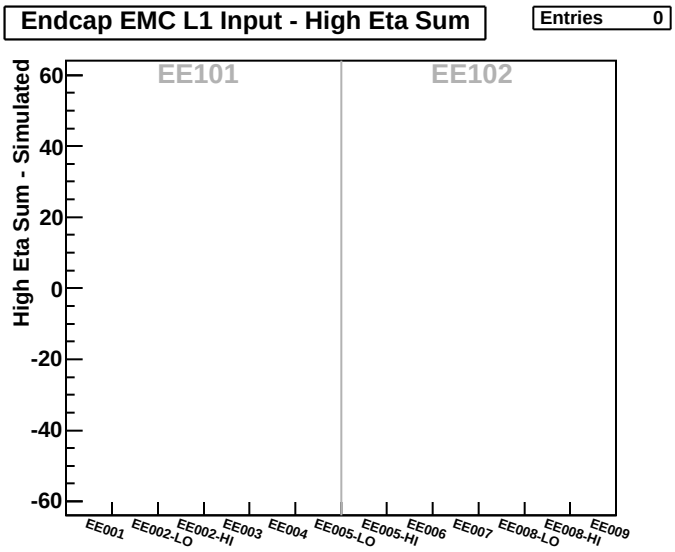

Barrel EMC L1 Input - High Tower Bits
Entries 3384000
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

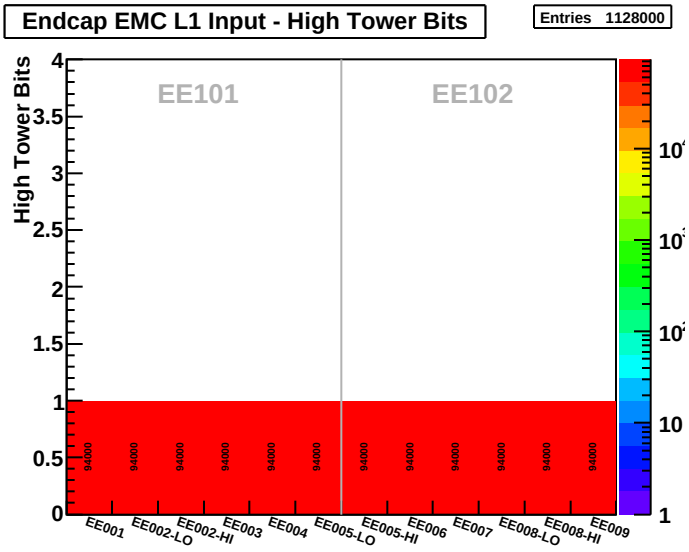

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

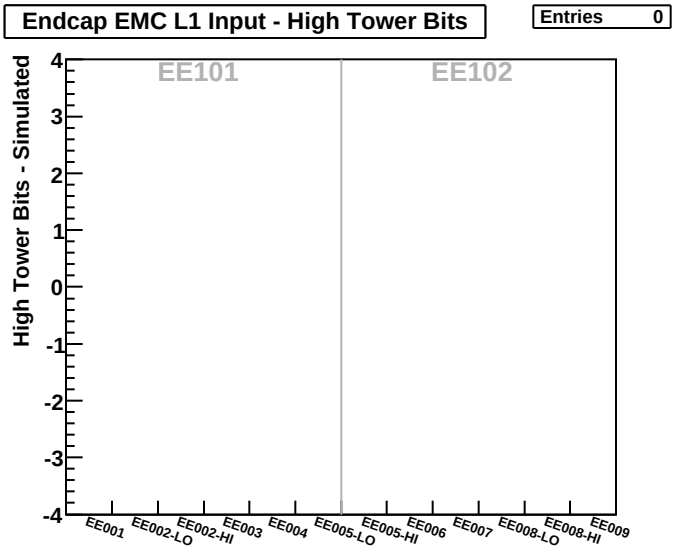

Endcap EMC L1 Input - Low Eta Sum
Entries 1128000
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

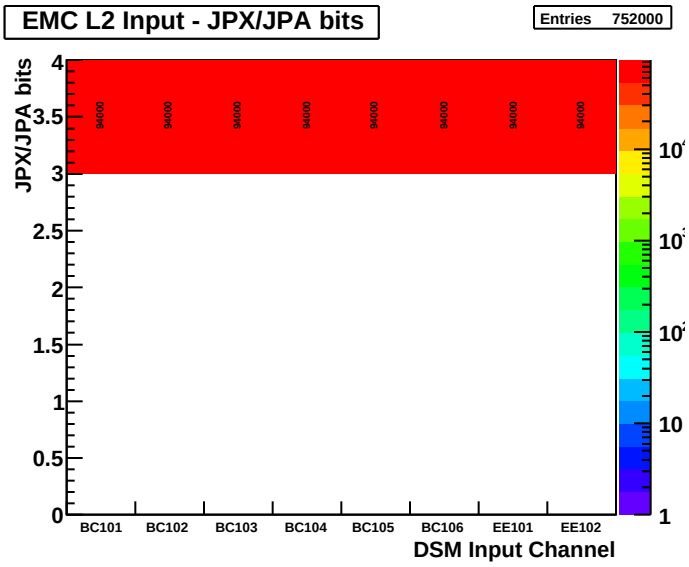

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

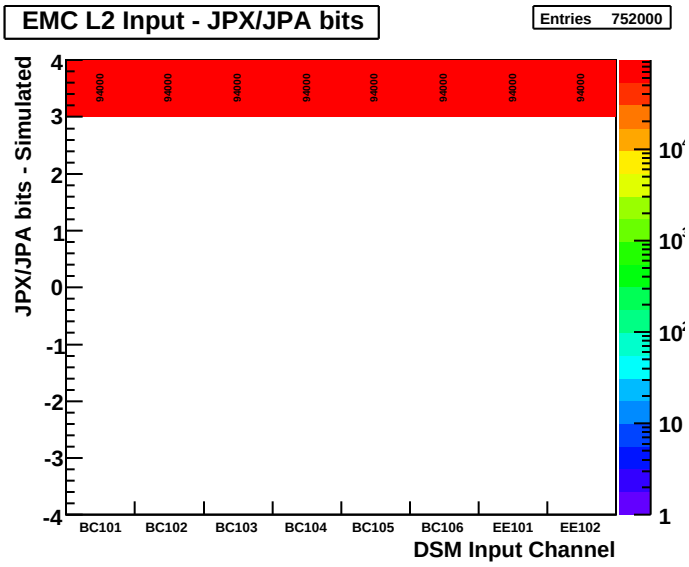

Endcap EMC L1 Input - High Eta Sum
Entries 1128000
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

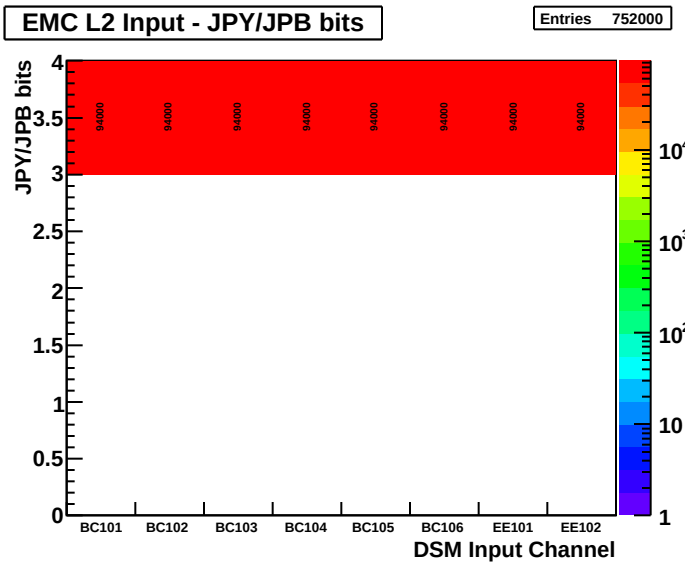

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

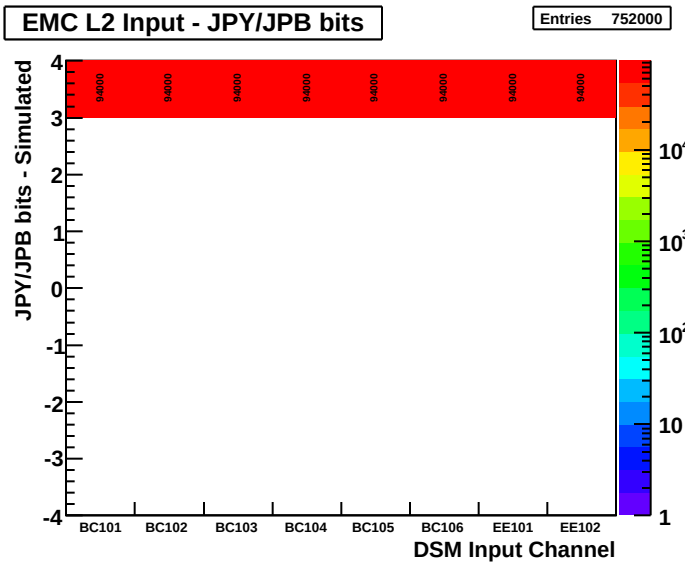

Endcap EMC L1 Input - High Tower Bits
Entries 1128000
High Tower Bits
EE101
EE102
94000
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

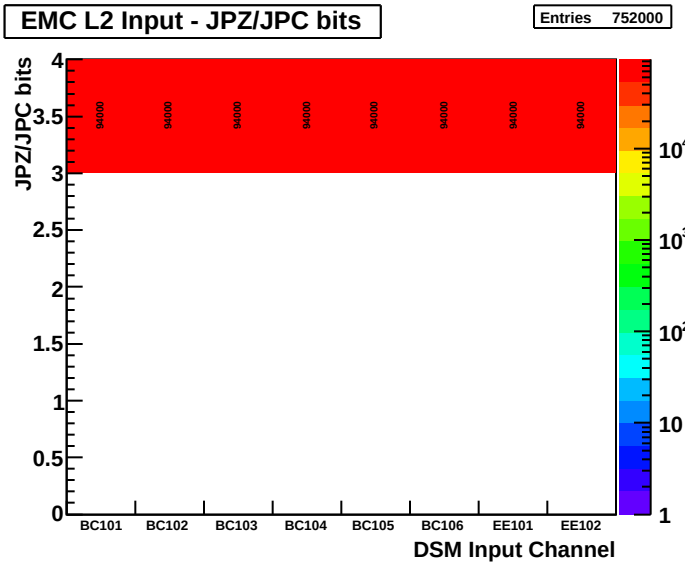

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

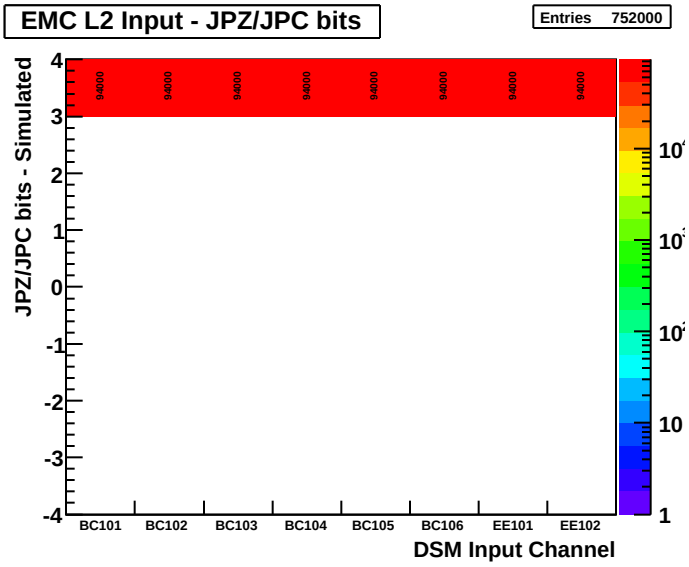

EMC L2 Input - JPX/JPA bits
Entries 752000
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
94000

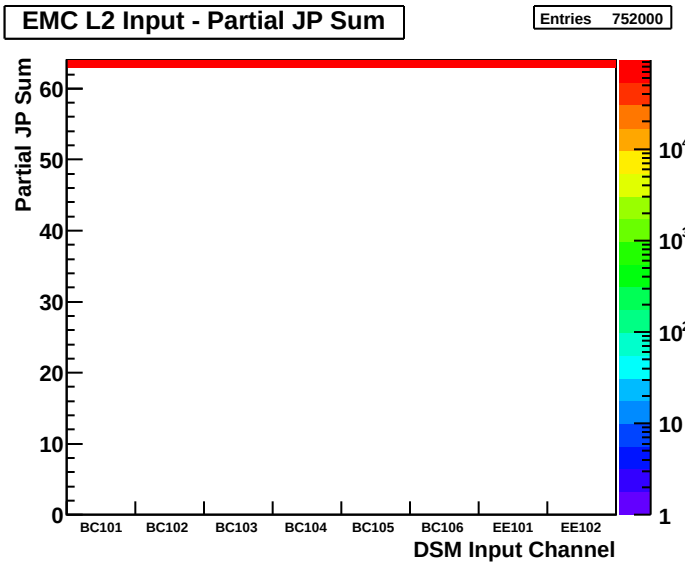

EMC L2 Input - JPX/JPA bits
Entries 752000
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
94000

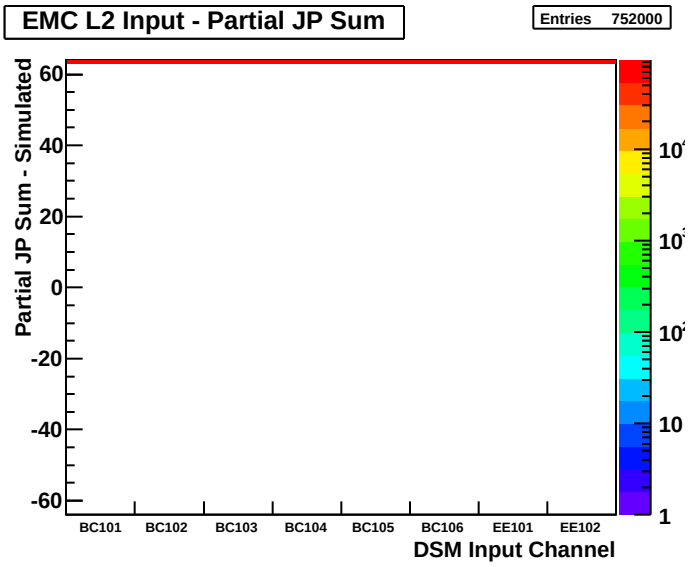

EMC L2 Input - JPY/JPB bits
Entries 752000
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
94000

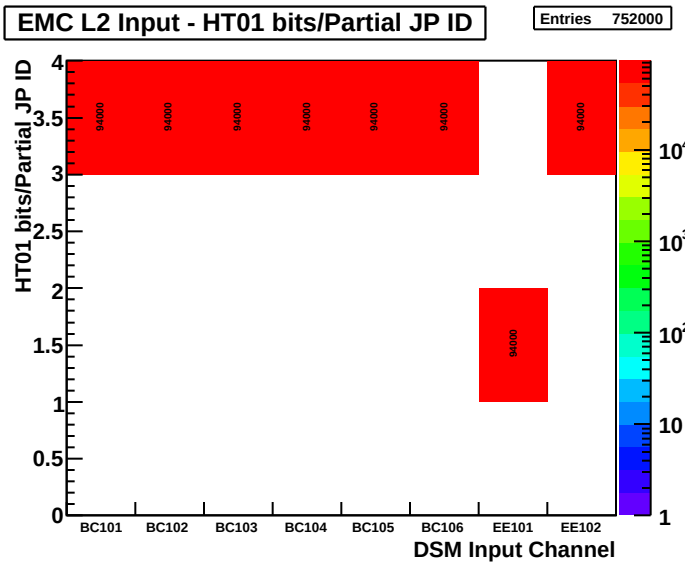

EMC L2 Input - JPY/JPB bits
Entries 752000
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
94000

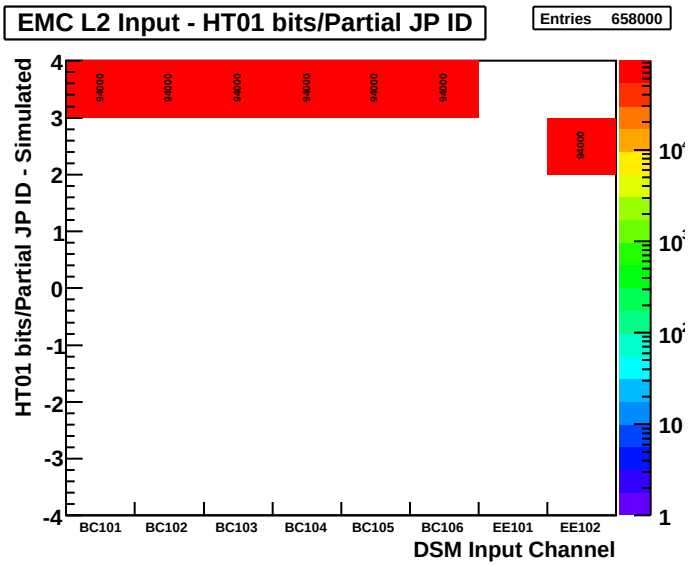

EMC L2 Input - JPZ/JPC bits
Entries 752000
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
94000

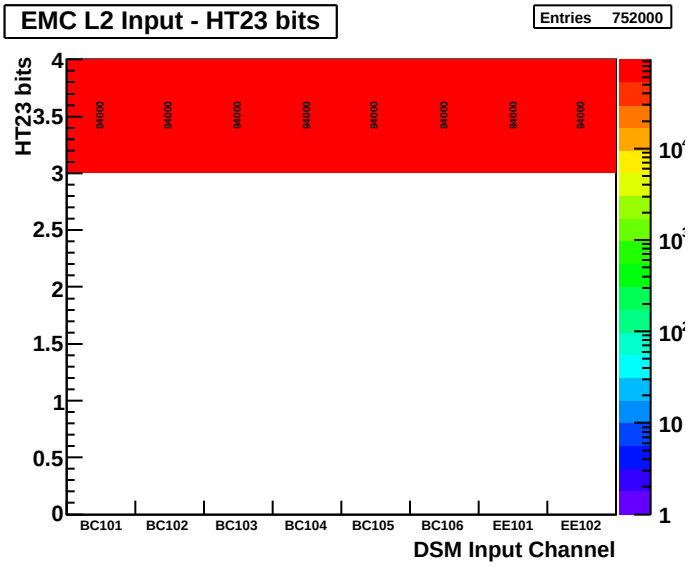

EMC L2 Input - JPZ/JPC bits
Entries 752000
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
94000

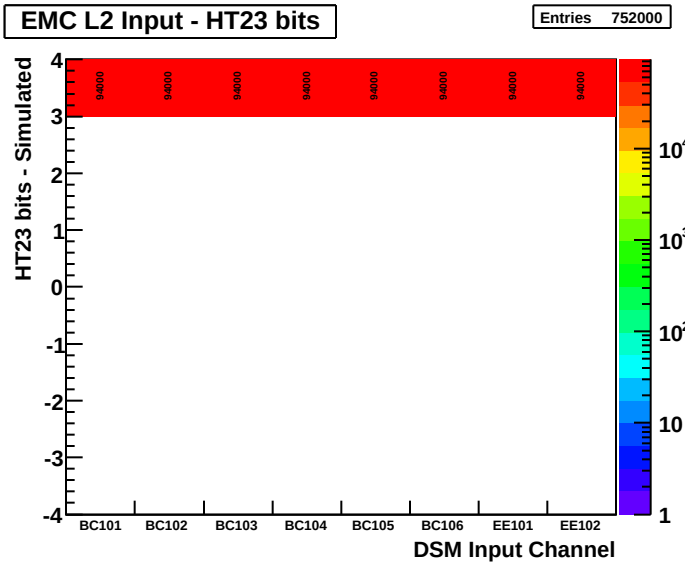

EMC L2 Input - Partial JP Sum
Entries 752000
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

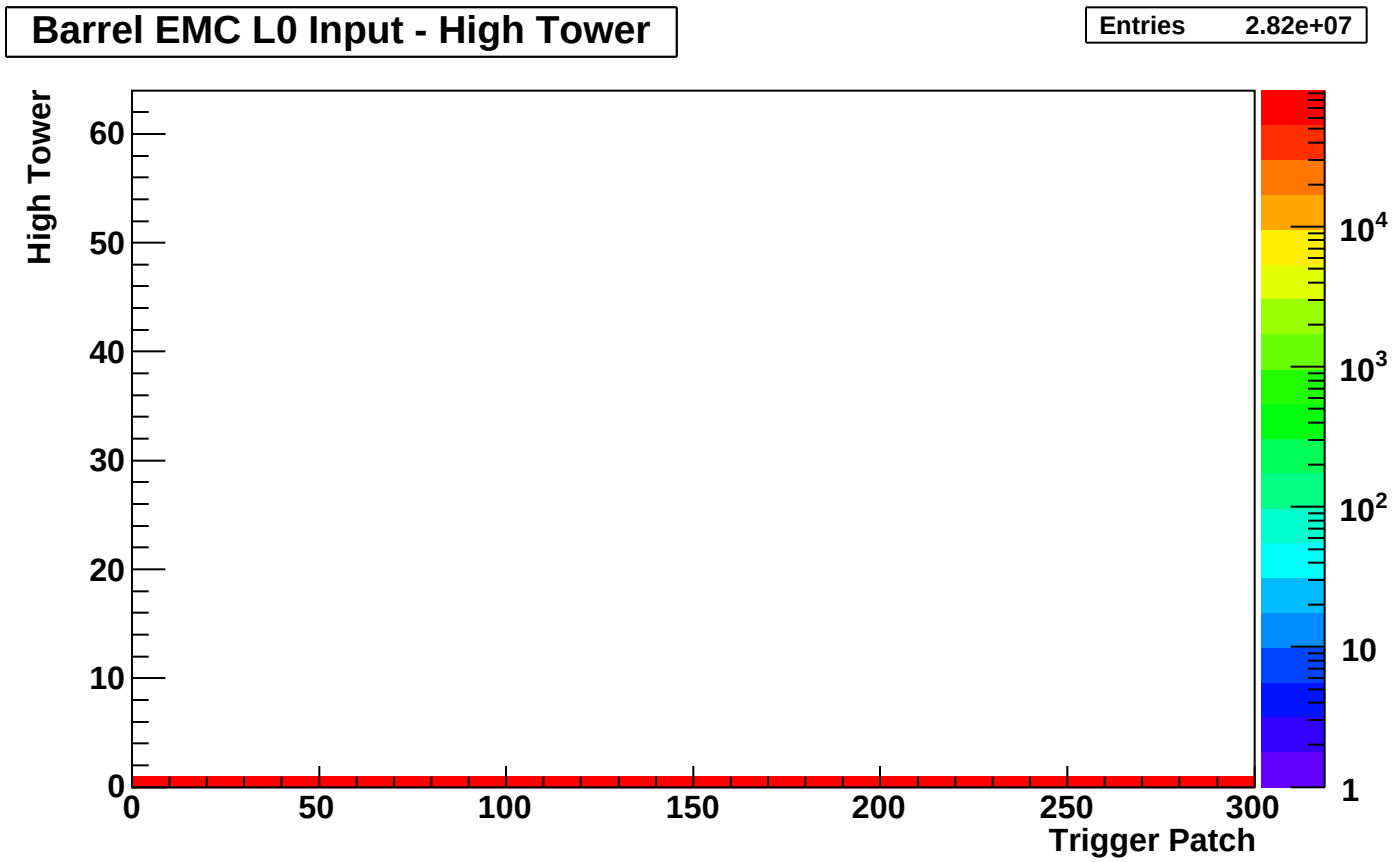

EMC L2 Input - Partial JP Sum
Entries 752000
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

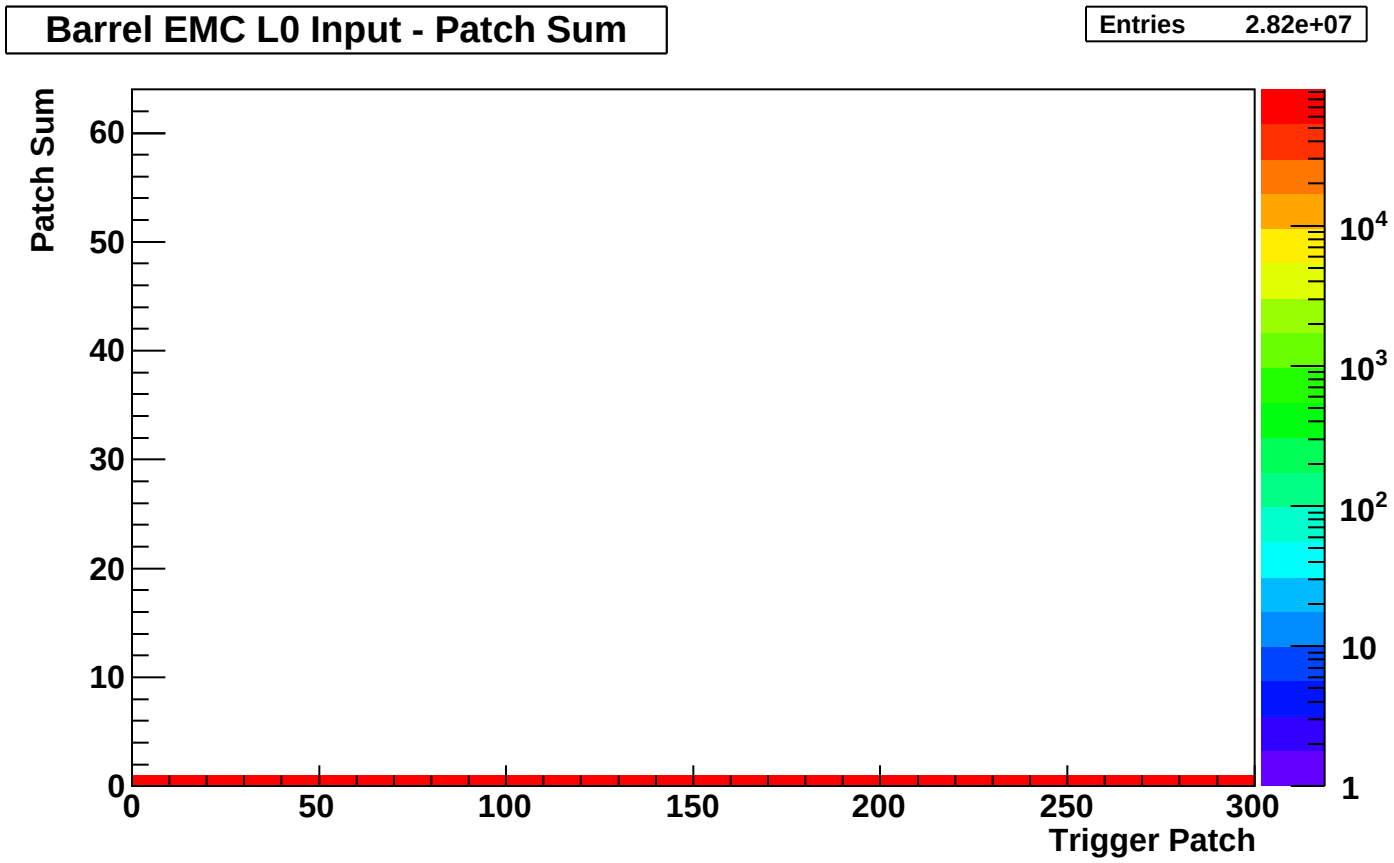

EMC L2 Input - HT01 bits/Partial JP ID
Entries 752000
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

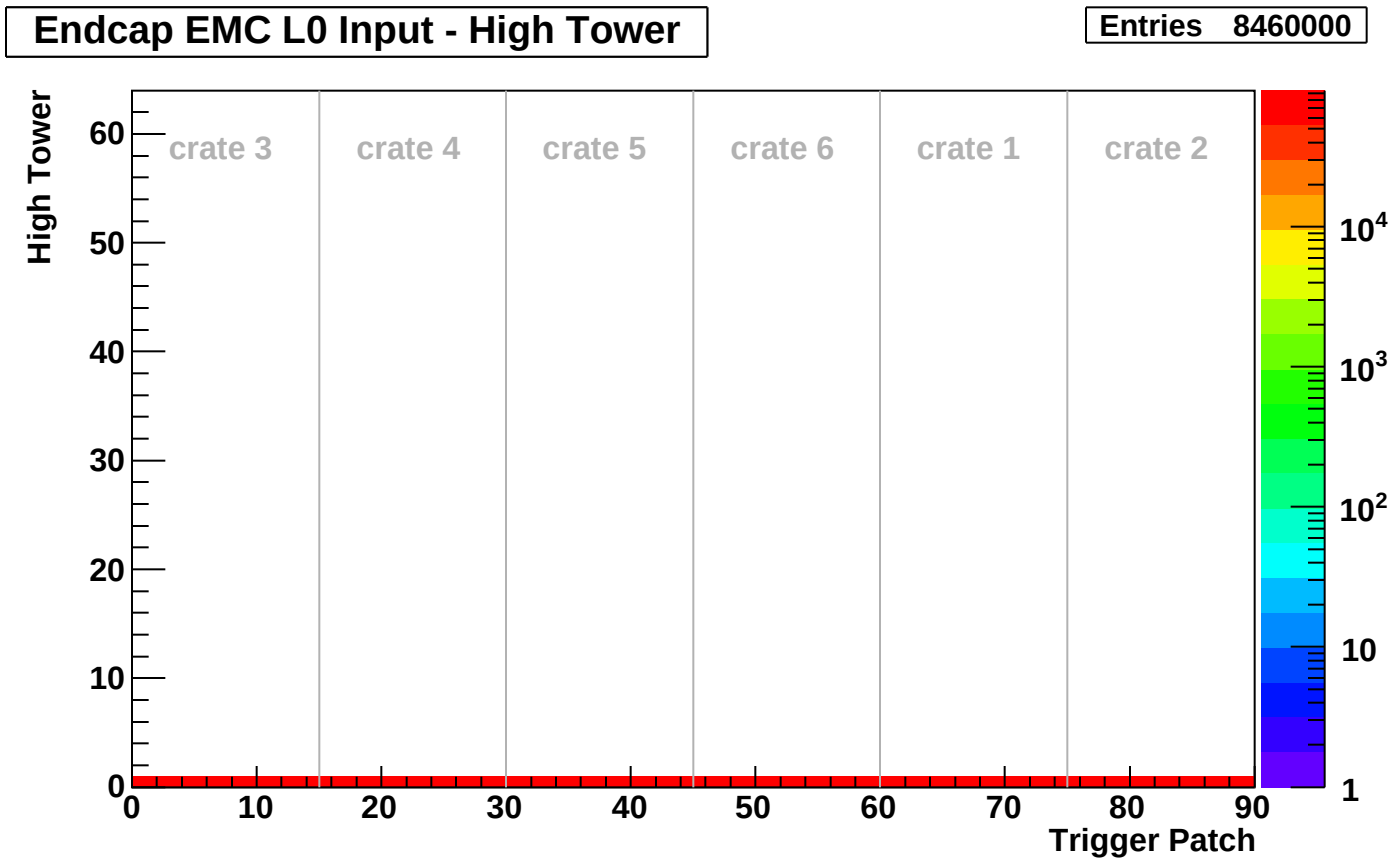

EMC L2 Input - HT01 bits/Partial JP ID
Entries 658000
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

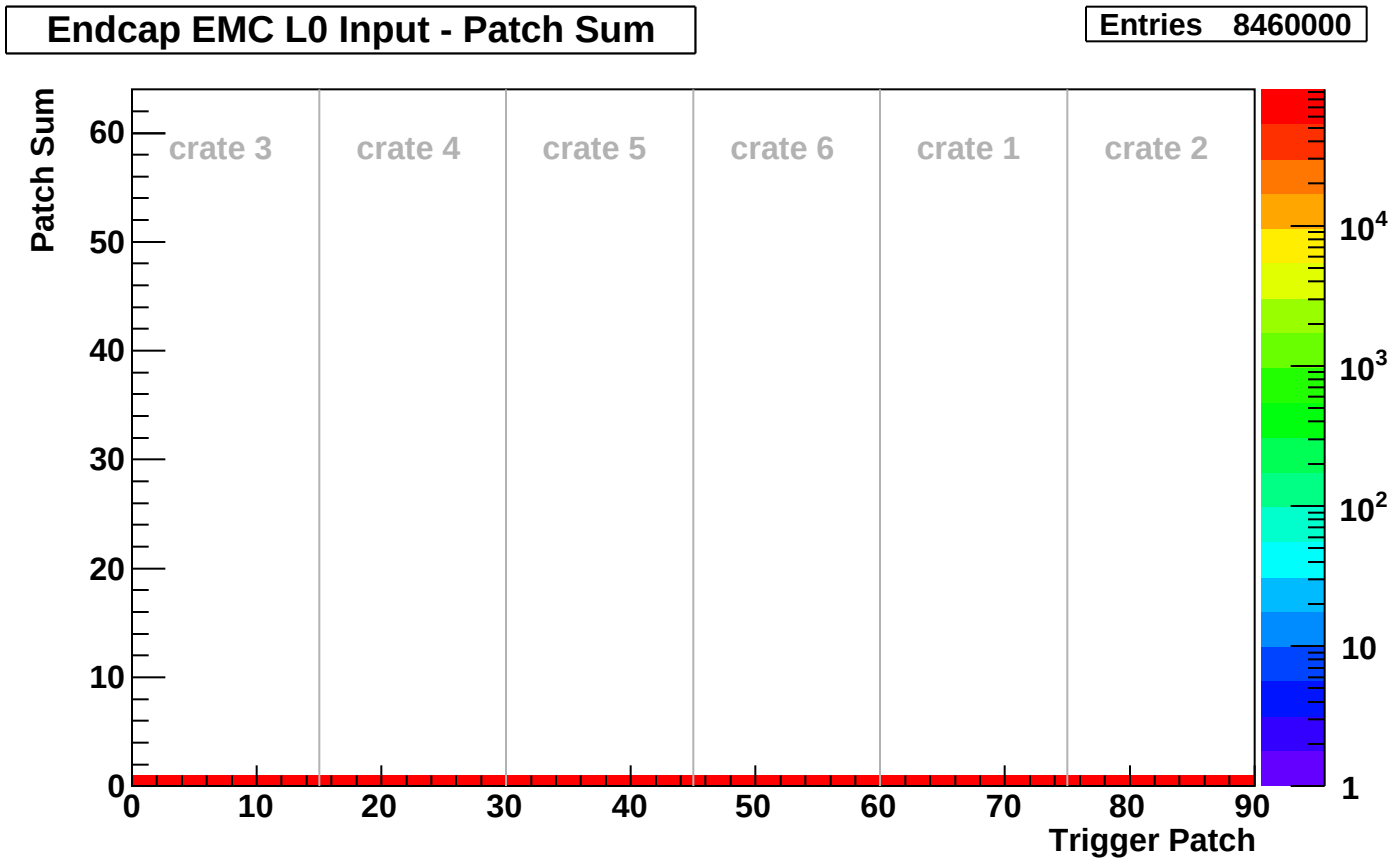

EMC L2 Input - HT23 bits
Entries 752000
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

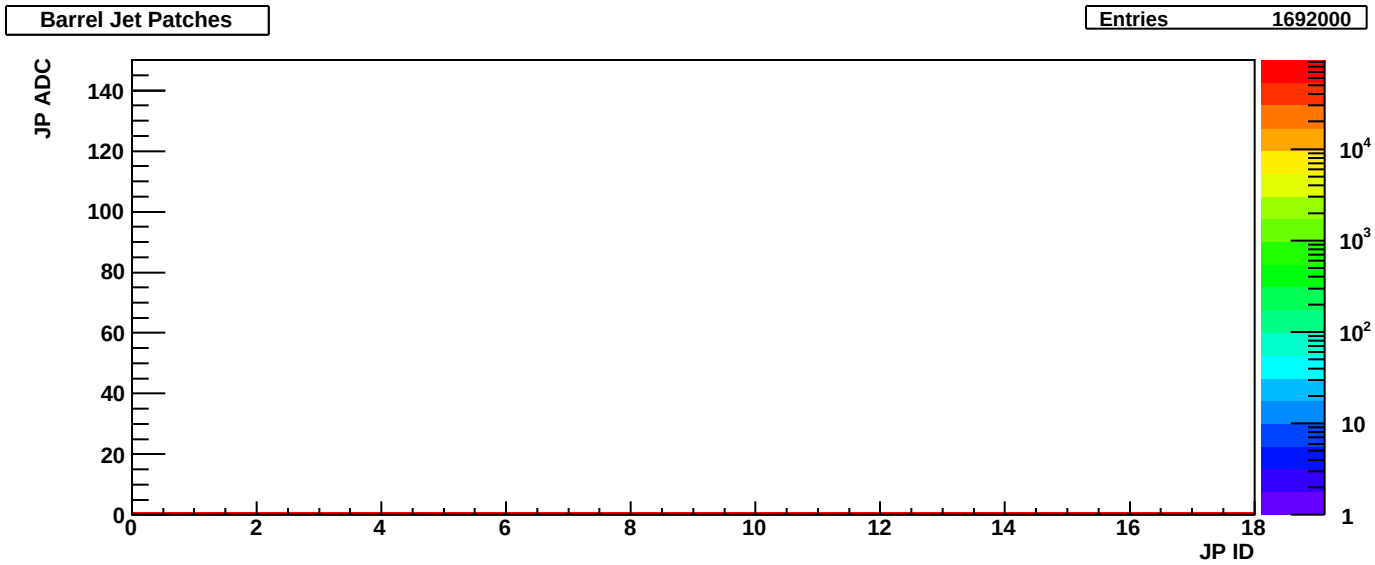

EMC L2 Input - HT23 bits
Entries 752000
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

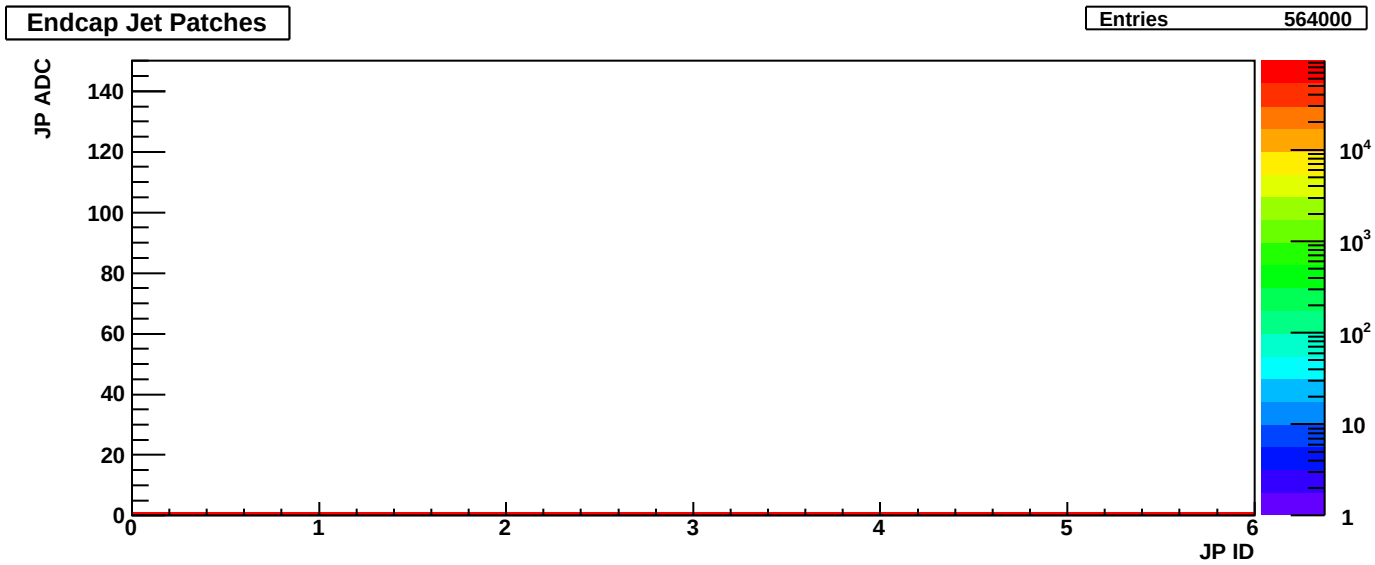

Barrel EMC L0 Input - High Tower
Entries 2.82e+07
High Tower
0
10
20
30
40
50
60
0
50
100
150
200
250
300
Trigger Patch
1
10
10^2
10^3
10^4

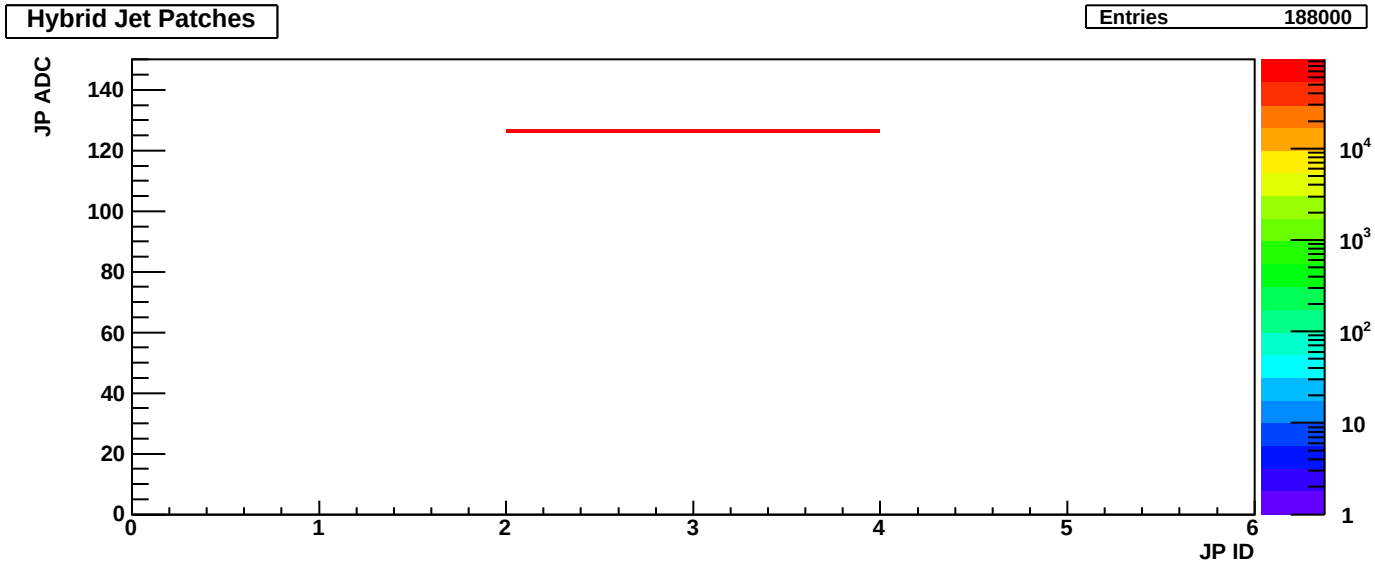

Barrel EMC L0 Input - Patch Sum
Entries 2.82e+07
Patch Sum
0
10
20
30
40
50
60
0
50
100
150
200
250
300
Trigger Patch
1
10
10^2
10^3
10^4

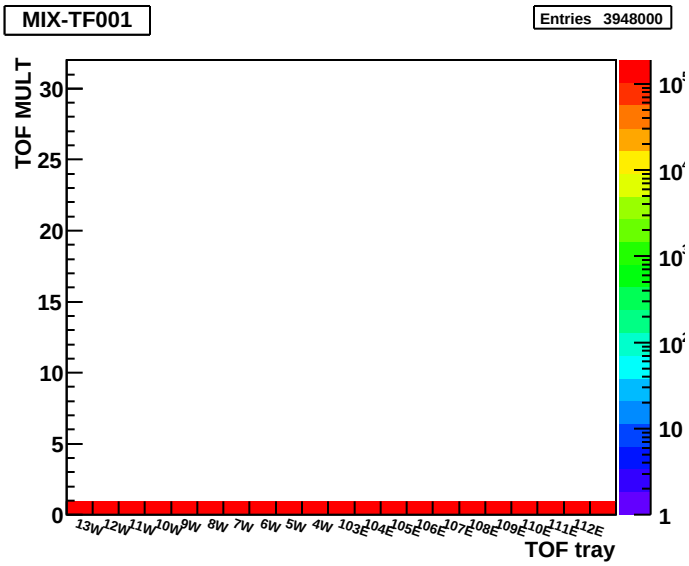

Endcap EMC L0 Input - High Tower
Entries 8460000
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3
10^4

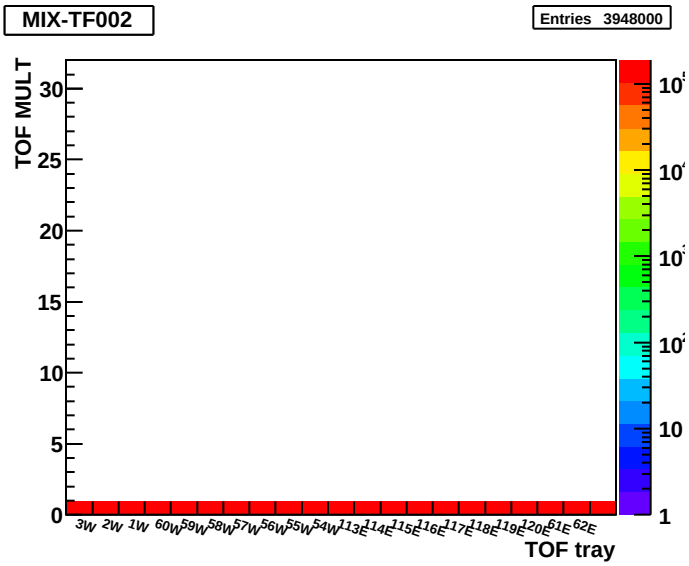

Endcap EMC L0 Input - Patch Sum
Entries 8460000
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3
10^4

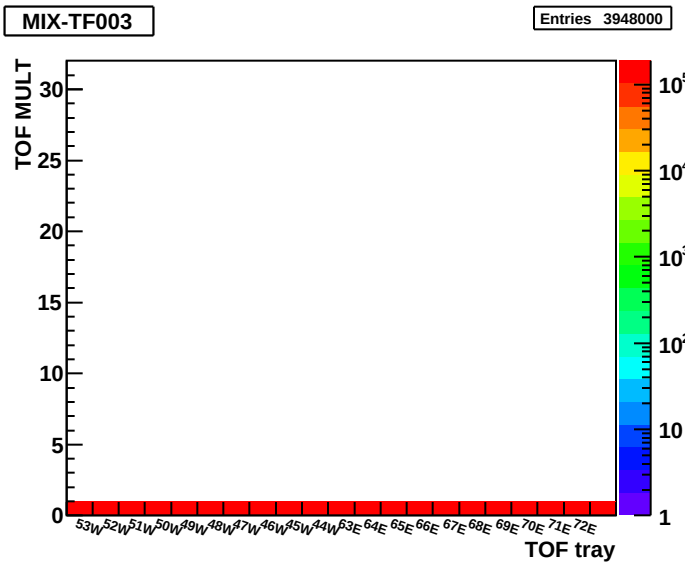

Barrel Jet Patches
Entries 1692000
JP ADC
JP ID

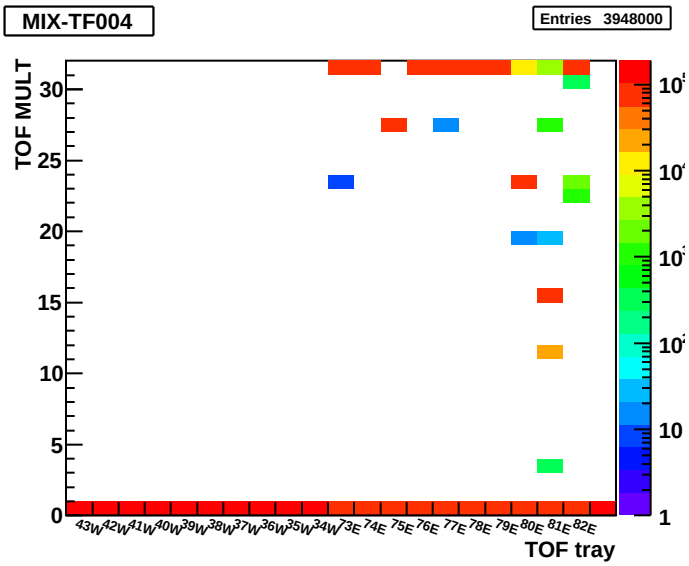

Endcap Jet Patches
Entries 564000
JP ADC
JP ID

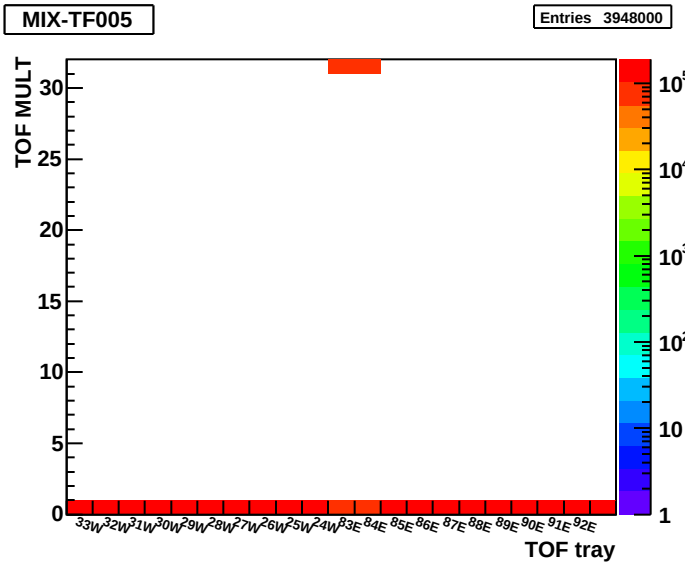

Hybrid Jet Patches
Entries 188000
JP ADC
JP ID

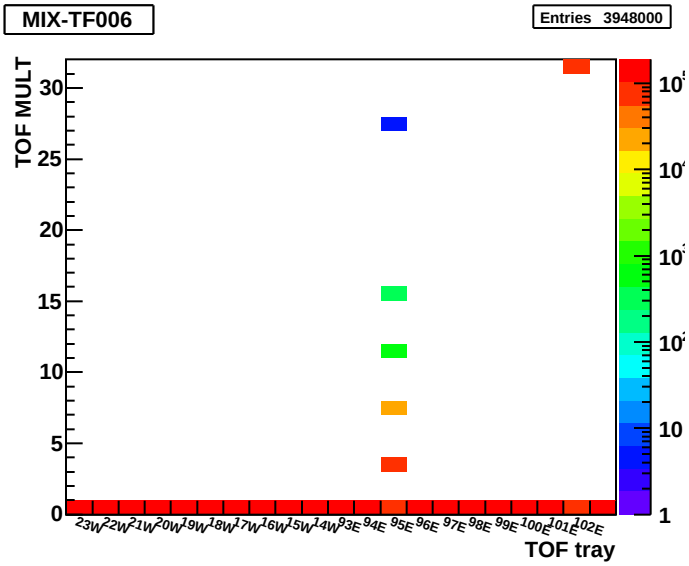

MIX-TF001
Entries 3948000
TOF MULT
TOF tray

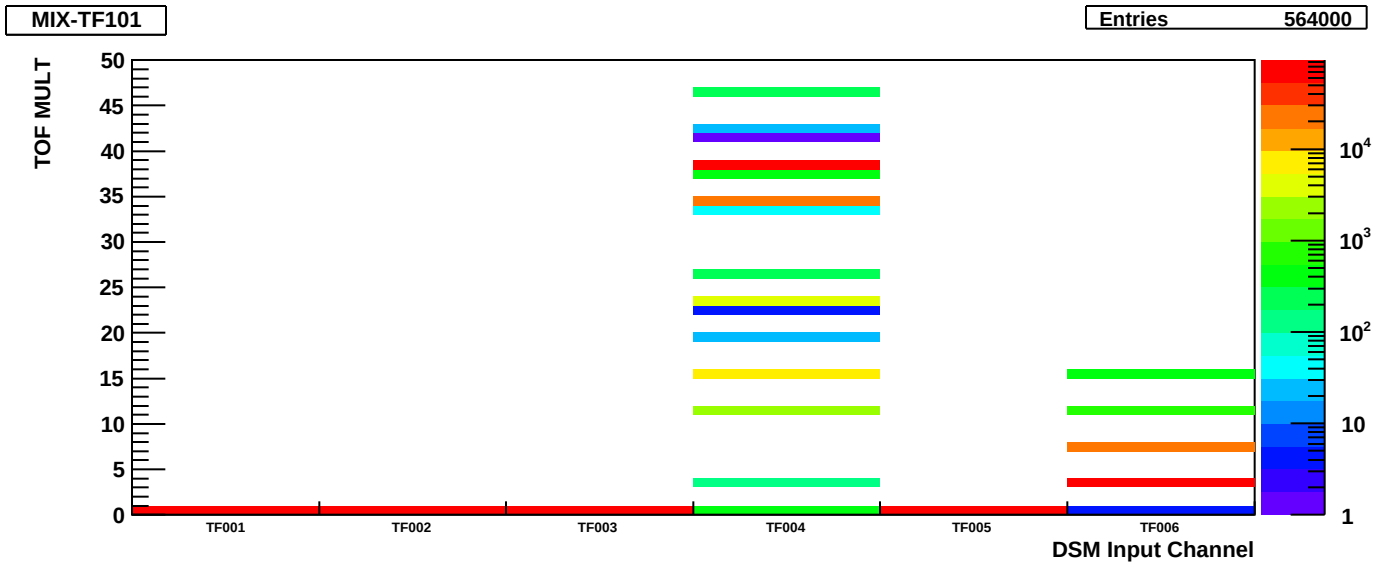

MIX-TF002
Entries 3948000
TOF MULT
TOF tray

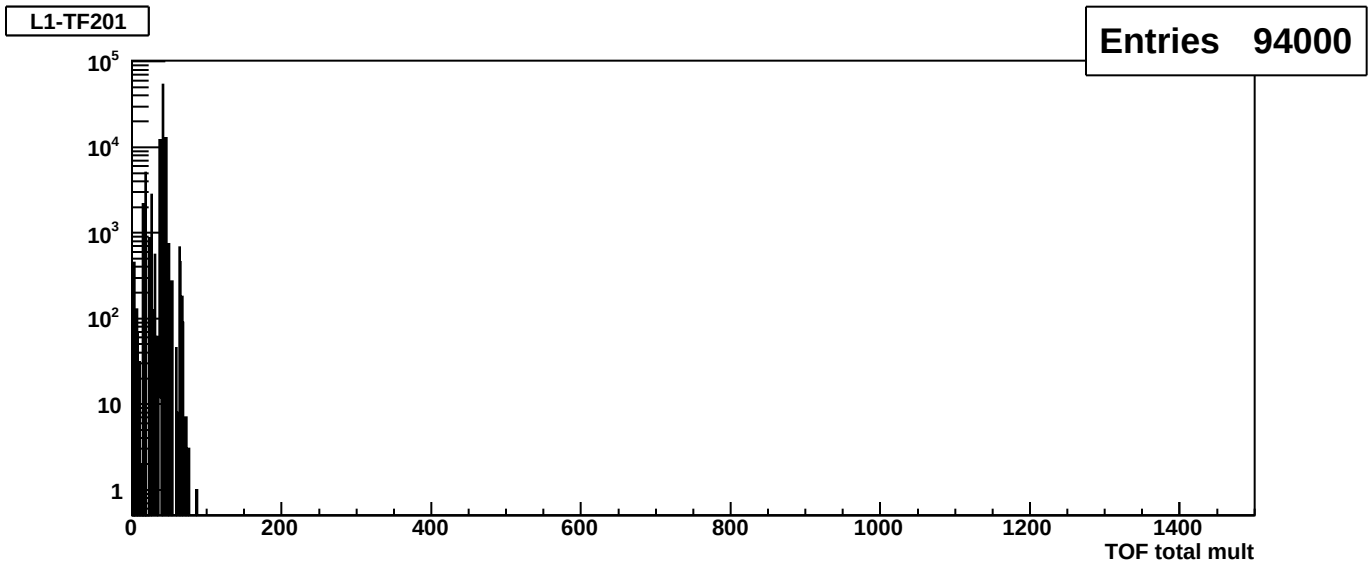

MIX-TF003
Entries 3948000
TOF MULT
TOF tray

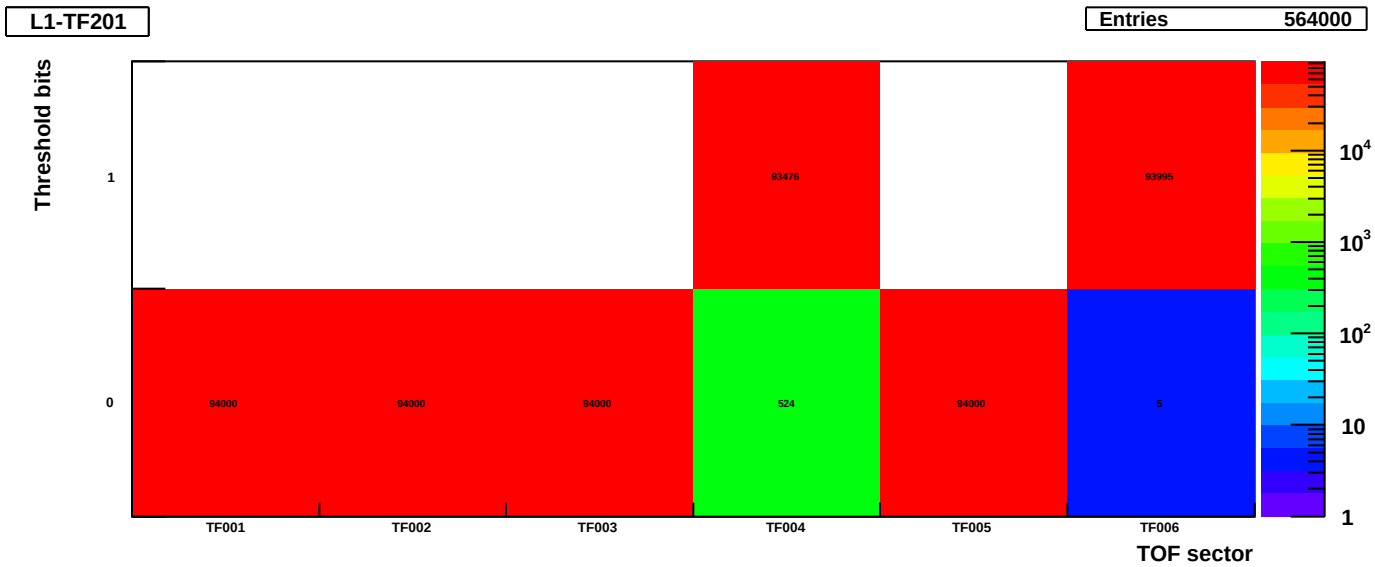

MIX-TF004
Entries 3948000
TOF MULT
TOF tray

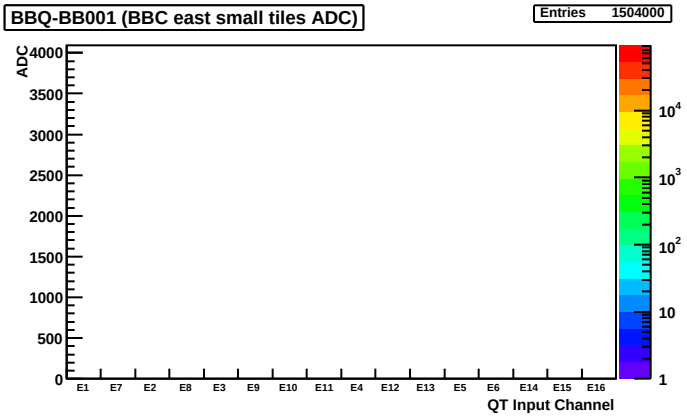

MIX-TF005
Entries 3948000
TOF MULT
TOF tray

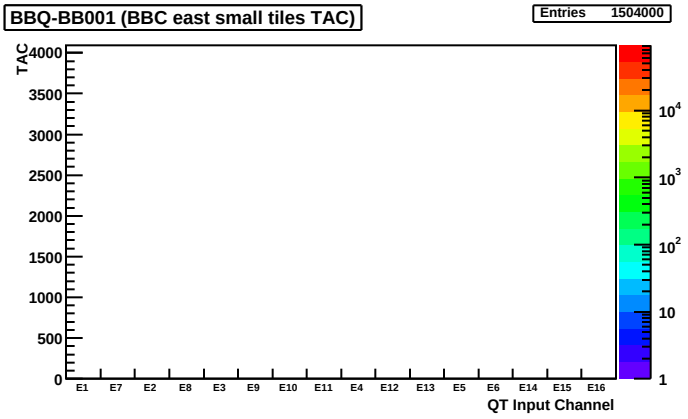

MIX-TF006
Entries 3948000
TOF MULT
TOF tray

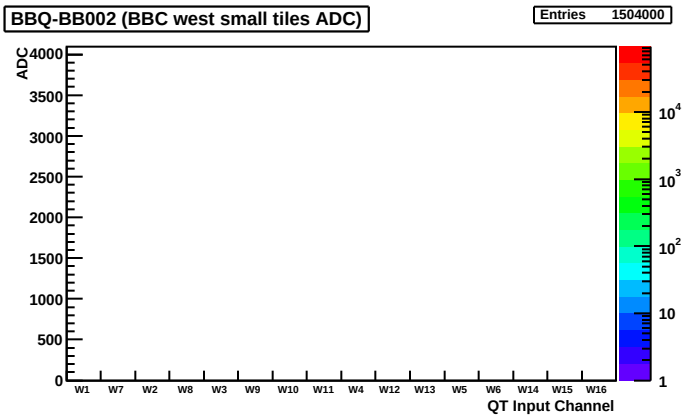

MIX-TF101
Entries 564000
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

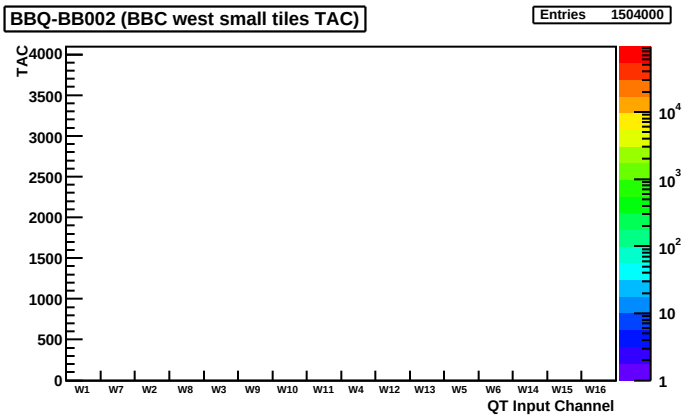

L1-TF201
Entries 94000
10^5
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

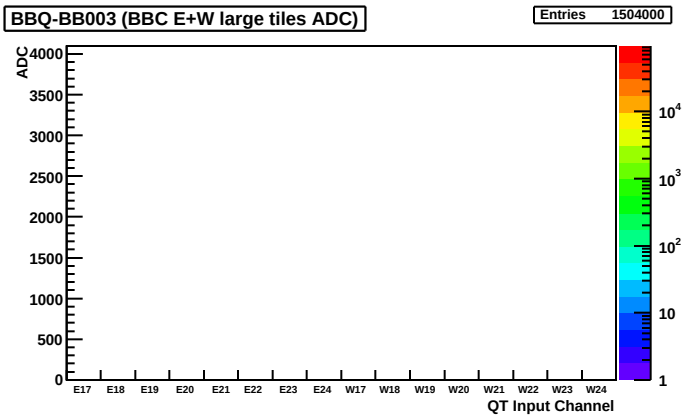

L1-TF201
Entries 564000
Threshold bits
1
0
93476
93995
94000
94000
94000
524
94000
5
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3
10^2
10
1

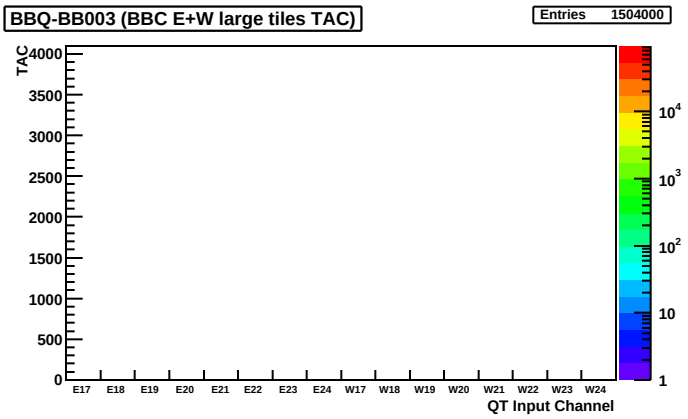

BBQ-BB001 (BBC east small tiles ADC)
Entries 1504000
ADC
QT Input Channel

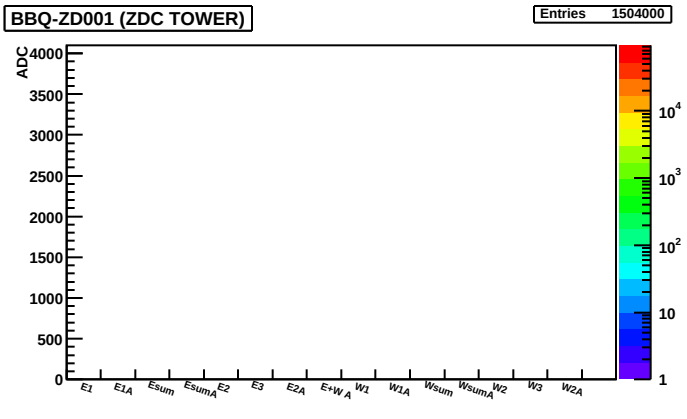

BBQ-BB001 (BBC east small tiles TAC)
Entries 1504000
TAC
QT Input Channel

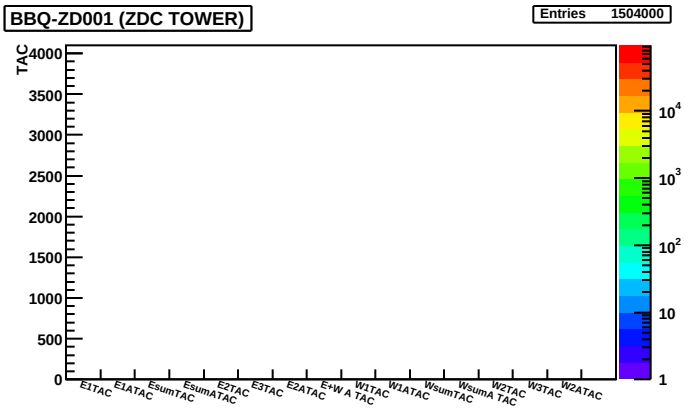

BBQ-BB002 (BBC west small tiles ADC)
Entries 1504000
ADC
QT Input Channel

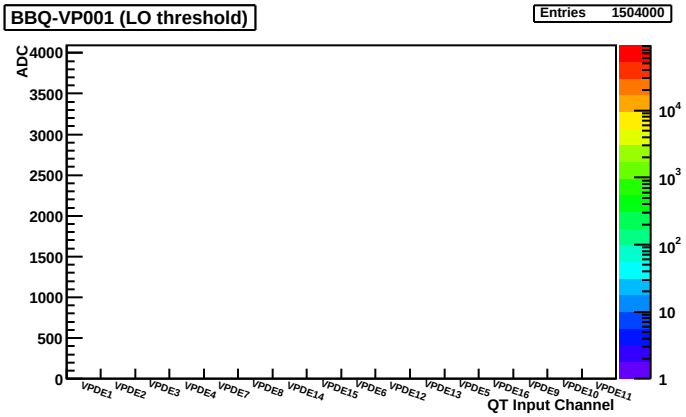

BBQ-BB002 (BBC west small tiles TAC)
Entries 1504000
TAC
QT Input Channel

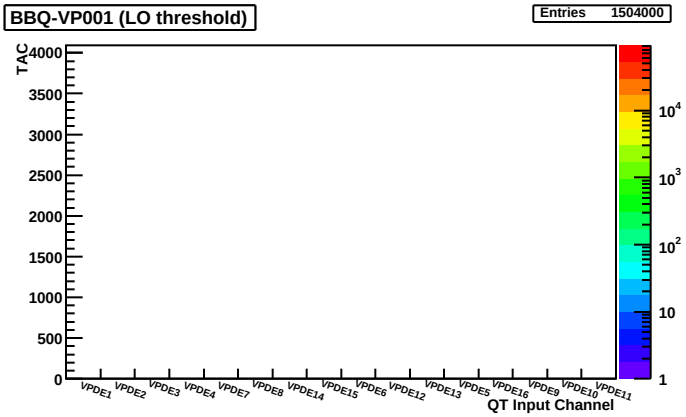

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1504000
ADC
QT Input Channel

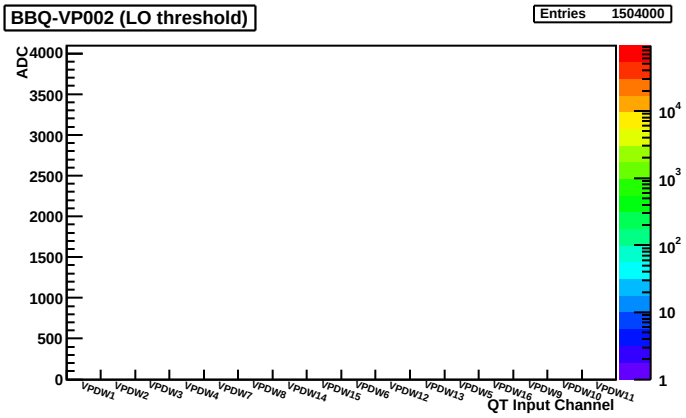

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1504000
TAC
QT Input Channel

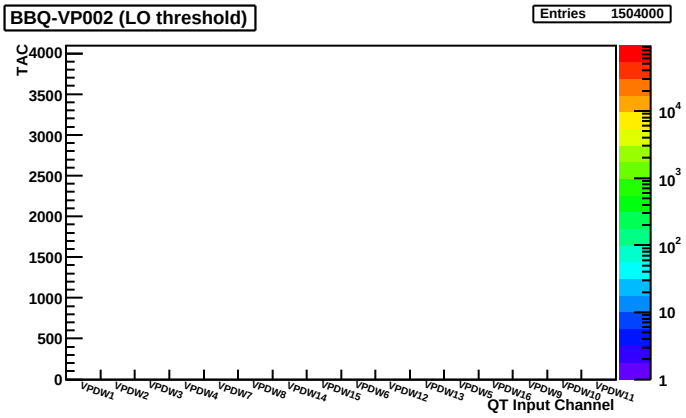

BBQ-ZD001 (ZDC TOWER)
Entries 1504000
ADC

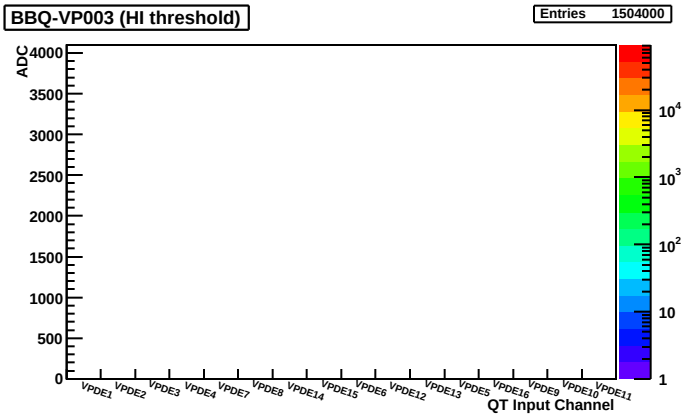

BBQ-ZD001 (ZDC TOWER)
Entries 1504000
TAC

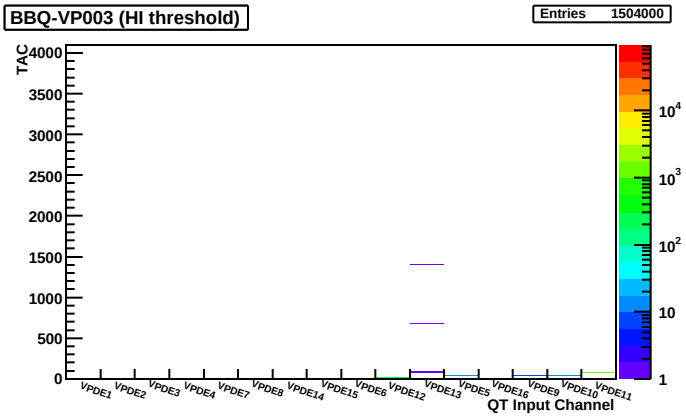

BBQ-VP001 (LO threshold)
Entries 1504000
ADC
QT Input Channel

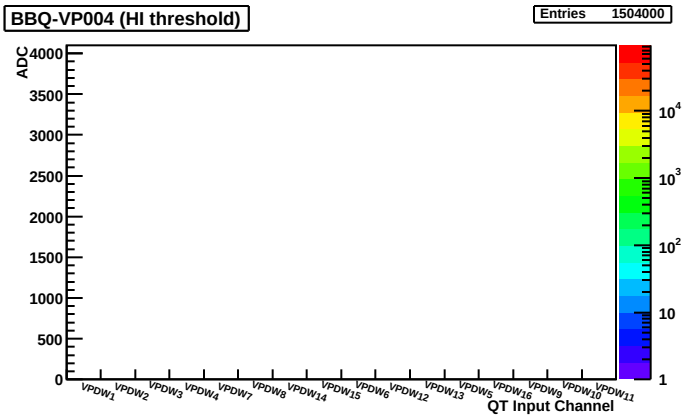

BBQ-VP001 (LO threshold)
Entries 1504000
TAC
QT Input Channel

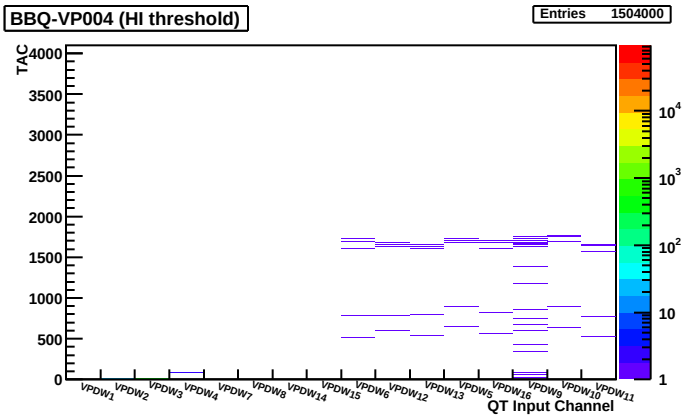

BBQ-VP002 (LO threshold)
Entries 1504000
ADC
QT Input Channel

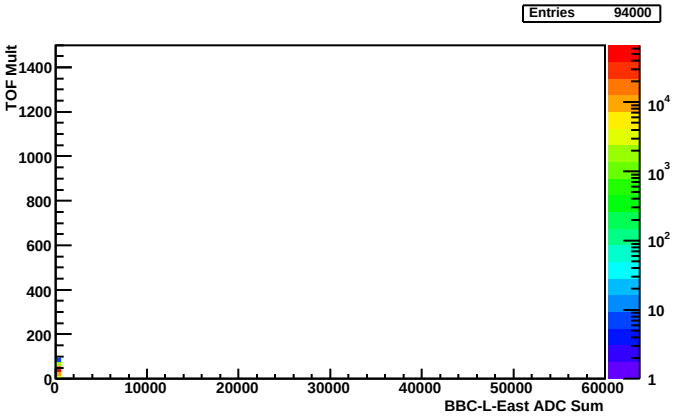

BBQ-VP002 (LO threshold)
Entries 1504000
TAC
QT Input Channel

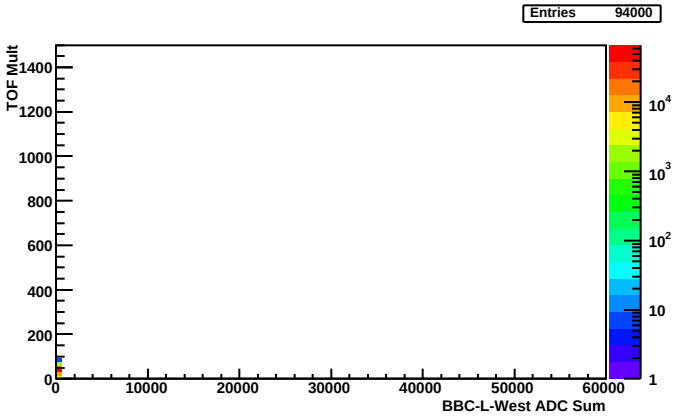

BBQ-VP003 (HI threshold)
Entries 1504000
ADC
QT Input Channel

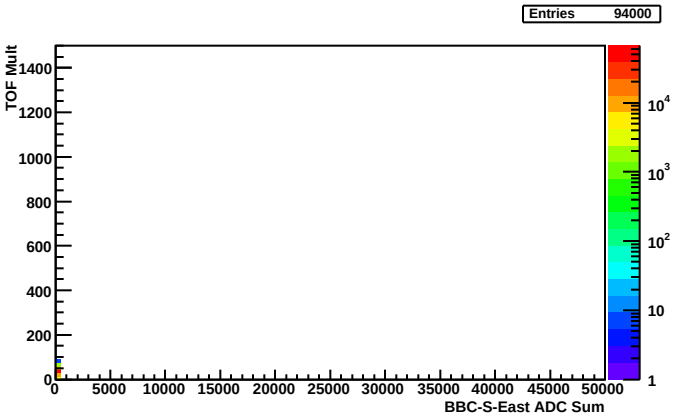

BBQ-VP003 (HI threshold)
Entries 1504000
TAC
QT Input Channel

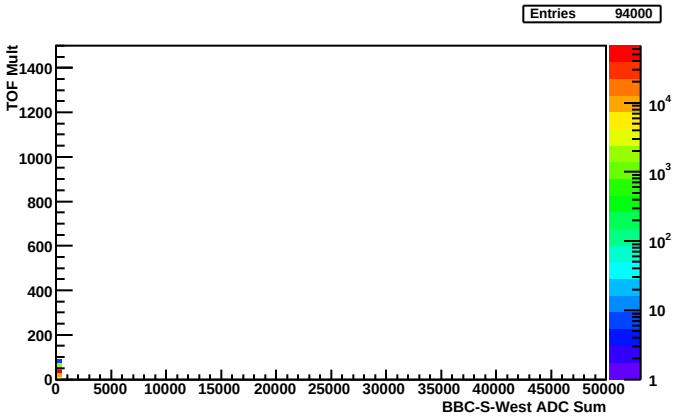

BBQ-VP004 (HI threshold)
Entries 1504000
ADC
QT Input Channel

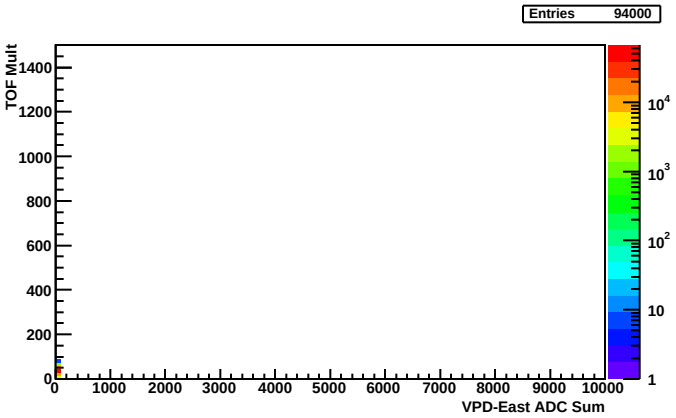

BBQ-VP004 (HI threshold)
Entries 1504000
TAC
QT Input Channel

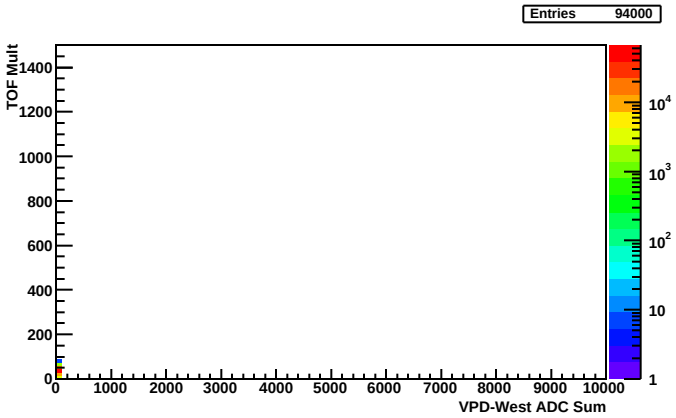

Entries 94000
TOF Mult
BBC-L-East ADC Sum

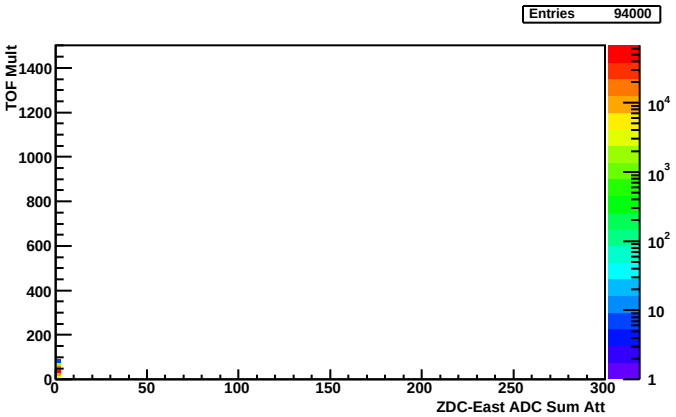

Entries 94000
TOF Mult
BBC-L-West ADC Sum

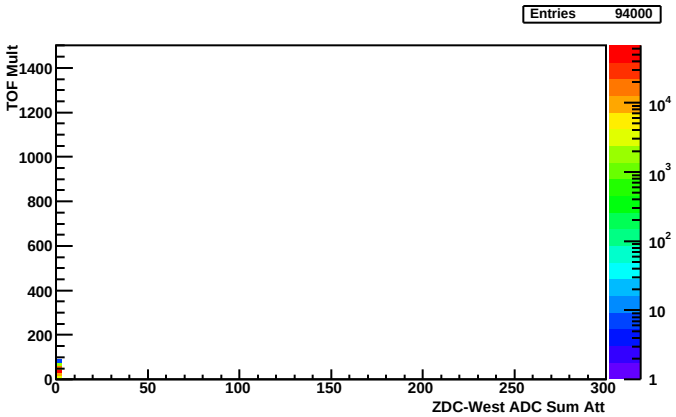

Entries 94000
TOF Mult
BBC-S-East ADC Sum

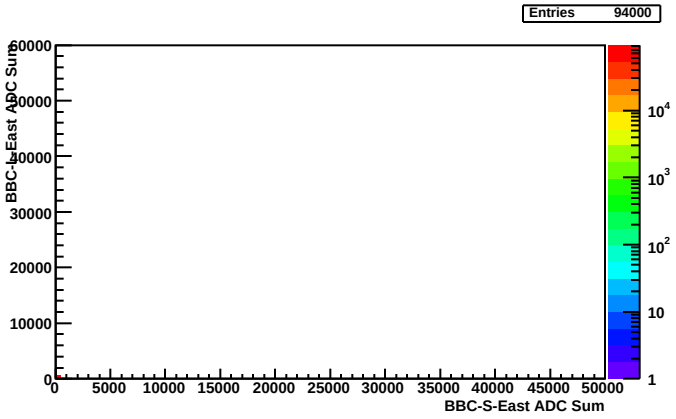

Entries 94000
TOF Mult
BBC-S-West ADC Sum

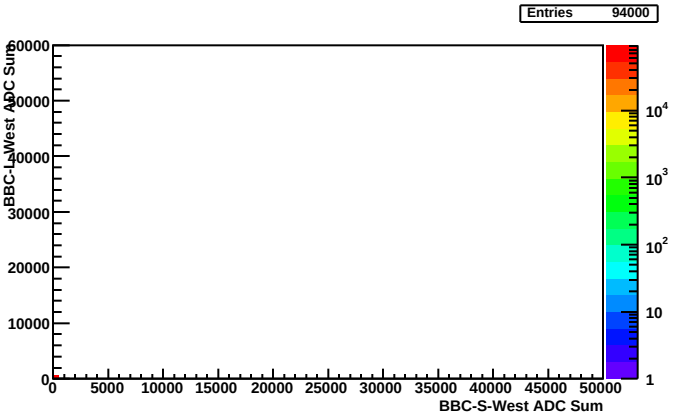

Entries 94000
TOF Mult
VPD-East ADC Sum

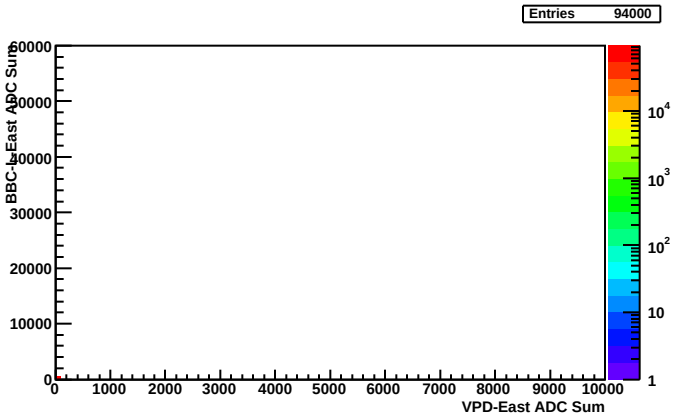

Entries 94000
TOF Mult
VPD-West ADC Sum

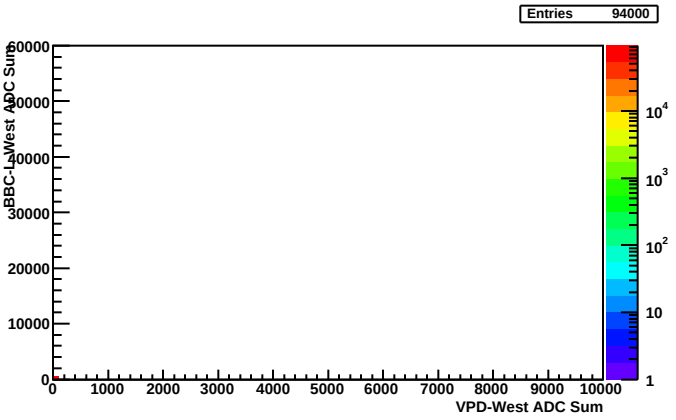

Entries 94000
TOF Mult
ZDC-East ADC Sum Att

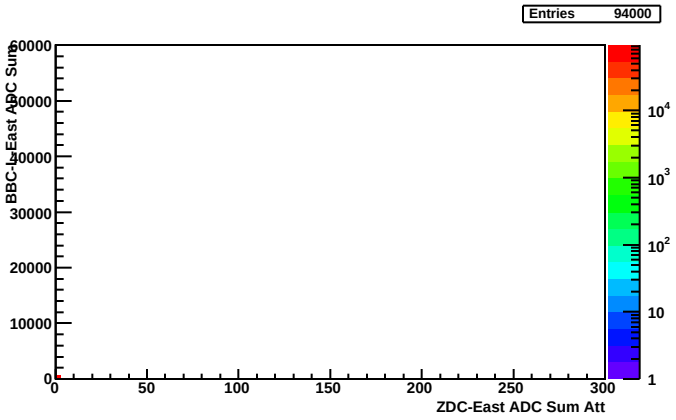

Entries 94000
TOF Mult
ZDC-West ADC Sum Att

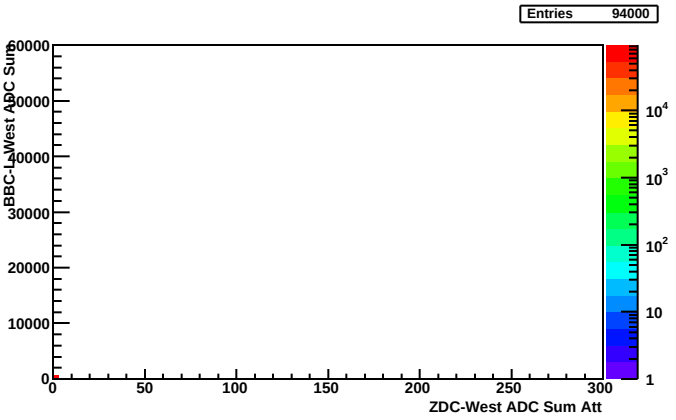

Entries 94000
BBC-Ls-East ADC Sum
BBC-S-East ADC Sum

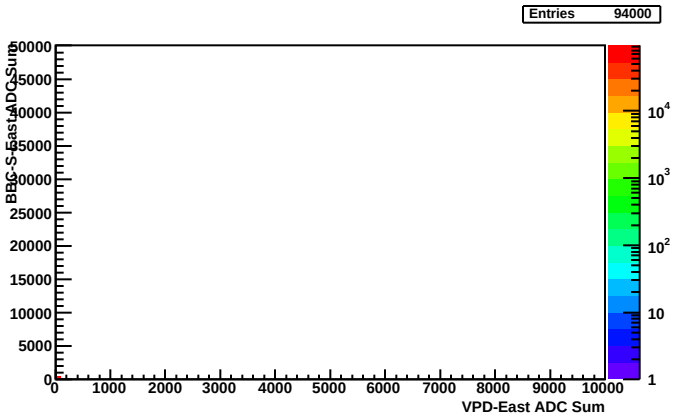

Entries 94000
BBC-Ls-West ADC Sum
BBC-S-West ADC Sum

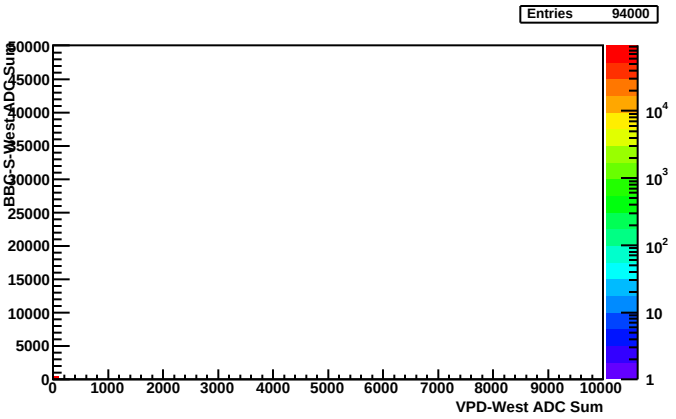

Entries 94000
BBC-Ls-East ADC Sum
VPD-East ADC Sum

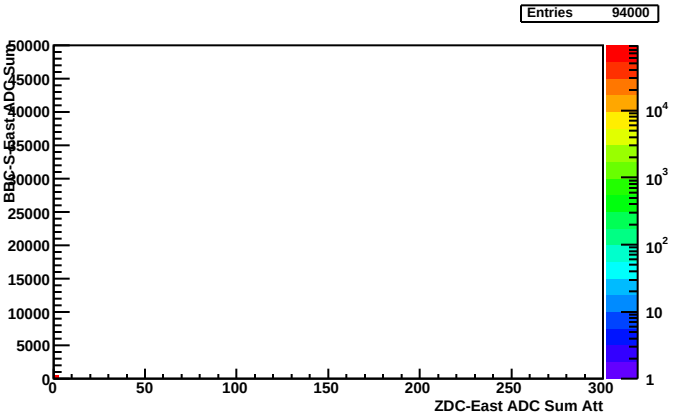

Entries 94000
BBC-Ls-West ADC Sum
VPD-West ADC Sum

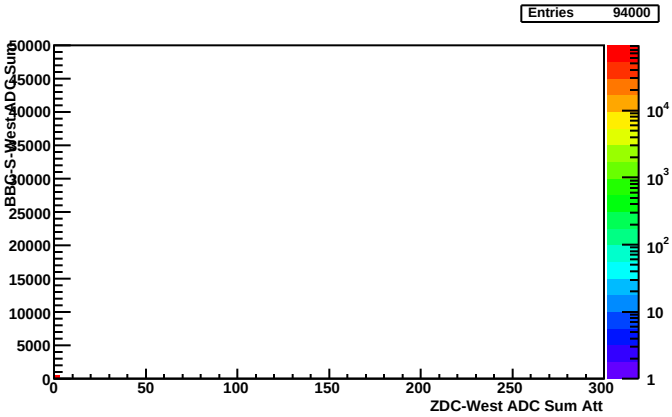

Entries 94000
BBC-Ls-East ADC Sum
ZDC-East ADC Sum Att

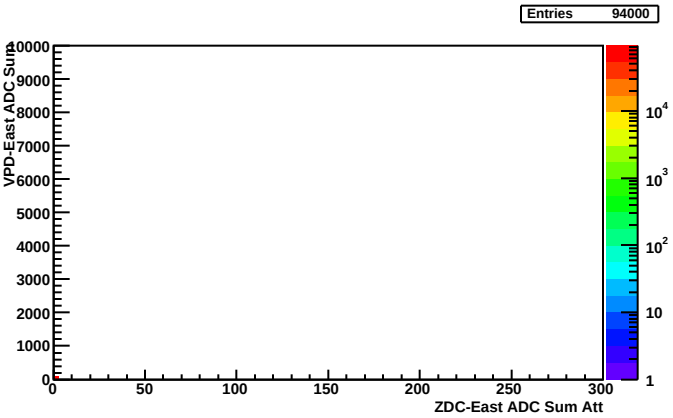

Entries 94000
BBC-Ls-West ADC Sum
ZDC-West ADC Sum Att

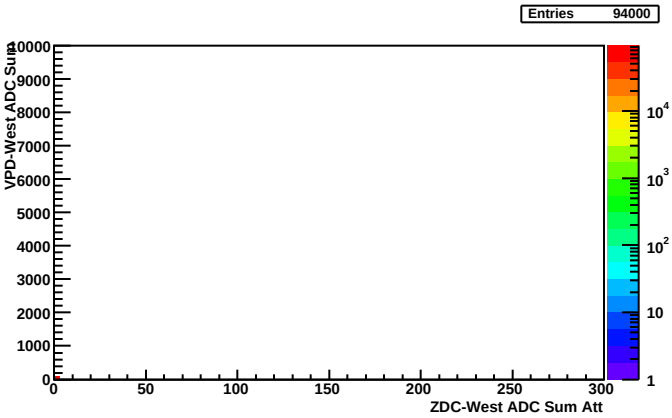

Entries 94000
BBC-S-East ADC Sum
VPD-East ADC Sum

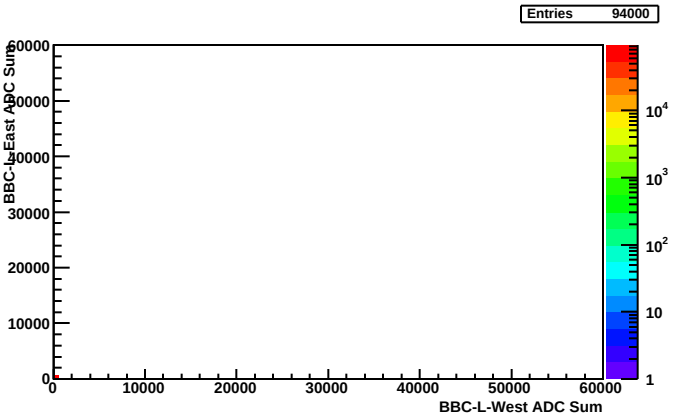

Entries 94000
BBC-S-West ADC Sum
VPD-West ADC Sum

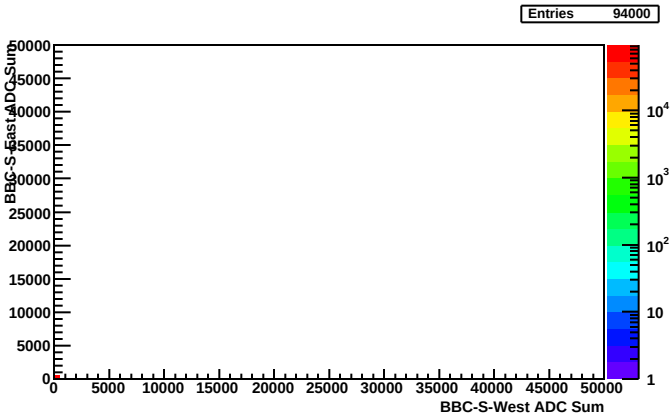

Entries 94000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

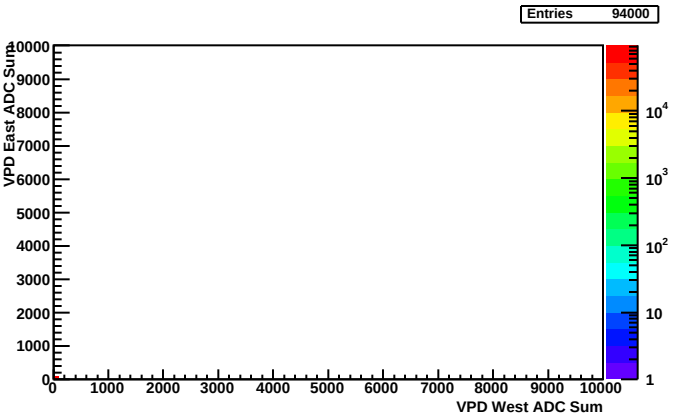

Entries 94000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

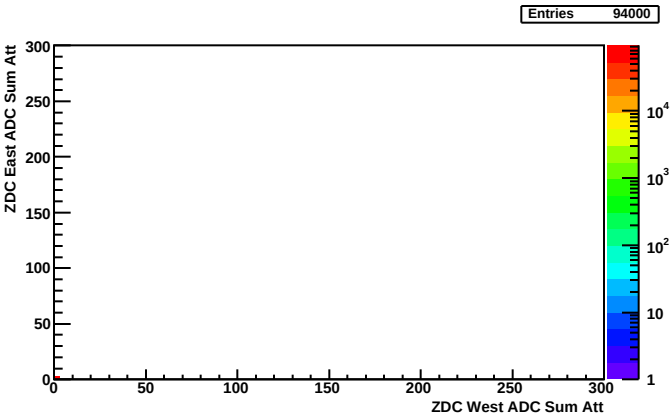

Entries 94000
VPD-East ADC Sum
ZDC-East ADC Sum Att

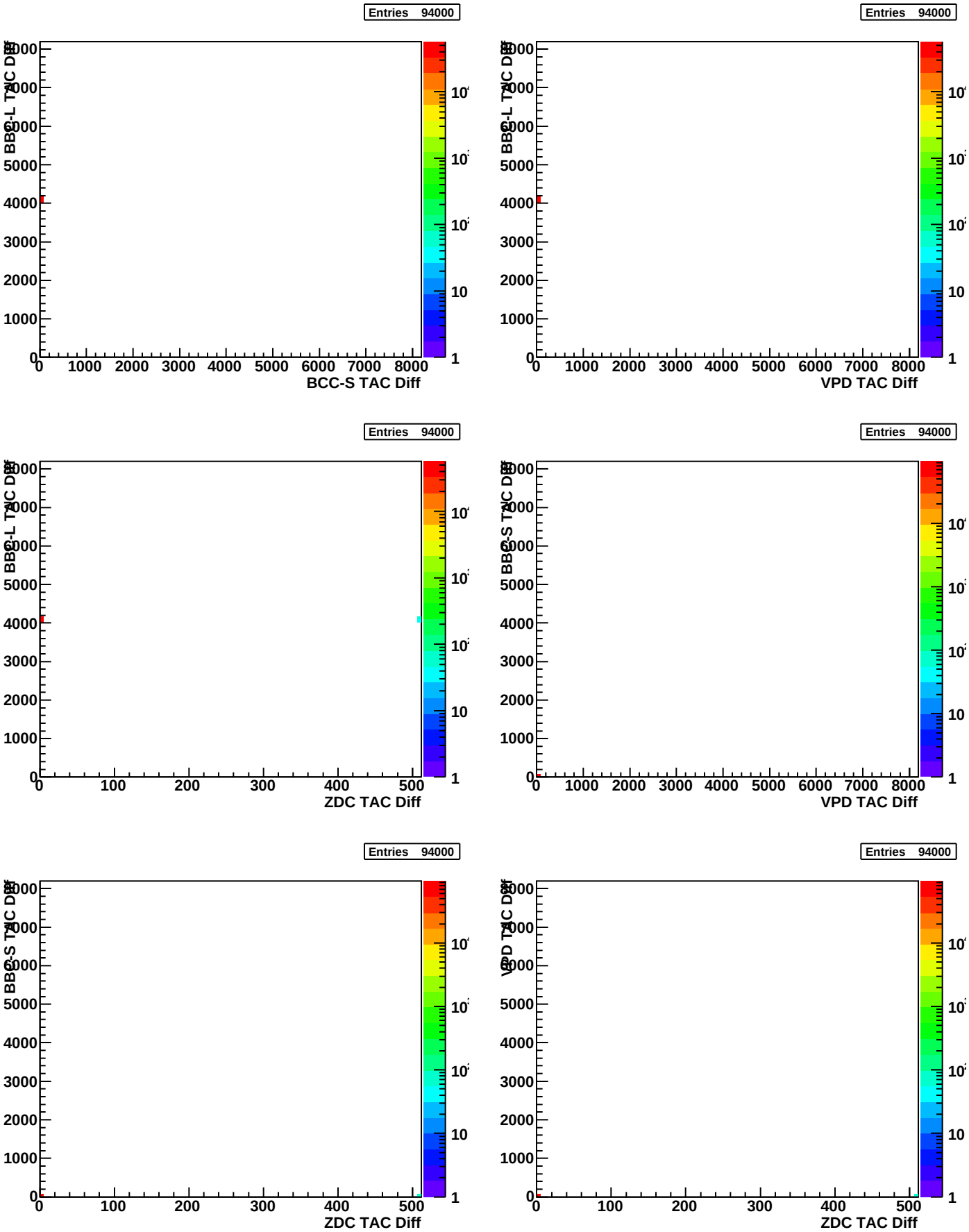

Entries 94000
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 94000
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 94000
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 94000
VPD East ADC Sum
VPD West ADC Sum


Entries 94000
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 94000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 94000
BBC-L TAC Diff
VPD TAC Diff
Entries 94000
BBC-L TAC Diff
ZDC TAC Diff
Entries 94000
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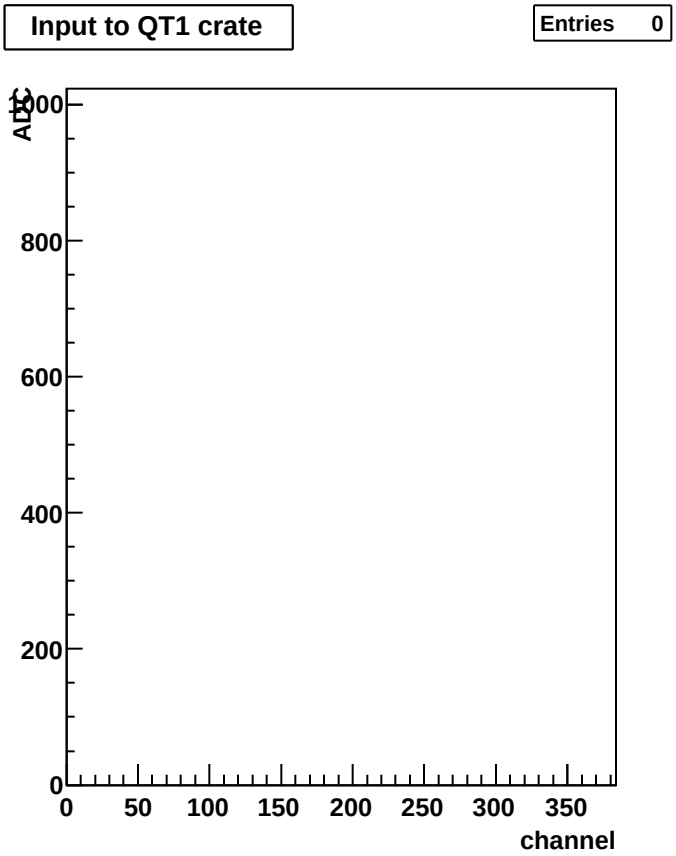
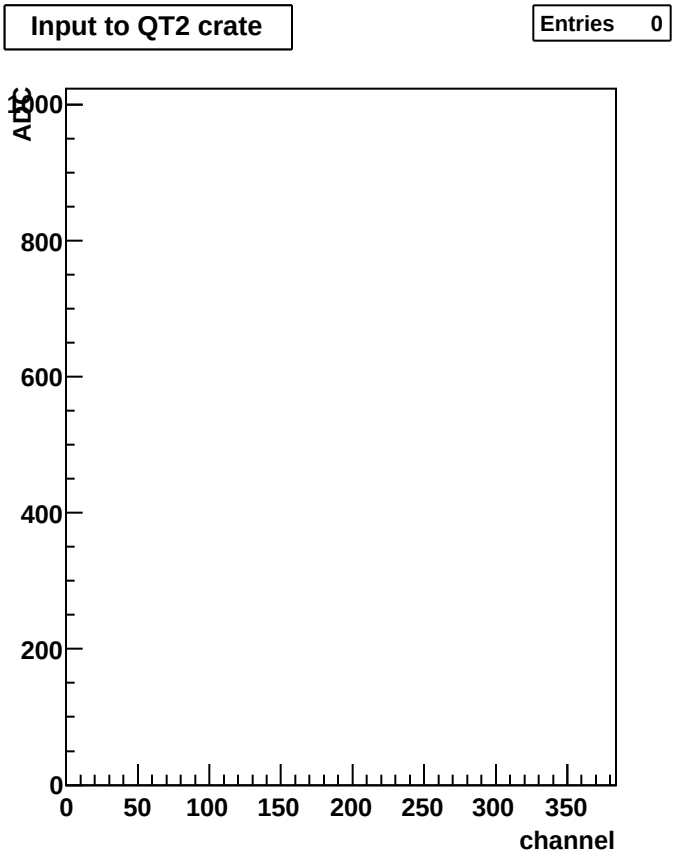
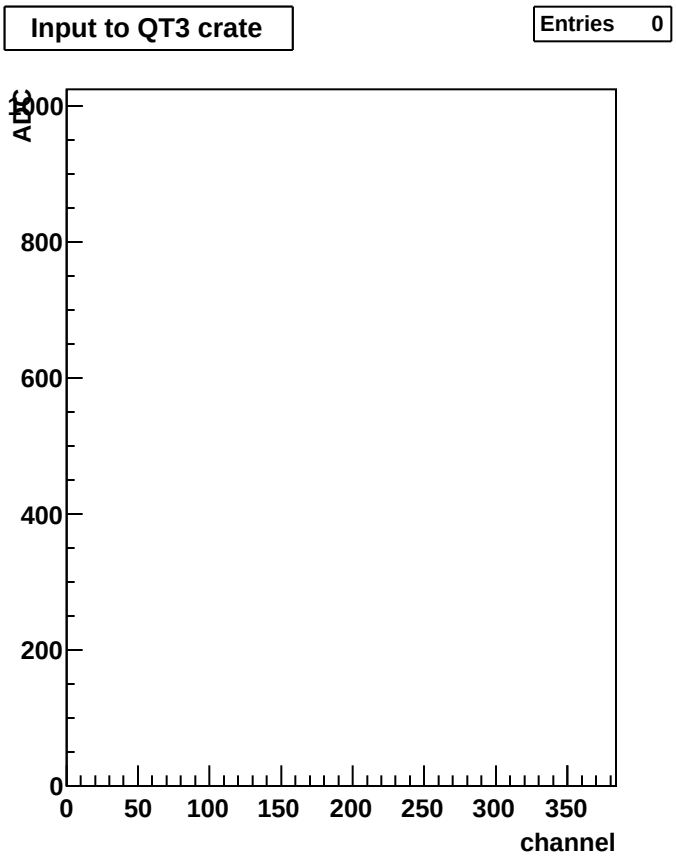
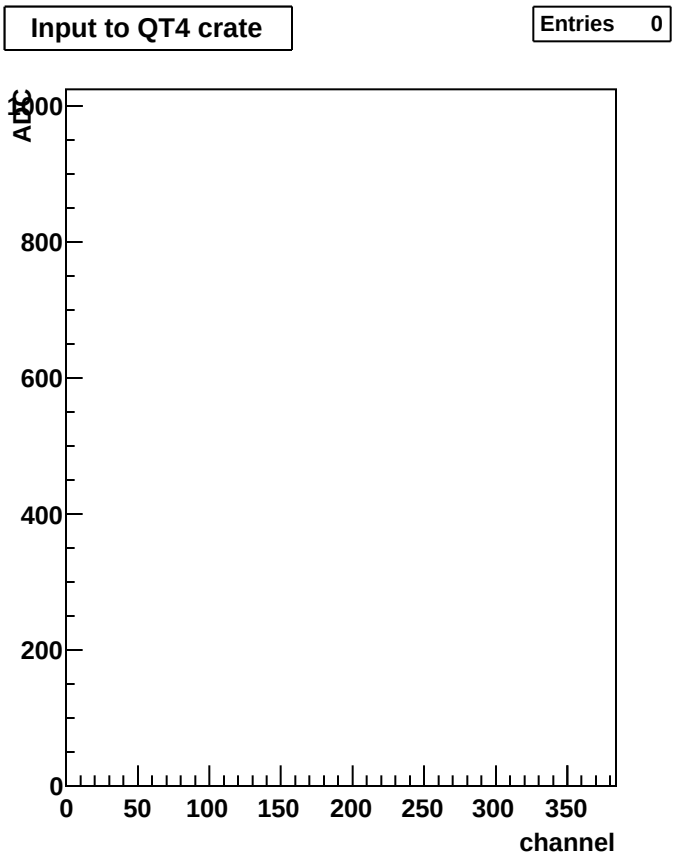
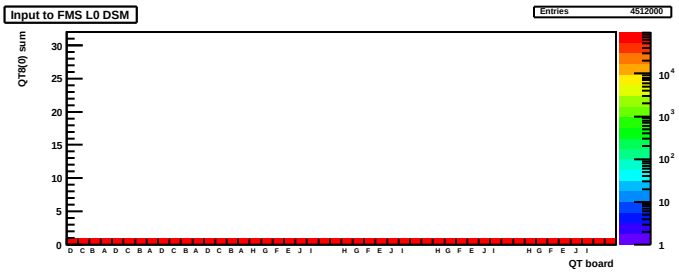
VPD TAC Diff
Entries 94000
BBC-S TAC Diff
ZDC TAC Diff
Entries 94000
VPD TAC Diff
ZDC TAC Diff

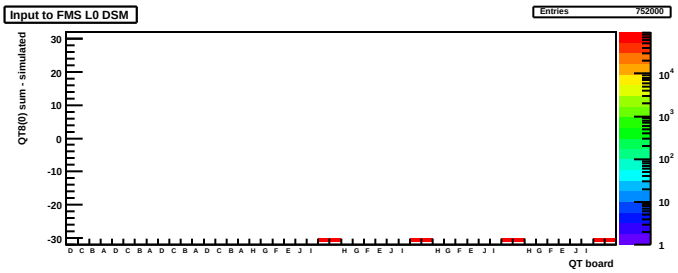

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

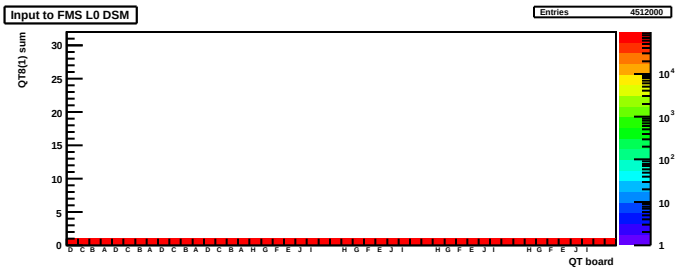

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

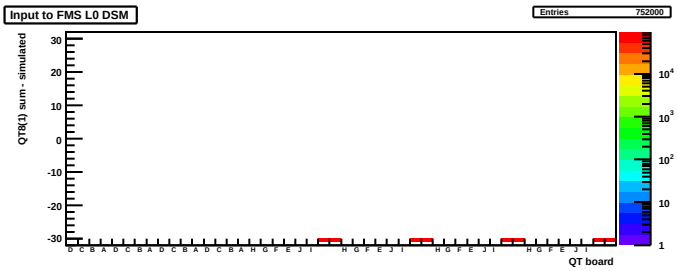

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

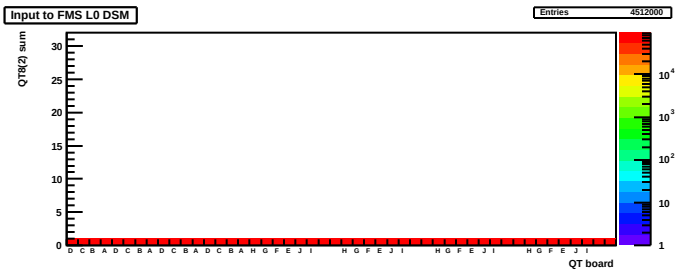

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

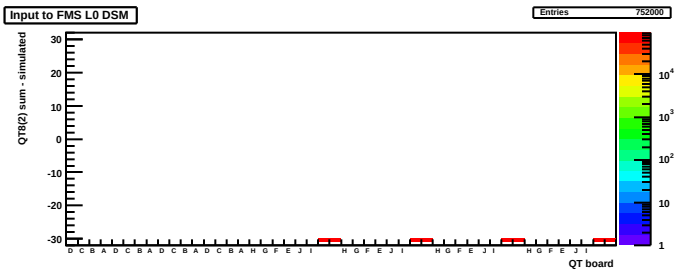

Input to FMS L0 DSM
Entries 4512000
QT8(0) sum
QT board

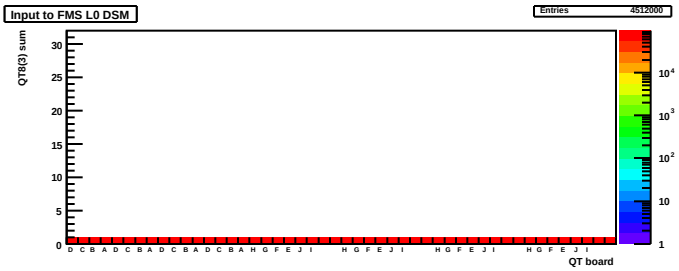

Input to FMS L0 DSM
Entries 752000
QT8(0) sum - simulated
QT board

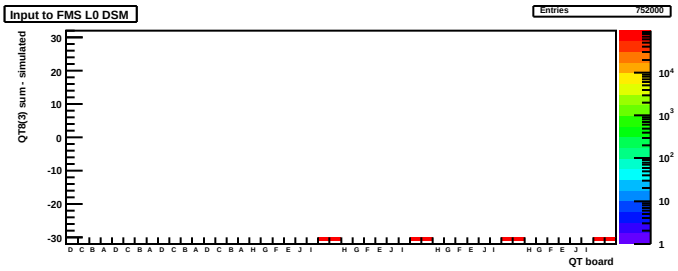

Input to FMS L0 DSM
Entries 4512000
QT8(1) sum
QT board

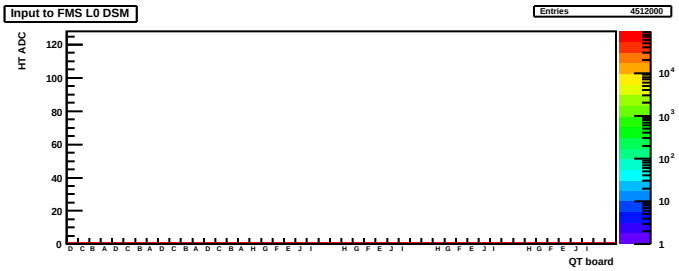

Input to FMS L0 DSM
Entries 752000
QT8(1) sum - simulated
QT board

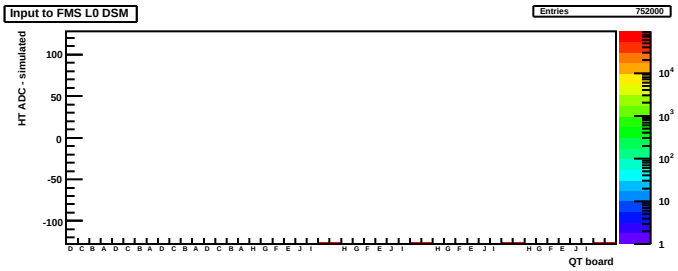

Input to FMS L0 DSM
Entries 4512000
QT8(2) sum
QT board

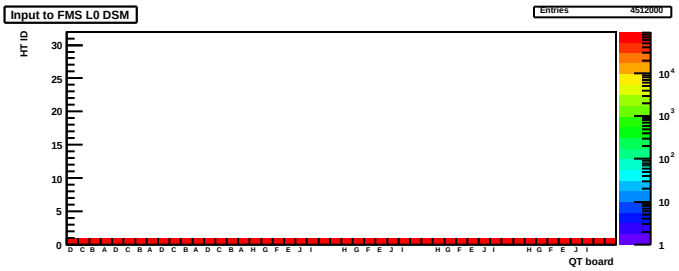

Input to FMS L0 DSM
Entries 752000
QT8(2) sum - simulated
QT board

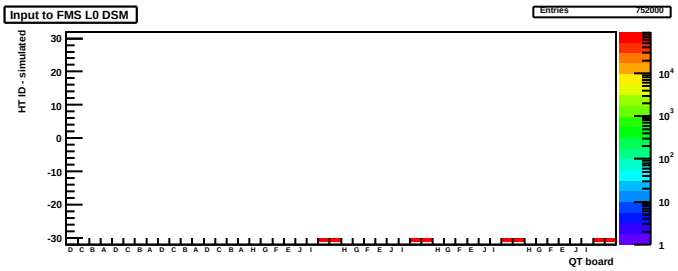

Input to FMS L0 DSM
Entries 4512000
QT8(3) sum
QT board

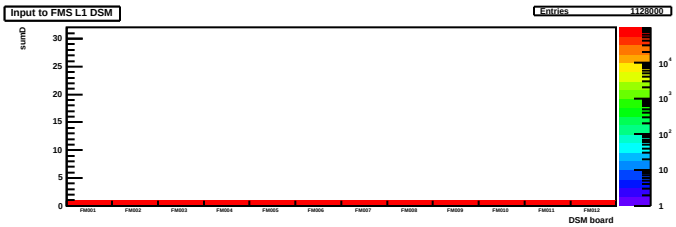

Input to FMS L0 DSM
Entries 752000
QT8(3) sum - simulated
QT board

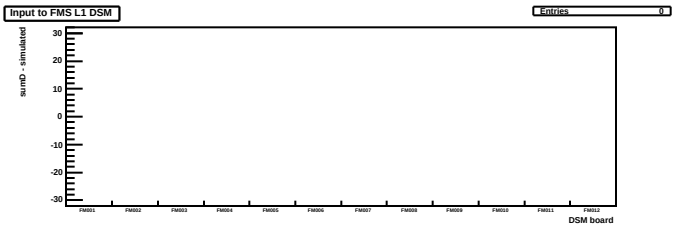

Input to FMS L0 DSM
Entries 4512000
HT ADC
QT board

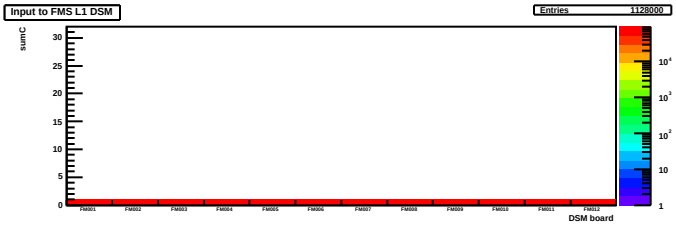

Input to FMS L0 DSM
Entries 752000
HT ADC - simulated
QT board

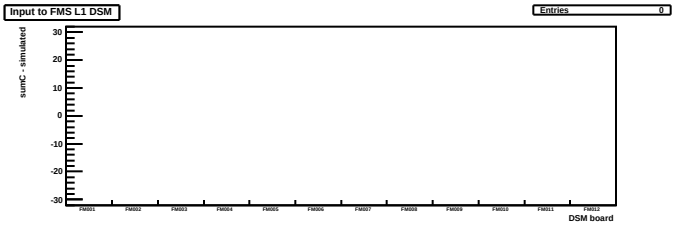

Input to FMS L0 DSM
Entries 4512000
HT ID
QT board

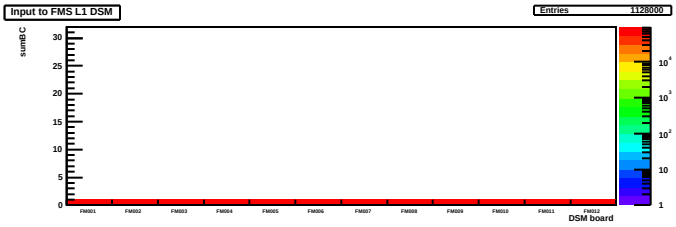

Input to FMS L0 DSM
Entries 752000
HT ID - simulated
QT board

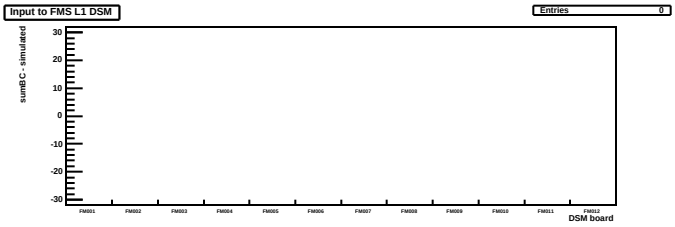

Input to FMS L1 DSM
Entries 1128000
sumD
DSM board

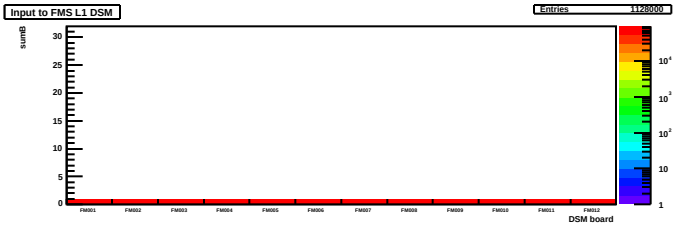

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

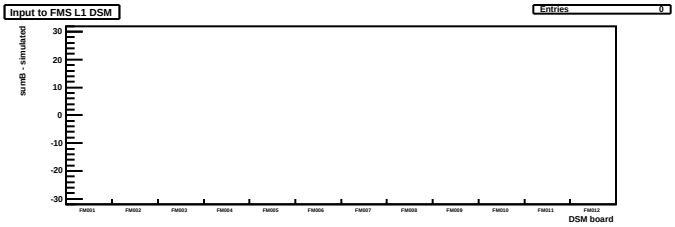

Input to FMS L1 DSM
Entries 1128000
sumC
DSM board

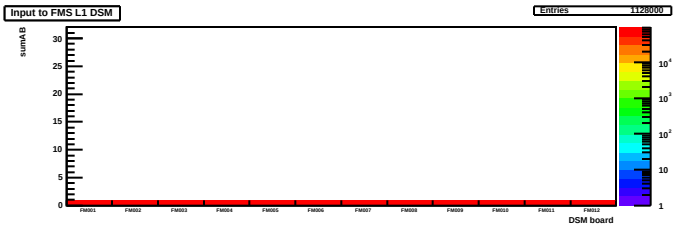

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

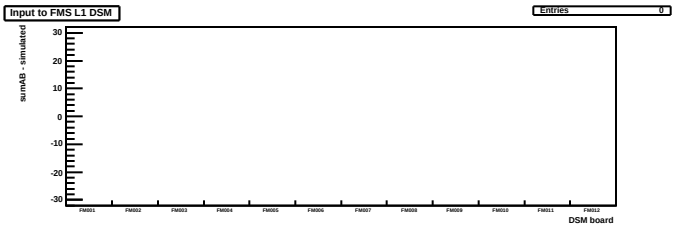

Input to FMS L1 DSM
Entries 1128000
sumBC
DSM board

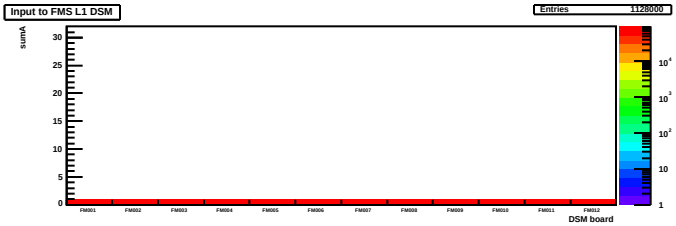

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

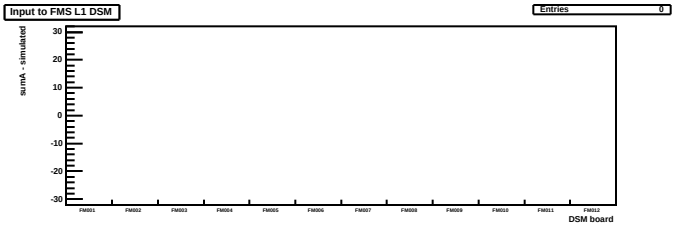

Input to FMS L1 DSM
Entries 1128000
sumB
DSM board

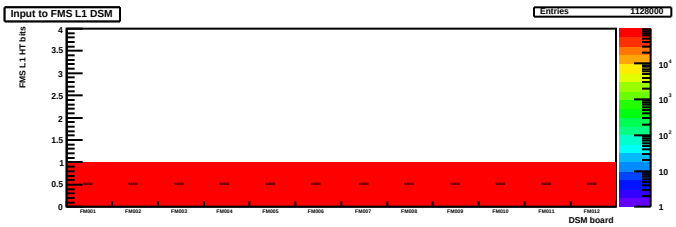

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

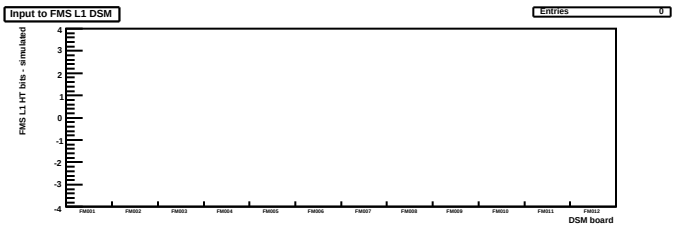

Input to FMS L1 DSM
Entries 1128000
sumAB
DSM board

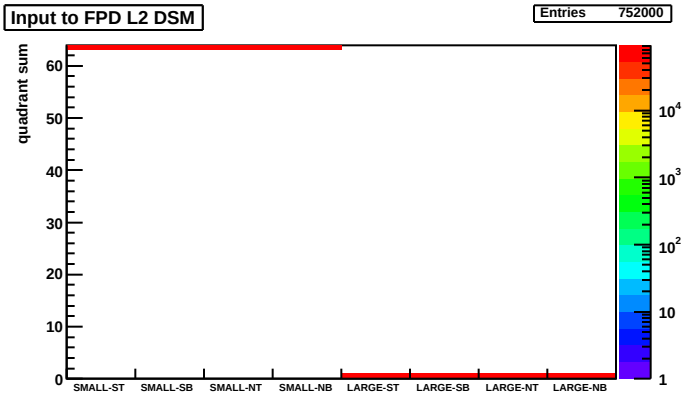

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

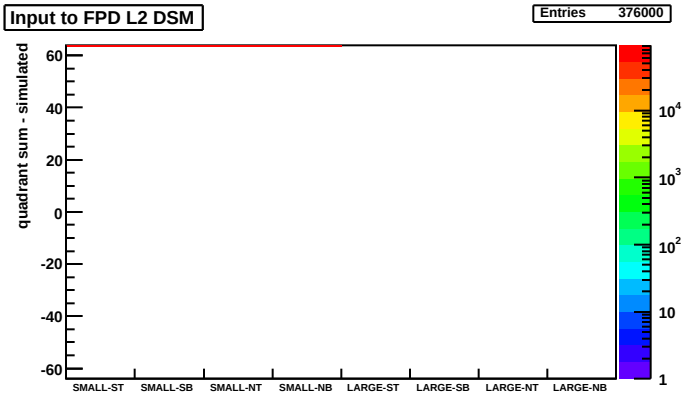

Input to FMS L1 DSM
Entries 1128000
sumA
DSM board

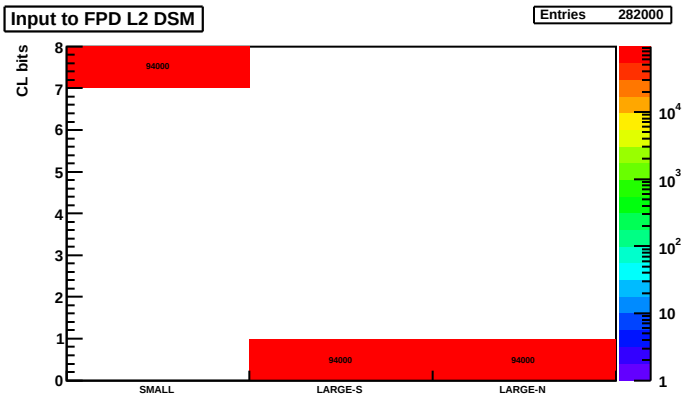

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

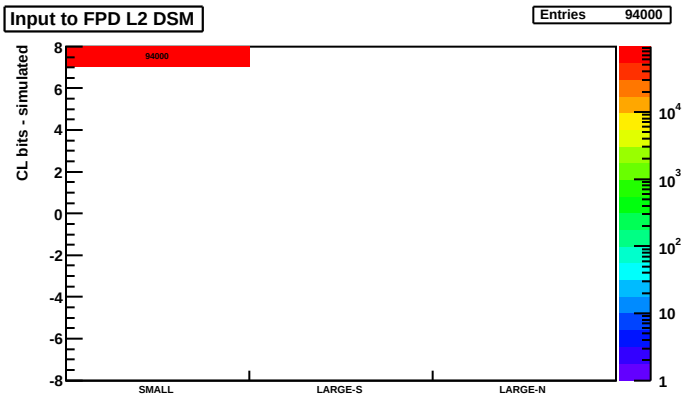

Input to FMS L1 DSM
Entries 1128000
FMS L1 HT bits
DSM board

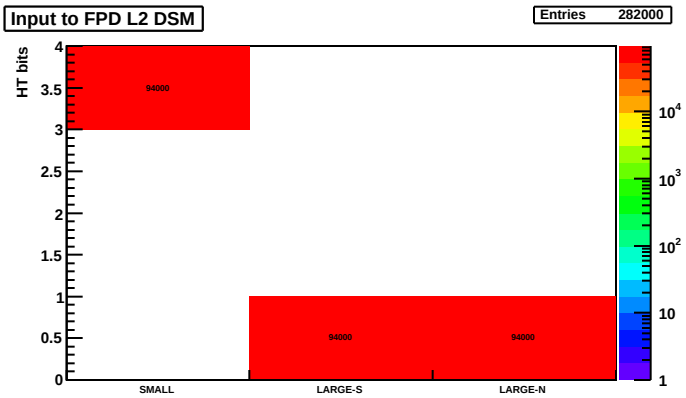

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

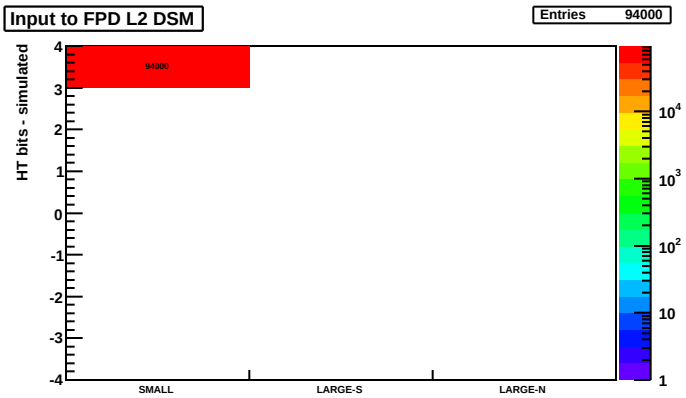

Input to FPD L2 DSM
Entries 752000
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

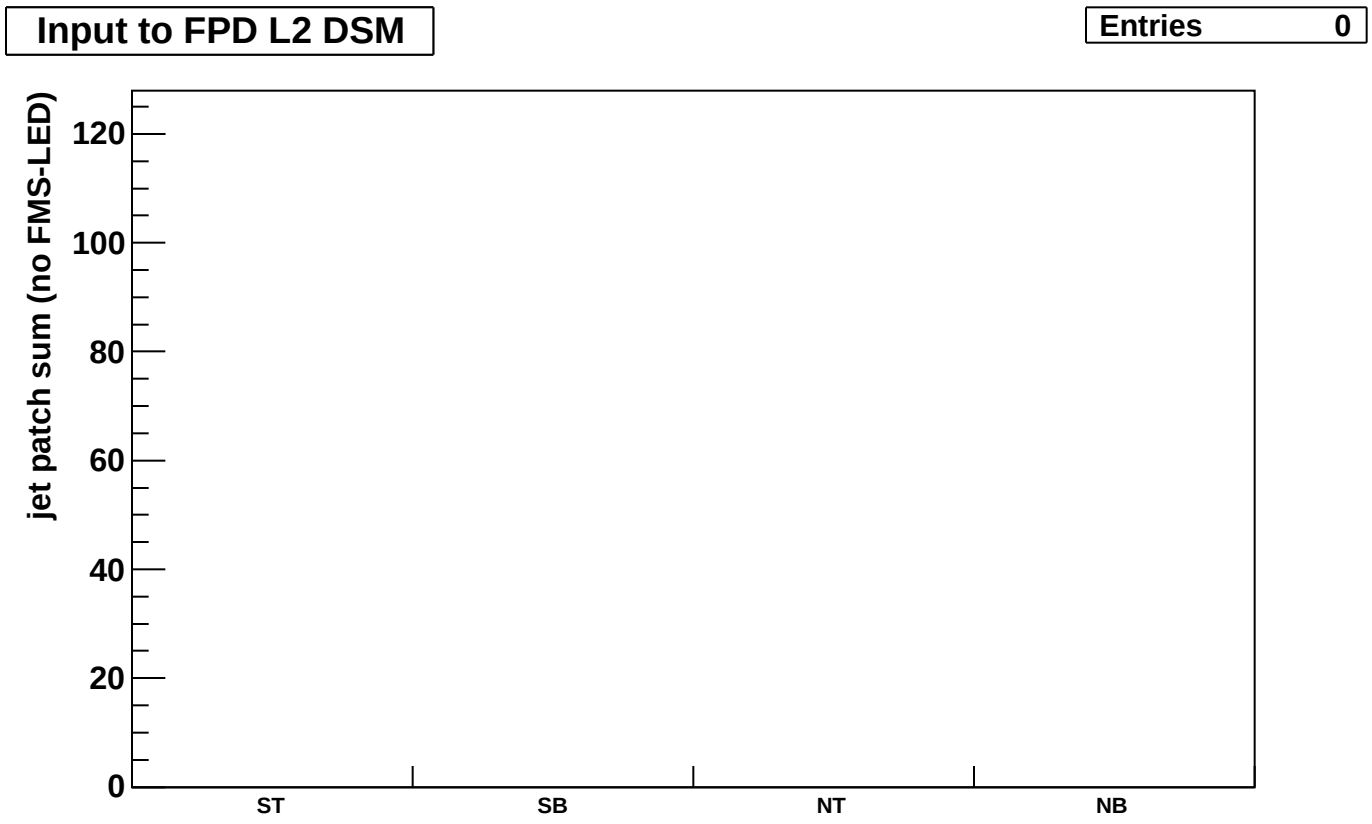

Input to FPD L2 DSM
Entries 376000
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

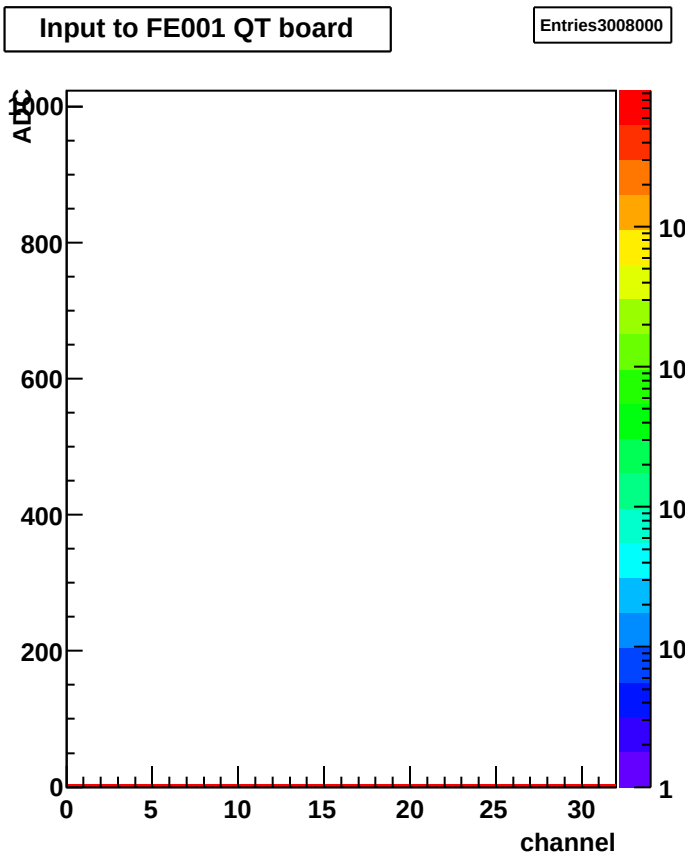

Input to FPD L2 DSM
Entries 282000
CL bits
94000
94000
94000
SMALL
LARGE-S
LARGE-N

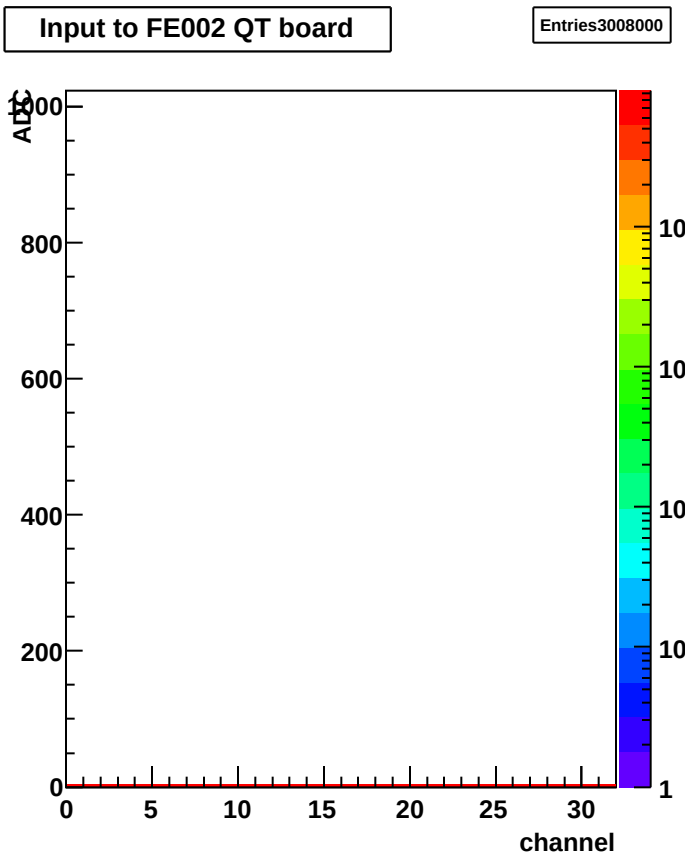

Input to FPD L2 DSM
Entries 94000
CL bits - simulated
94000
SMALL
LARGE-S
LARGE-N

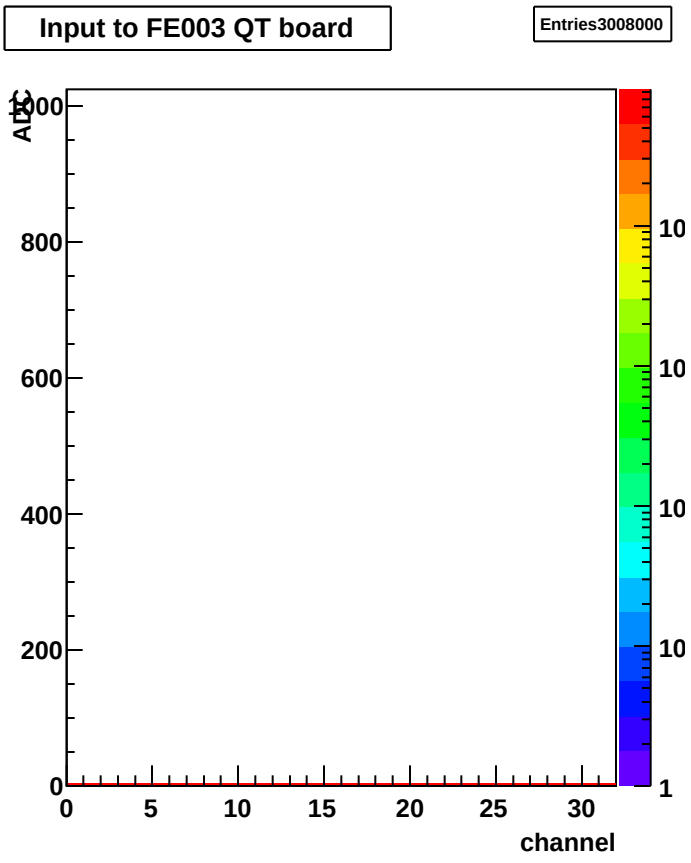

Input to FPD L2 DSM
Entries 282000
HT bits
94000
94000
94000
SMALL
LARGE-S
LARGE-N

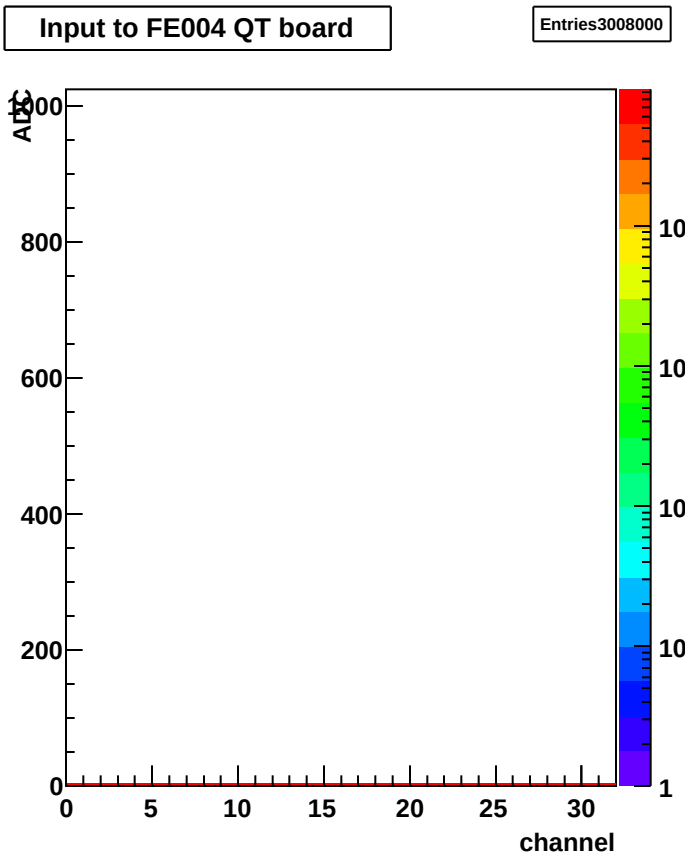

Input to FPD L2 DSM
Entries 94000
HT bits - simulated
94000
SMALL
LARGE-S
LARGE-N

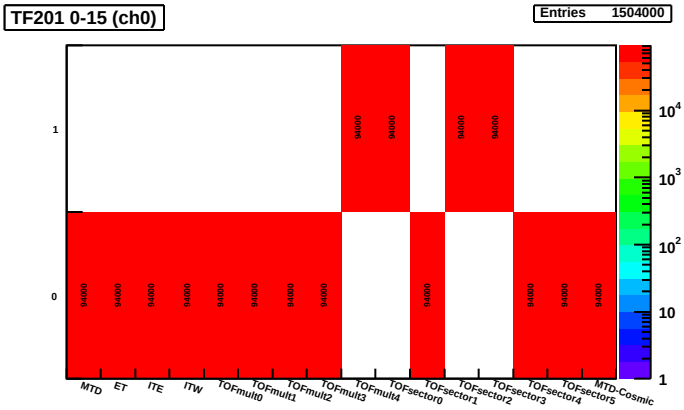

Input to FPD L2 DSM
Entries 0
jet patch sum (no FMS-LED)
120
100
80
60
40
20
0
ST
SB
NT
NB

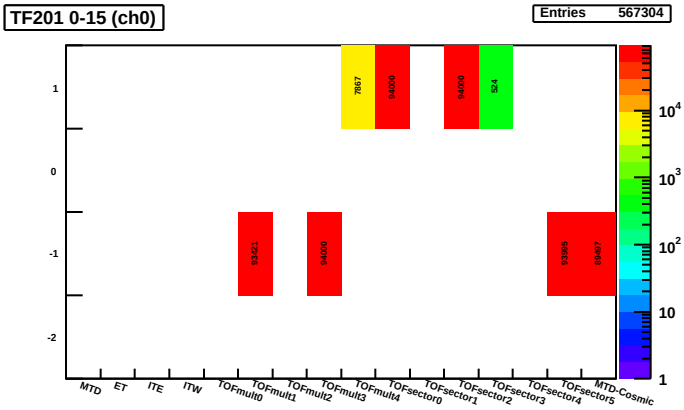

Input to FE001 QT board
Entries3008000
ADC
channel

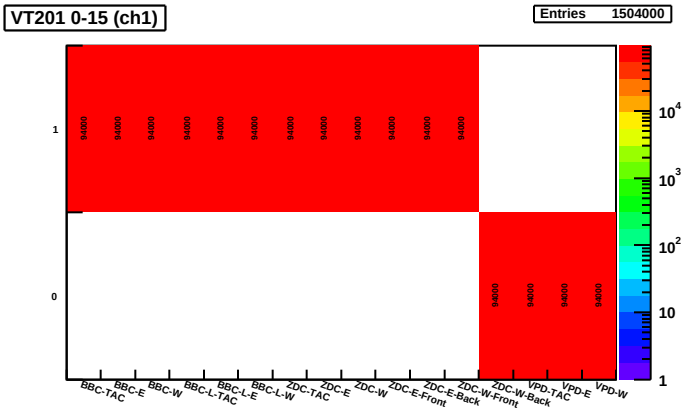

Input to FE002 QT board
Entries3008000
ADC
channel

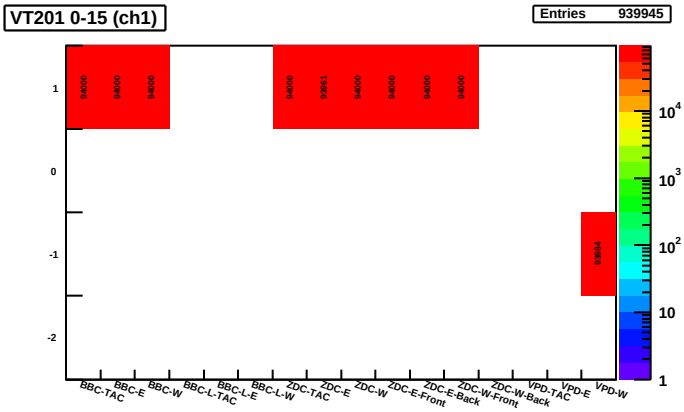

Input to FE003 QT board
Entries3008000
ADC
channel

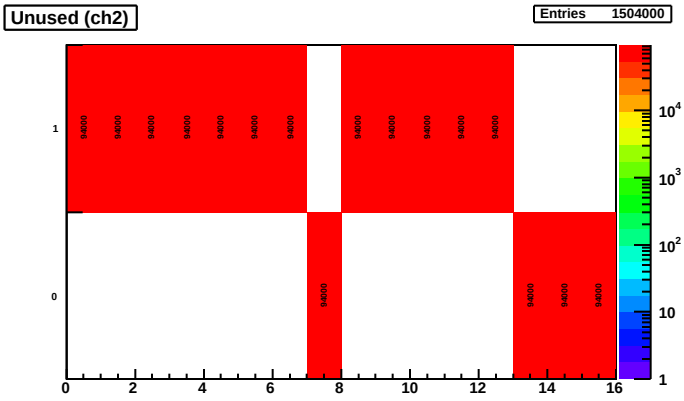

Input to FE004 QT board
Entries3008000
ADC
channel

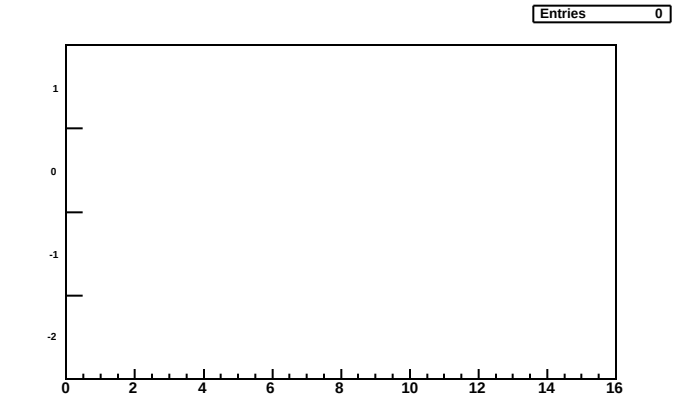

TF201 0-15 (ch0)
Entries 1504000
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

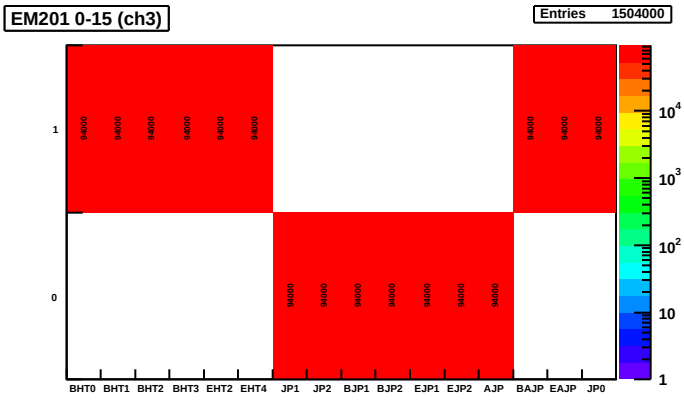

TF201 0-15 (ch0)
Entries 567304
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

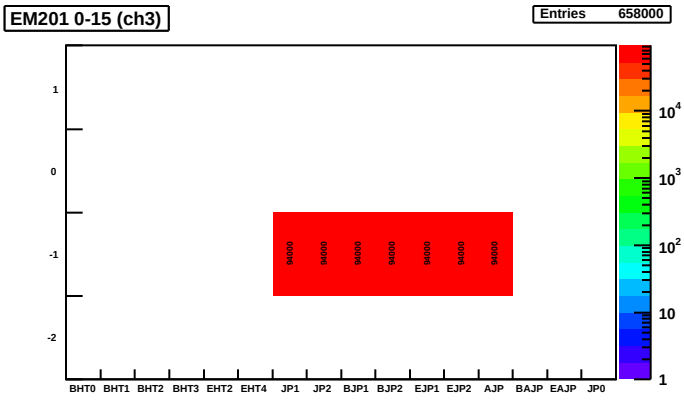

VT201 0-15 (ch1)
Entries 1504000
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 939945
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

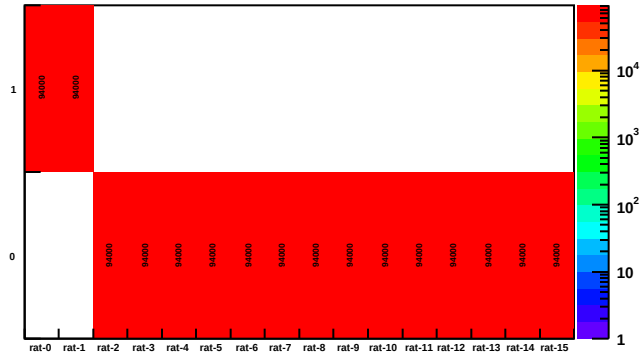

Unused (ch2)
Entries 1504000

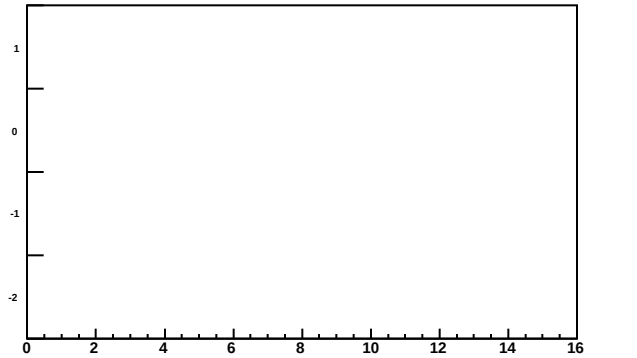

Entries 0


EM201 0-15 (ch3)
Entries 1504000
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

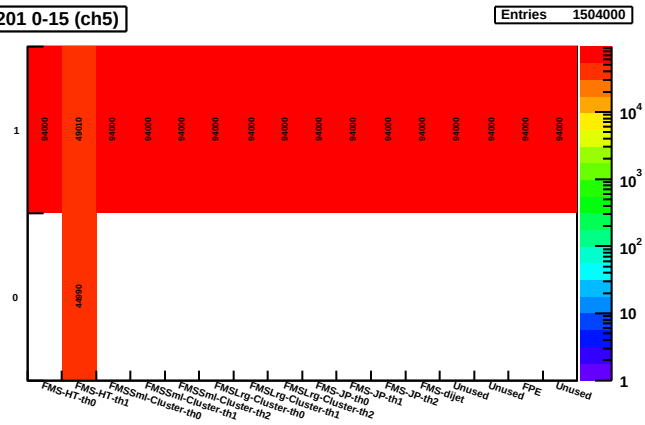

EM201 0-15 (ch3)
Entries 658000
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

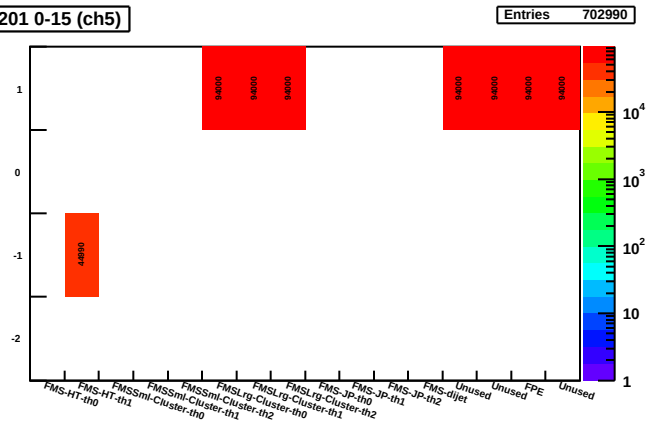




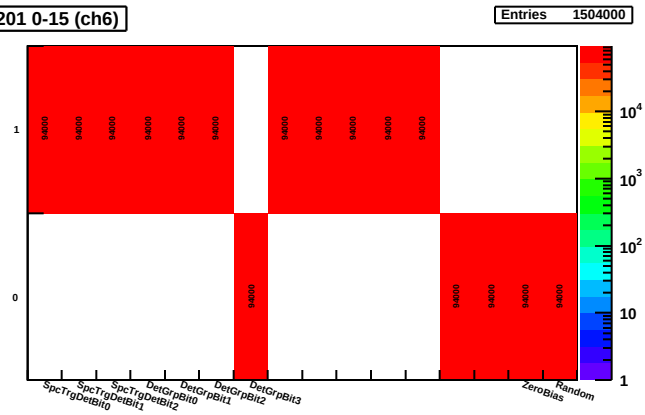
FP201 0-15 (ch5)

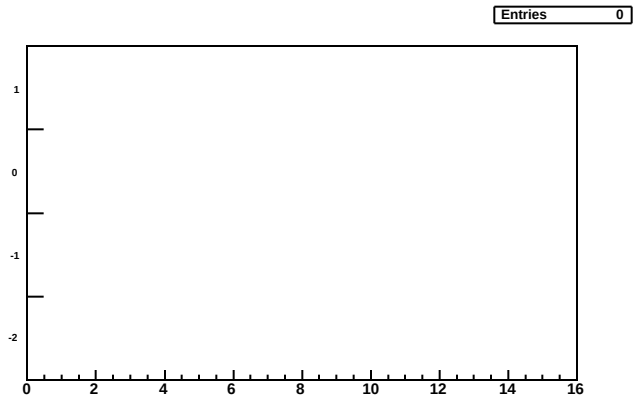
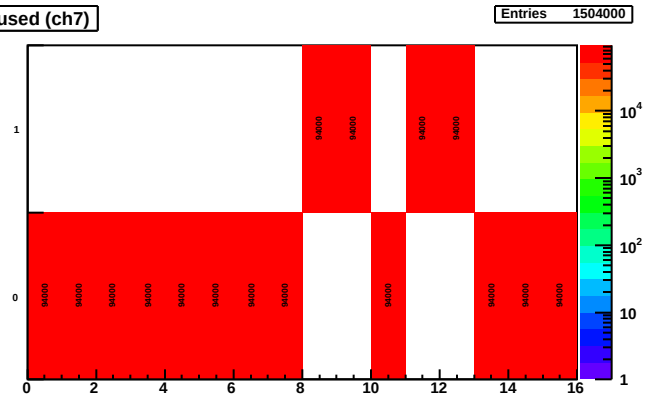
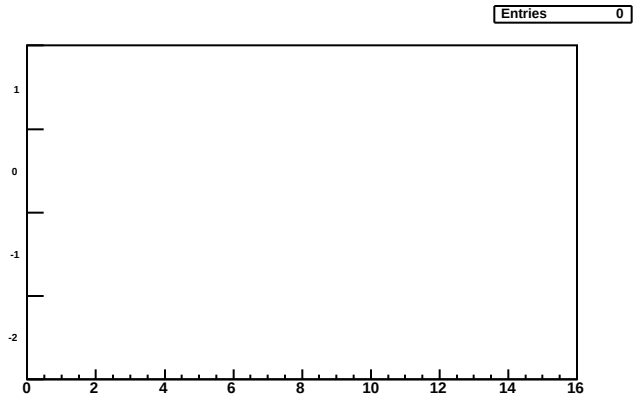


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)