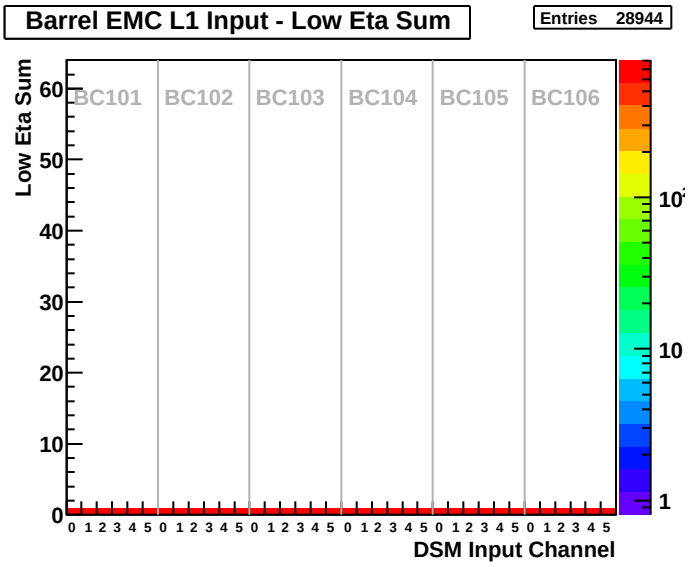
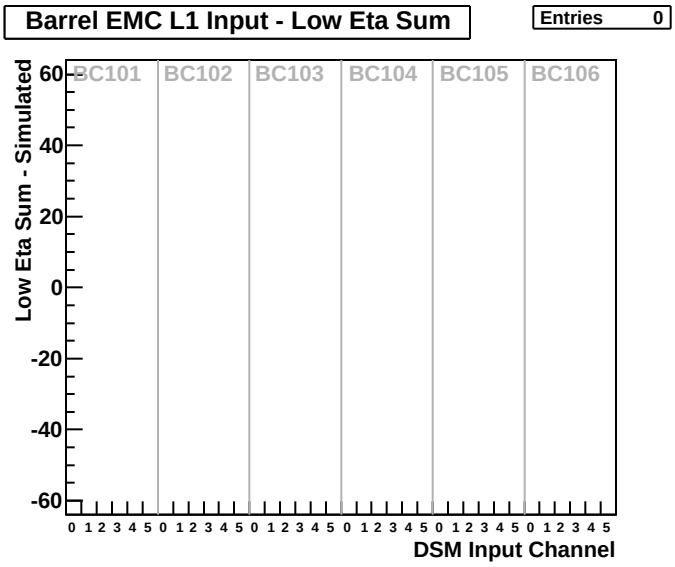
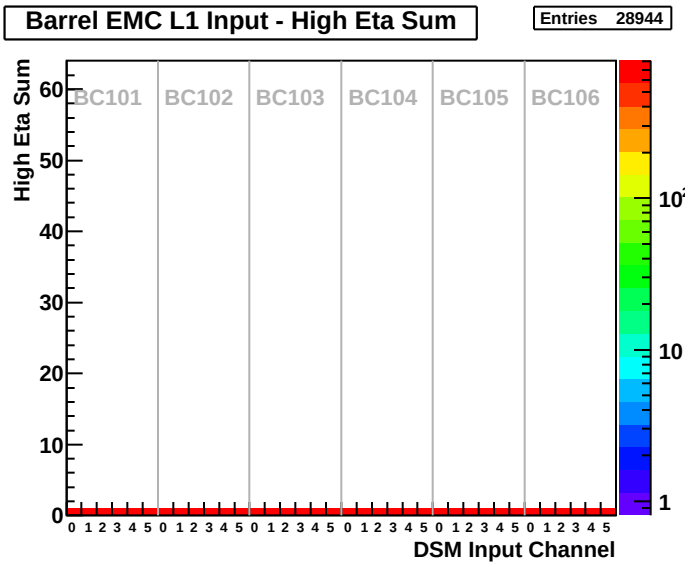
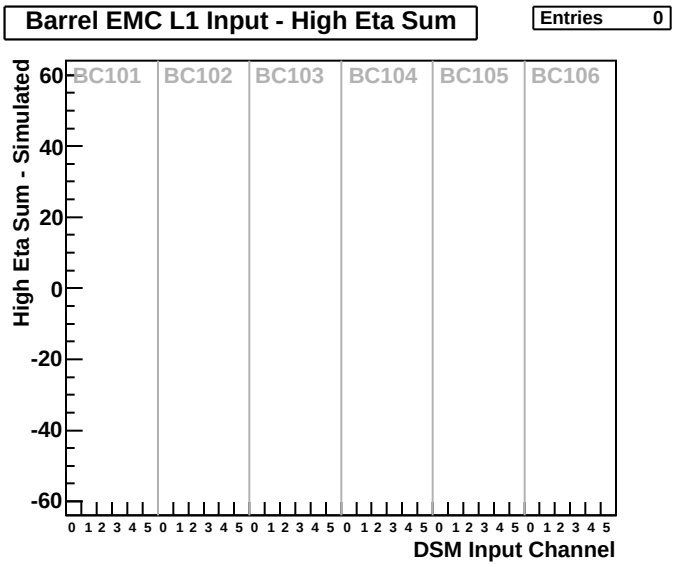
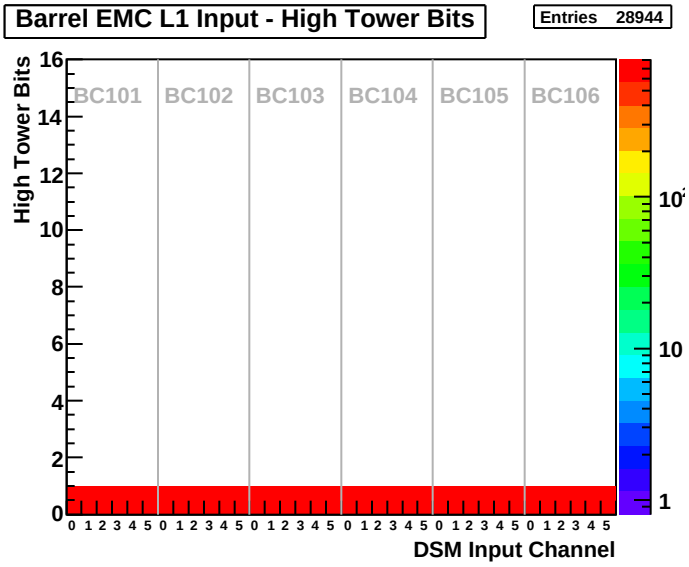
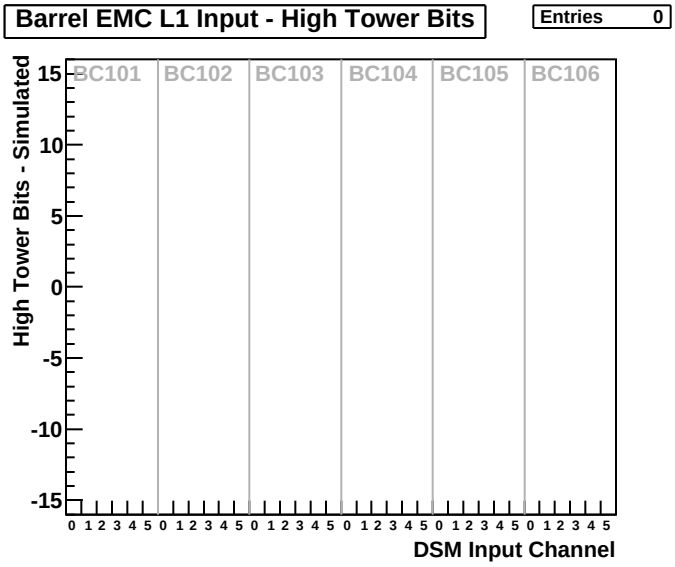

Barrel EMC L1 Input - Low Eta Sum
Entries 28944
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

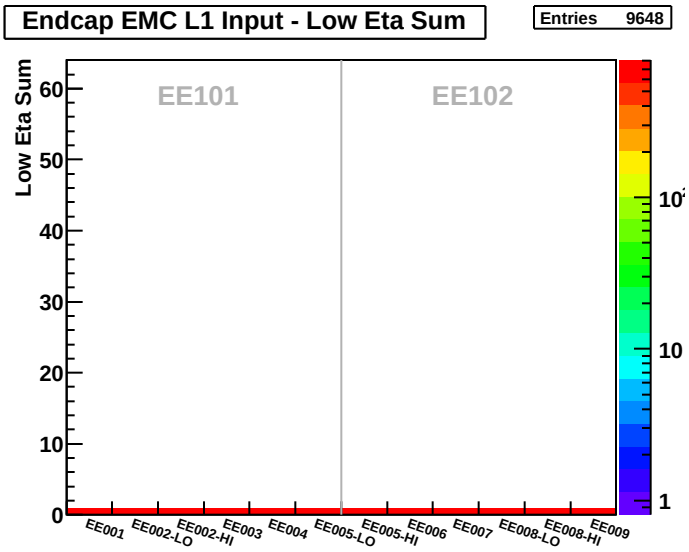

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

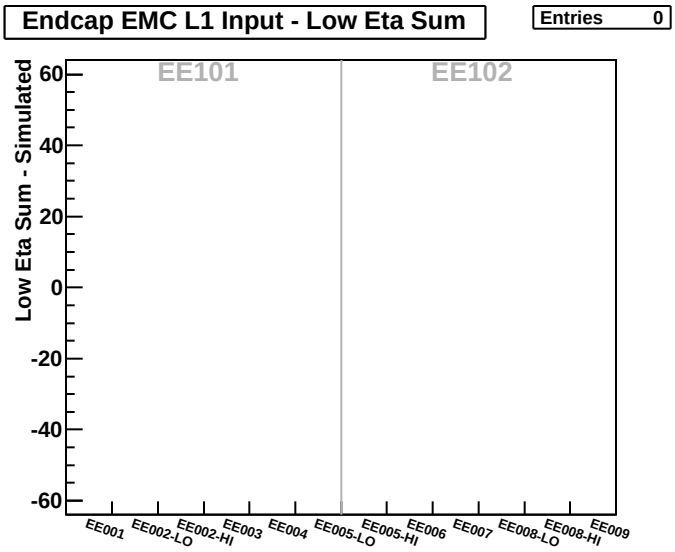

Barrel EMC L1 Input - High Eta Sum
Entries 28944
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

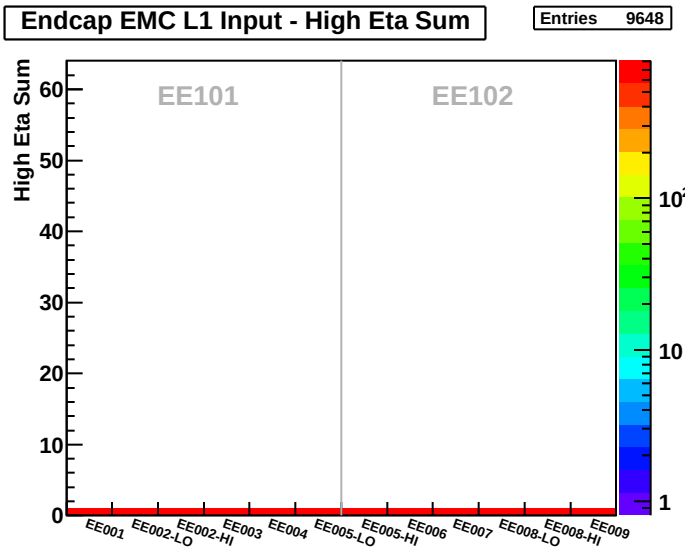

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

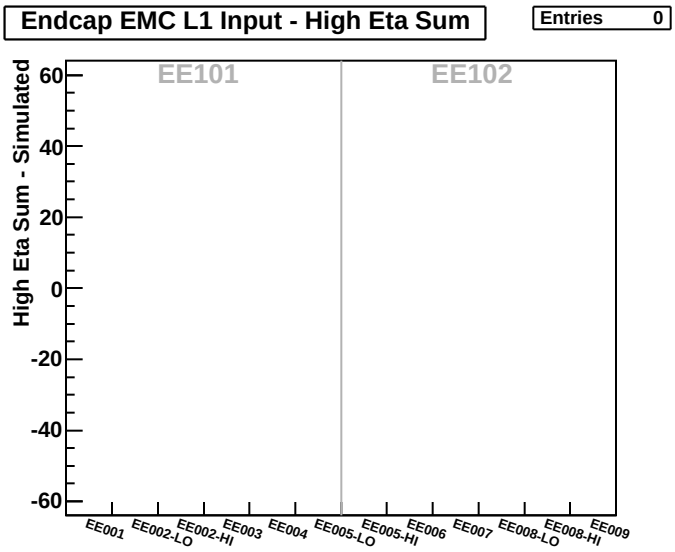

Barrel EMC L1 Input - High Tower Bits
Entries 28944
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

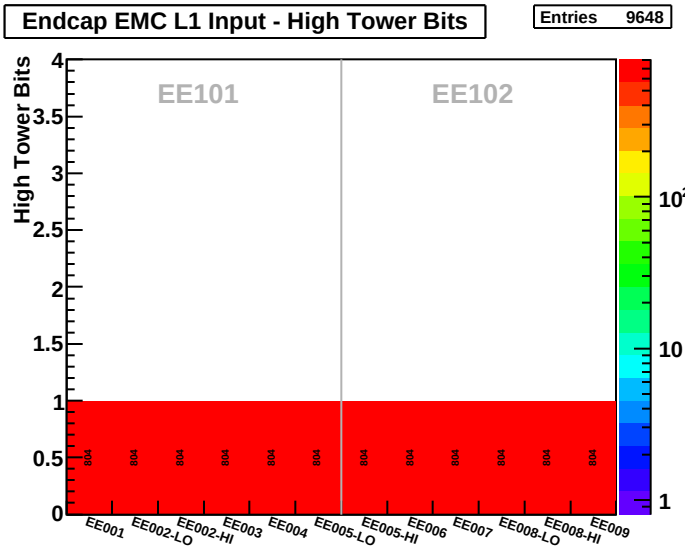

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

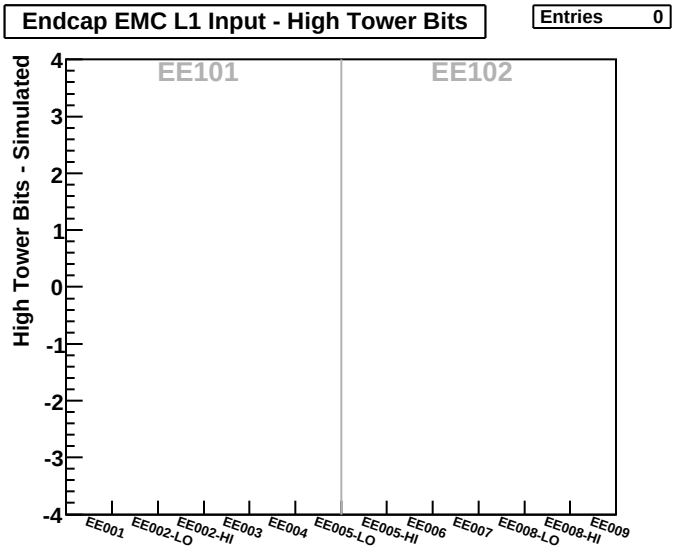

Endcap EMC L1 Input - Low Eta Sum
Entries 9648
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

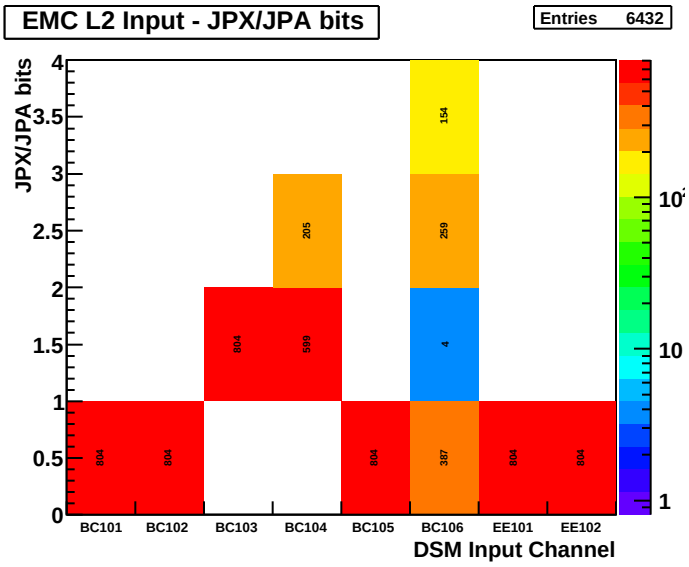

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

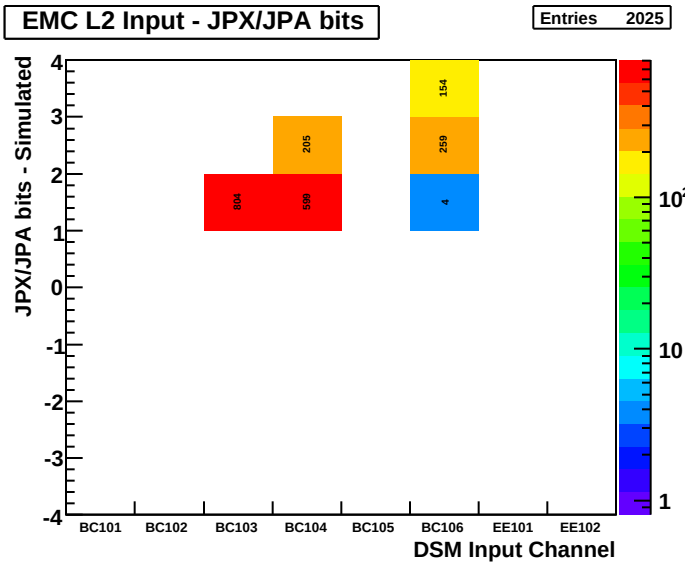

Endcap EMC L1 Input - High Eta Sum
Entries 9648
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

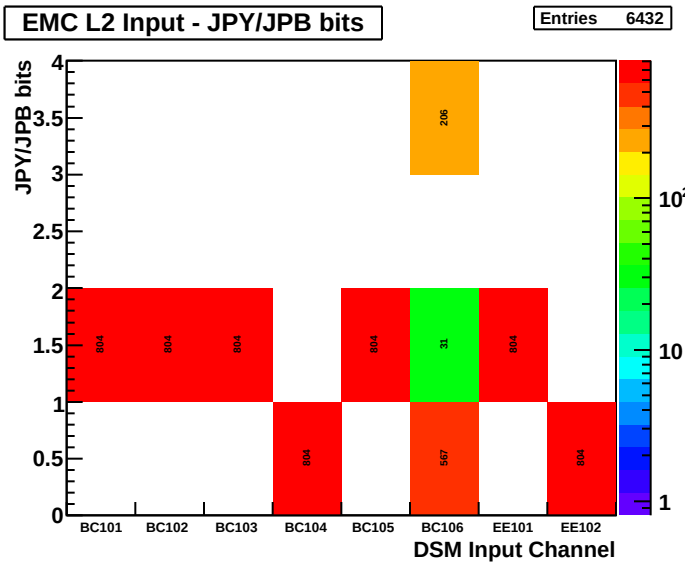

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

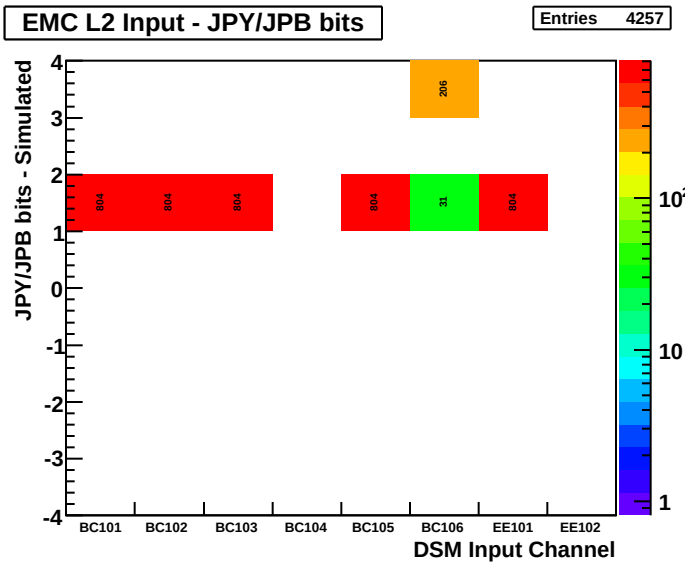

Endcap EMC L1 Input - High Tower Bits
Entries 9648
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

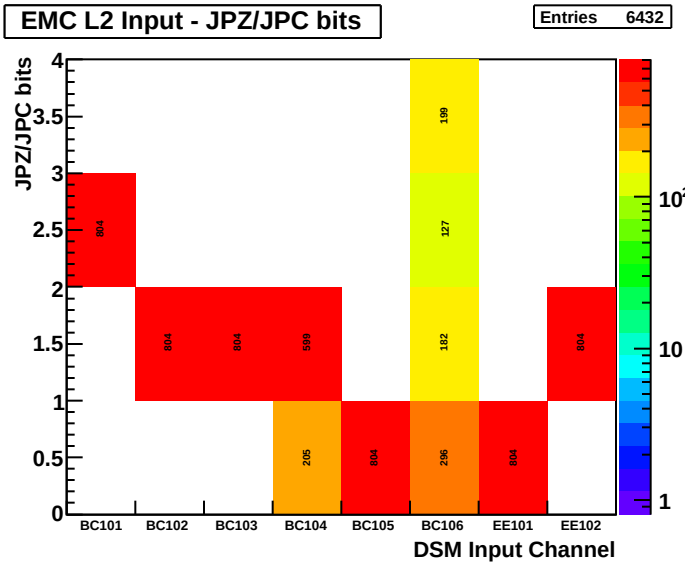

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

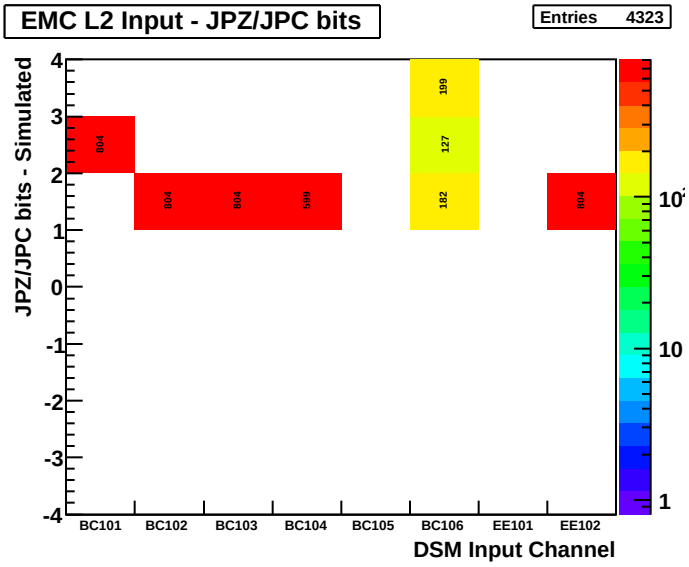

EMC L2 Input - JPX/JPA bits
Entries 6432
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
804 804 804 599 205 804 387 4 259 154 804 804

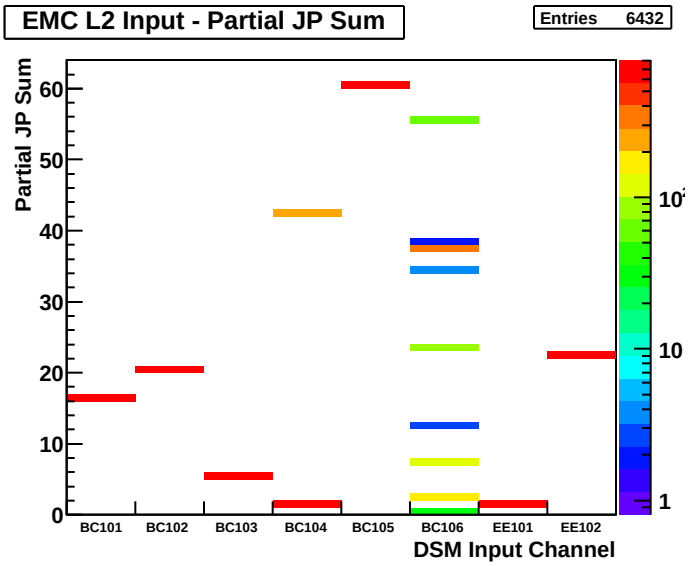

EMC L2 Input - JPX/JPA bits
Entries 2025
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
804 599 205 4 259 154

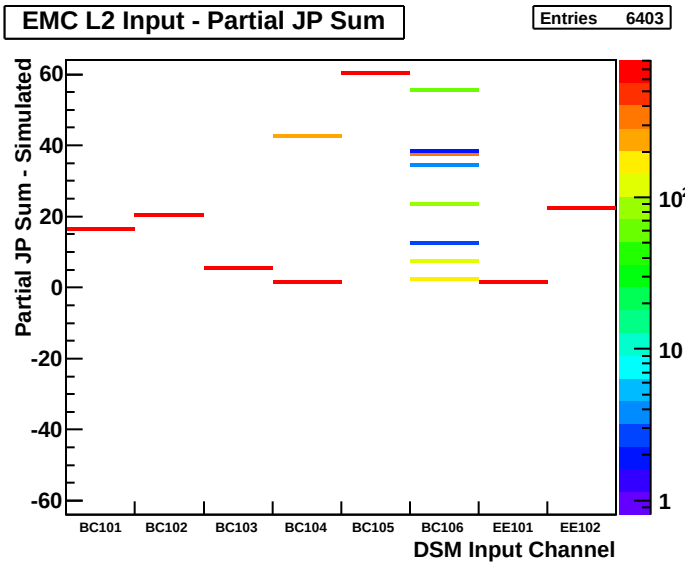

EMC L2 Input - JPY/JPB bits
Entries 6432
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
804 804 804 804 804 31 804 567 206

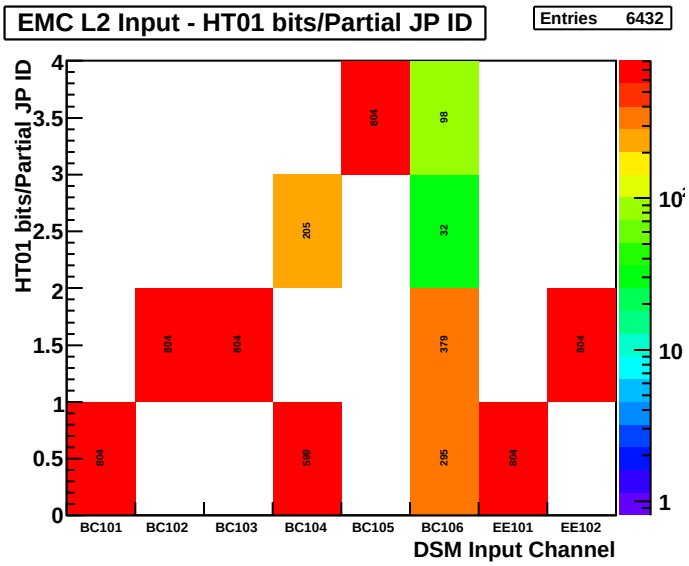

EMC L2 Input - JPY/JPB bits
Entries 4257
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
804 804 804 804 31 804 206

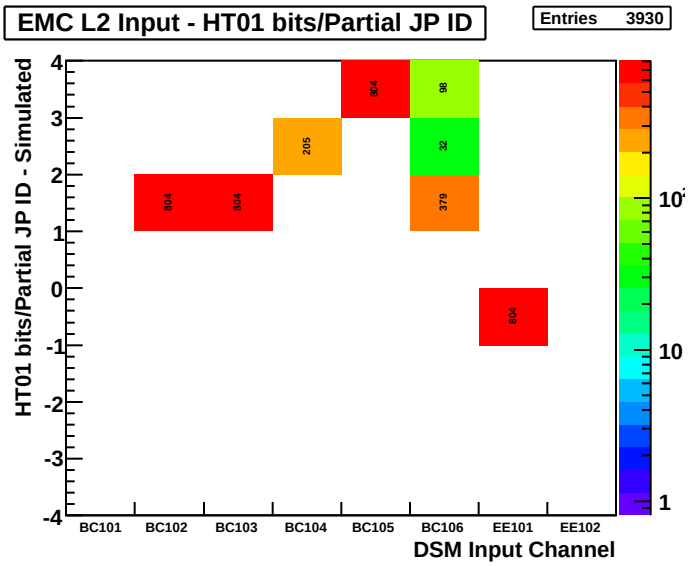

EMC L2 Input - JPZ/JPC bits
Entries 6432
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
804 804 804 599 205 804 296 804 182 127 199 804

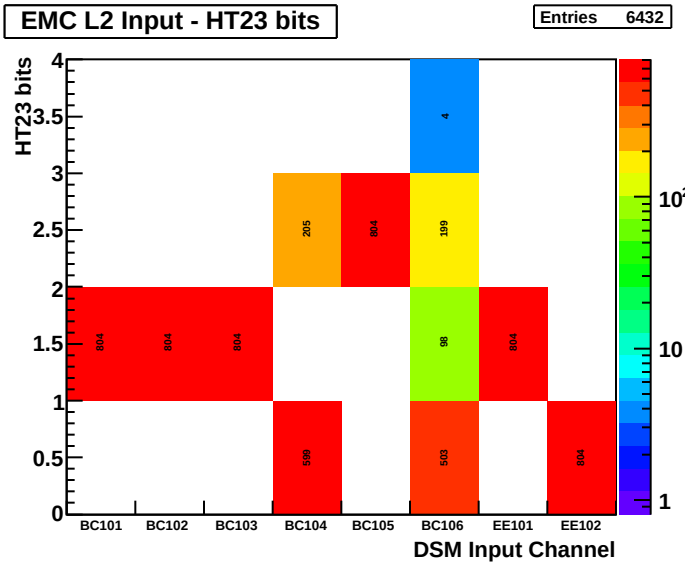

EMC L2 Input - JPZ/JPC bits
Entries 4323
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
804 804 804 599 182 127 199 804

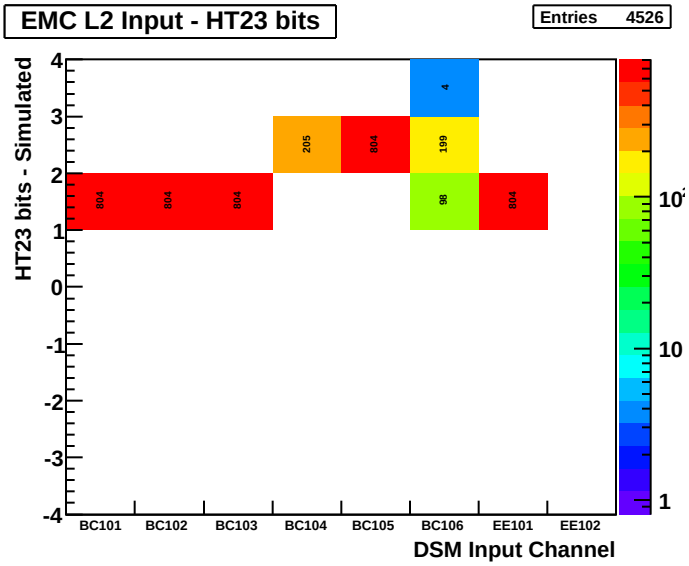

EMC L2 Input - Partial JP Sum
Entries 6432
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

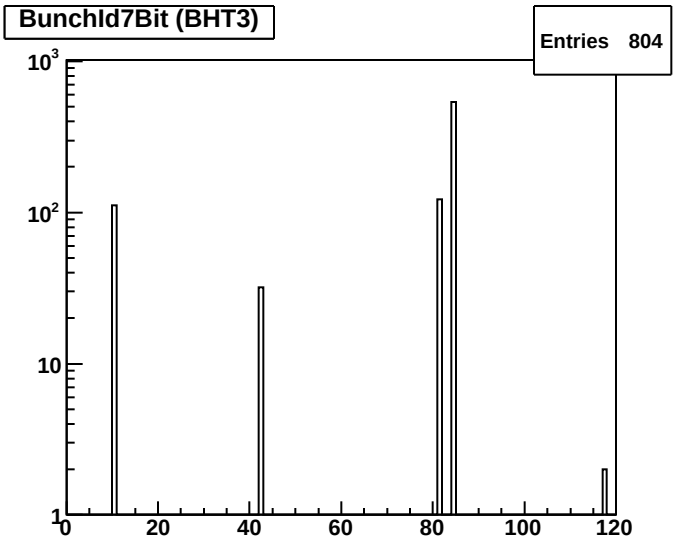

EMC L2 Input - Partial JP Sum
Entries 6403
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

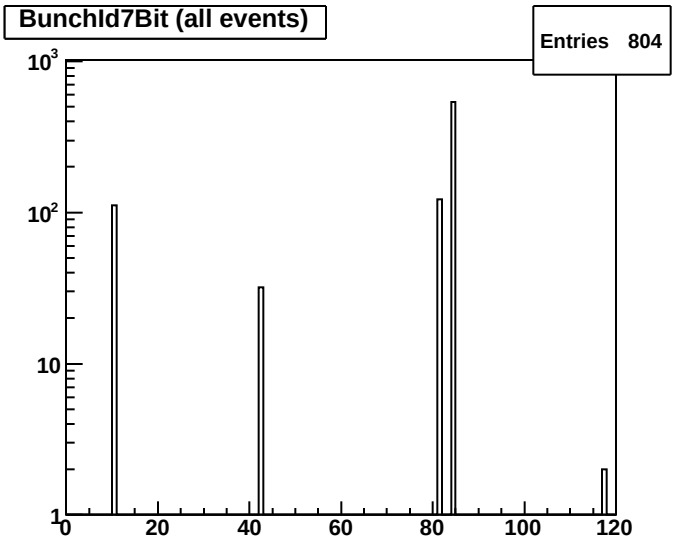

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6432
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

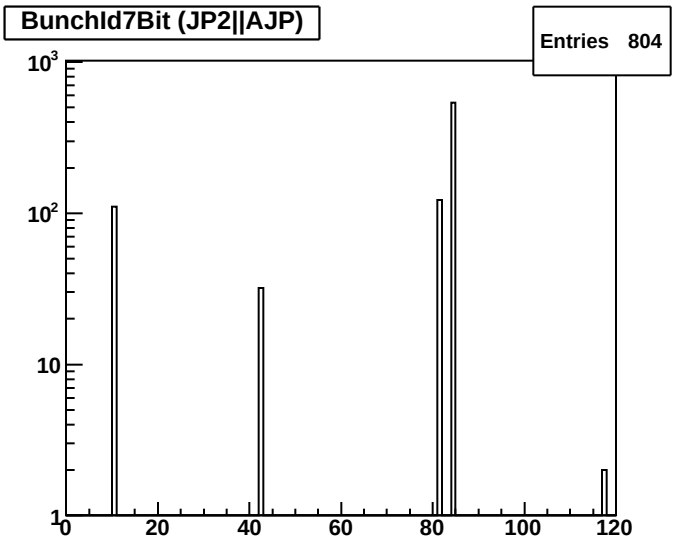

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3930
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

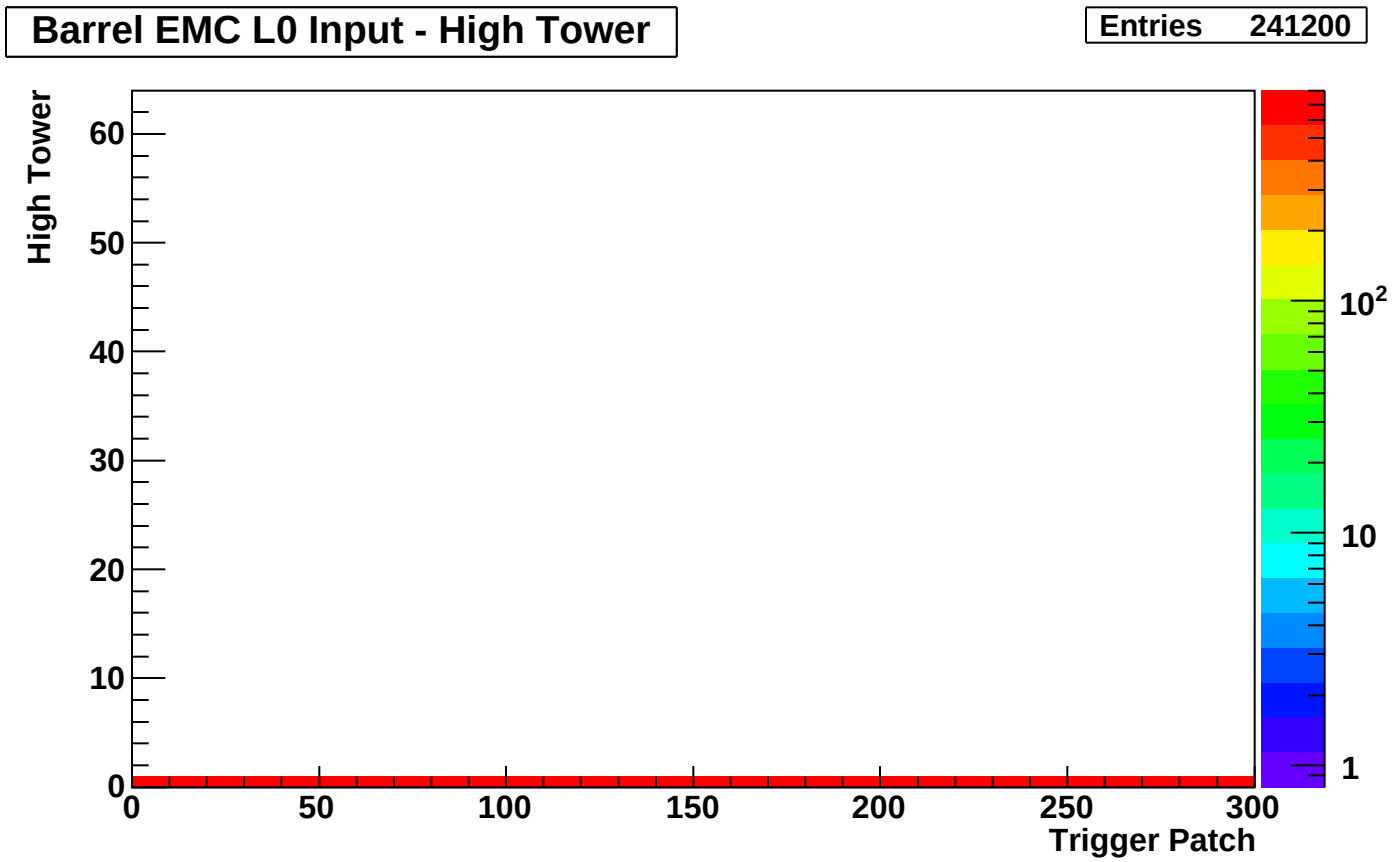

EMC L2 Input - HT23 bits
Entries 6432
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

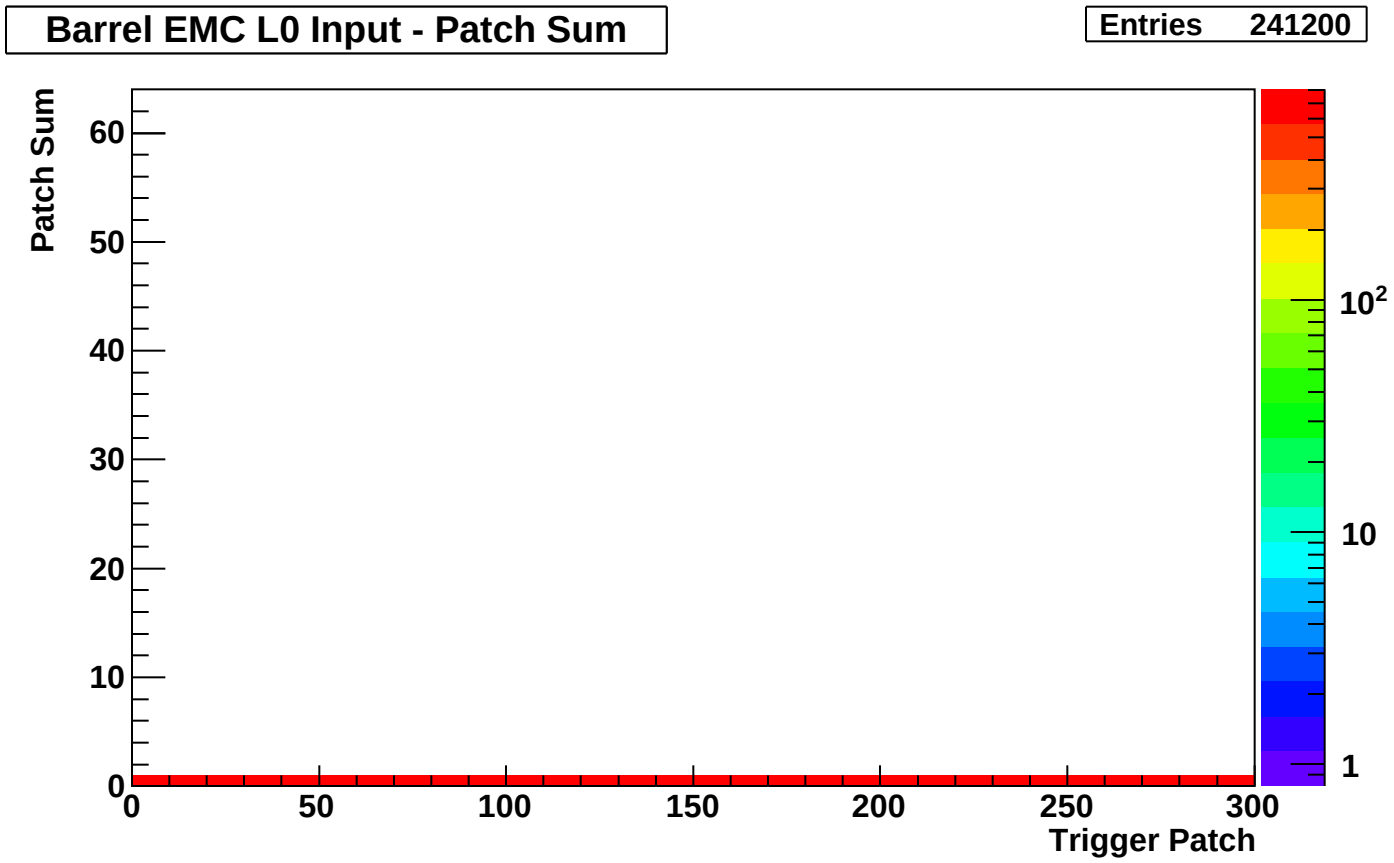

EMC L2 Input - HT23 bits
Entries 4526
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

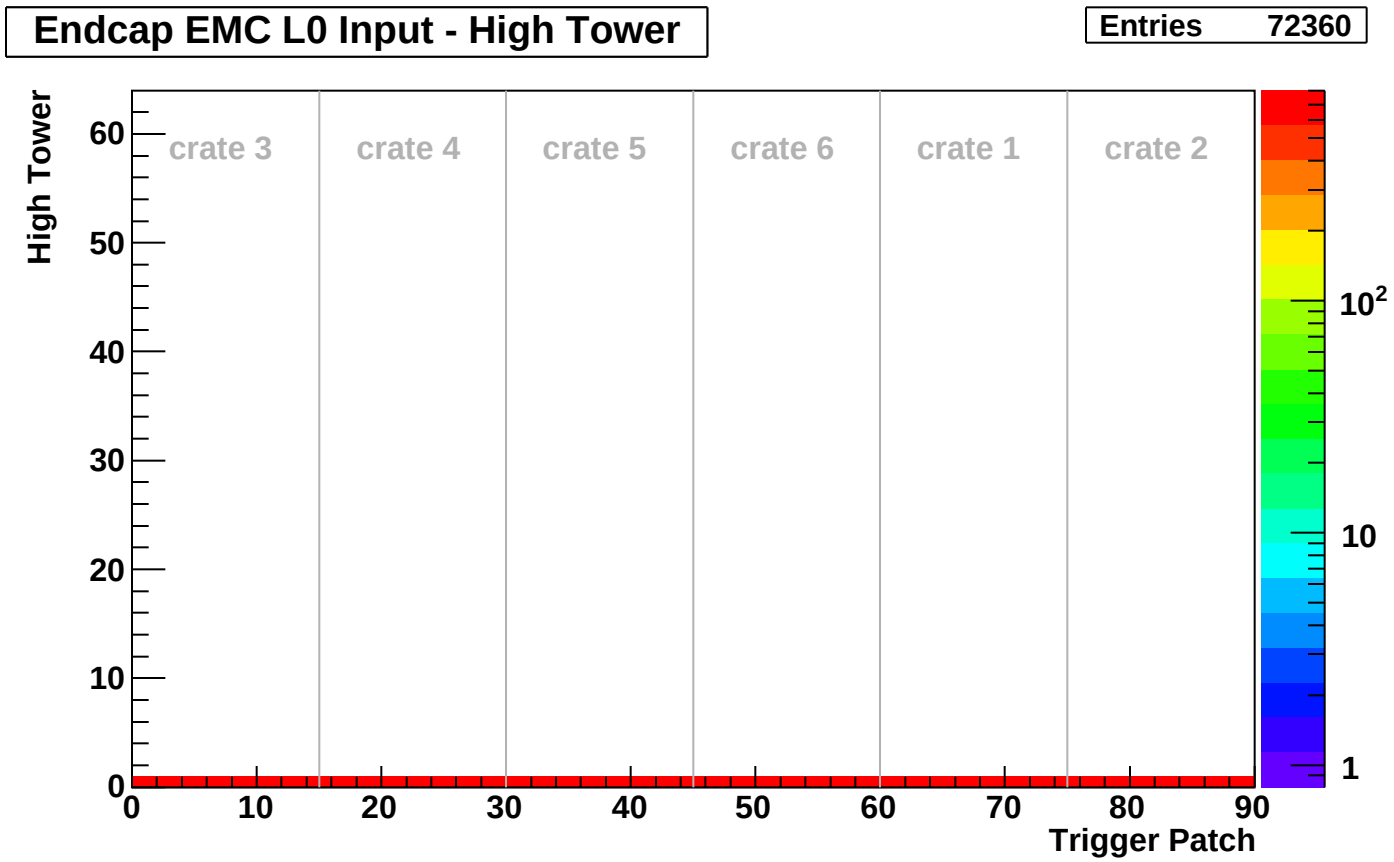

BunchId7Bit (BHT3)
Entries 804
10^3
10^2
10
1
0
20
40
60
80
100
120

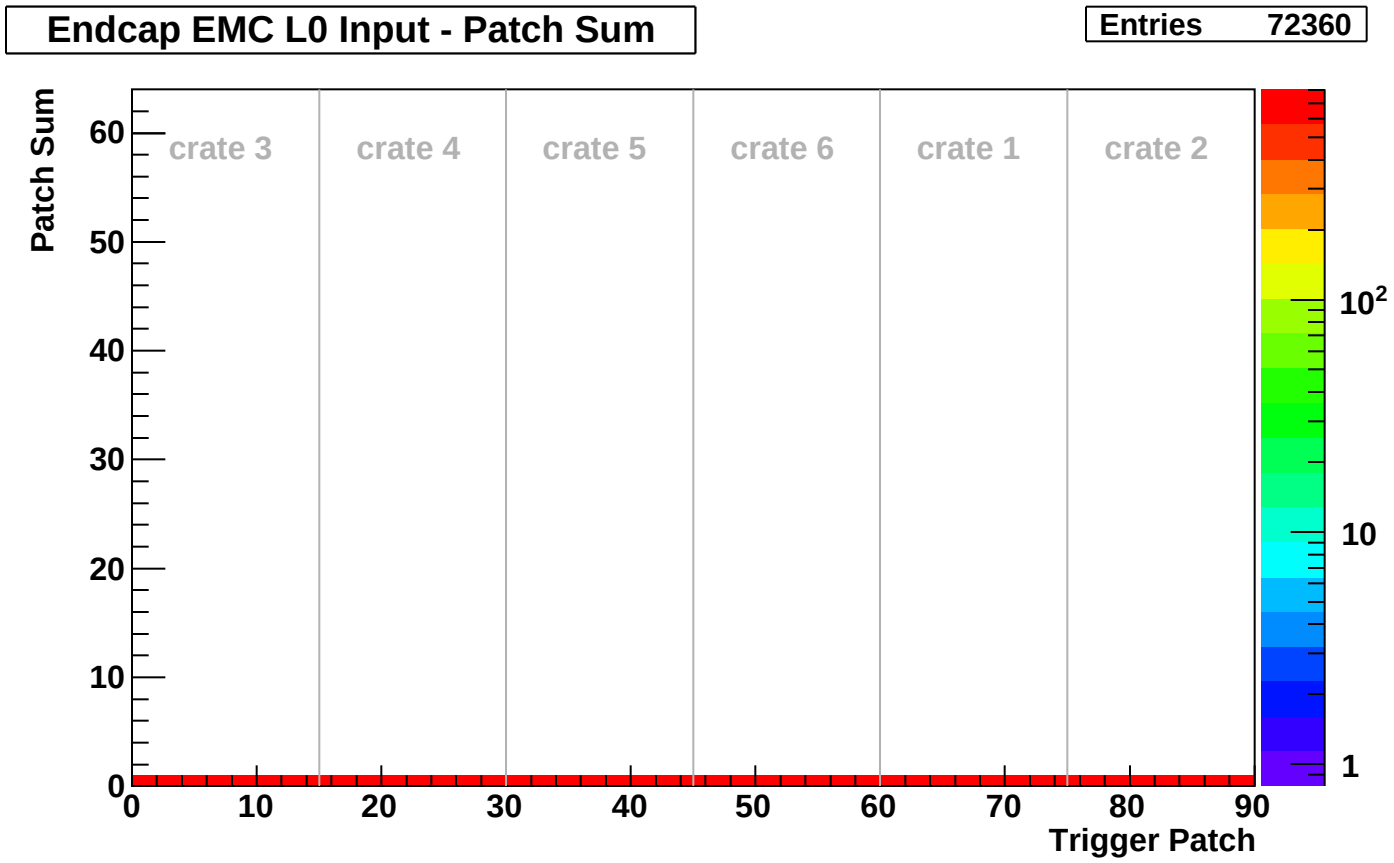

BunchId7Bit (all events)
Entries 804
10^3
10^2
10
1
0
20
40
60
80
100
120

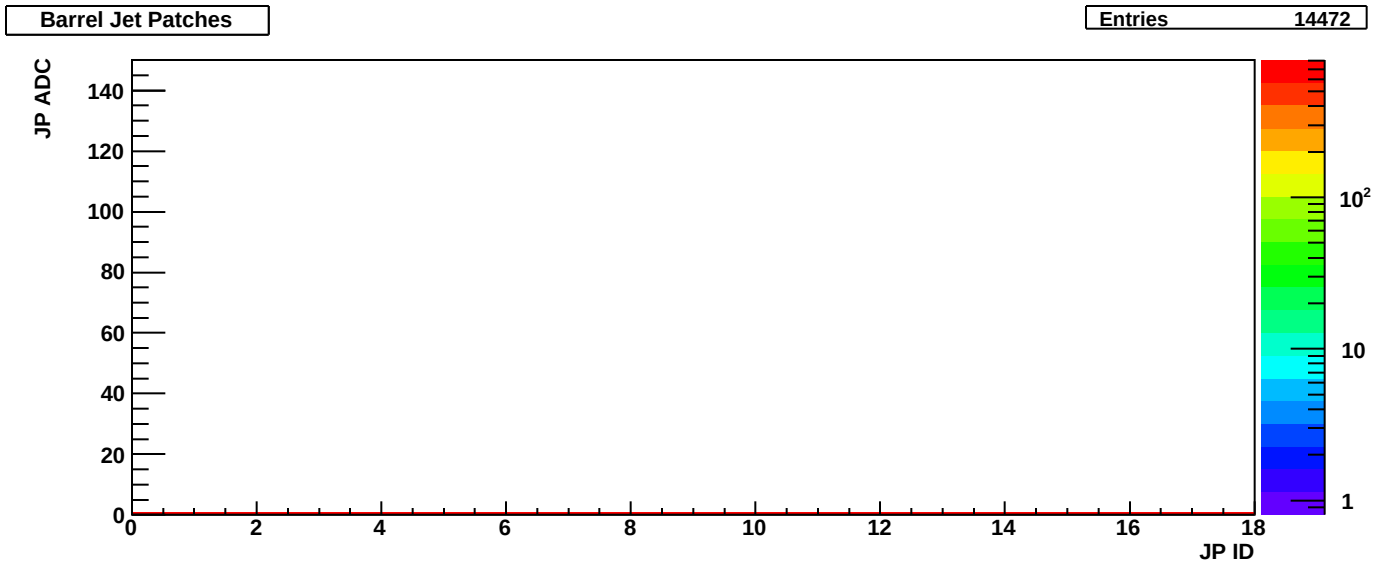

BunchId7Bit (JP2||AJP)
Entries 804
10^3
10^2
10
1
0
20
40
60
80
100
120

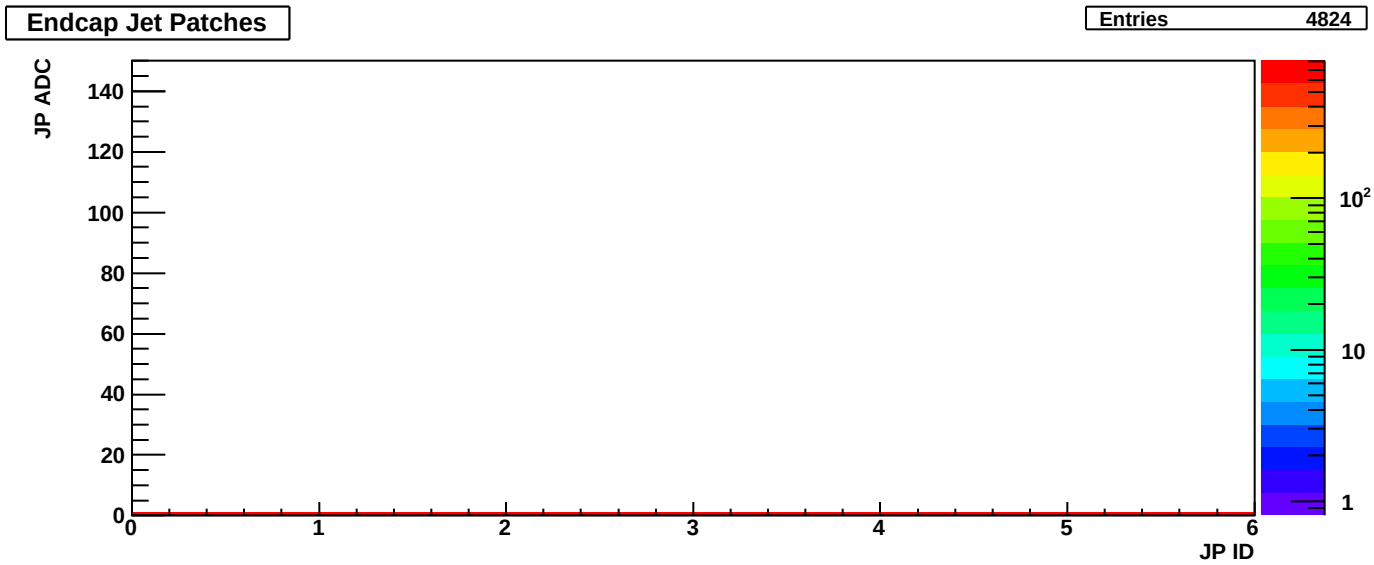

Barrel EMC L0 Input - High Tower
Entries 241200
High Tower
0
10
20
30
40
50
60
0
50
100
150
200
250
300
Trigger Patch
1
10
10^2

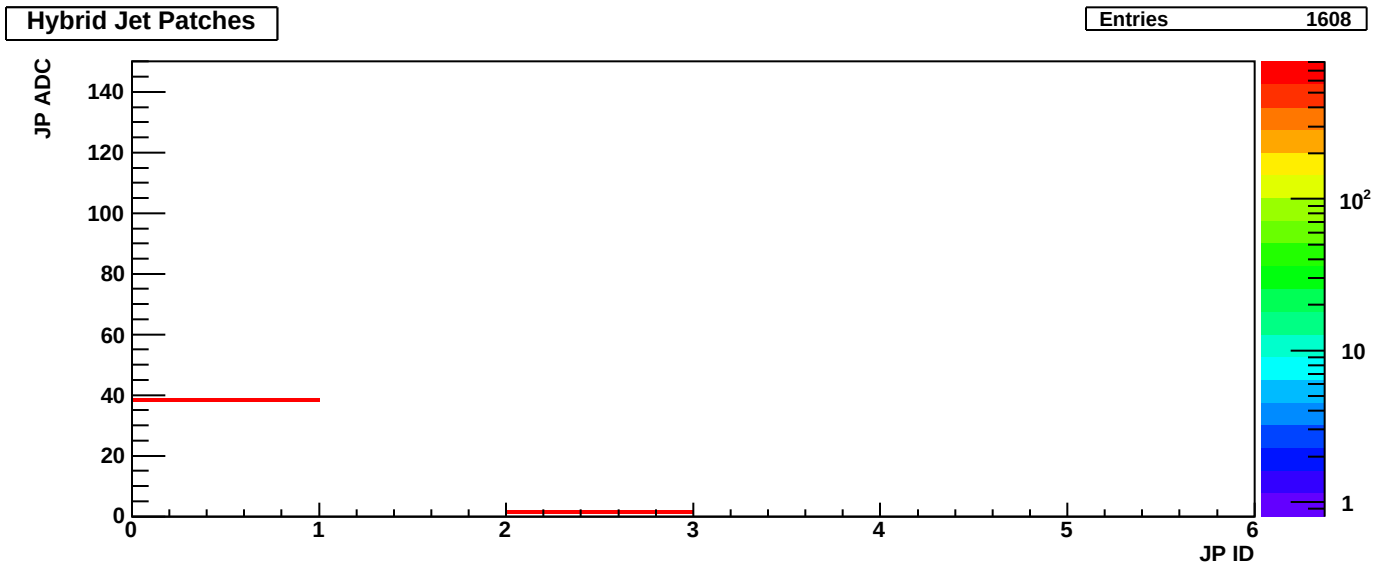

Barrel EMC L0 Input - Patch Sum
Entries 241200
Patch Sum
0
10
20
30
40
50
60
0
50
100
150
200
250
300
Trigger Patch
1
10
10^2

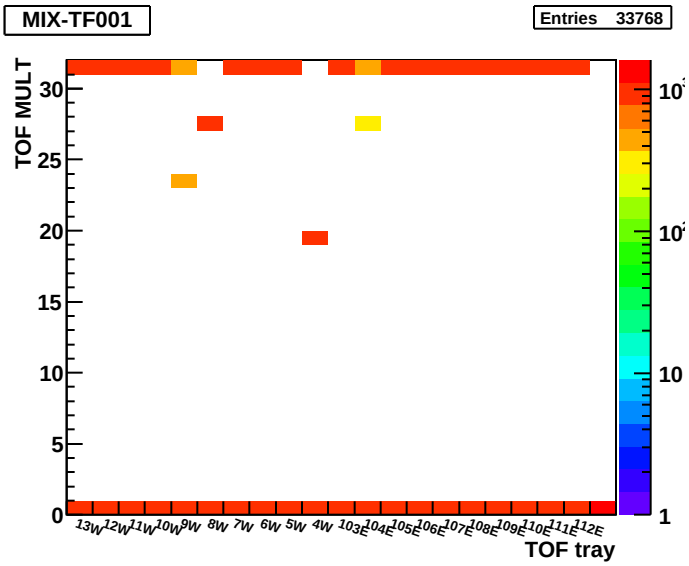

Endcap EMC L0 Input - High Tower
Entries 72360
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

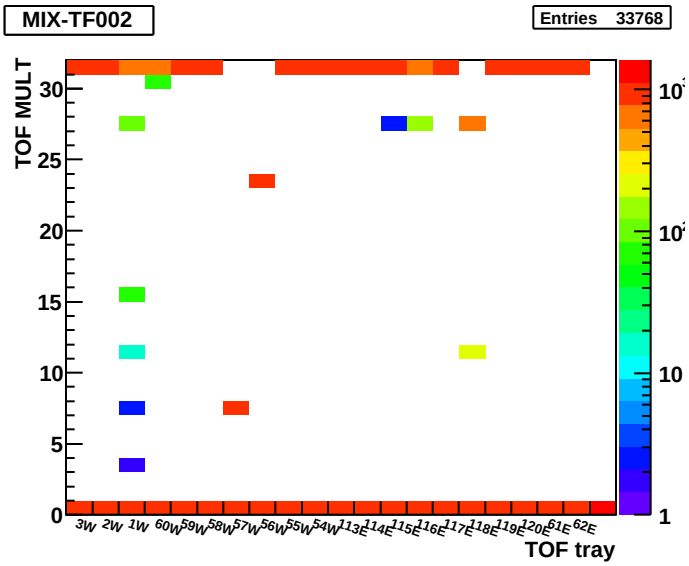

Endcap EMC L0 Input - Patch Sum
Entries 72360
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

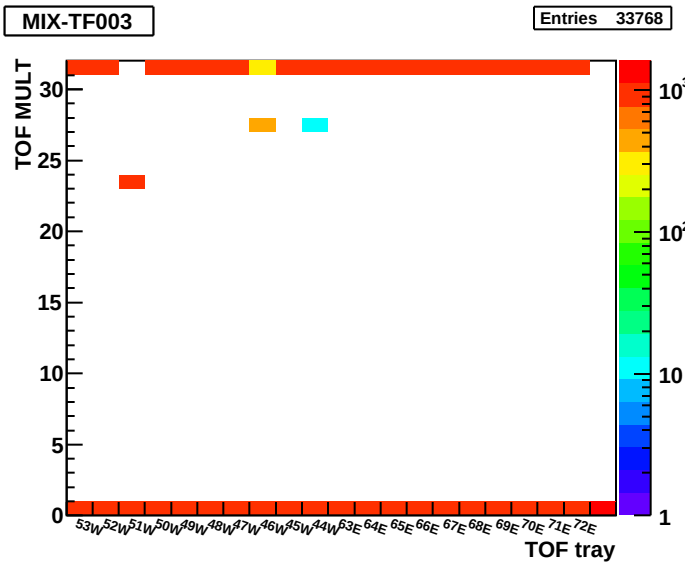

Barrel Jet Patches
Entries 14472
JP ADC
JP ID

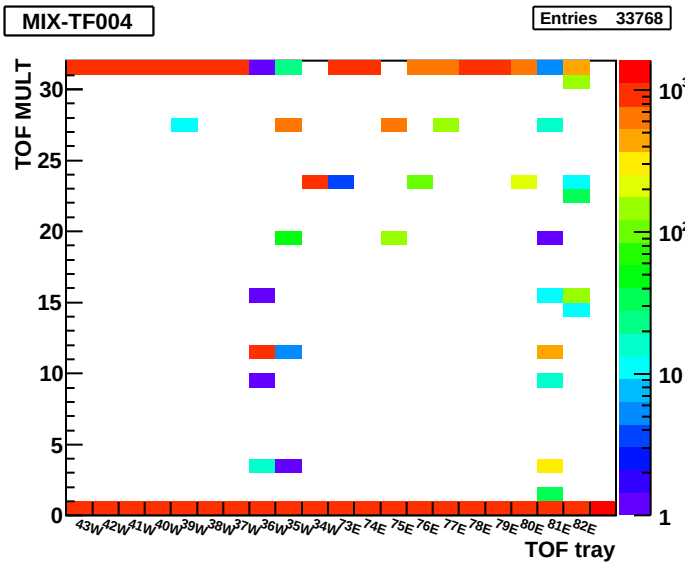

Endcap Jet Patches
Entries 4824
JP ADC
JP ID

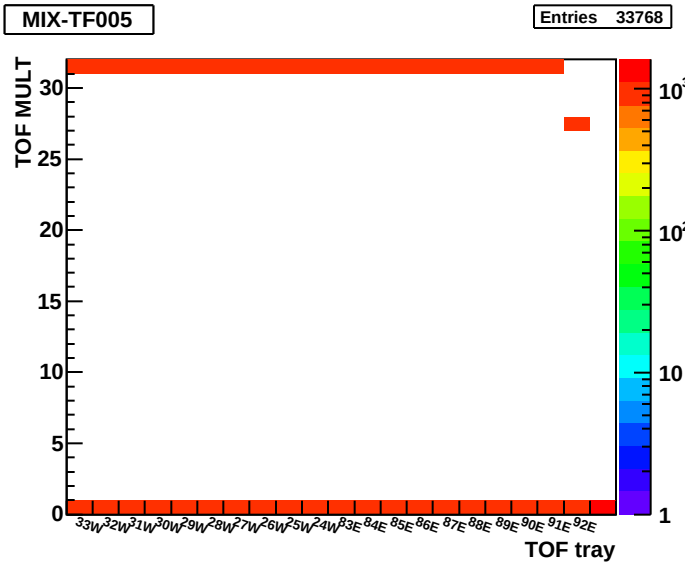

Hybrid Jet Patches
Entries 1608
JP ADC
JP ID

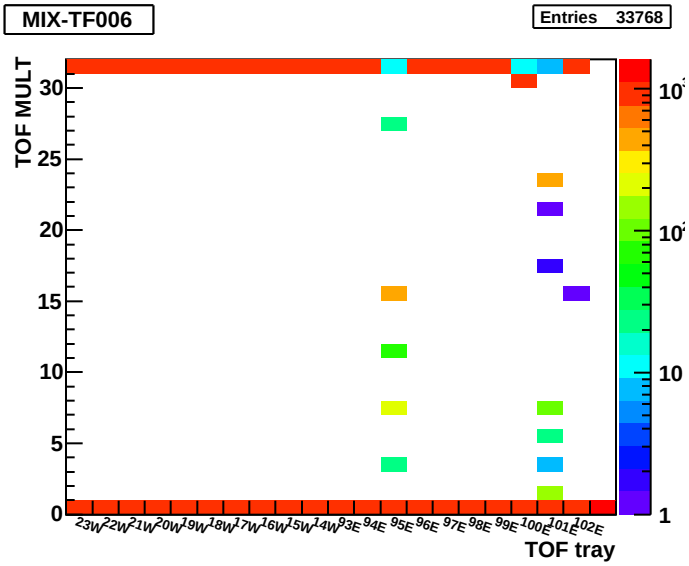

MIX-TF001
Entries 33768
TOF MULT
TOF tray

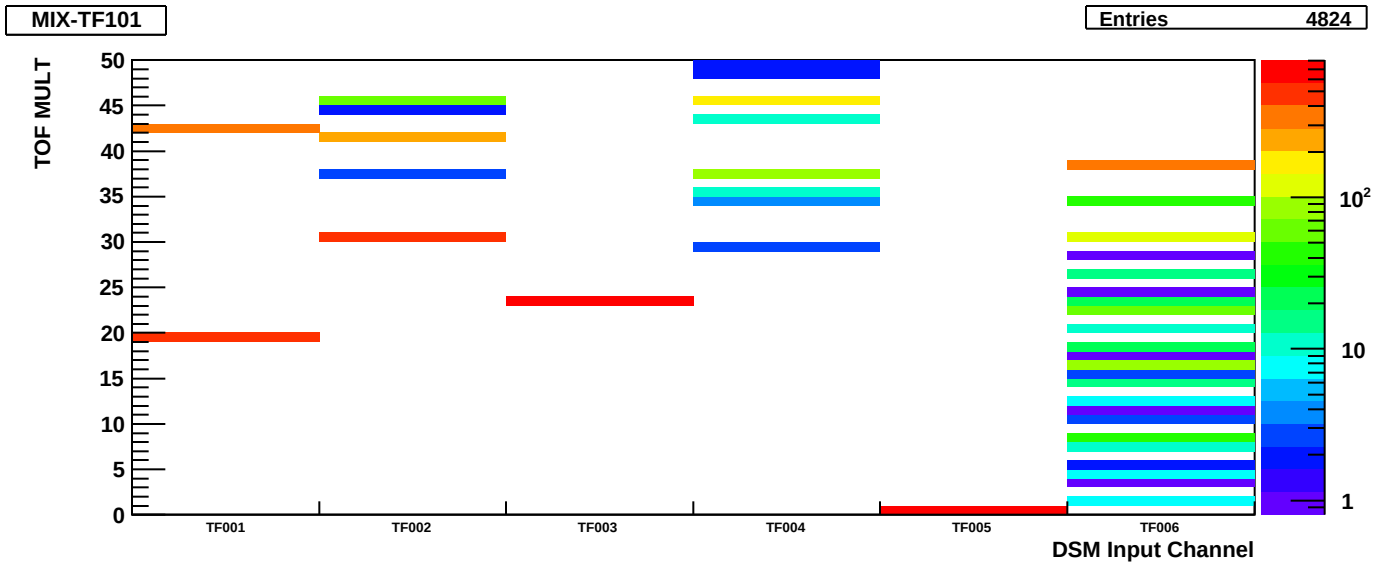

MIX-TF002
Entries 33768
TOF MULT
TOF tray

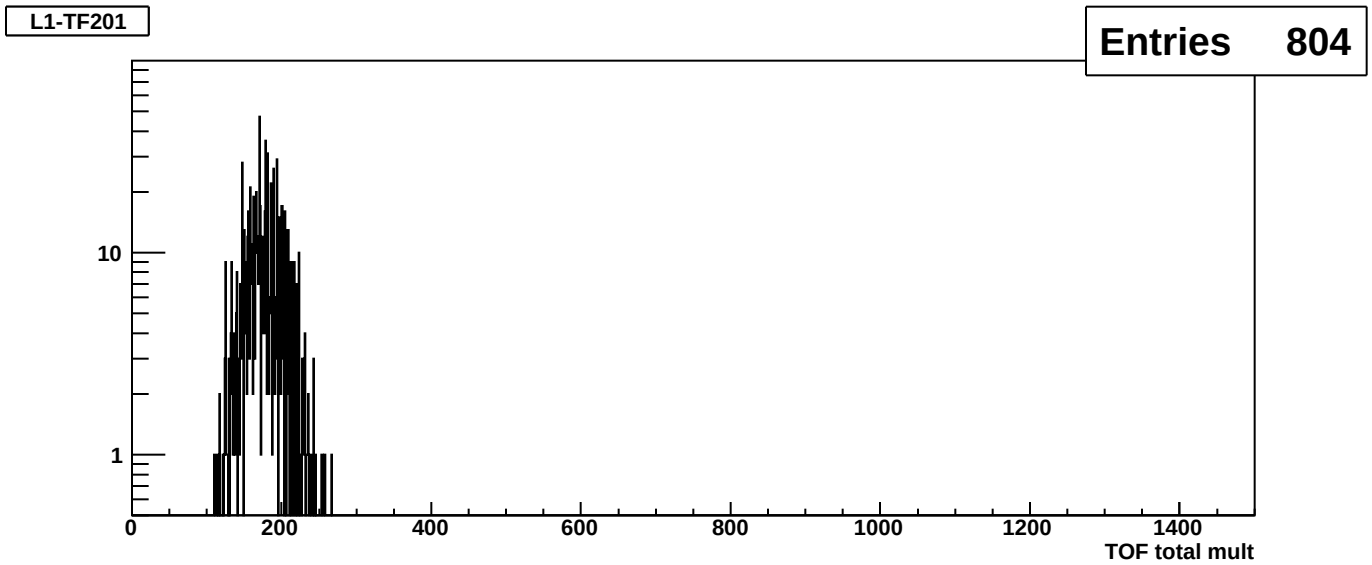

MIX-TF003
Entries 33768
TOF MULT
TOF tray

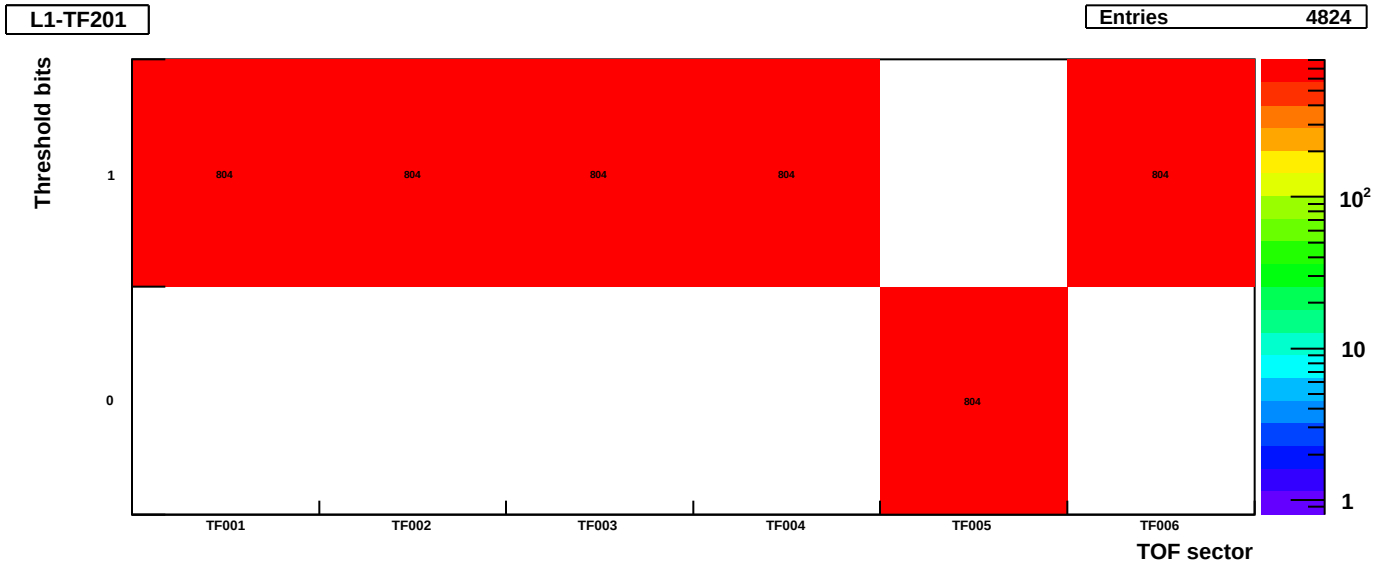

MIX-TF004
Entries 33768
TOF MULT
TOF tray

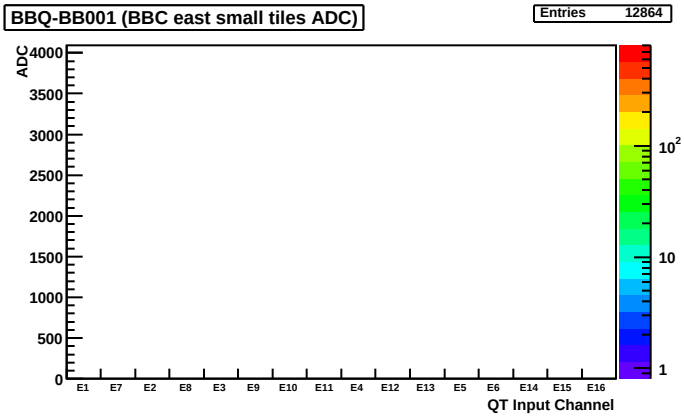

MIX-TF005
Entries 33768
TOF MULT
TOF tray

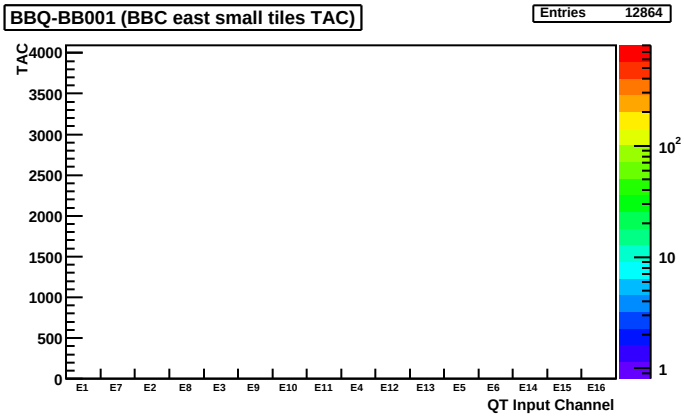

MIX-TF006
Entries 33768
TOF MULT
TOF tray

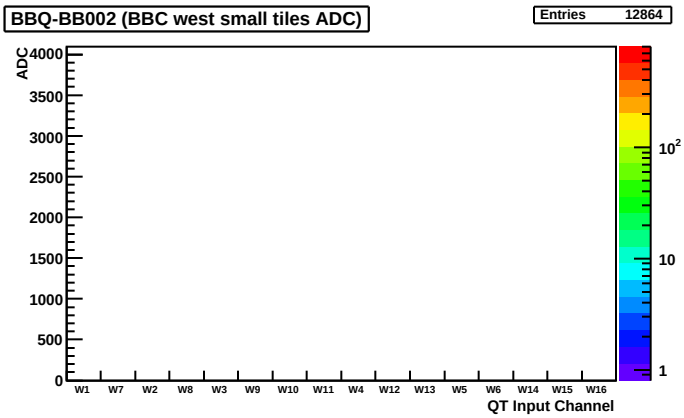

MIX-TF101
Entries 4824
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^2
10
1

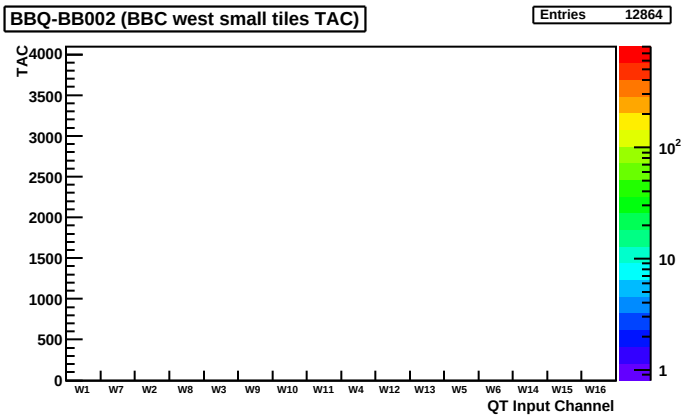

L1-TF201
Entries 804
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

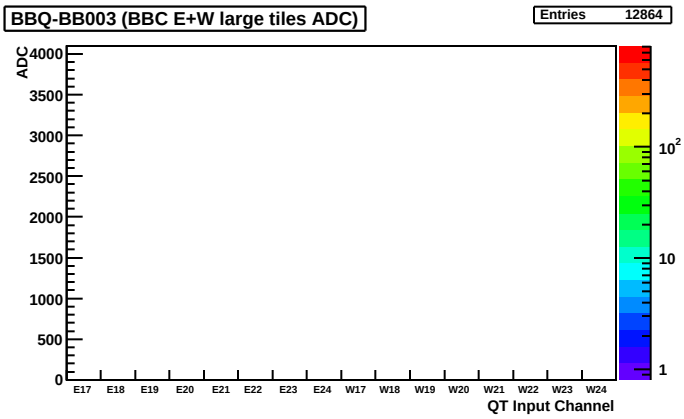

L1-TF201
Entries 4824
Threshold bits
1
0
804
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^2
10
1

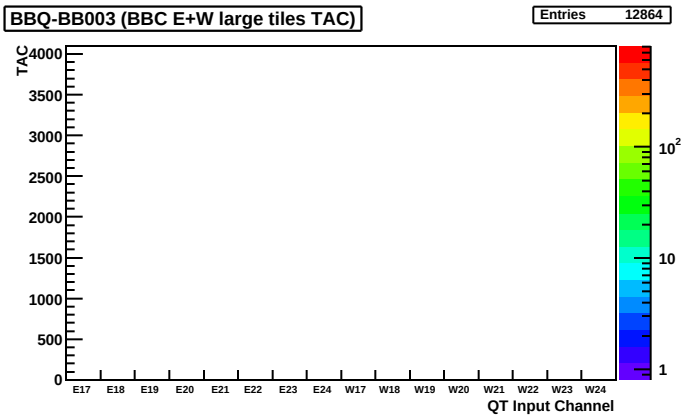

BBQ-BB001 (BBC east small tiles ADC)
Entries 12864
ADC
QT Input Channel

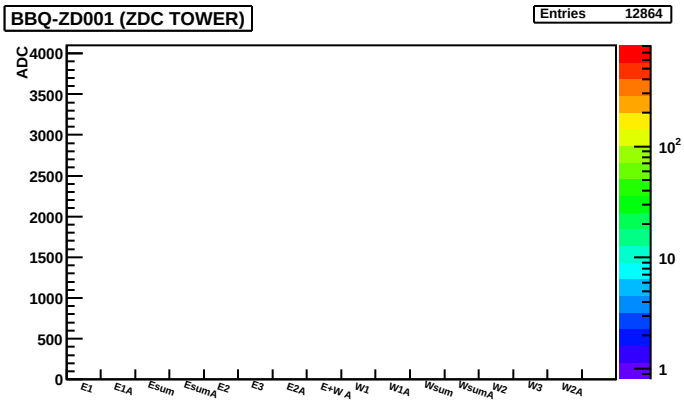

BBQ-BB001 (BBC east small tiles TAC)
Entries 12864
TAC
QT Input Channel

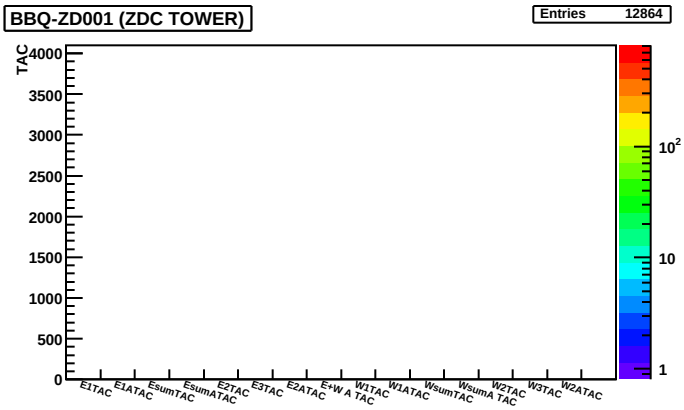

BBQ-BB002 (BBC west small tiles ADC)
Entries 12864
ADC
QT Input Channel

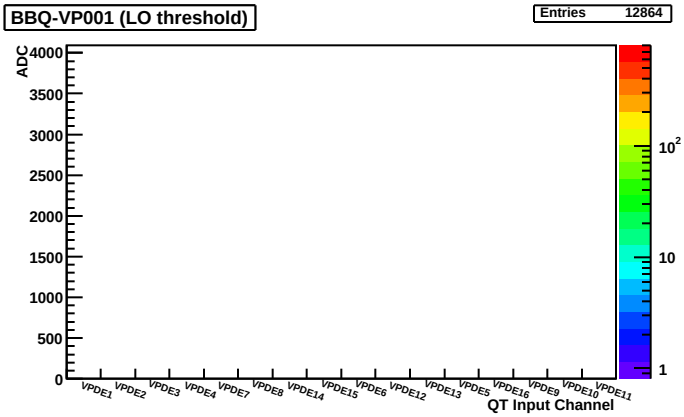

BBQ-BB002 (BBC west small tiles TAC)
Entries 12864
TAC
QT Input Channel

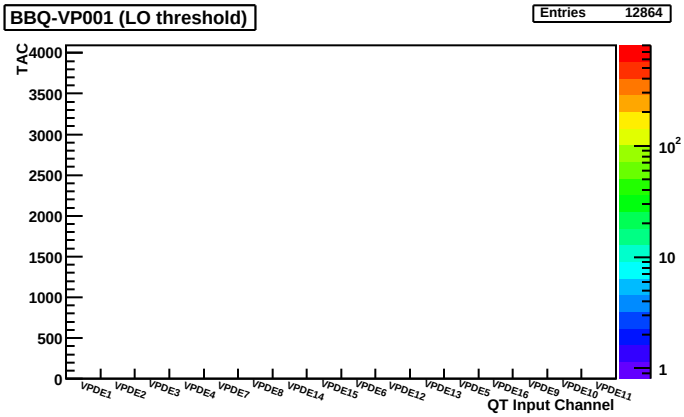

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 12864
ADC
QT Input Channel

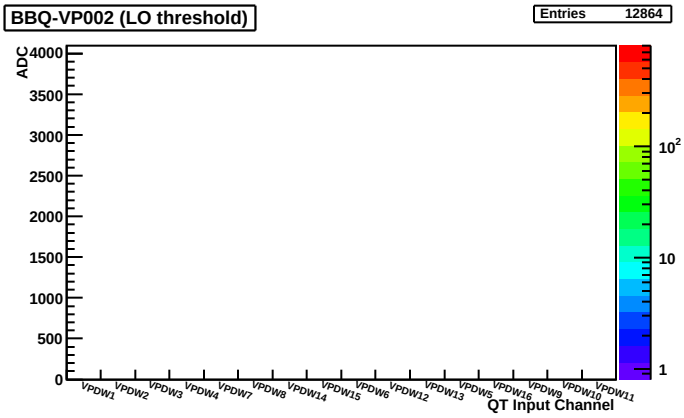

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 12864
TAC
QT Input Channel

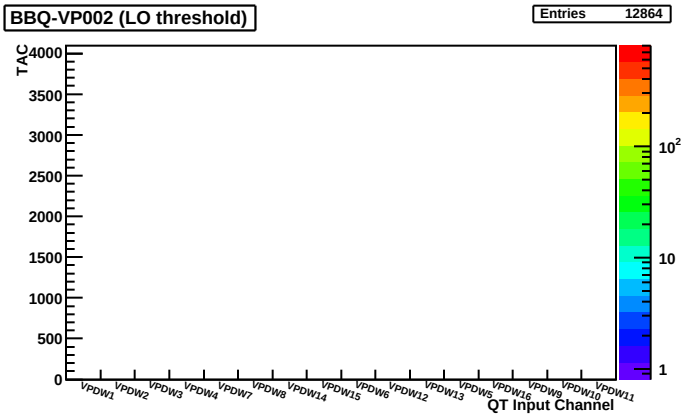

BBQ-ZD001 (ZDC TOWER)
Entries 12864
ADC

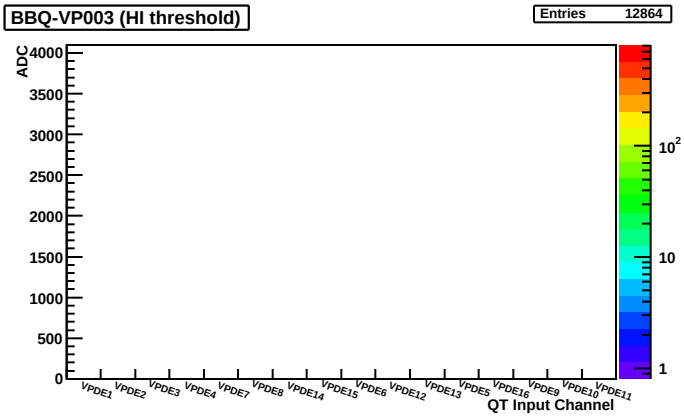

BBQ-ZD001 (ZDC TOWER)
Entries 12864
TAC

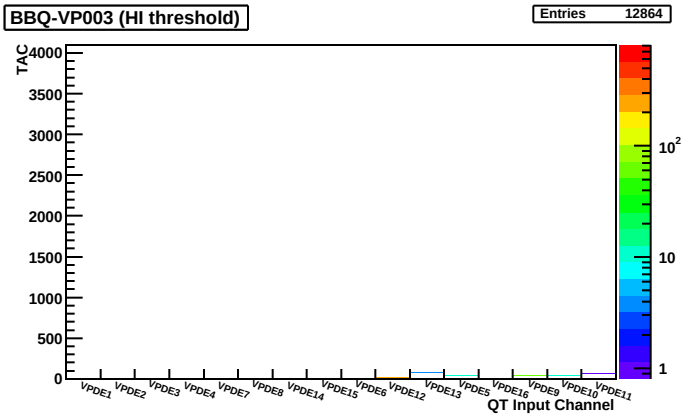

BBQ-VP001 (LO threshold)
Entries 12864
ADC
QT Input Channel

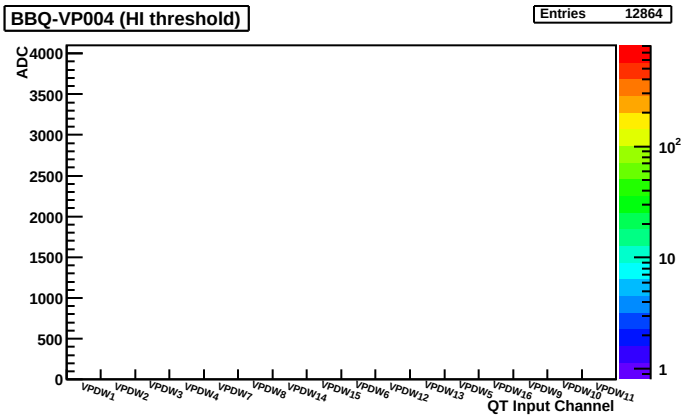

BBQ-VP001 (LO threshold)
Entries 12864
TAC
QT Input Channel

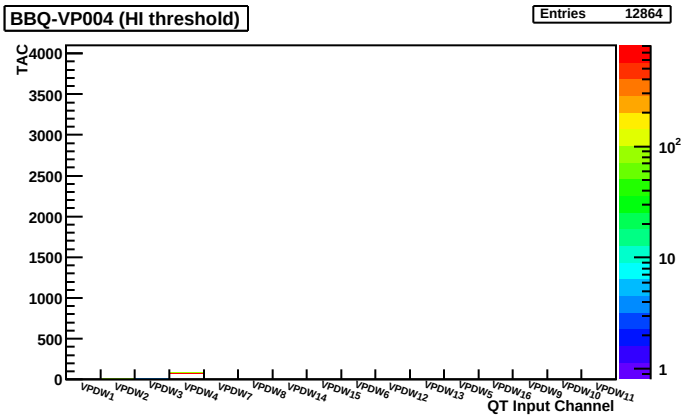

BBQ-VP002 (LO threshold)
Entries 12864
ADC
QT Input Channel

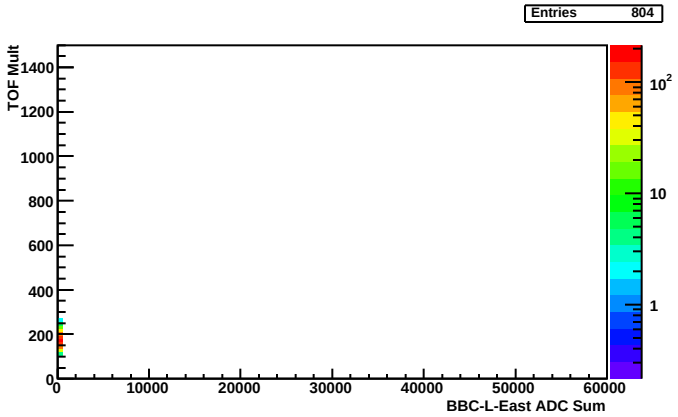

BBQ-VP002 (LO threshold)
Entries 12864
TAC
QT Input Channel

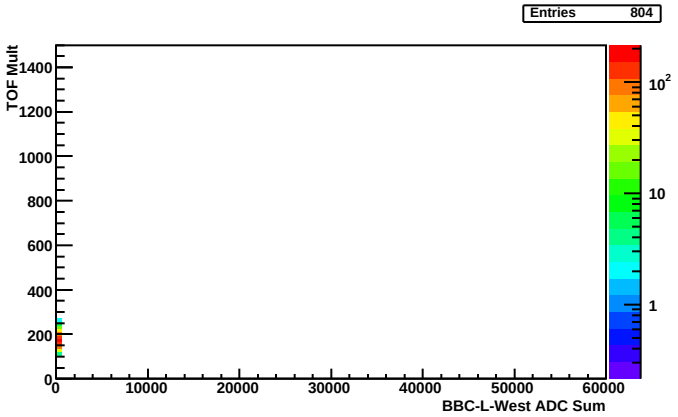

BBQ-VP003 (HI threshold)
Entries 12864
ADC
QT Input Channel

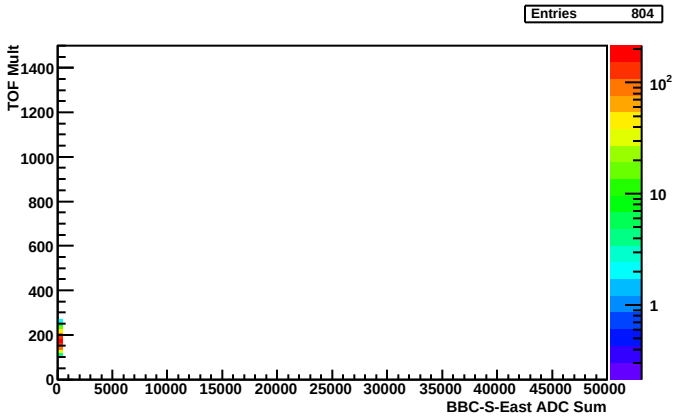

BBQ-VP003 (HI threshold)
Entries 12864
TAC
QT Input Channel

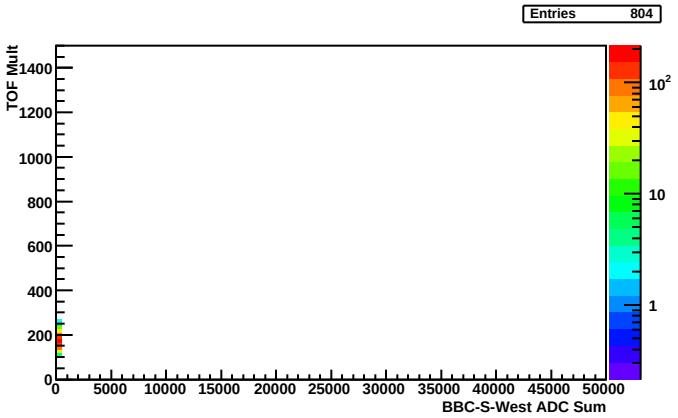

BBQ-VP004 (HI threshold)
Entries 12864
ADC
QT Input Channel

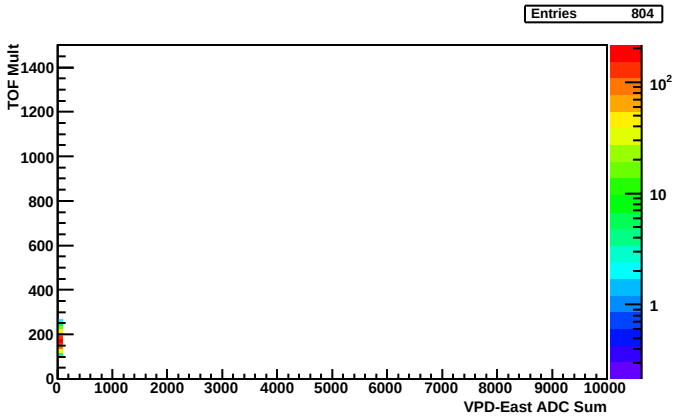

BBQ-VP004 (HI threshold)
Entries 12864
TAC
QT Input Channel

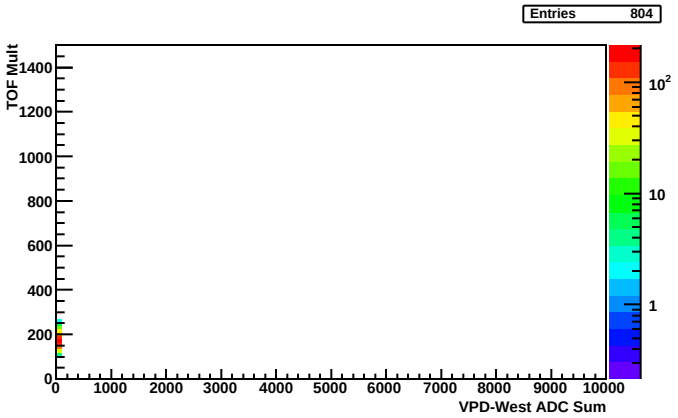

Entries 804
TOF Mult
BBC-L-East ADC Sum

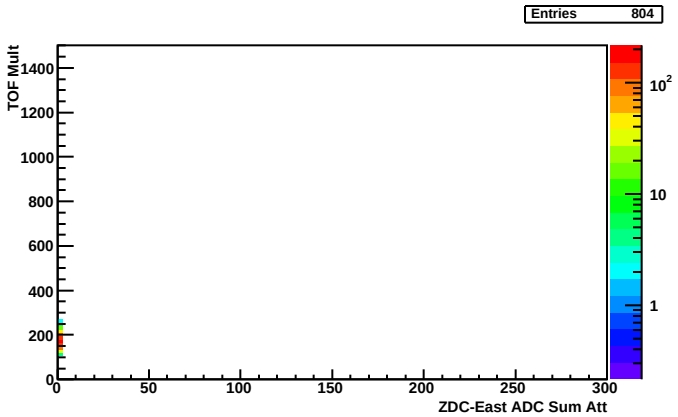

Entries 804
TOF Mult
BBC-L-West ADC Sum

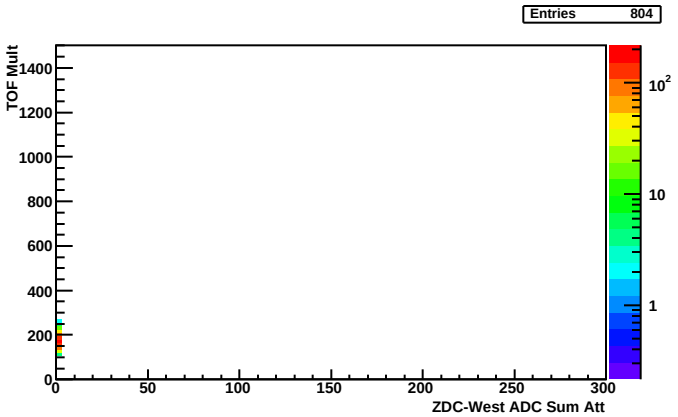

Entries 804
TOF Mult
BBC-S-East ADC Sum

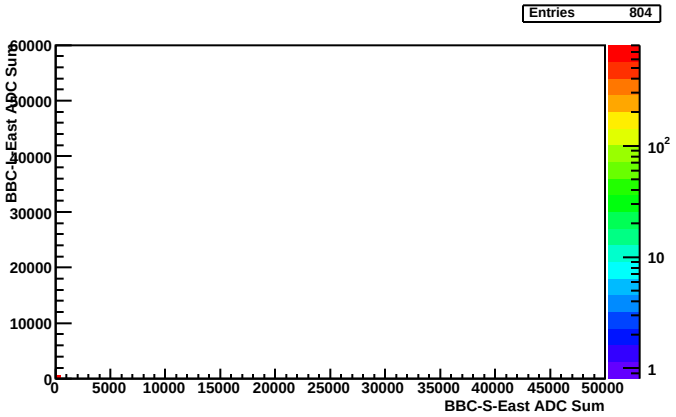

Entries 804
TOF Mult
BBC-S-West ADC Sum

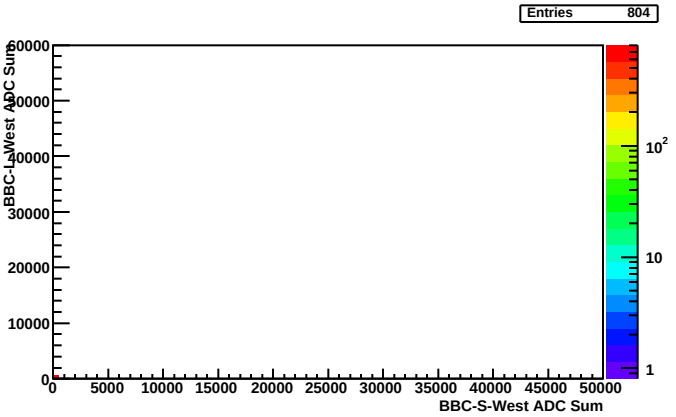

Entries 804
TOF Mult
VPD-East ADC Sum

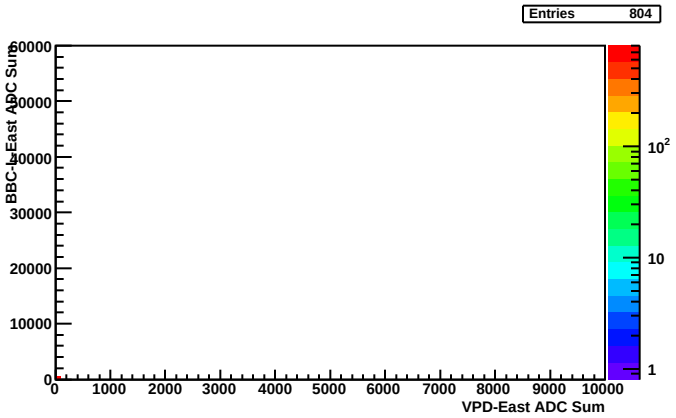

Entries 804
TOF Mult
VPD-West ADC Sum

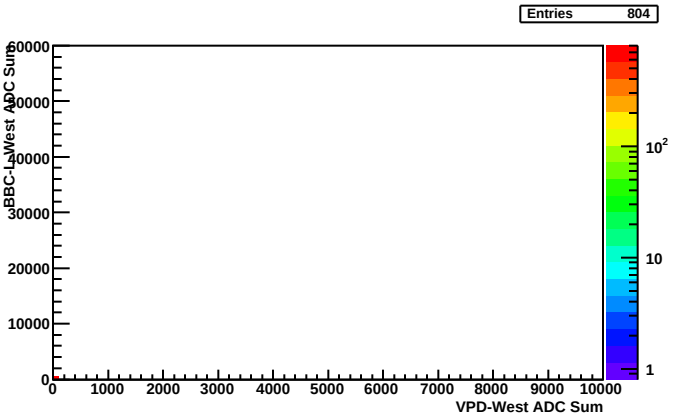

Entries 804
TOF Mult
ZDC-East ADC Sum Att

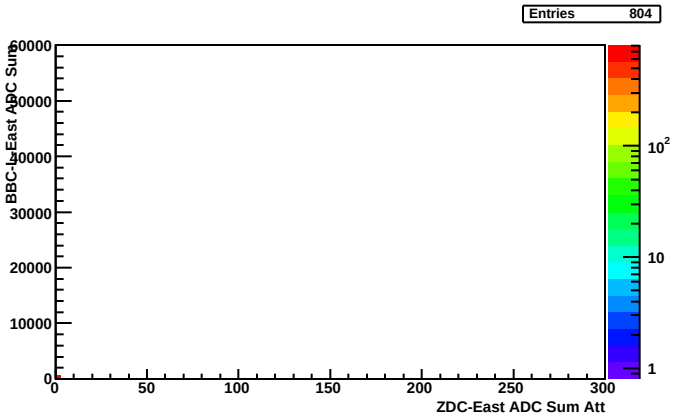

Entries 804
TOF Mult
ZDC-West ADC Sum Att

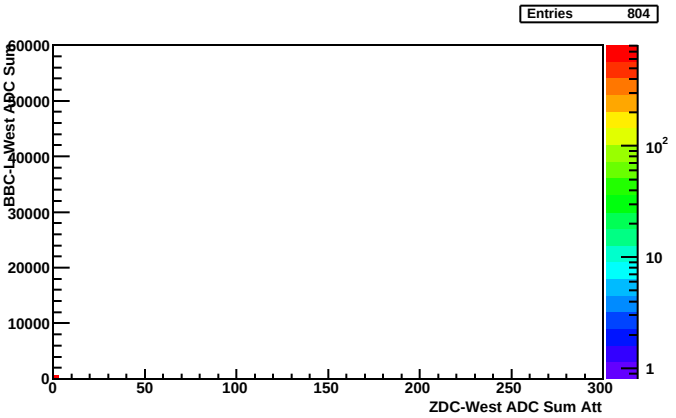

Entries 804
BBC-Lu-East ADC Sum
BBC-S-East ADC Sum

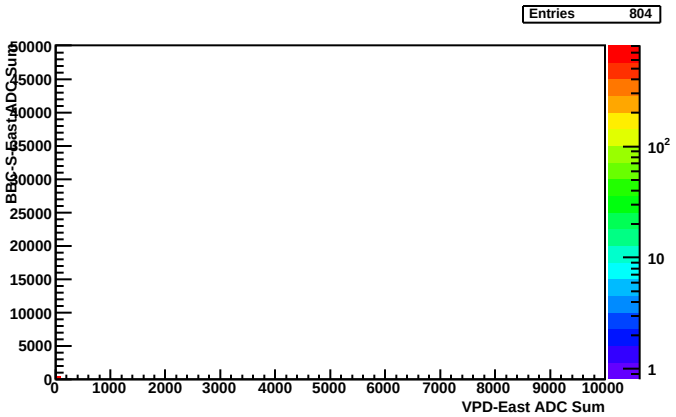

Entries 804
BBC-Lu-West ADC Sum
BBC-S-West ADC Sum

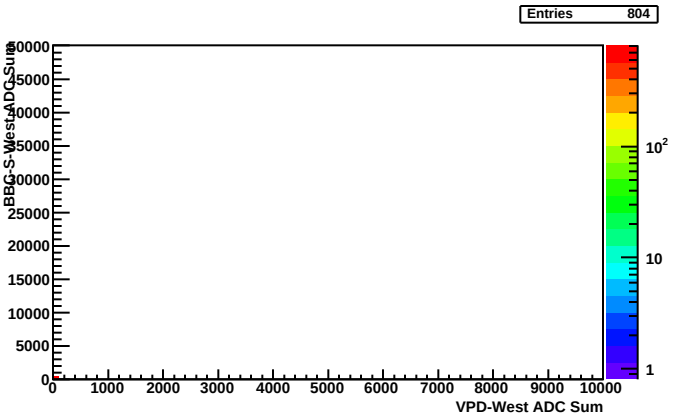

Entries 804
BBC-Lu-East ADC Sum
VPD-East ADC Sum

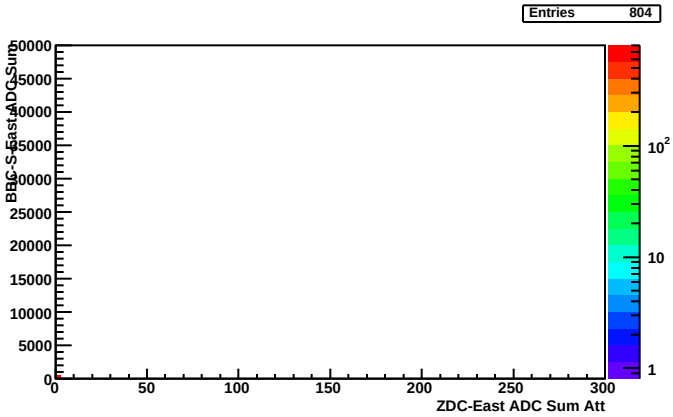

Entries 804
BBC-Lu-West ADC Sum
VPD-West ADC Sum

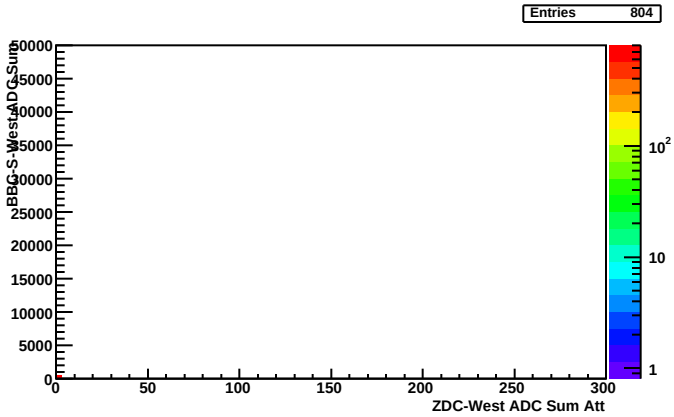

Entries 804
BBC-Lu-East ADC Sum
ZDC-East ADC Sum Att

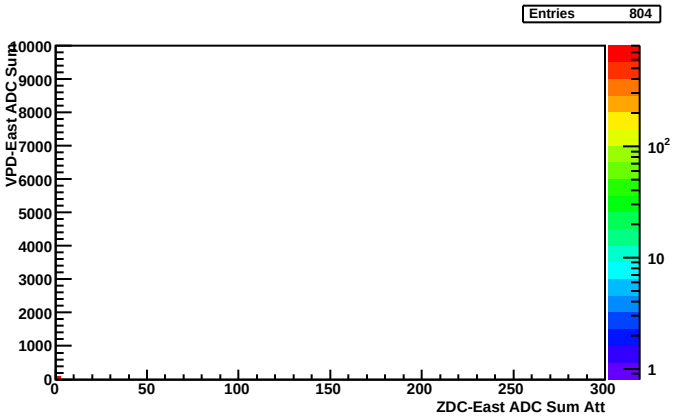

Entries 804
BBC-Lu-West ADC Sum
ZDC-West ADC Sum Att

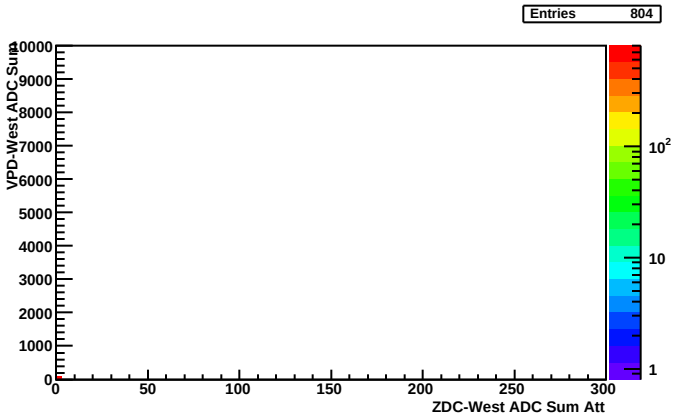

Entries 804
BBC-S-East ADC Sum
VPD-East ADC Sum

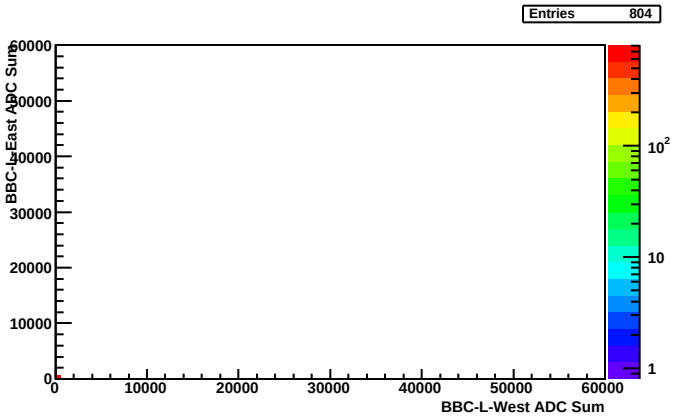

Entries 804
BBC-S-West ADC Sum
VPD-West ADC Sum

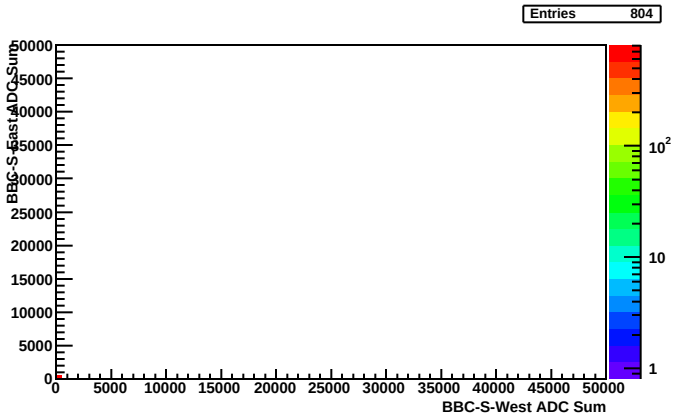

Entries 804
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

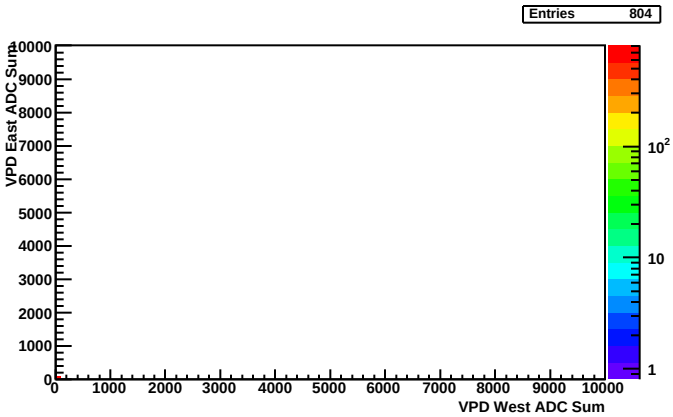

Entries 804
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

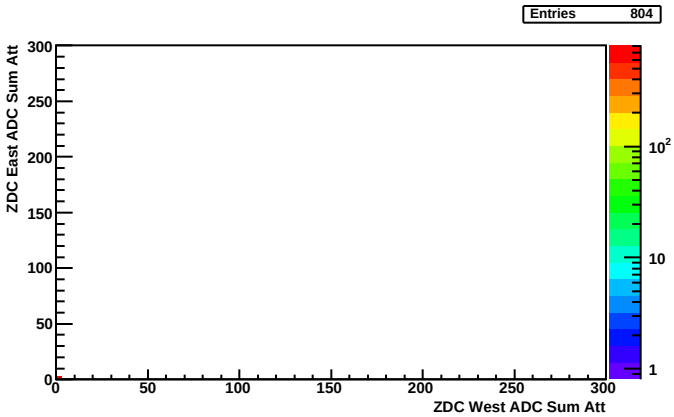

Entries 804
VPD-East ADC Sum
ZDC-East ADC Sum Att

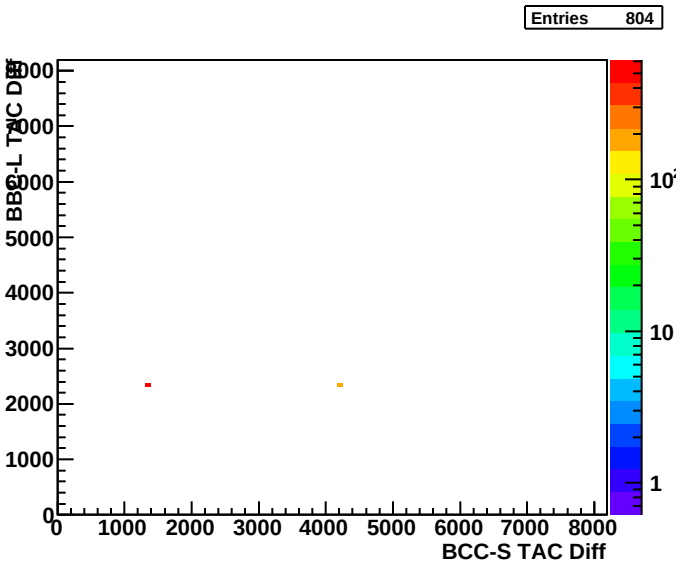

Entries 804
VPD-West ADC Sum
ZDC-West ADC Sum Att

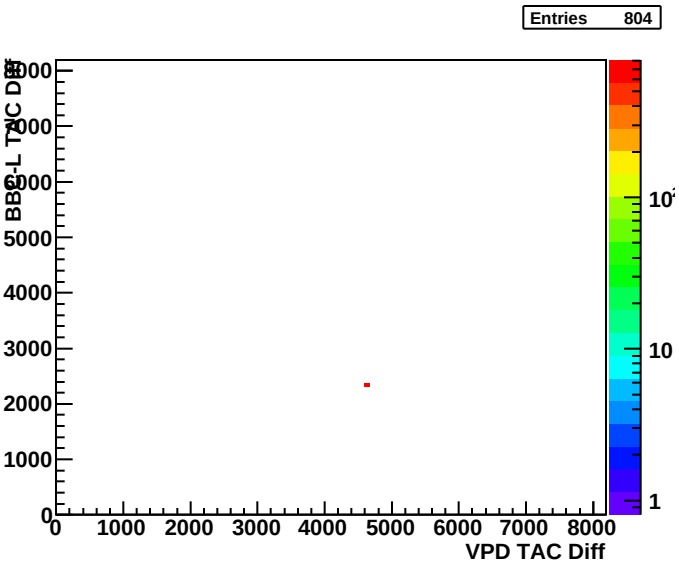

Entries 804
BBC-L-East ADC Sum
BBC-L-West ADC Sum

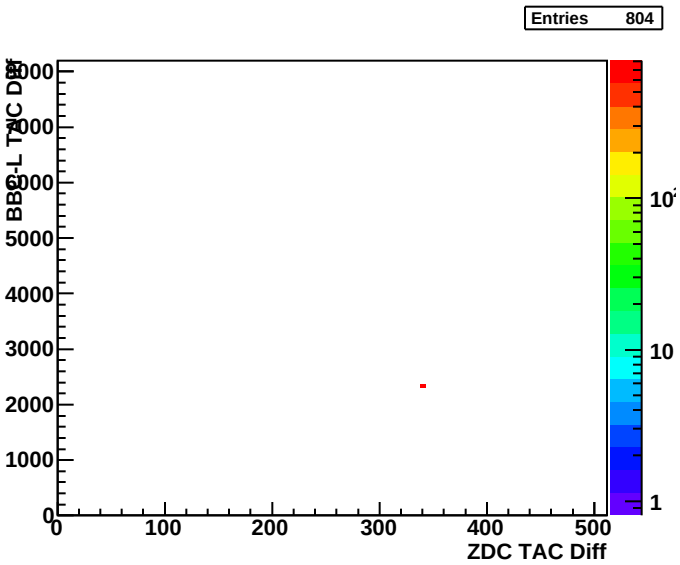

Entries 804
BBC-S-East ADC Sum
BBC-S-West ADC Sum

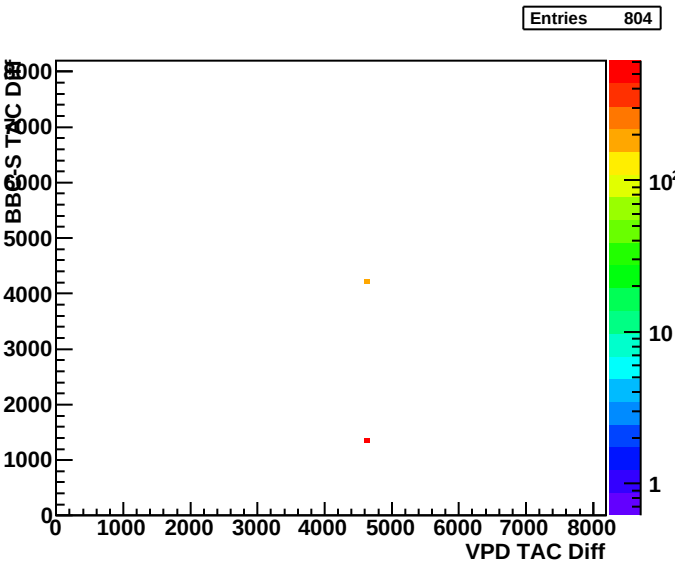

Entries 804
VPD East ADC Sum
VPD West ADC Sum

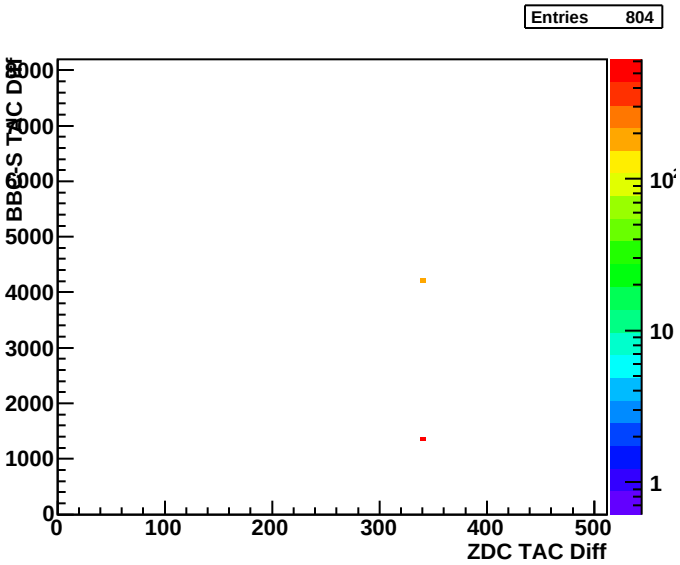

Entries 804
ZDC East ADC Sum Att
ZDC West ADC Sum Att

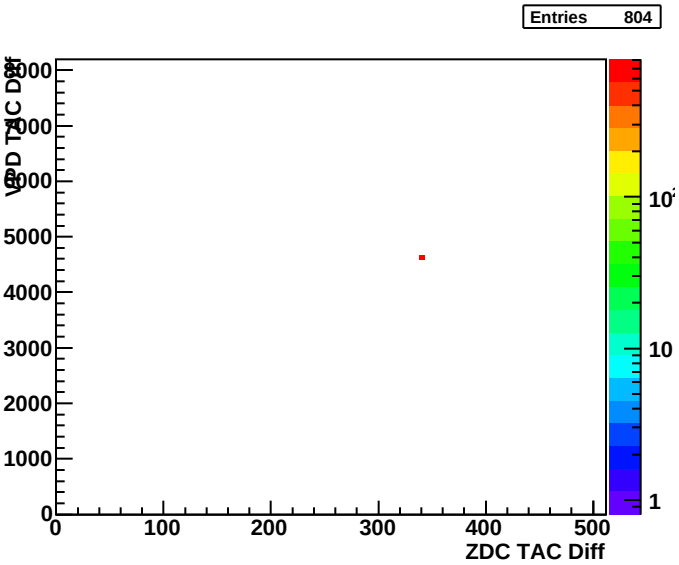

Entries 804
BBC-L TAC Diff
BCC-S TAC Diff

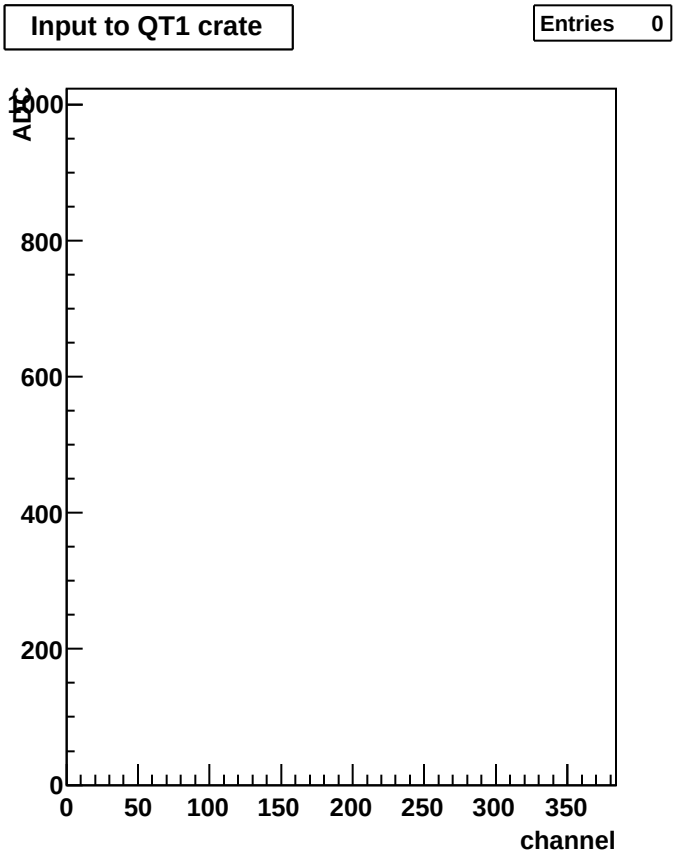

Entries 804
BBC-L TAC Diff
VPD TAC Diff

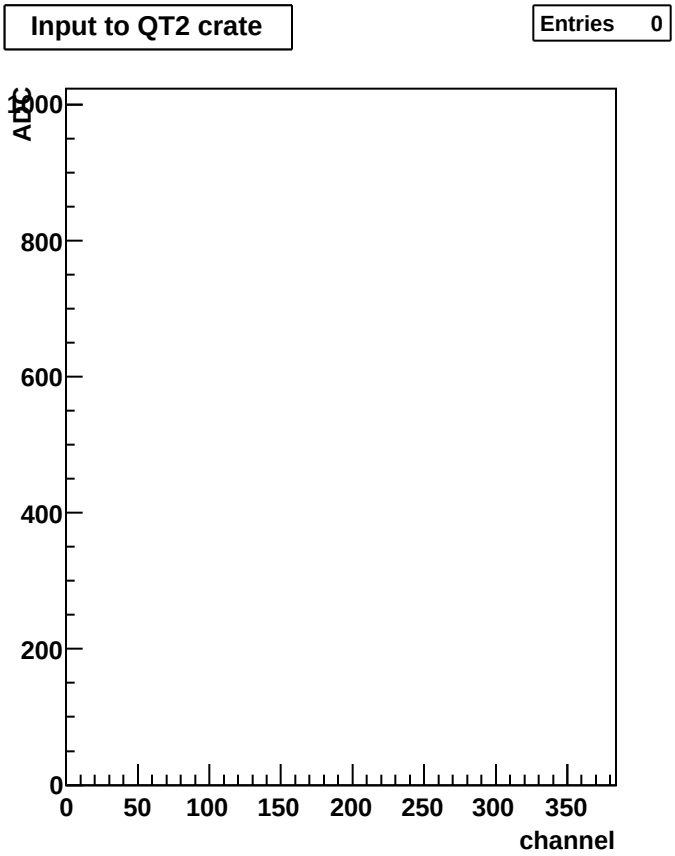

Entries 804
BBC-L TAC Diff
ZDC TAC Diff

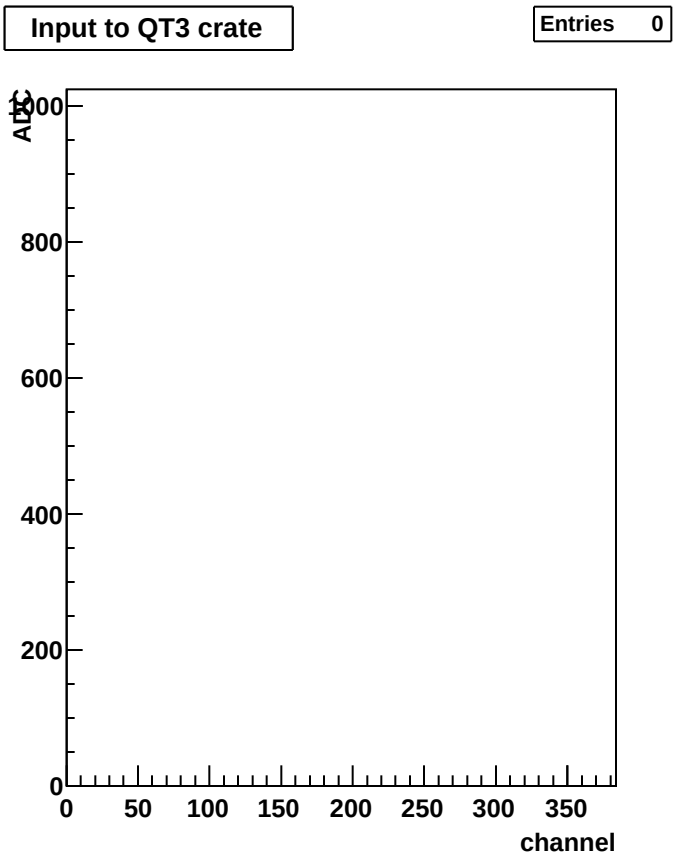

Entries 804
BBC-S TAC Diff
VPD TAC Diff

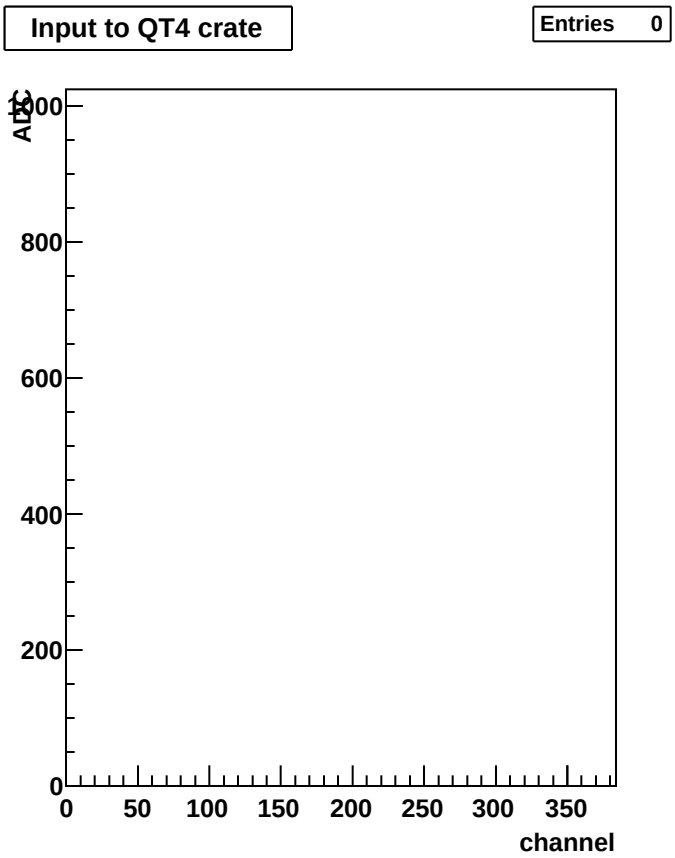

Entries 804
BBC-S TAC Diff
ZDC TAC Diff

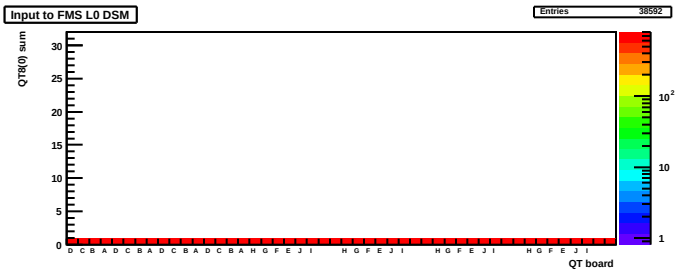

Entries 804
VPD TAC Diff
ZDC TAC Diff

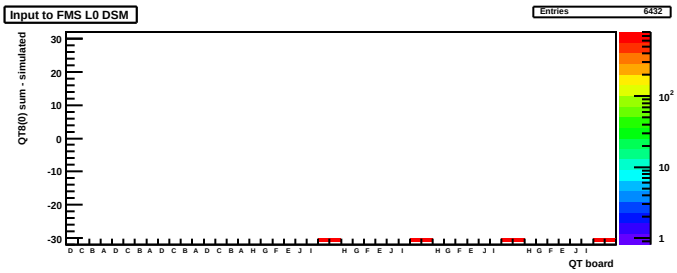

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

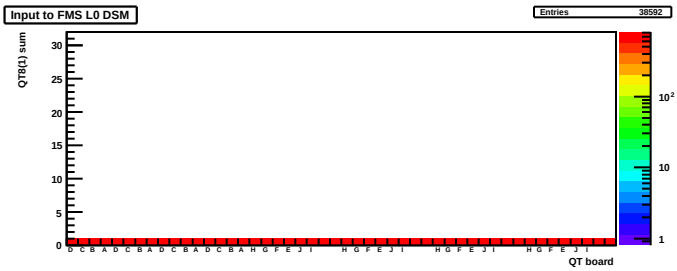

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

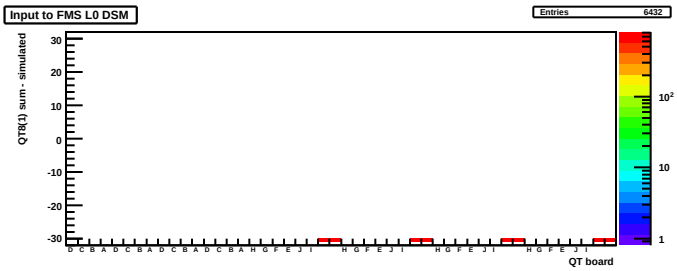

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

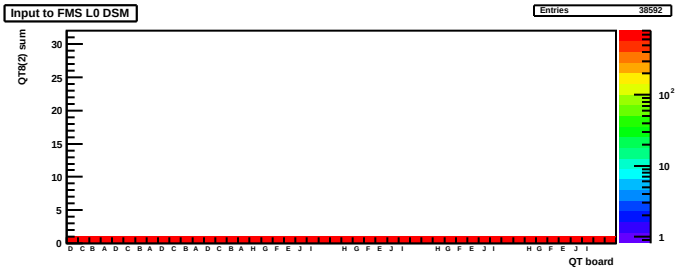

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

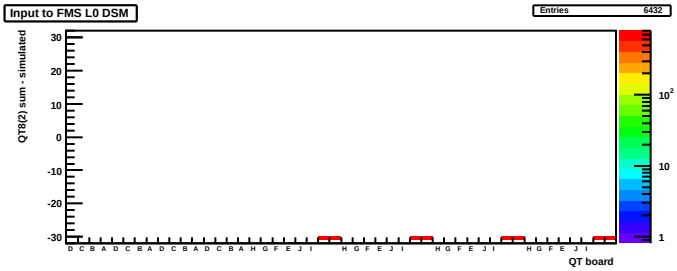

Input to FMS L0 DSM
Entries 38592
QT8(0) sum
QT board

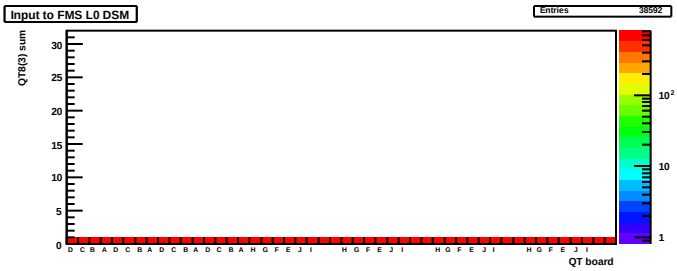

Input to FMS L0 DSM
Entries 6432
QT8(0) sum - simulated
QT board

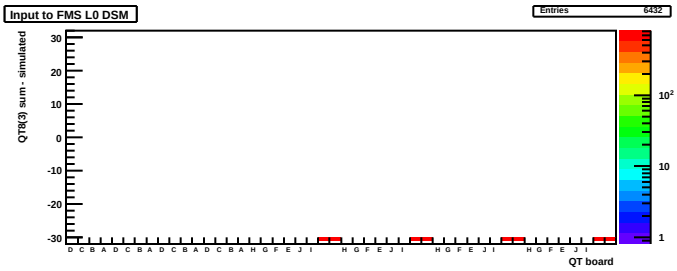

Input to FMS L0 DSM
Entries 38592
QT8(1) sum
QT board

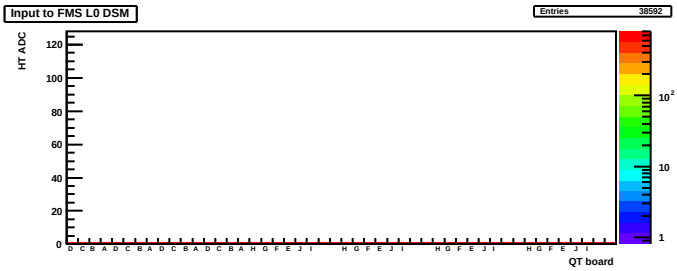

Input to FMS L0 DSM
Entries 6432
QT8(1) sum - simulated
QT board

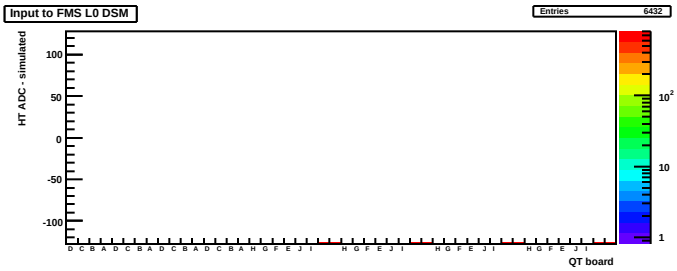

Input to FMS L0 DSM
Entries 38592
QT8(2) sum
QT board

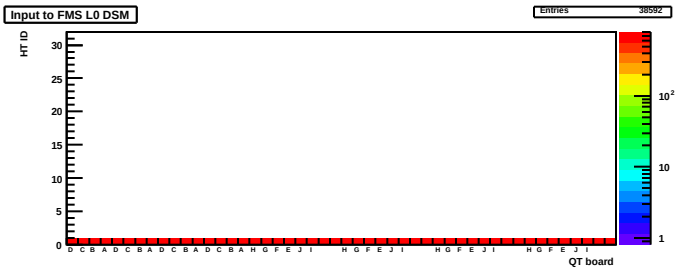

Input to FMS L0 DSM
Entries 6432
QT8(2) sum - simulated
QT board

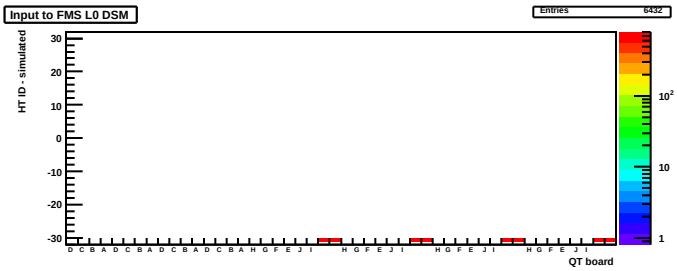

Input to FMS L0 DSM
Entries 38592
QT8(3) sum
QT board

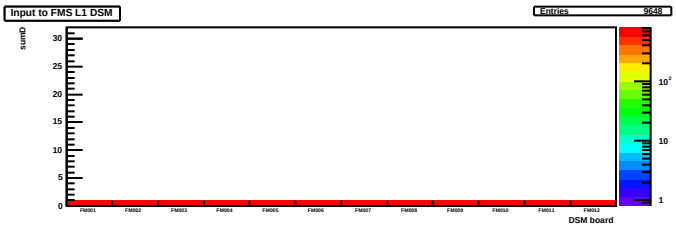

Input to FMS L0 DSM
Entries 6432
QT8(3) sum - simulated
QT board

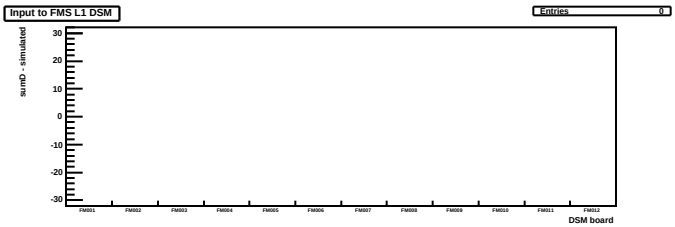

Input to FMS L0 DSM
Entries 38592
HT ADC
QT board

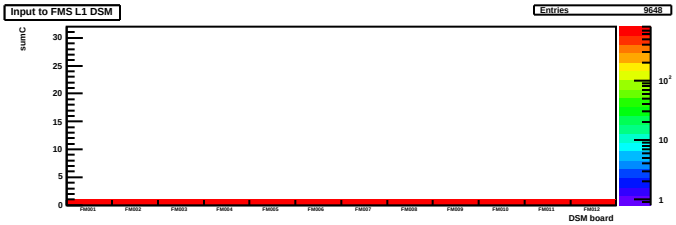

Input to FMS L0 DSM
Entries 6432
HT ADC - simulated
QT board

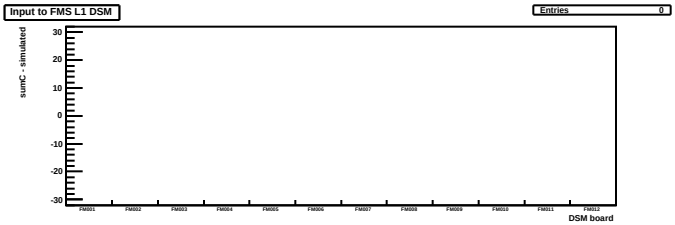

Input to FMS L0 DSM
Entries 38592
HT ID
QT board

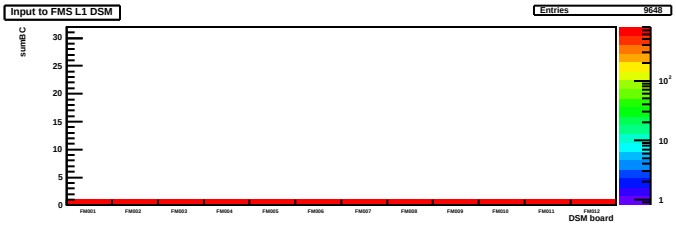

Input to FMS L0 DSM
Entries 6432
HT ID - simulated
QT board

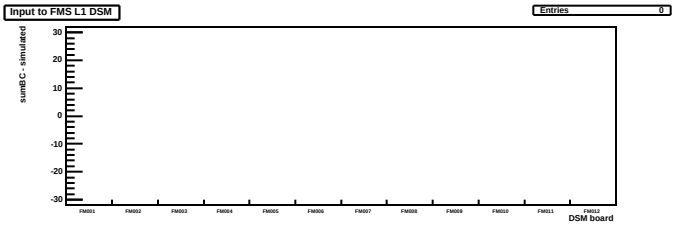

Input to FMS L1 DSM
Entries 9648
sumD
DSM board

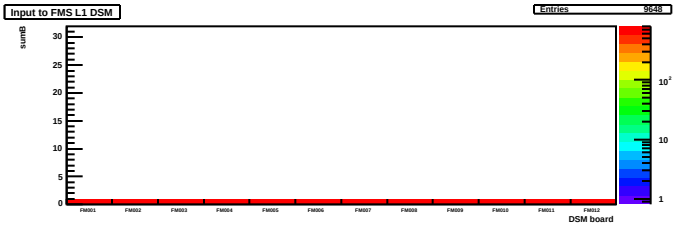

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

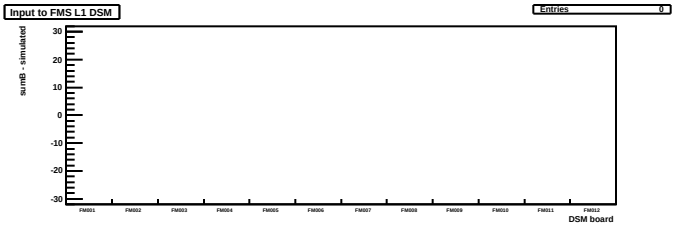

Input to FMS L1 DSM
Entries 9648
sumC
DSM board

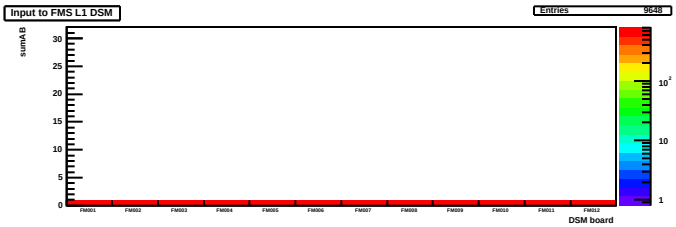

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

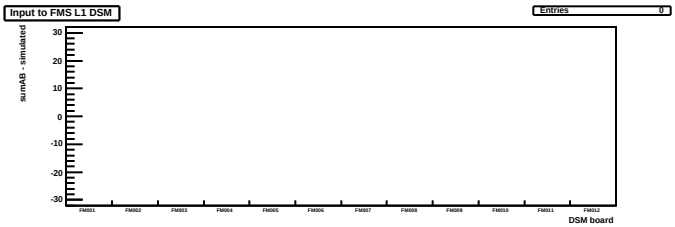

Input to FMS L1 DSM
Entries 9648
sumBC
DSM board

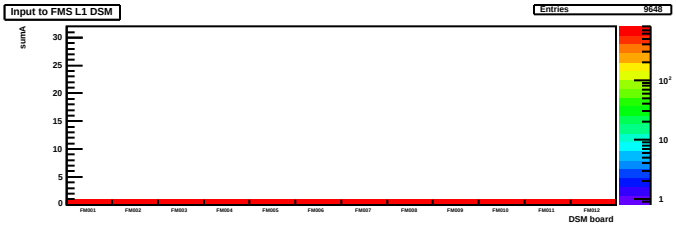

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

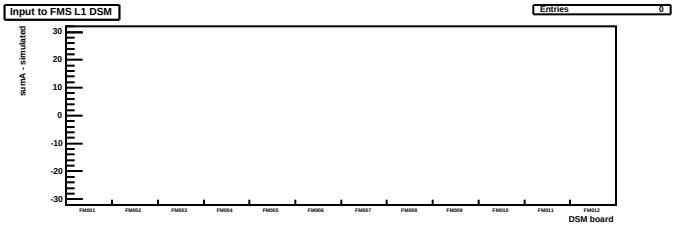

Input to FMS L1 DSM
Entries 9648
sumB
DSM board

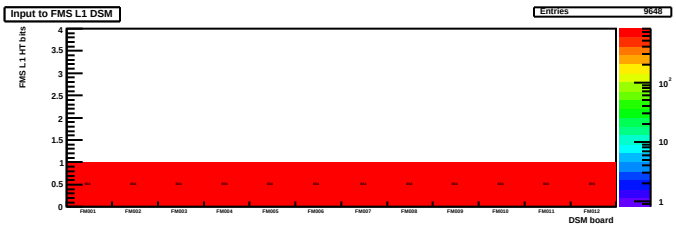

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

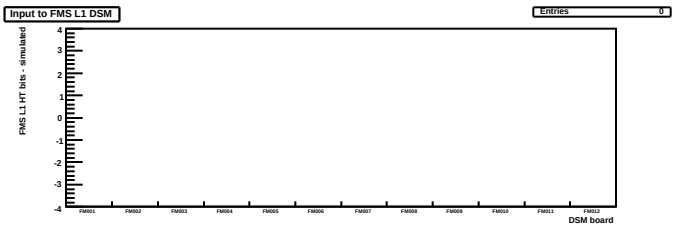

Input to FMS L1 DSM
Entries 9648
sumAB
DSM board

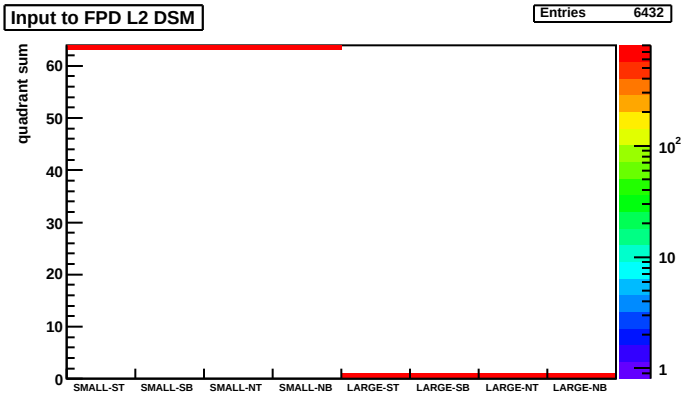

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

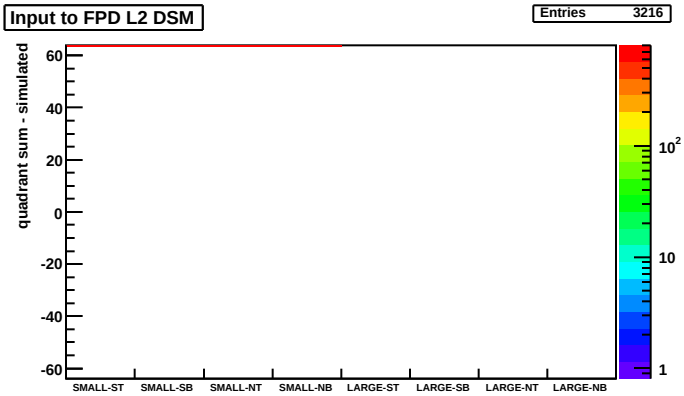

Input to FMS L1 DSM
Entries 9648
sumA
DSM board

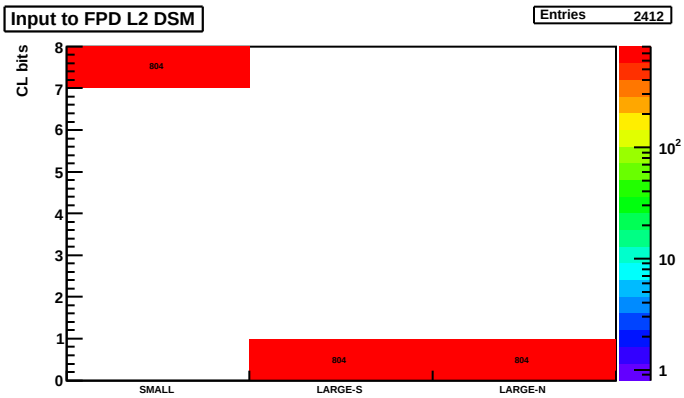

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

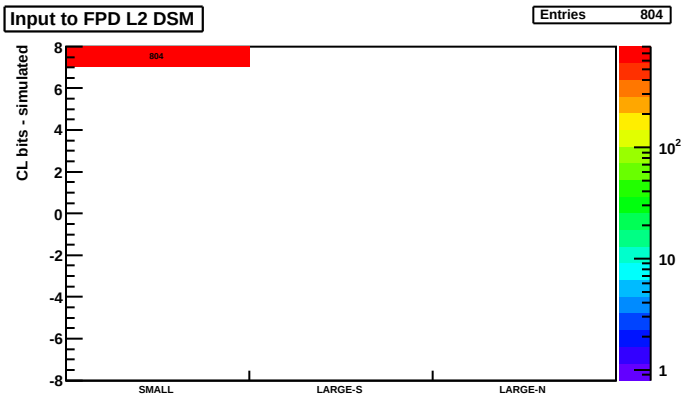

Input to FMS L1 DSM
Entries 9648
FMS L1 HT bits
DSM board

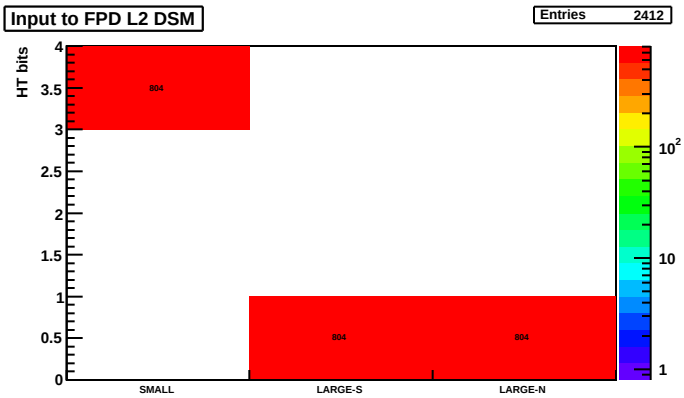

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

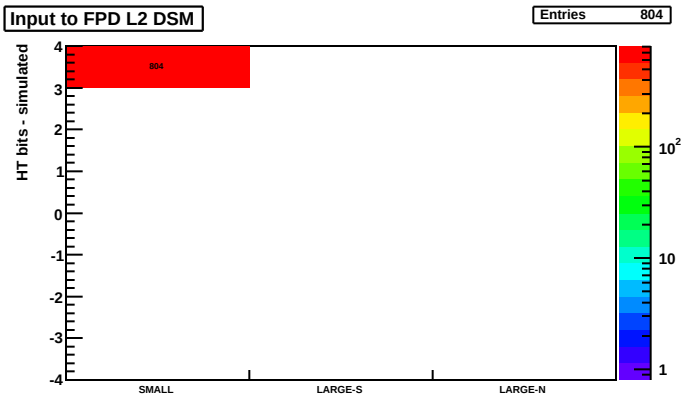

Input to FPD L2 DSM
Entries 6432
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

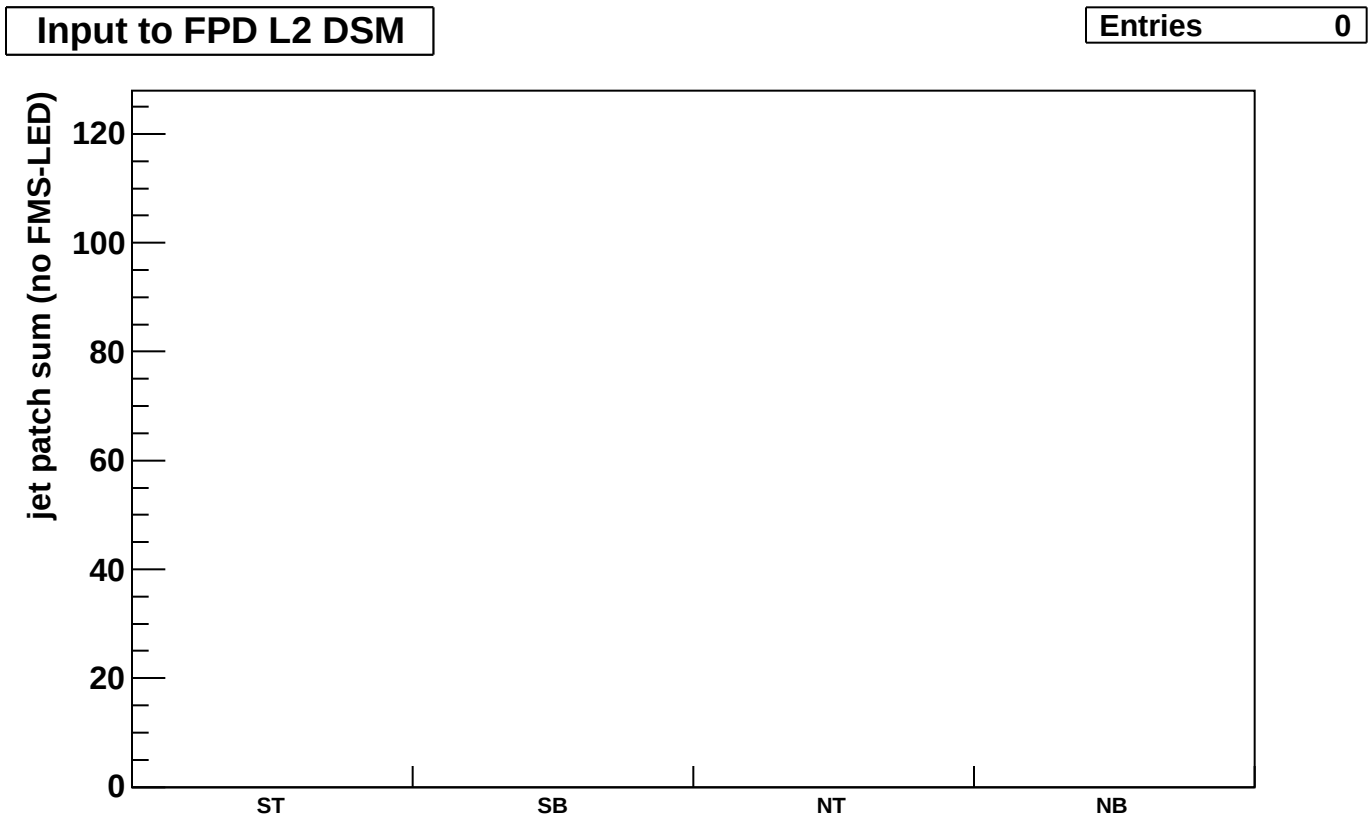

Input to FPD L2 DSM
Entries 3216
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

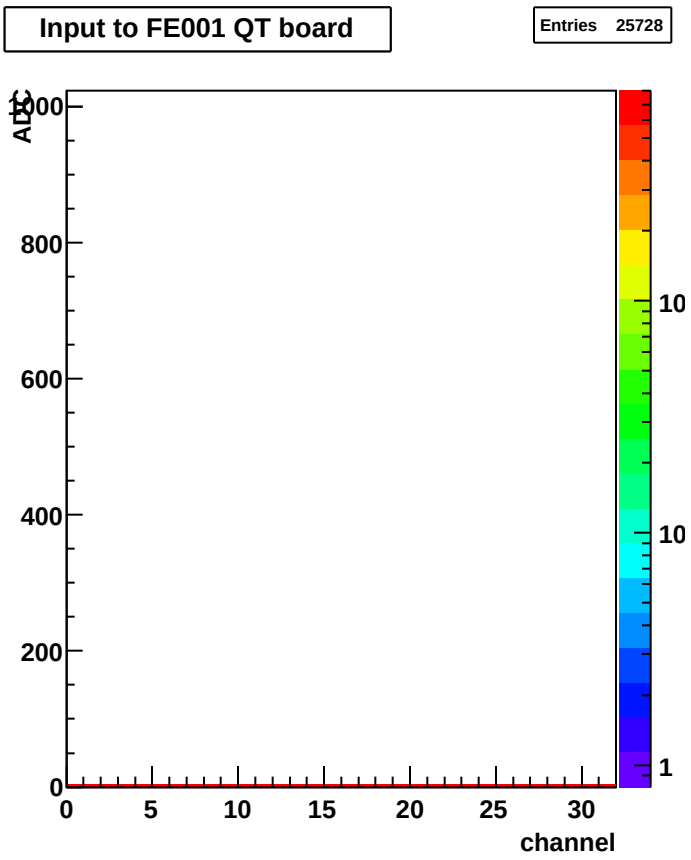

Input to FPD L2 DSM
Entries 2412
CL bits
804
804
804
SMALL LARGE-S LARGE-N

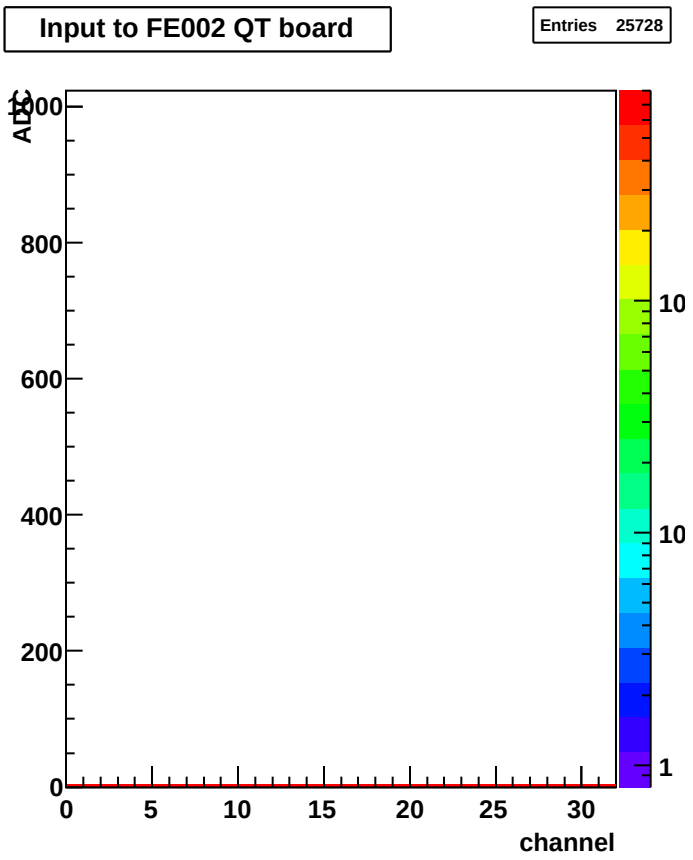

Input to FPD L2 DSM
Entries 804
CL bits - simulated
804
SMALL LARGE-S LARGE-N

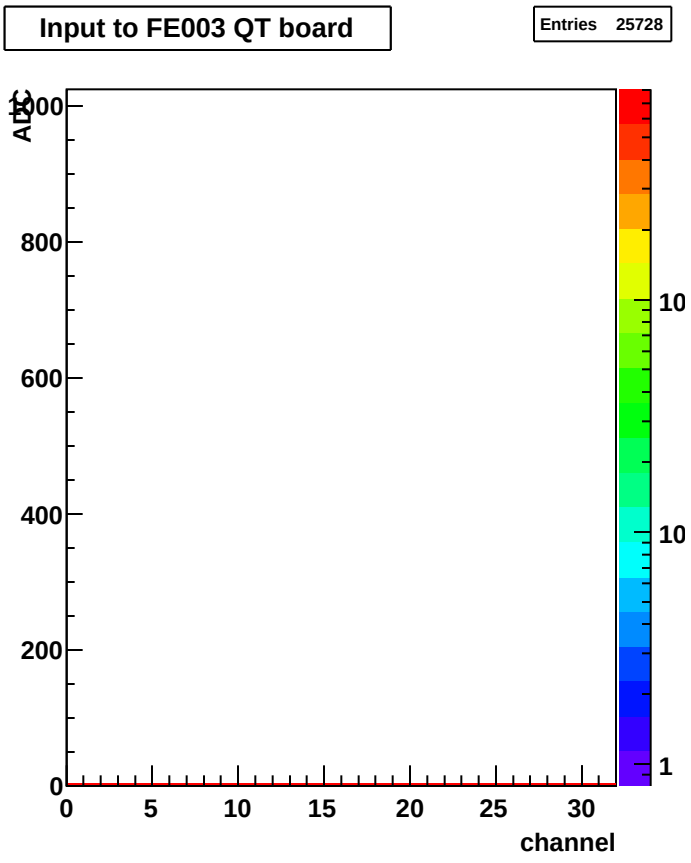

Input to FPD L2 DSM
Entries 2412
HT bits
804
804
804
SMALL LARGE-S LARGE-N

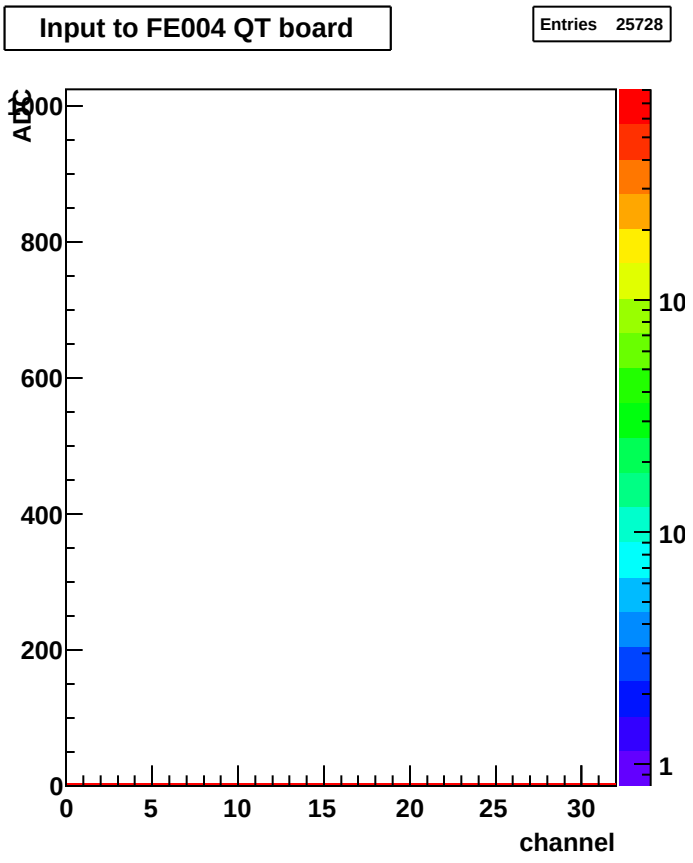

Input to FPD L2 DSM
Entries 804
HT bits - simulated
804
SMALL LARGE-S LARGE-N

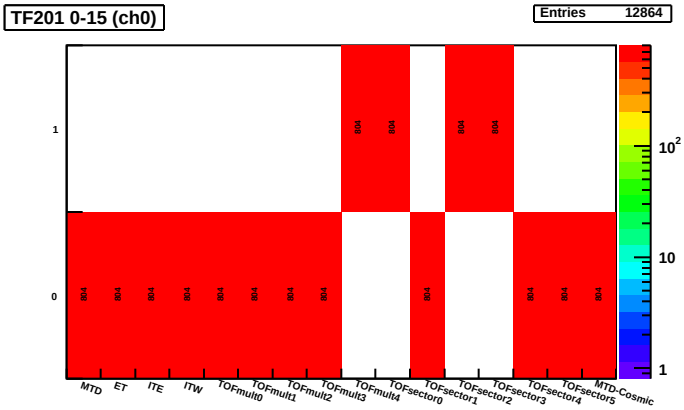

Input to FPD L2 DSM
Entries 0
jet patch sum (no FMS-LED)
120
100
80
60
40
20
0
ST
SB
NT
NB

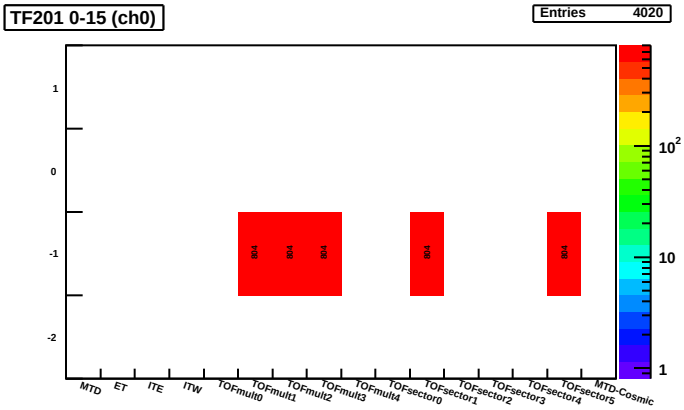

Input to FE001 QT board
Entries 25728
ADC
channel

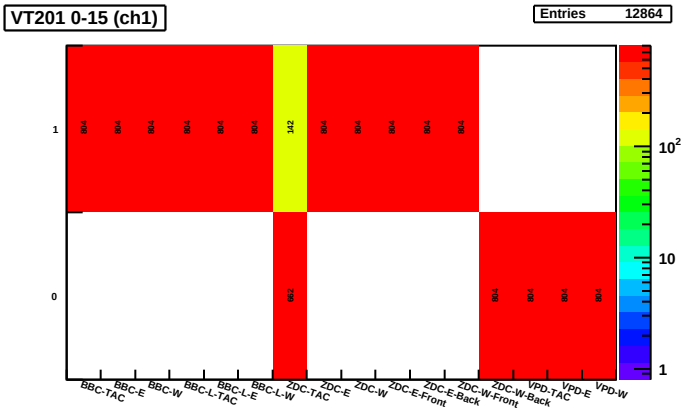

Input to FE002 QT board
Entries 25728
ADC
channel

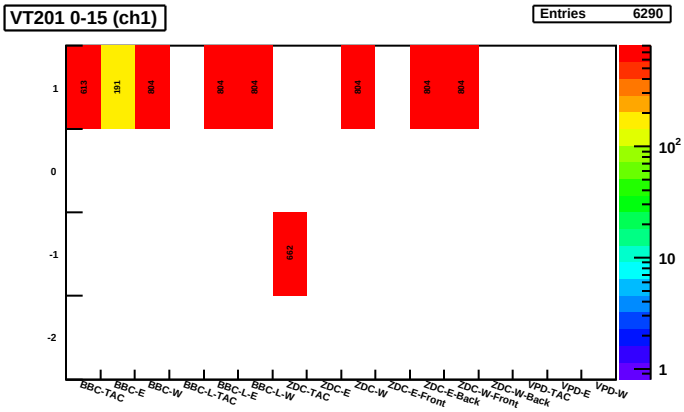

Input to FE003 QT board
Entries 25728
ADC
channel

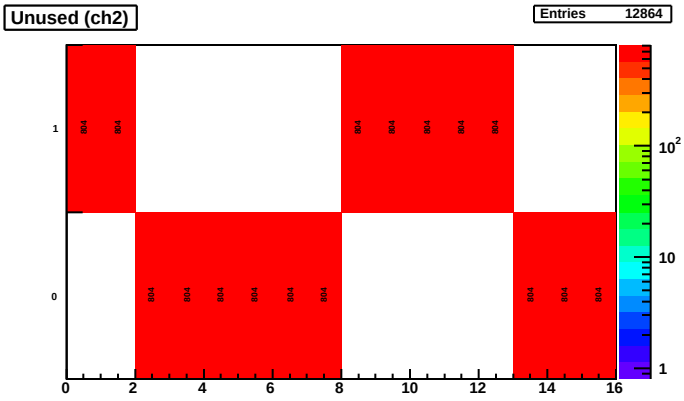

Input to FE004 QT board
Entries 25728
ADC
channel

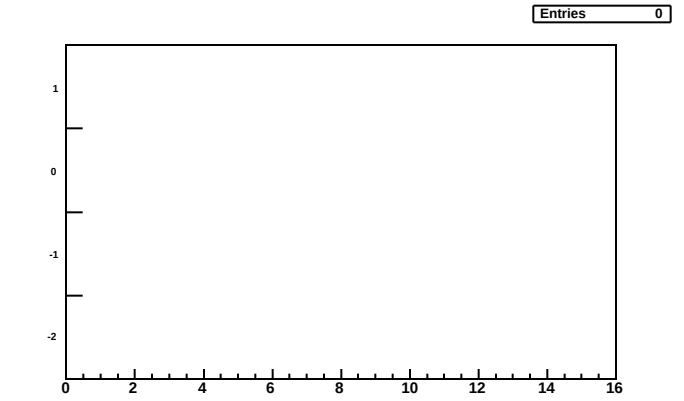

TF201 0-15 (ch0)
Entries 12864
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

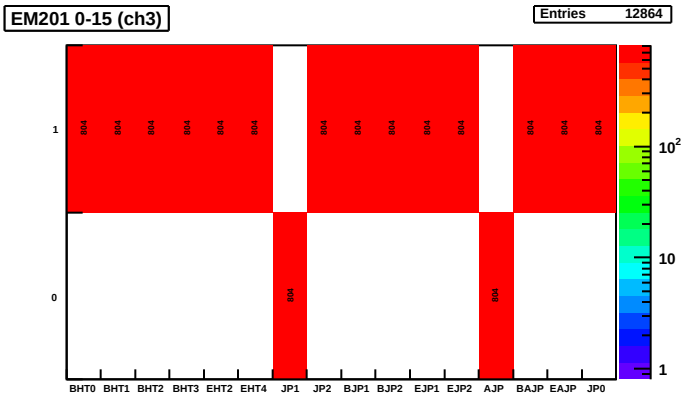

TF201 0-15 (ch0)
Entries 4020
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

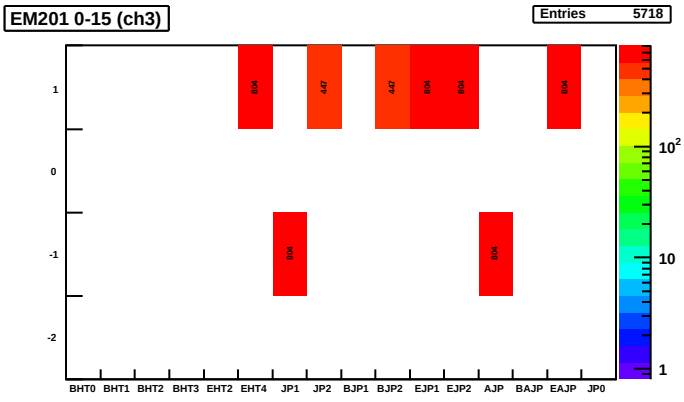

VT201 0-15 (ch1)
Entries 12864
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

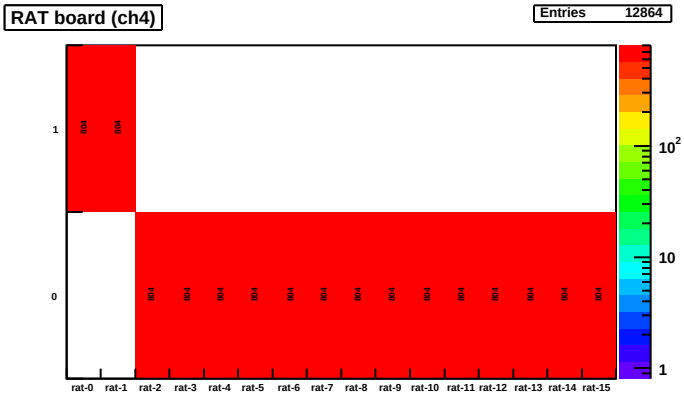

VT201 0-15 (ch1)
Entries 6290
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

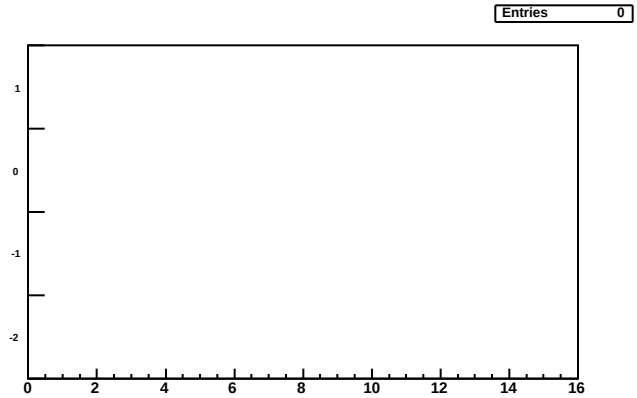

Unused (ch2)
Entries 12864

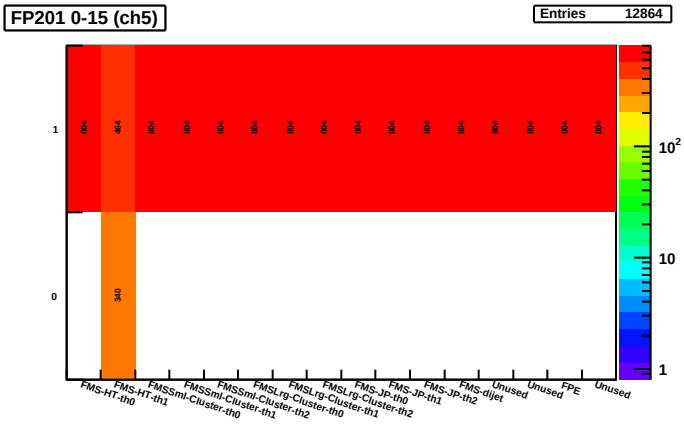

Entries 0

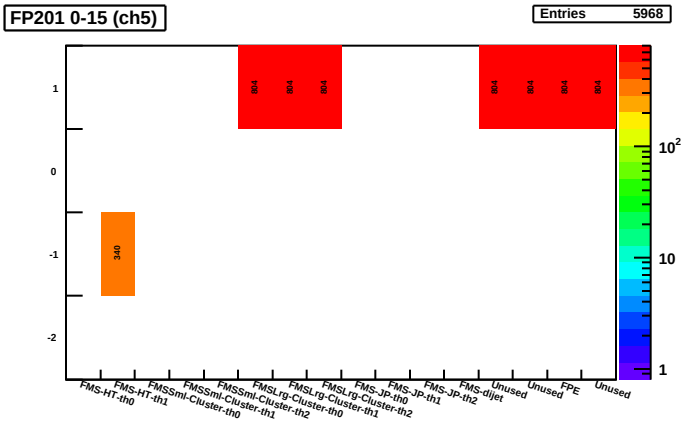

EM201 0-15 (ch3)
Entries 12864
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

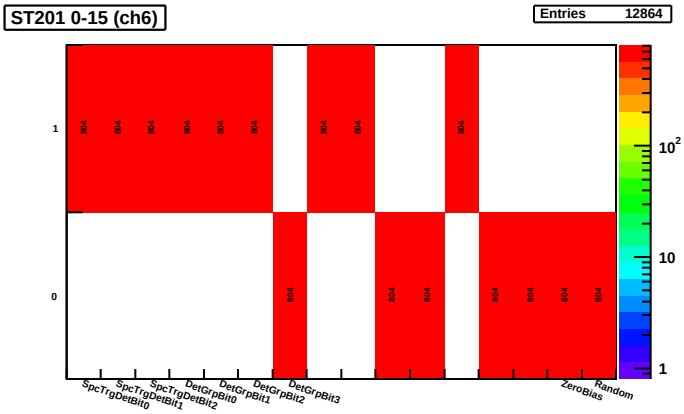

EM201 0-15 (ch3)
Entries 5718
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

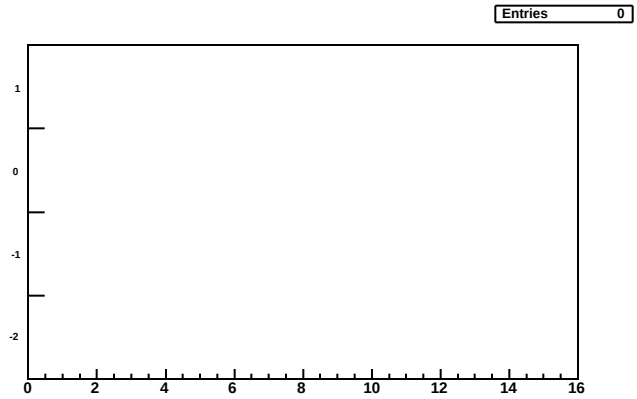

RAT board (ch4)
Entries 12864
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

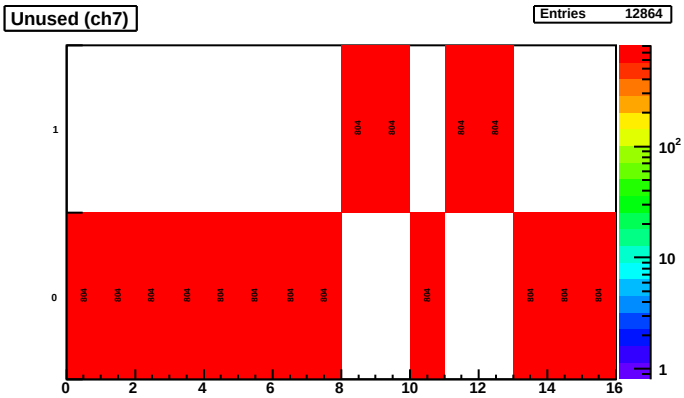

Entries 0

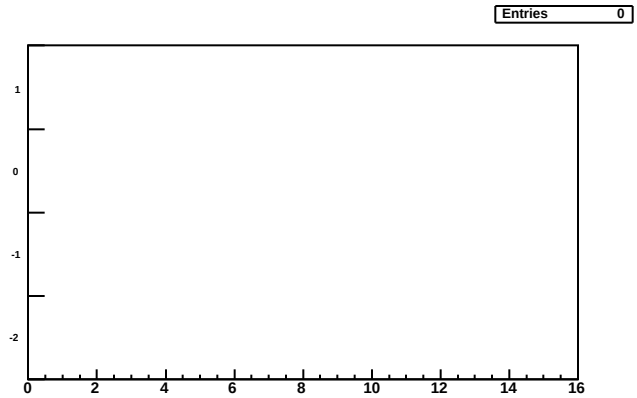

FP201 0-15 (ch5)
Entries 12864
FMS-HT-th0 FMS-HT-th1 FMSSml-Cluster-th0 FMSSml-Cluster-th1 FMSSml-Cluster-th2 FMSLrg-Cluster-th0 FMSLrg-Cluster-th1 FMSLrg-Cluster-th2 FMS-JP-th0 FMS-JP-th1 FMS-JP-th2 FMS-diJet Unused Unused FPE Unused


FP201 0-15 (ch5)
Entries 5968
FMS-HT-th0 FMS-HT-th1 FMSSml-Cluster-th0 FMSSml-Cluster-th1 FMSSml-Cluster-th2 FMSLrg-Cluster-th0 FMSLrg-Cluster-th1 FMSLrg-Cluster-th2 FMS-JP-th0 FMS-JP-th1 FMS-JP-th2 FMS-diJet Unused Unused FPE Unused


ST201 0-15 (ch6)
Entries 12864
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random


Entries 0


Unused (ch7)
Entries 12864


Entries 0