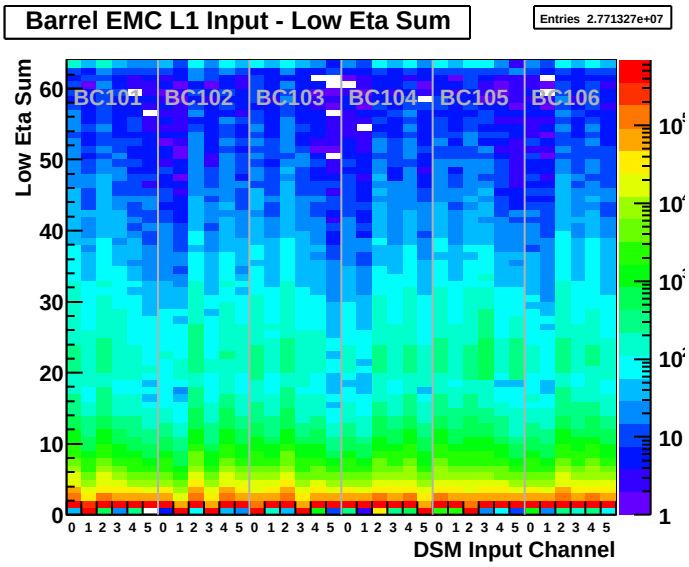
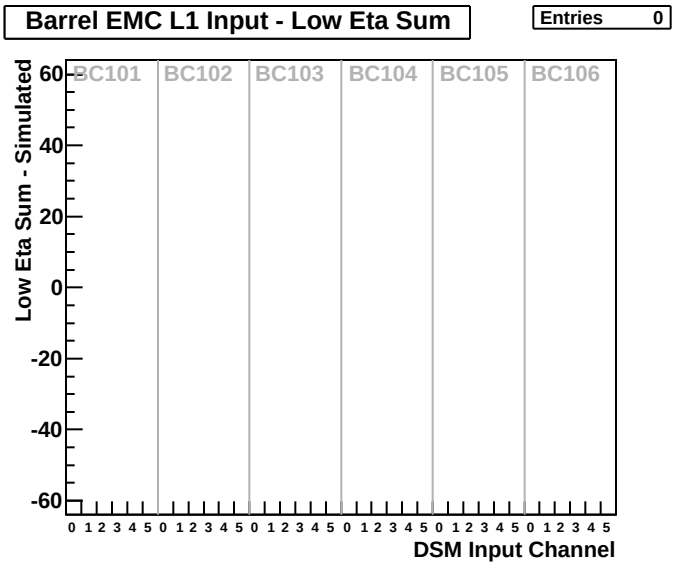
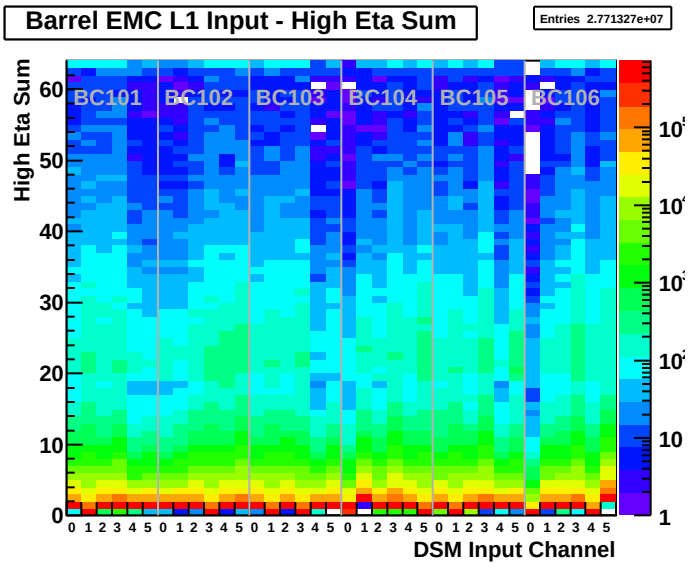
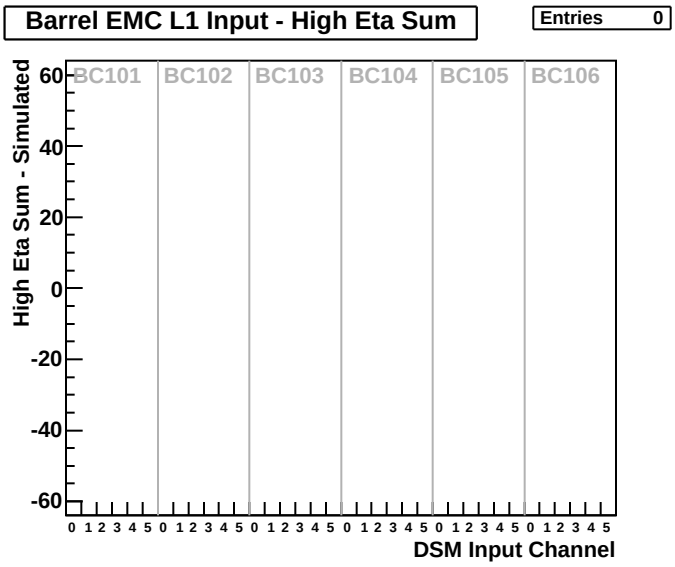
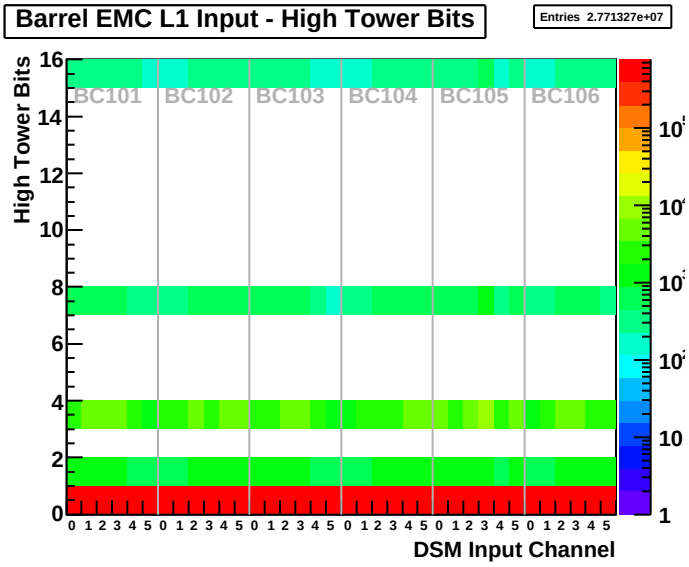
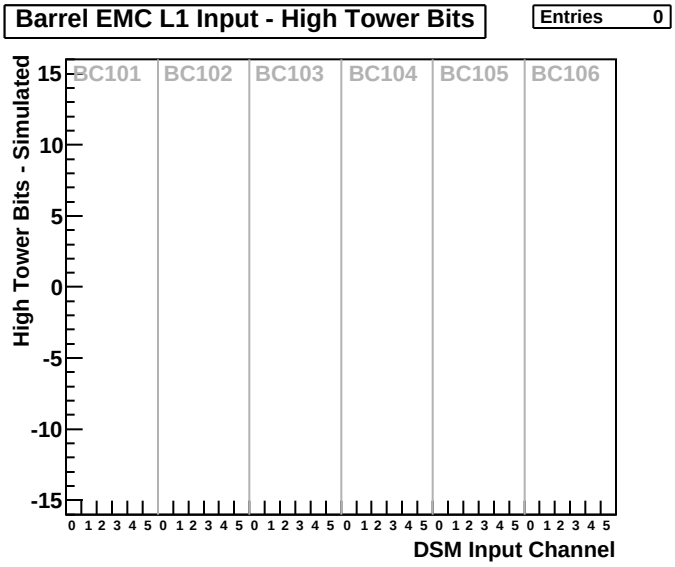

Barrel EMC L1 Input - Low Eta Sum
Entries 2.771327e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

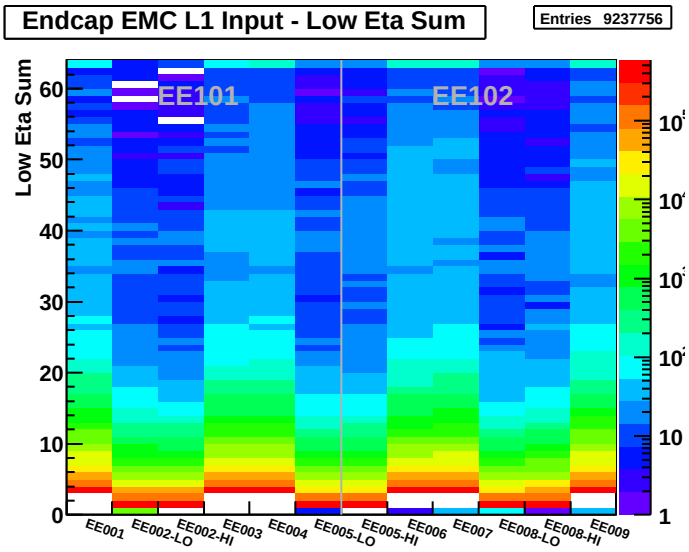

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

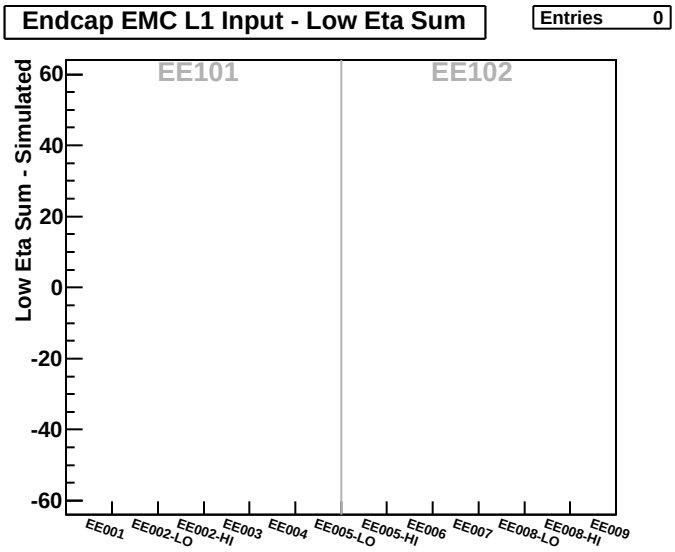

Barrel EMC L1 Input - High Eta Sum
Entries 2.771327e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

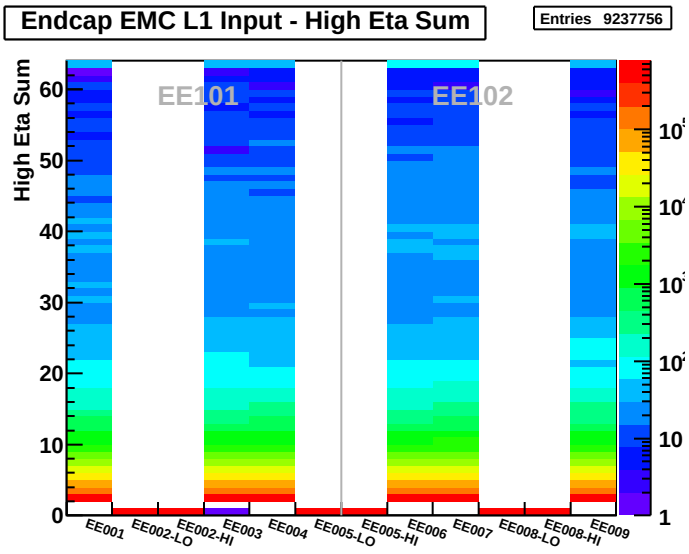

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

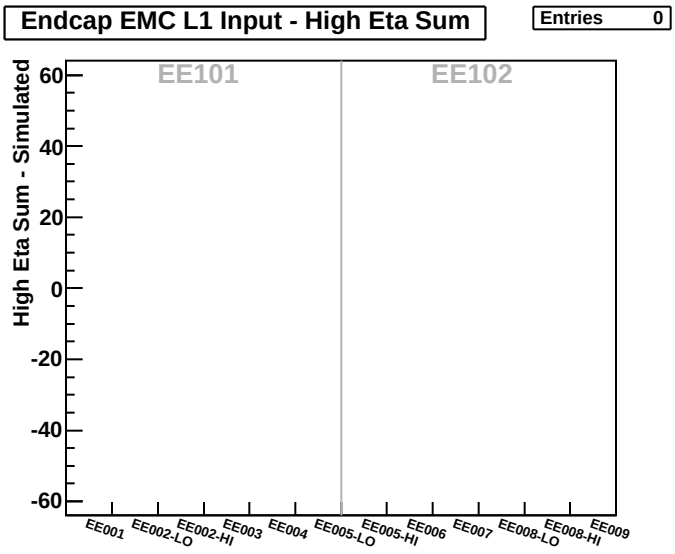

Barrel EMC L1 Input - High Tower Bits
Entries 2.771327e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

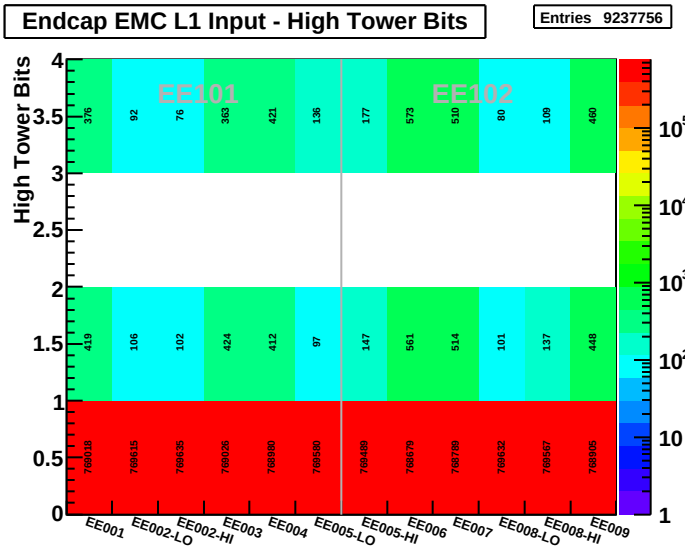

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

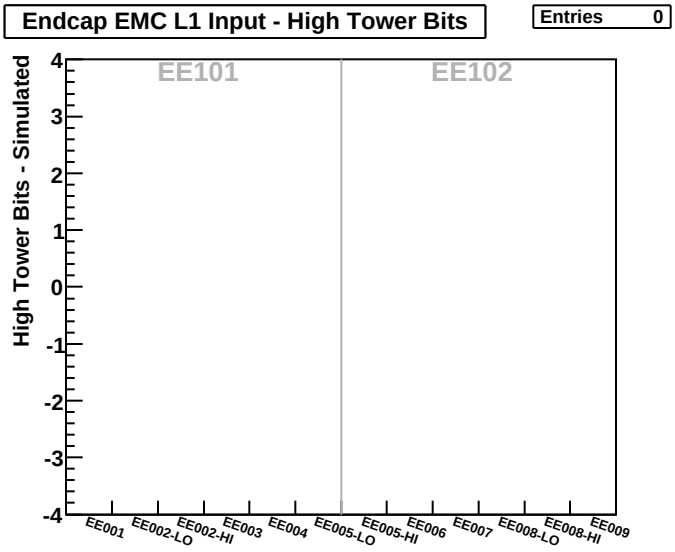

Endcap EMC L1 Input - Low Eta Sum
Entries 9237756
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

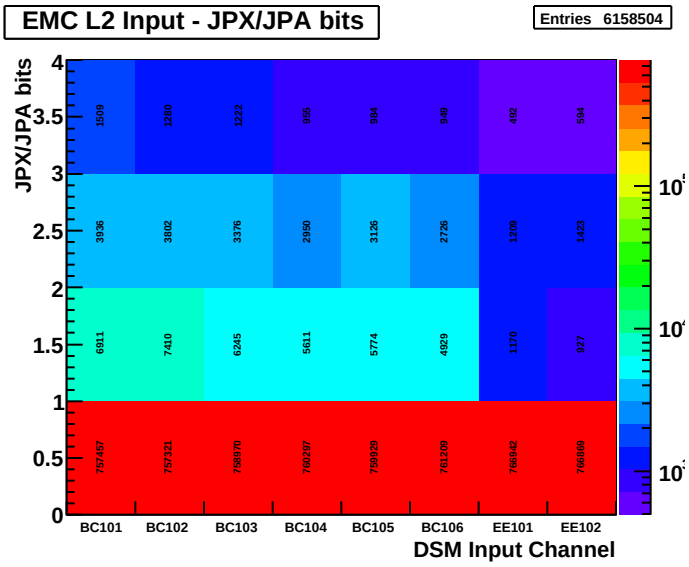

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

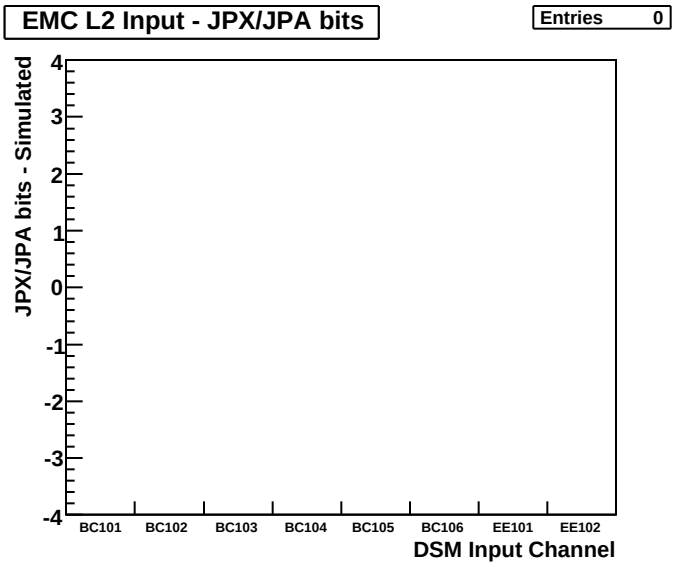

Endcap EMC L1 Input - High Eta Sum
Entries 9237756
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

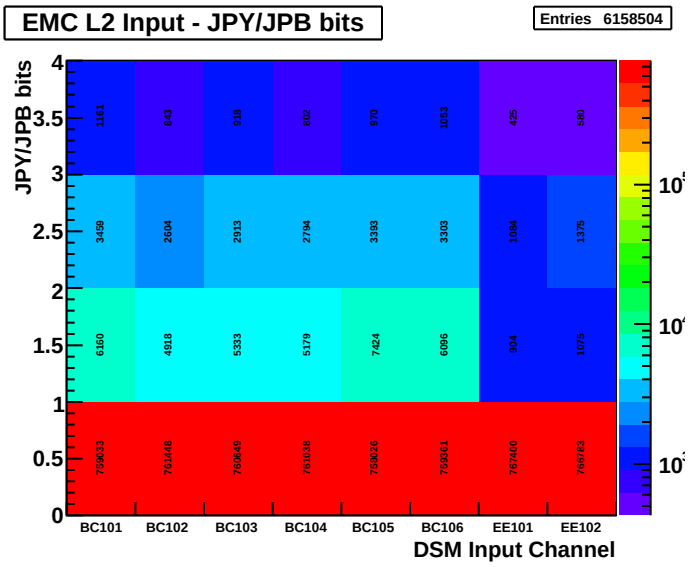

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

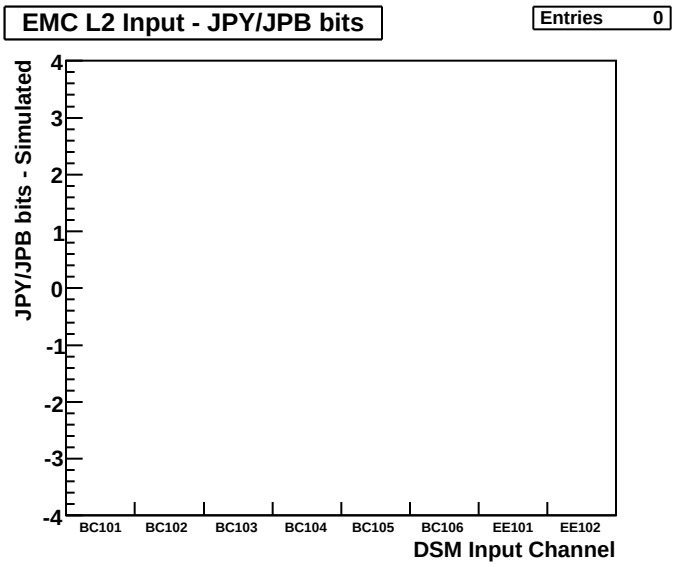

Endcap EMC L1 Input - High Tower Bits
Entries 9237756
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

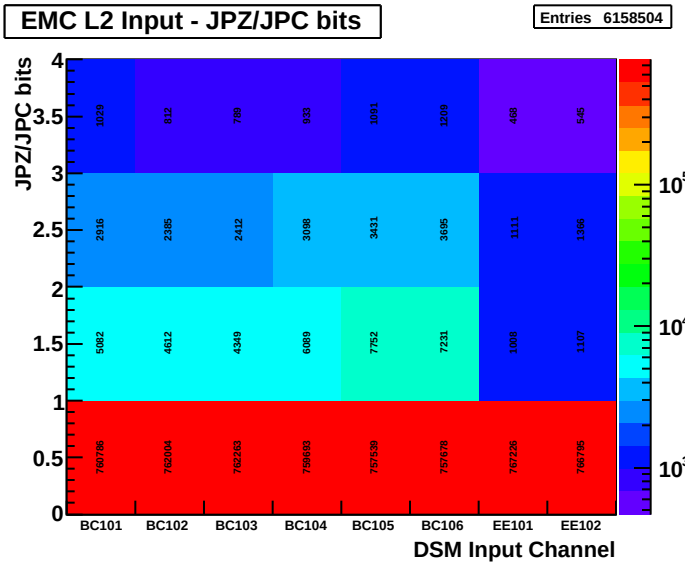

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

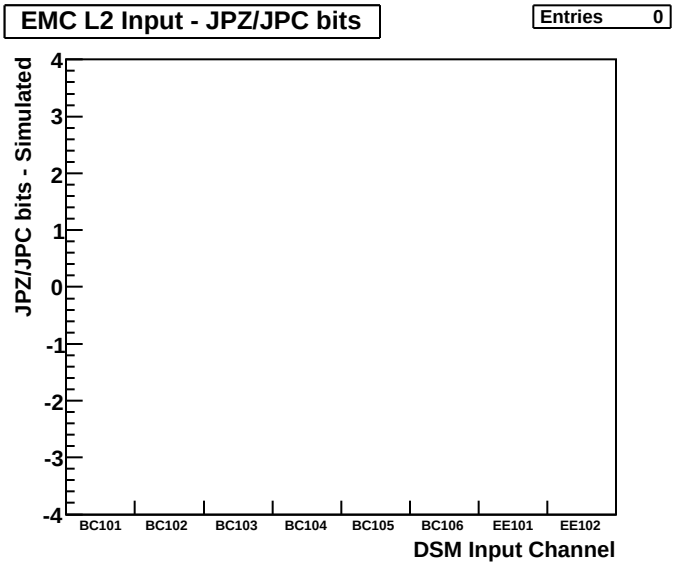

EMC L2 Input - JPX/JPA bits
Entries 6158504
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1509 1280 1222 955 984 949 492 594
3936 3802 3376 2950 3126 2726 1209 1423
6911 7410 6245 5611 5774 4929 1170 927
757457 757321 759370 760297 759929 761209 766942 766869

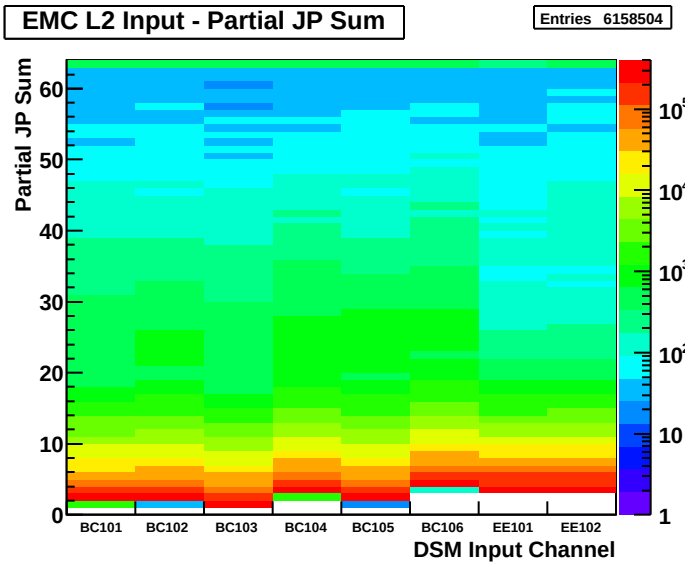

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

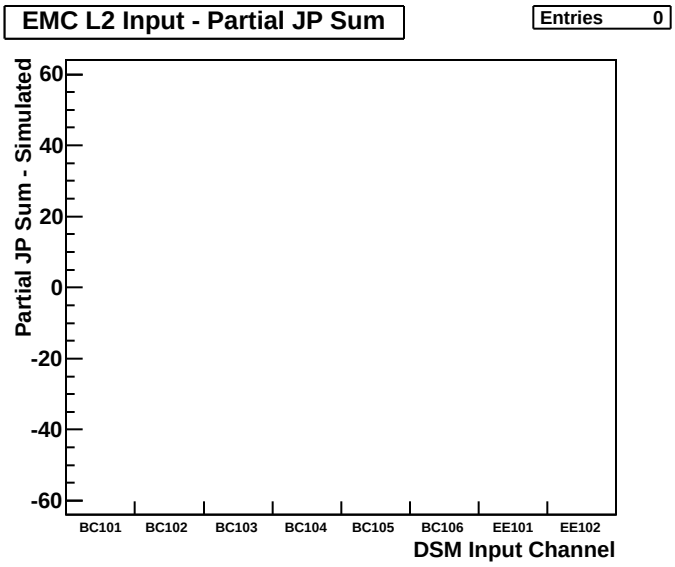

EMC L2 Input - JPY/JPB bits
Entries 6158504
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1161 842 918 802 970 1053 425 580
3459 2604 2913 2794 3393 3303 1084 1375
6160 4918 5333 5179 7424 6096 904 1075
755033 761448 760549 761038 759026 759381 767400 766783

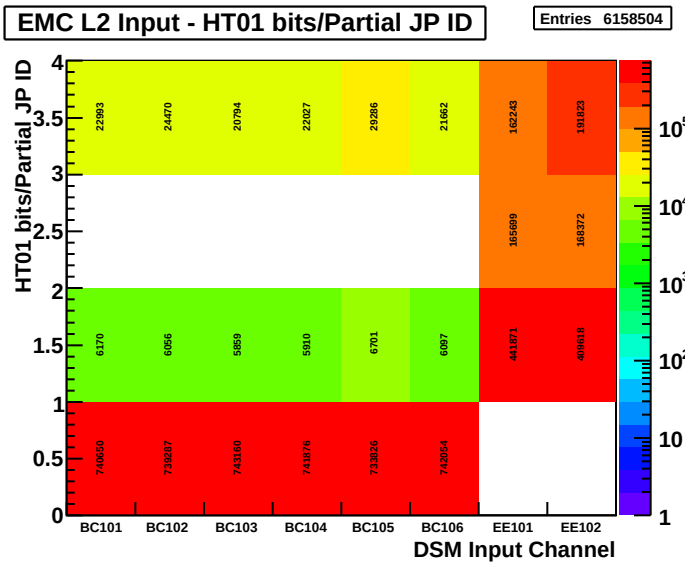

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

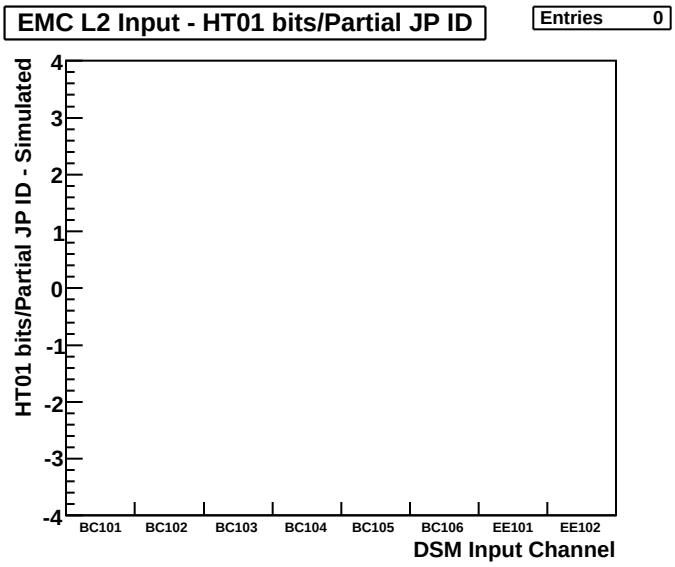

EMC L2 Input - JPZ/JPC bits
Entries 6158504
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1029 812 789 933 1091 1209 468 545
2916 2385 2412 3098 3431 3695 1111 1366
5082 4612 4349 6089 7752 7231 1008 1107
760786 762004 762263 759693 757539 757678 767226 766795

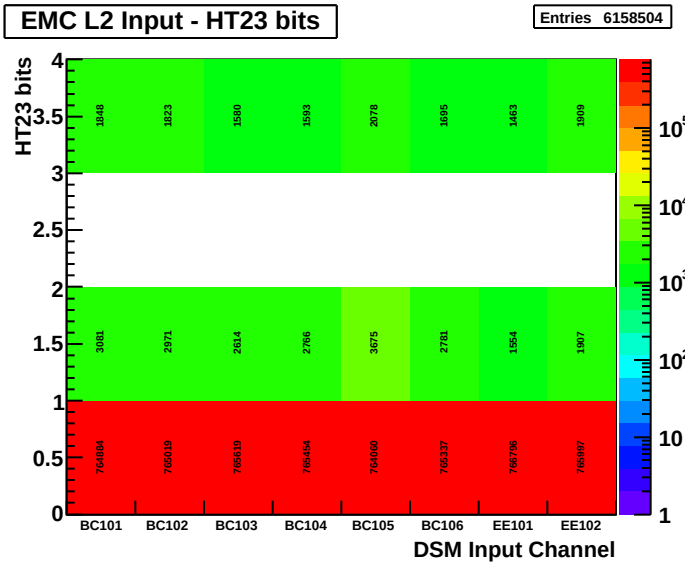

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

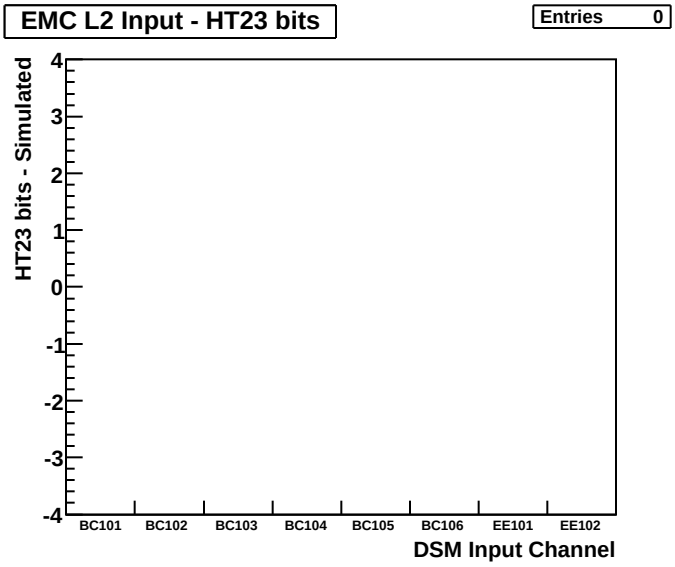

EMC L2 Input - Partial JP Sum
Entries 6158504
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

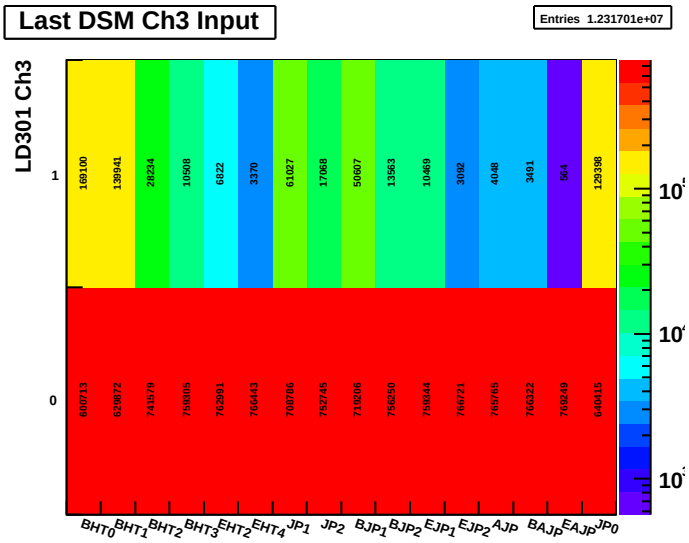

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

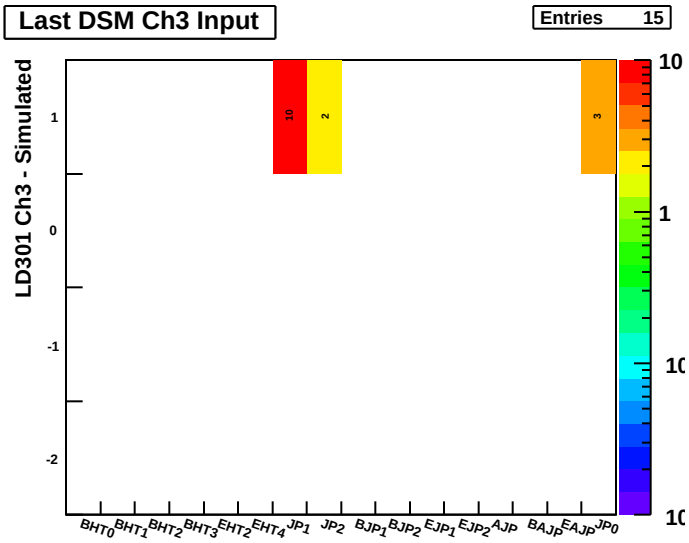

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6158504
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

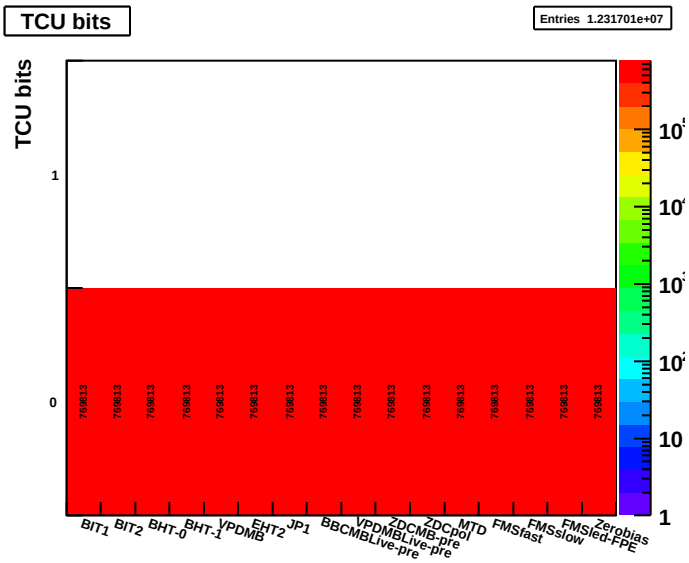

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

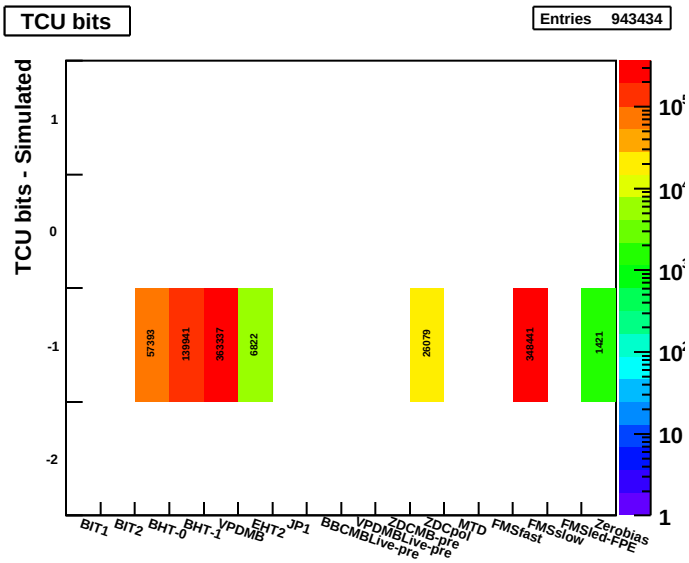

EMC L2 Input - HT23 bits
Entries 6158504
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

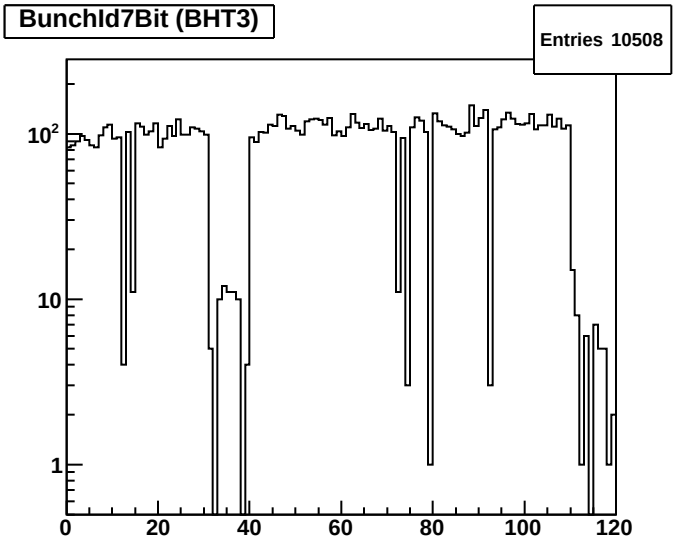

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

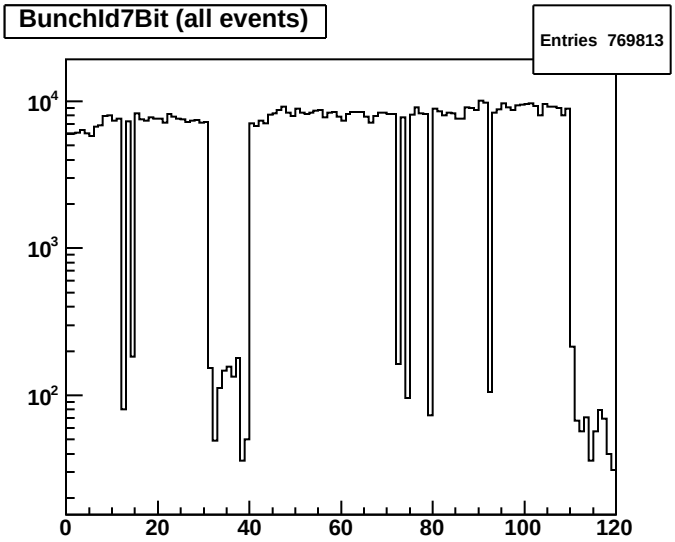

Last DSM Ch3 Input
Entries 1.231701e+07
LD301 Ch3
169100 139941 28234 10508 6822 3370 61027 17068 50607 13563 10469 3092 4048 3491 564 129398
600713 629872 741579 759305 762991 766443 708786 752745 719206 756250 759344 766721 765765 766322 768249 640415
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

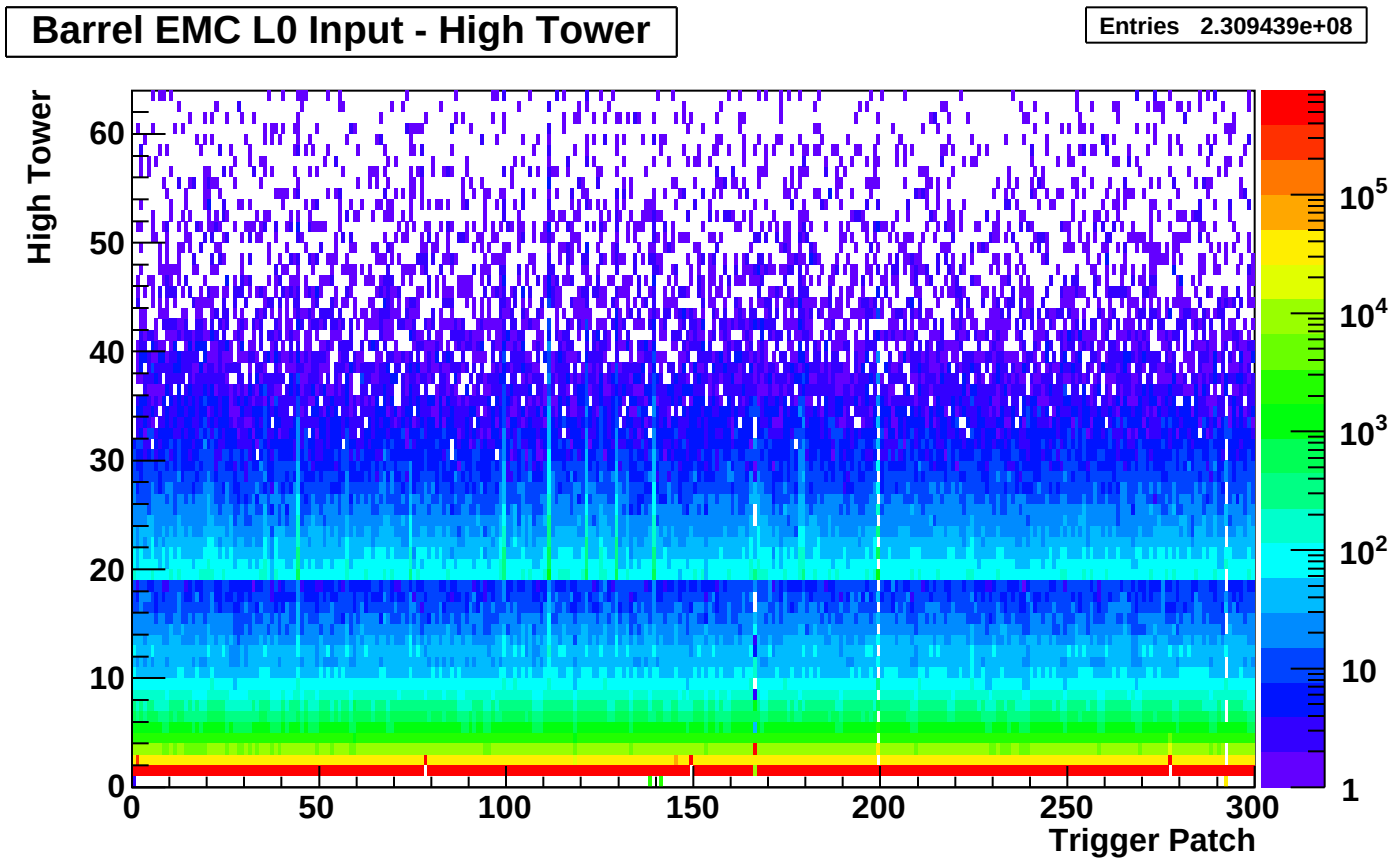

Last DSM Ch3 Input
Entries 15
LD301 Ch3 - Simulated
10 2 3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

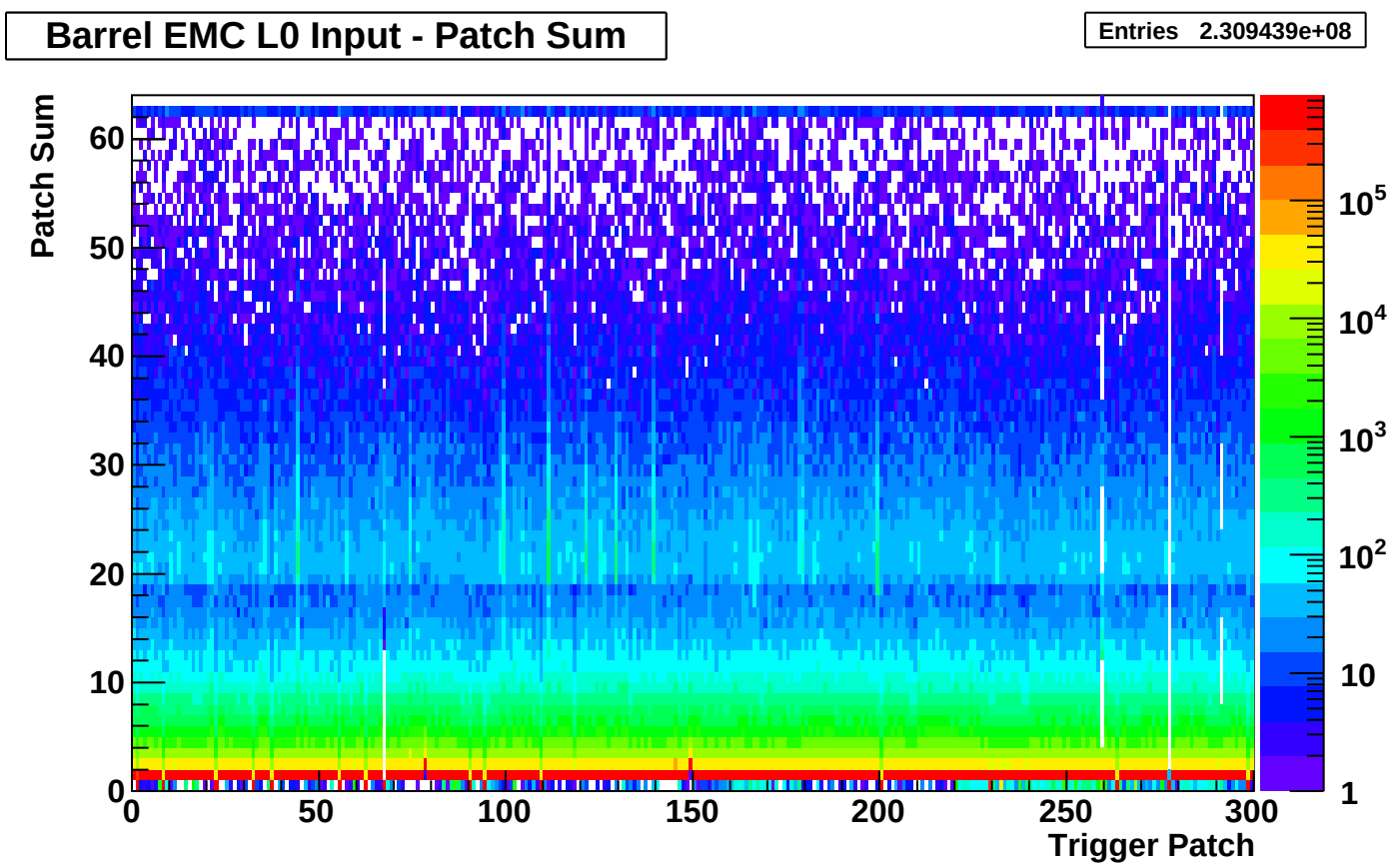

TCU bits
Entries 1.231701e+07
TCU bits
769813 769813 769813 769813 769813 769813 769813 769813 769813 769813 769813 769813 769813 769813 769813 769813
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

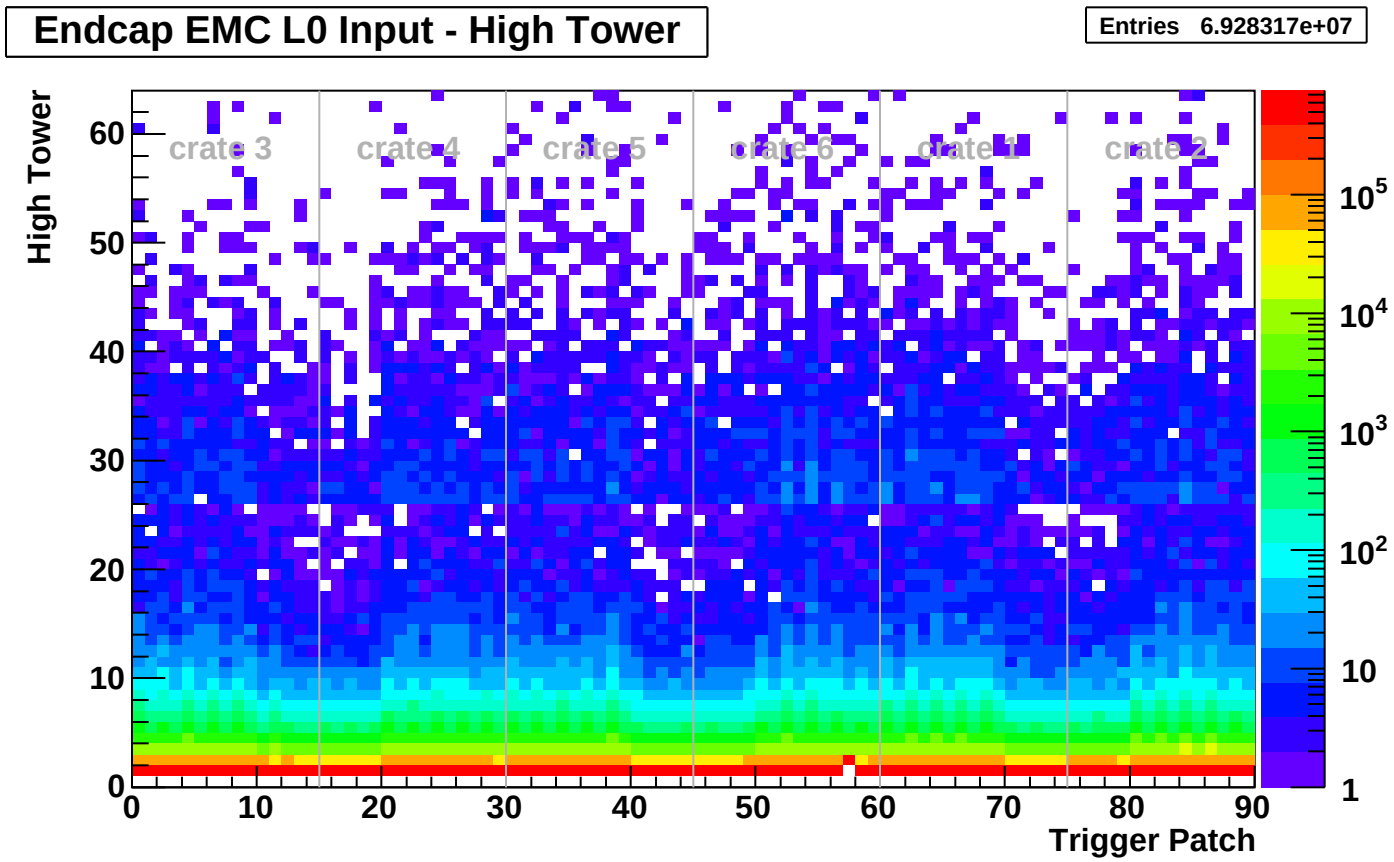

TCU bits
Entries 943434
TCU bits - Simulated
57393 139941 363337 6822 26079 348441 1421
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

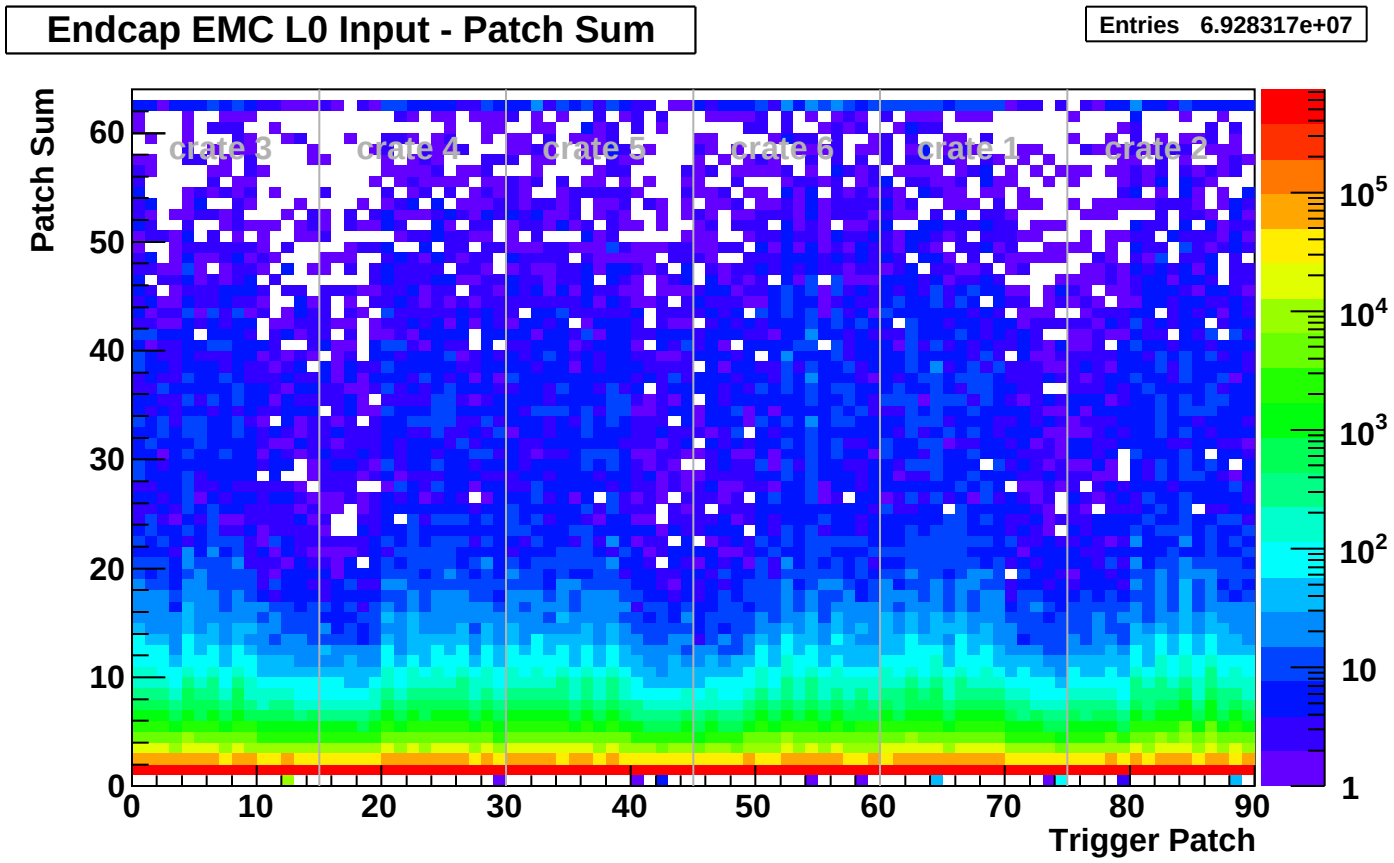

BunchId7Bit (BHT3)
Entries 10508

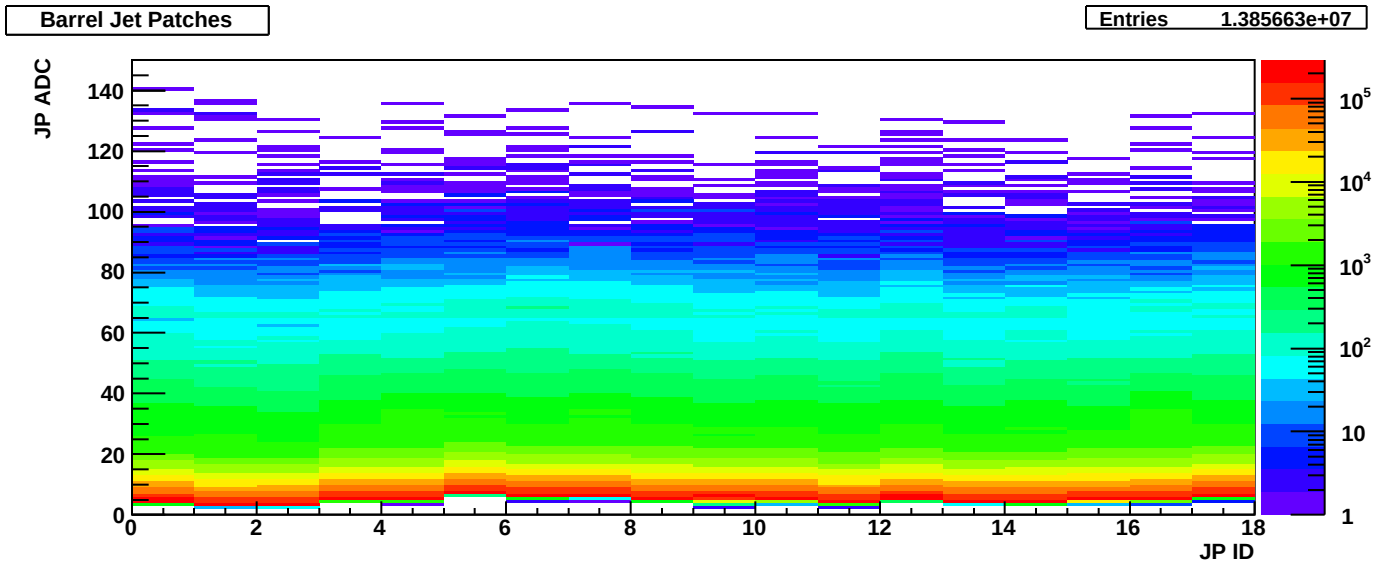

BunchId7Bit (all events)
Entries 769813

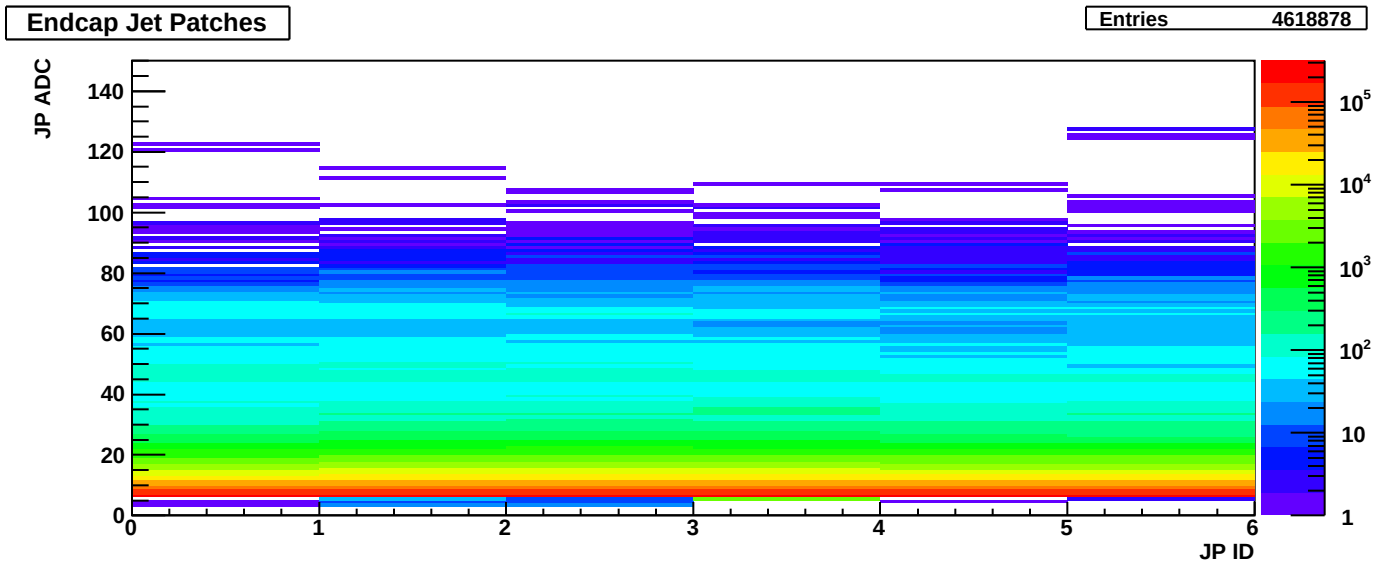

Barrel EMC L0 Input - High Tower
Entries 2.309439e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

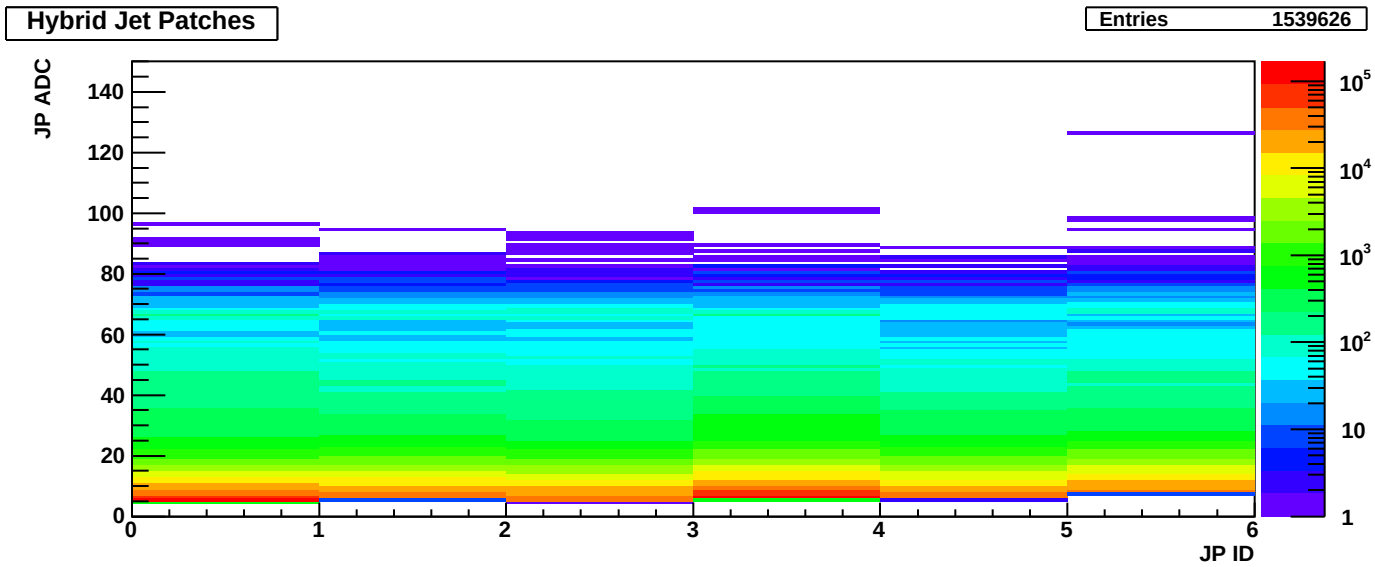

Barrel EMC L0 Input - Patch Sum
Entries 2.309439e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

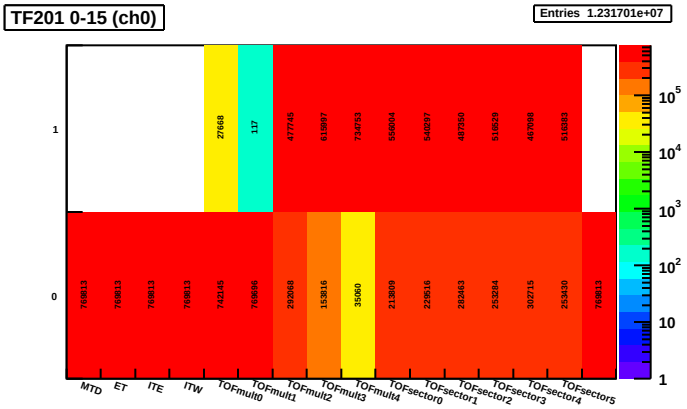

Endcap EMC L0 Input - High Tower
Entries 6.928317e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

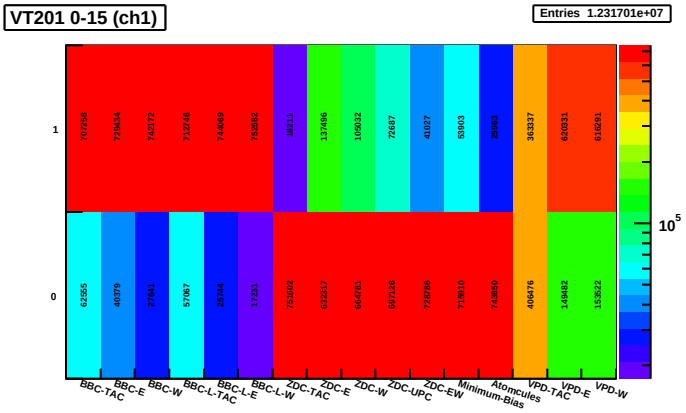

Endcap EMC L0 Input - Patch Sum
Entries 6.928317e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

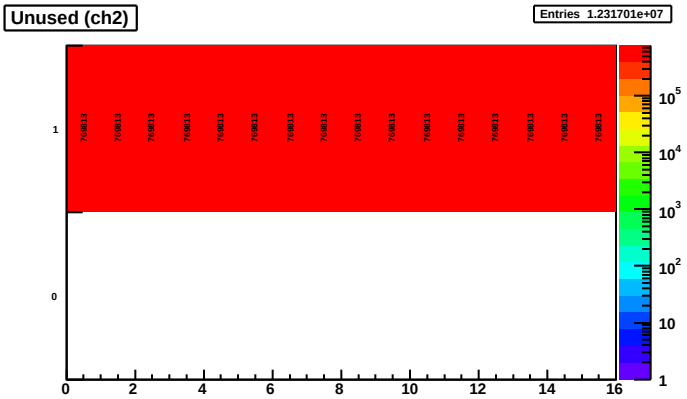

Barrel Jet Patches
Entries 1.385663e+07
JP ADC
JP ID

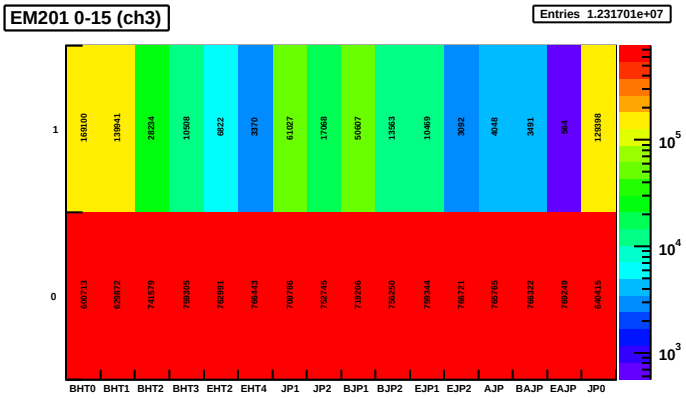

Endcap Jet Patches
Entries 4618878
JP ADC
JP ID

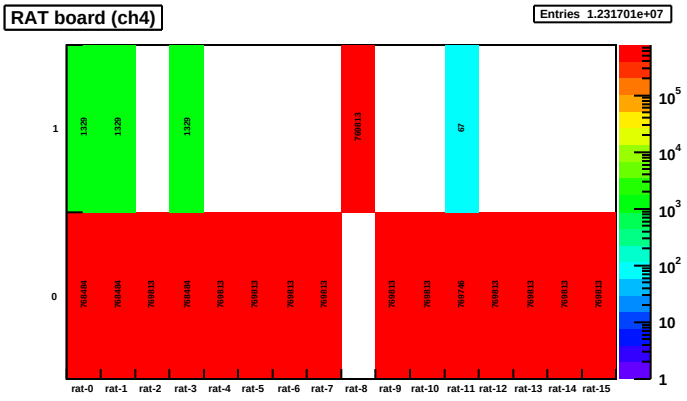

Hybrid Jet Patches
Entries 1539626
JP ADC
JP ID

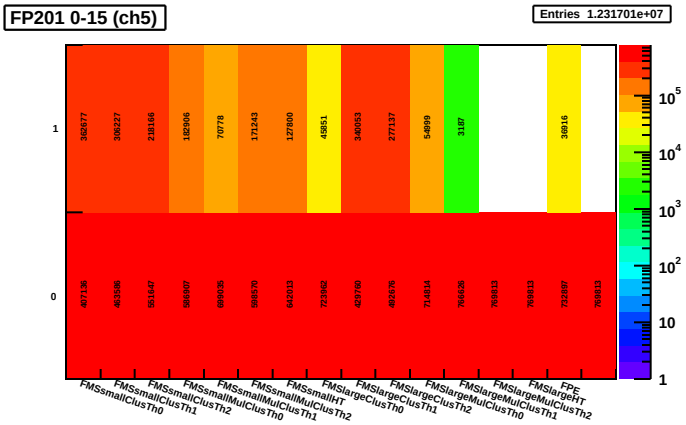

TF201 0-15 (ch0)
Entries 1.231701e+07
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

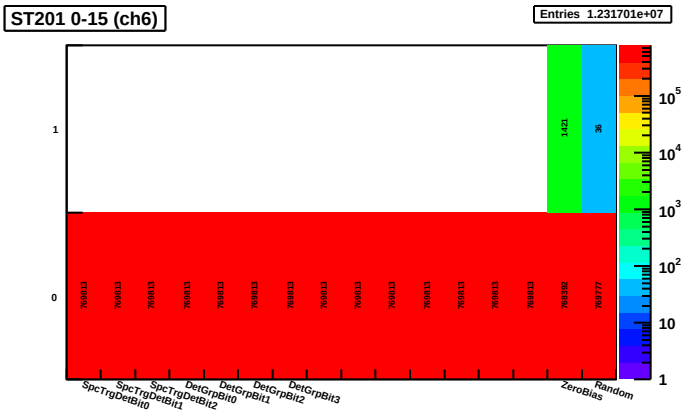

VT201 0-15 (ch1)
Entries 1.231701e+07
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

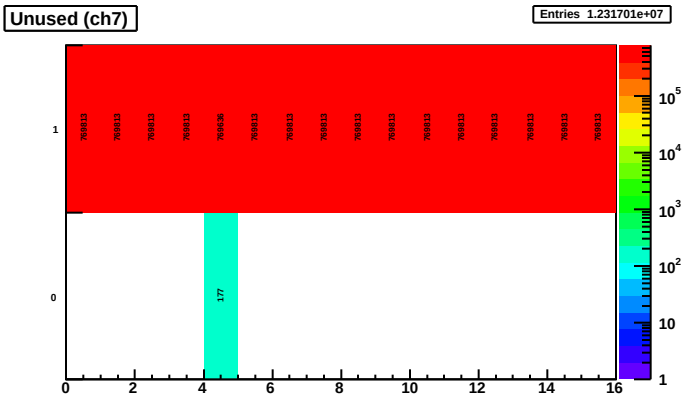

Unused (ch2)
Entries 1.231701e+07

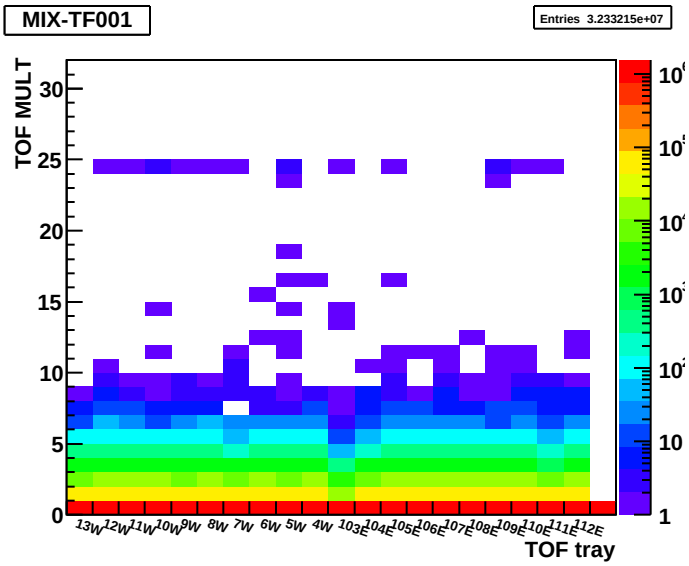

EM201 0-15 (ch3)
Entries 1.231701e+07
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

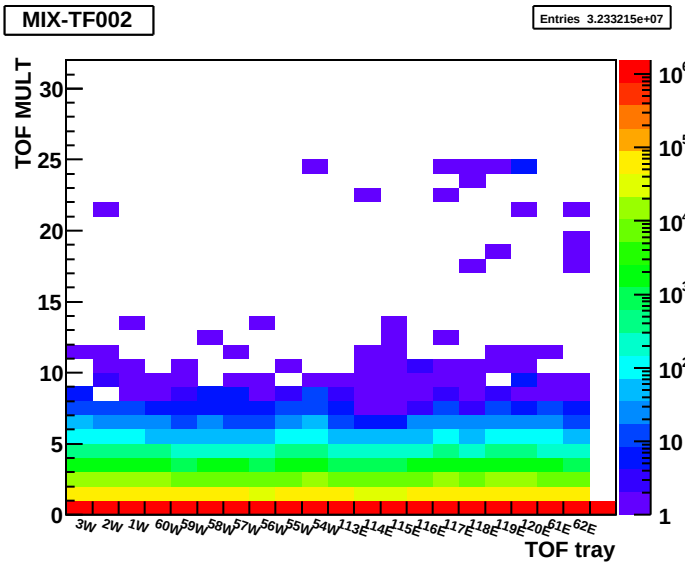

RAT board (ch4)
Entries 1.231701e+07
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

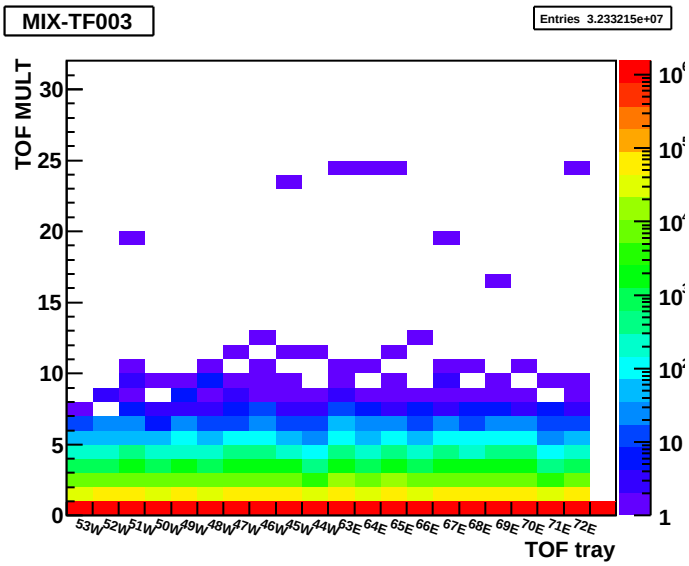

FP201 0-15 (ch5)
Entries 1.231701e+07
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

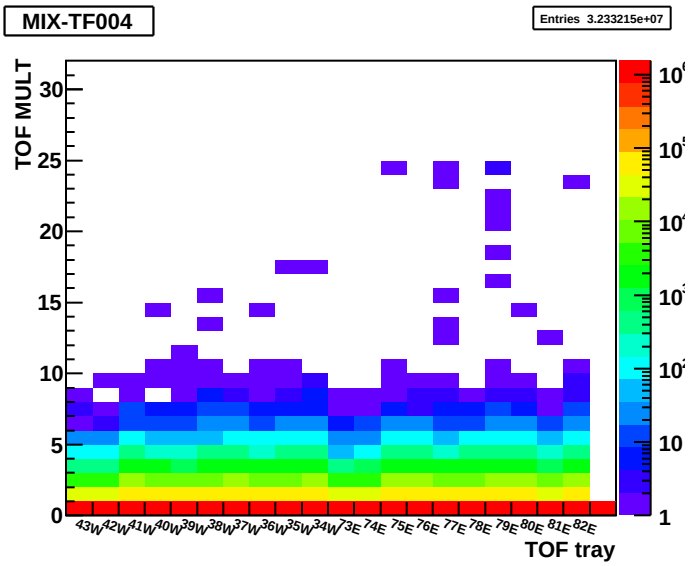

ST201 0-15 (ch6)
Entries 1.231701e+07
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

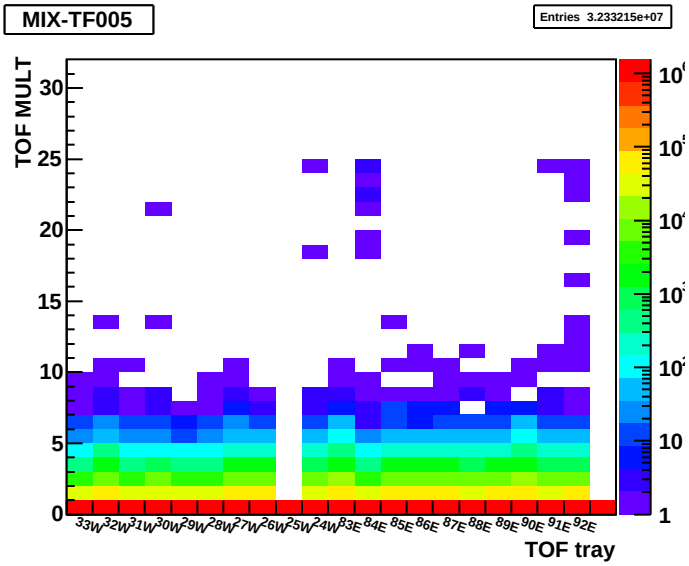

Unused (ch7)
Entries 1.231701e+07

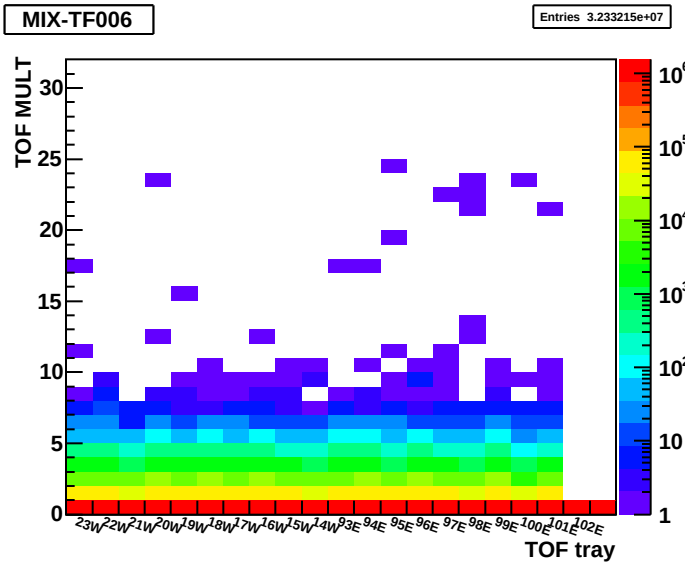

MIX-TF001
Entries 3.233215e+07
TOF MULT
TOF tray

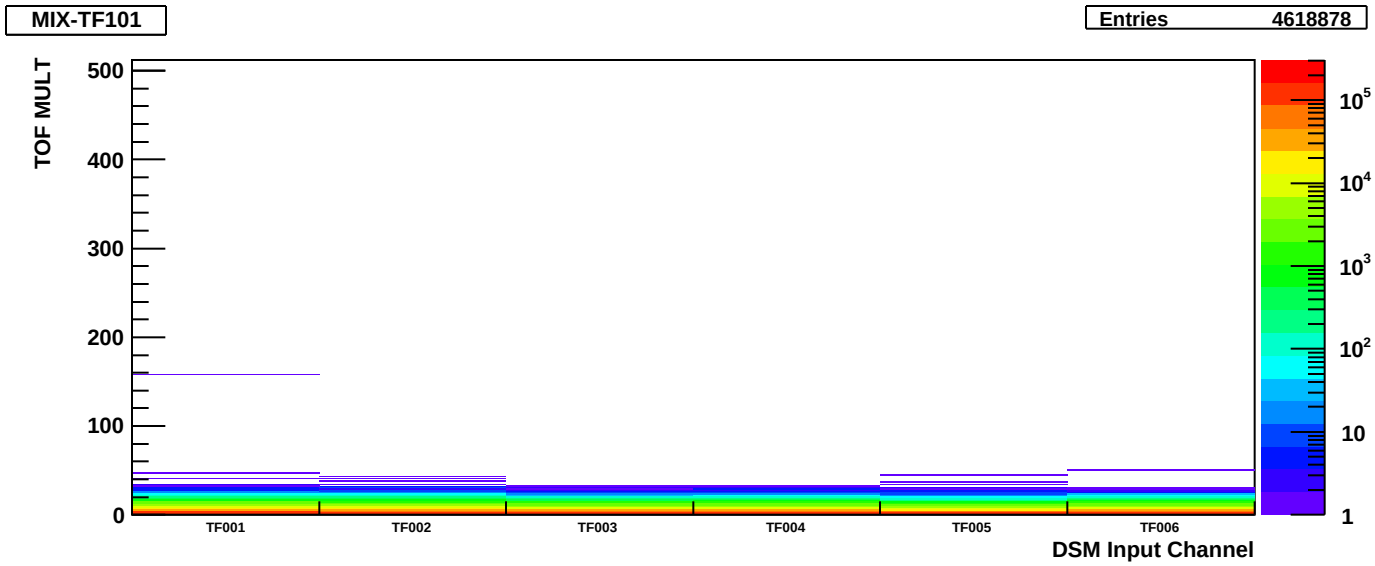

MIX-TF002
Entries 3.233215e+07
TOF MULT
TOF tray

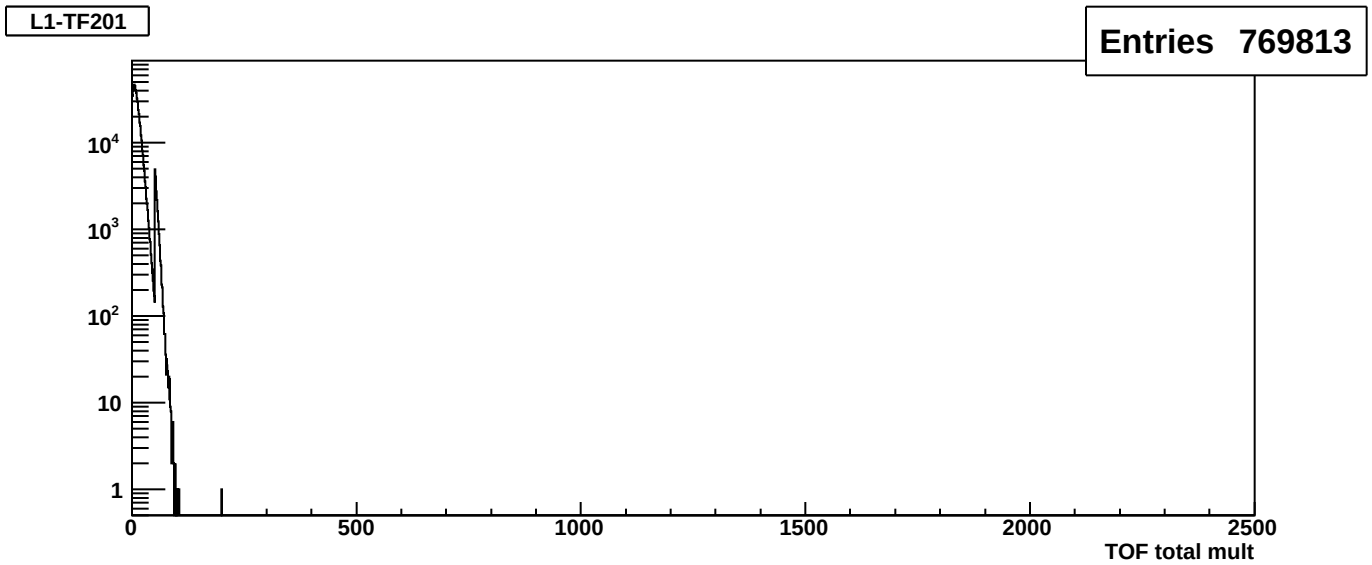

MIX-TF003
Entries 3.233215e+07
TOF MULT
TOF tray

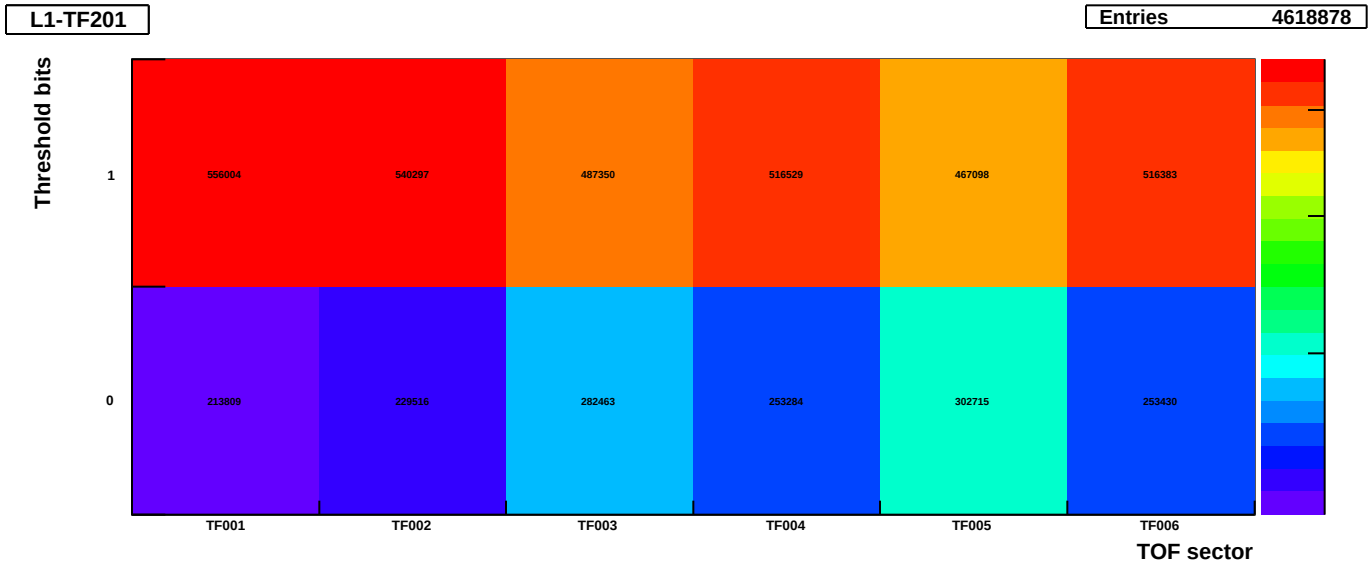

MIX-TF004
Entries 3.233215e+07
TOF MULT
TOF tray

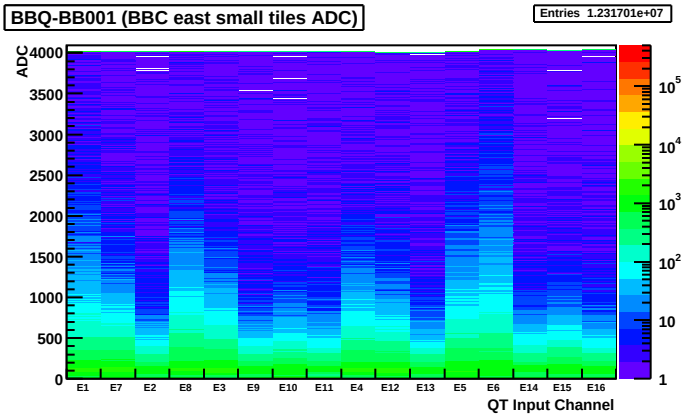

MIX-TF005
Entries 3.233215e+07
TOF MULT
TOF tray

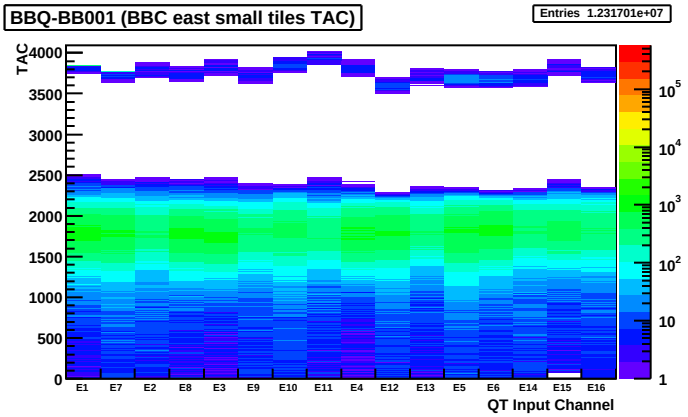

MIX-TF006
Entries 3.233215e+07
TOF MULT
TOF tray

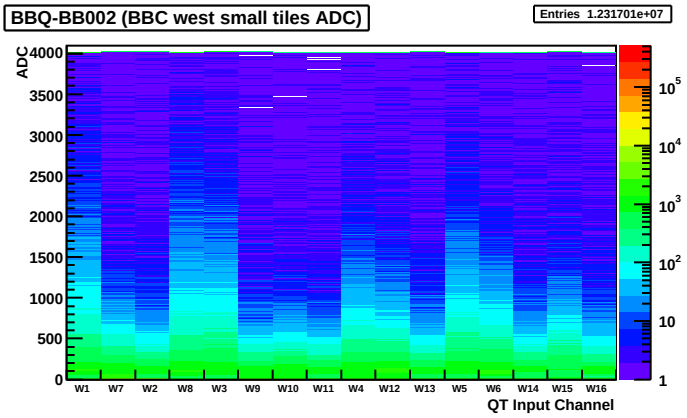

MIX-TF101
Entries 4618878
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

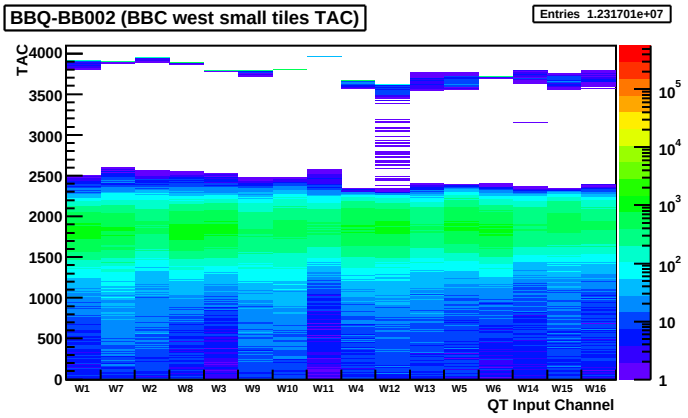

L1-TF201
Entries 769813
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

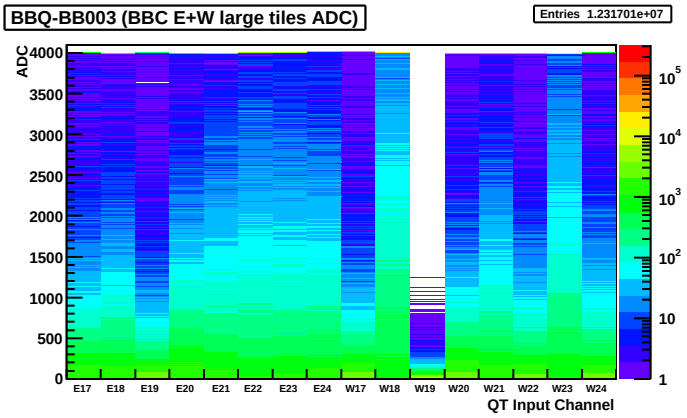

L1-TF201
Entries 4618878
Threshold bits
1
0
556004
540297
487350
516529
467098
516383
213809
229516
282463
253284
302715
253430
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

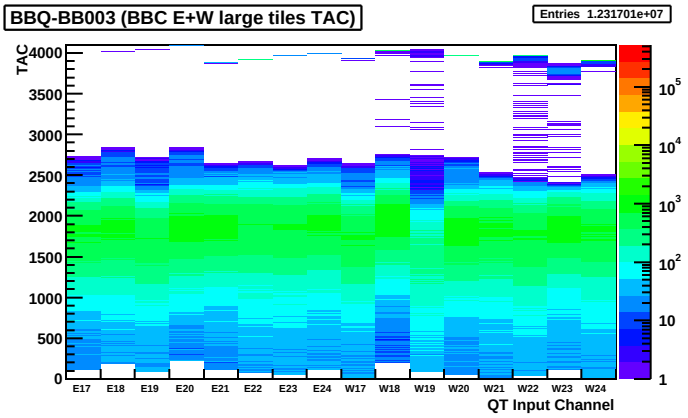

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.231701e+07
ADC
QT Input Channel

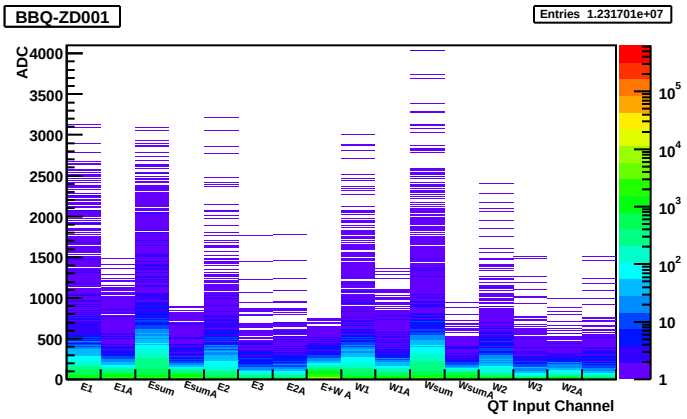

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.231701e+07
TAC
QT Input Channel

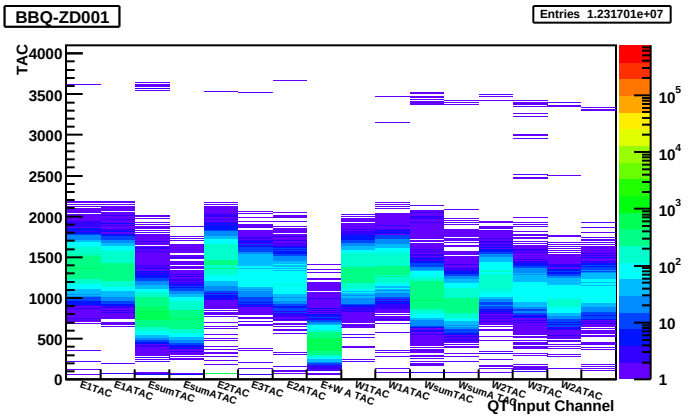

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.231701e+07
ADC
QT Input Channel

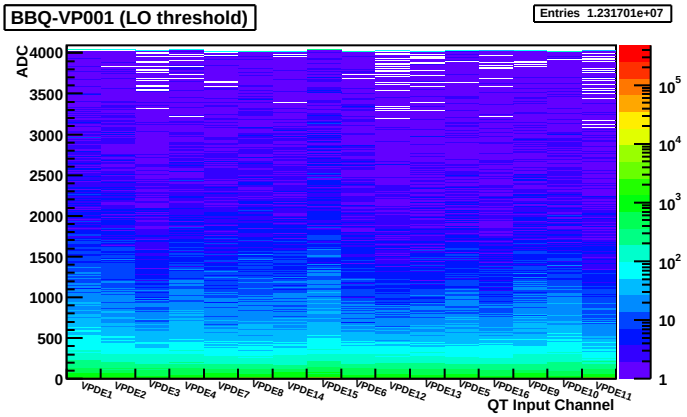

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.231701e+07
TAC
QT Input Channel

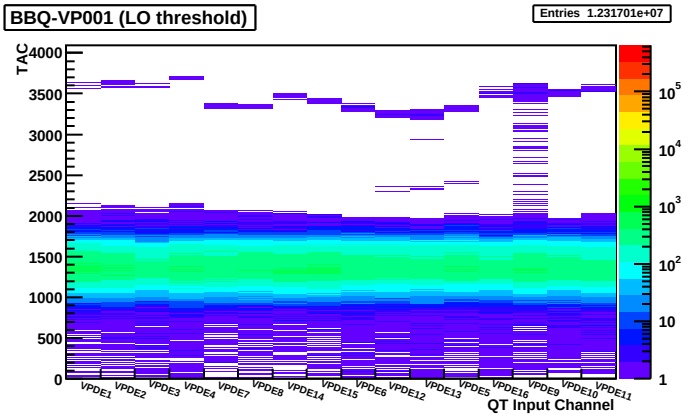

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.231701e+07
ADC
QT Input Channel

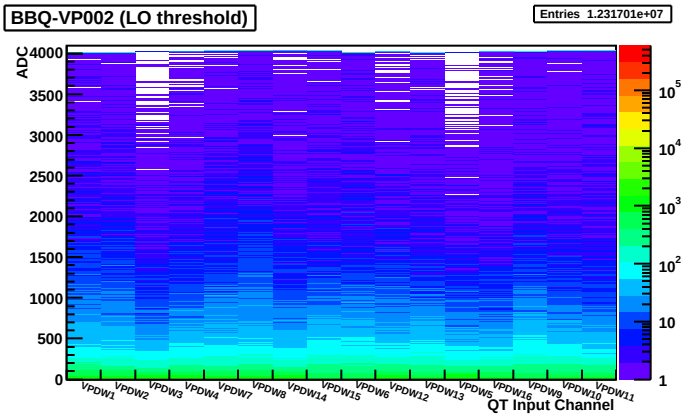

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.231701e+07
TAC
QT Input Channel

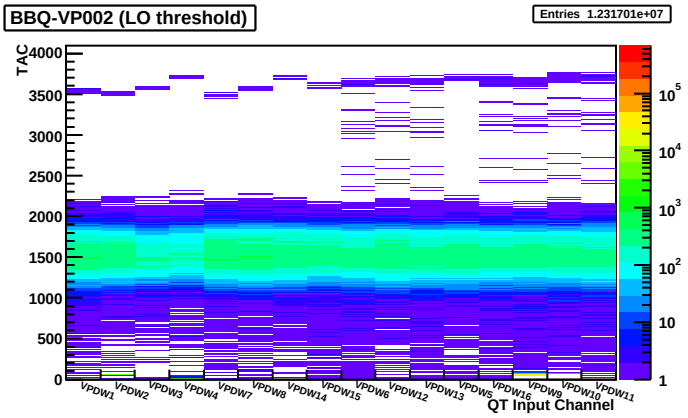

BBQ-ZD001
Entries 1.231701e+07
ADC
QT Input Channel

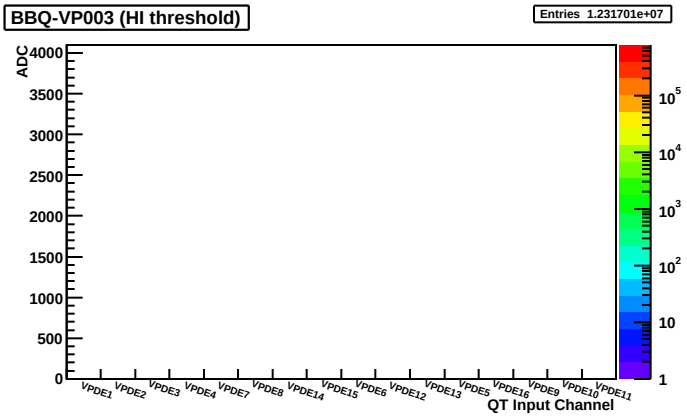

BBQ-ZD001
Entries 1.231701e+07
TAC
QT Input Channel

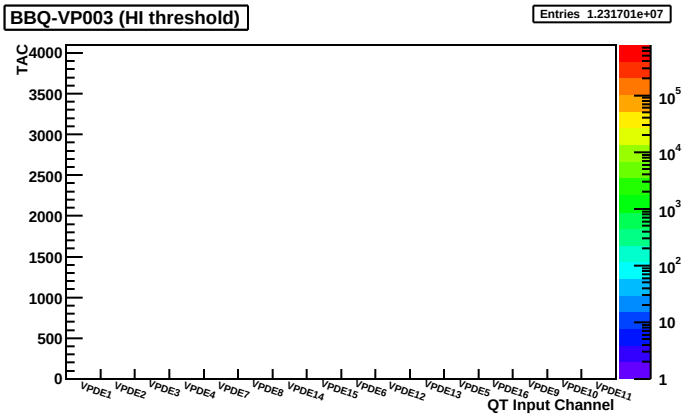

BBQ-VP001 (LO threshold)
Entries 1.231701e+07
ADC
QT Input Channel

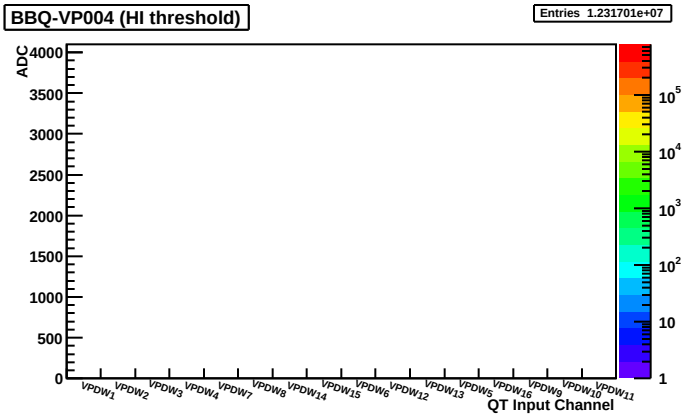

BBQ-VP001 (LO threshold)
Entries 1.231701e+07
TAC
QT Input Channel

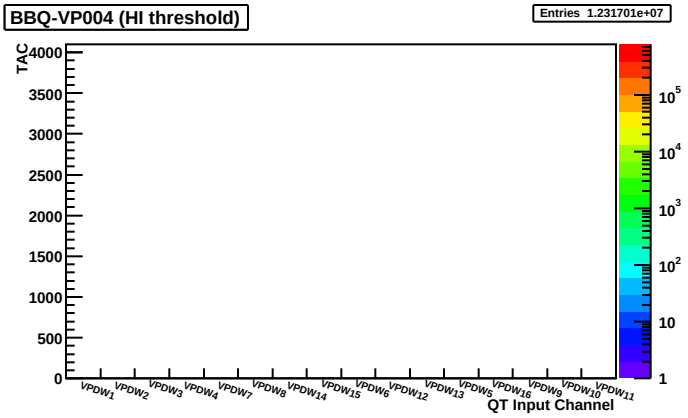

BBQ-VP002 (LO threshold)
Entries 1.231701e+07
ADC
QT Input Channel

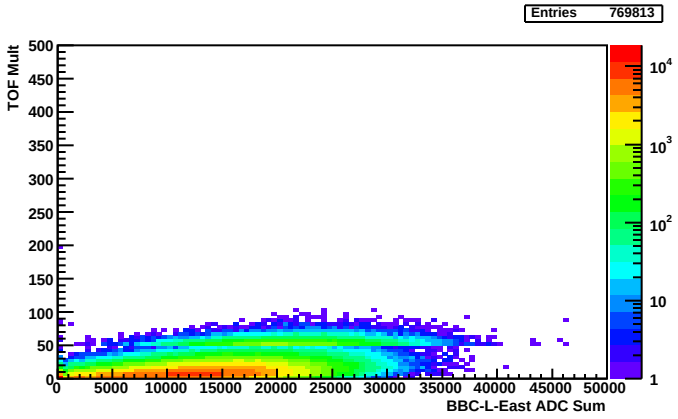

BBQ-VP002 (LO threshold)
Entries 1.231701e+07
TAC
QT Input Channel

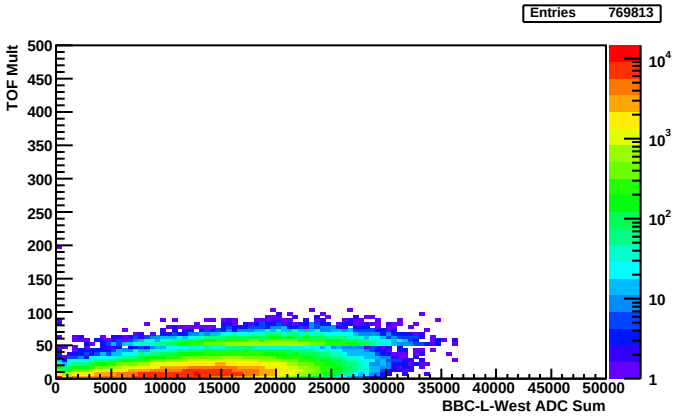

BBQ-VP003 (HI threshold)
Entries 1.231701e+07
ADC
QT Input Channel

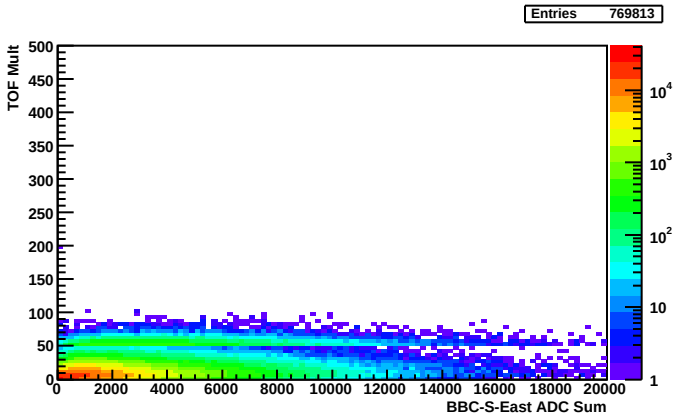

BBQ-VP003 (HI threshold)
Entries 1.231701e+07
TAC
QT Input Channel

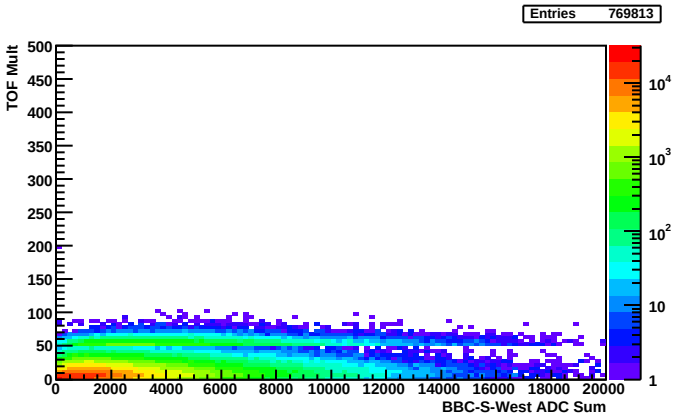

BBQ-VP004 (HI threshold)
Entries 1.231701e+07
ADC
QT Input Channel

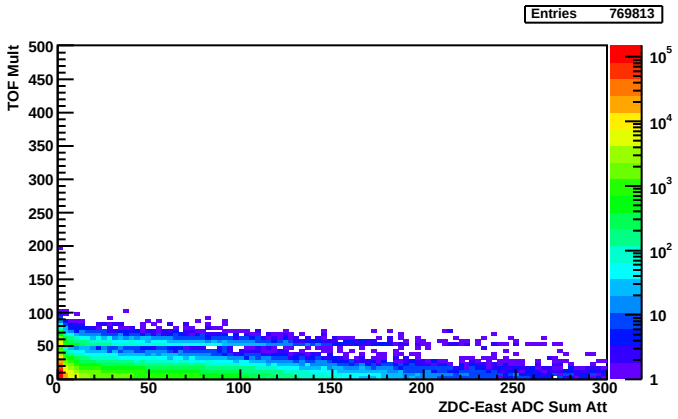

BBQ-VP004 (HI threshold)
Entries 1.231701e+07
TAC
QT Input Channel

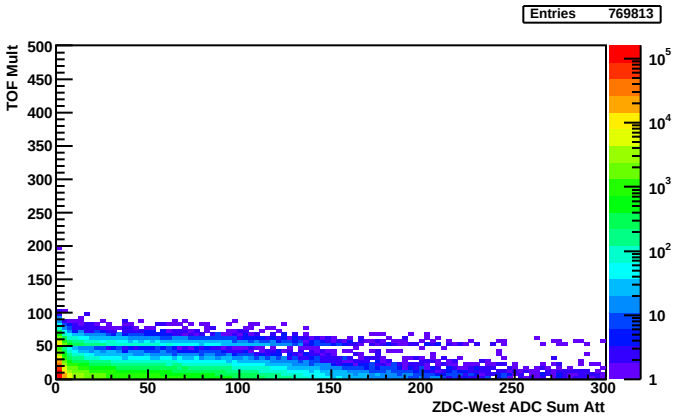

Entries 769813
TOF Mult
BBC-L-East ADC Sum

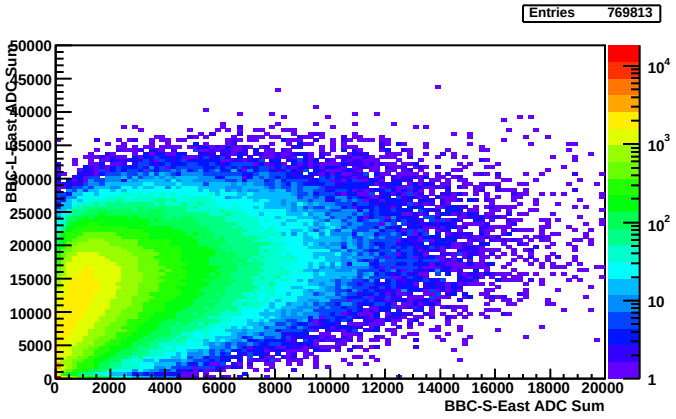

Entries 769813
TOF Mult
BBC-L-West ADC Sum

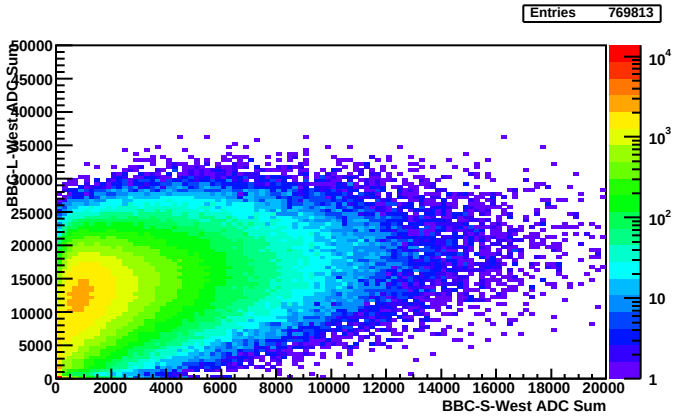

Entries 769813
TOF Mult
BBC-S-East ADC Sum

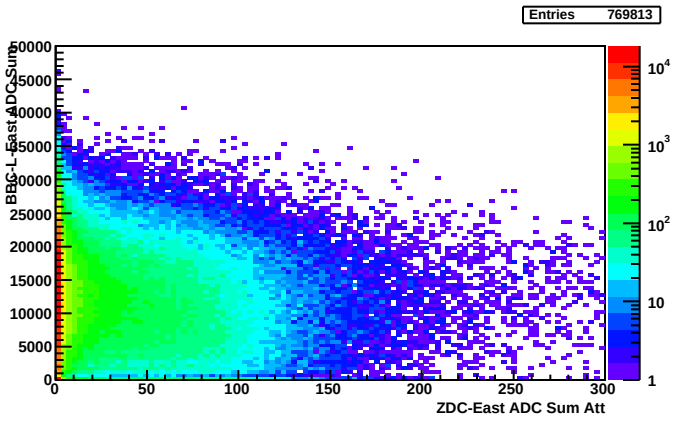

Entries 769813
TOF Mult
BBC-S-West ADC Sum

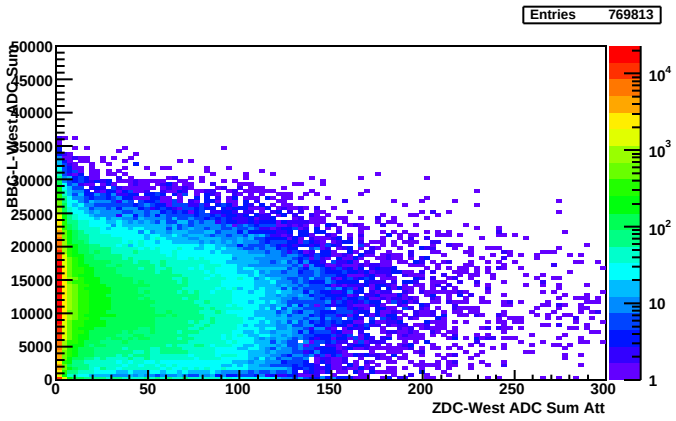

Entries 769813
TOF Mult
ZDC-East ADC Sum Att

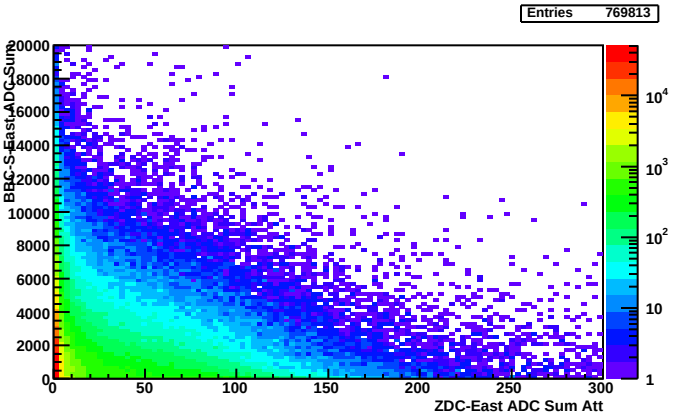

Entries 769813
TOF Mult
ZDC-West ADC Sum Att

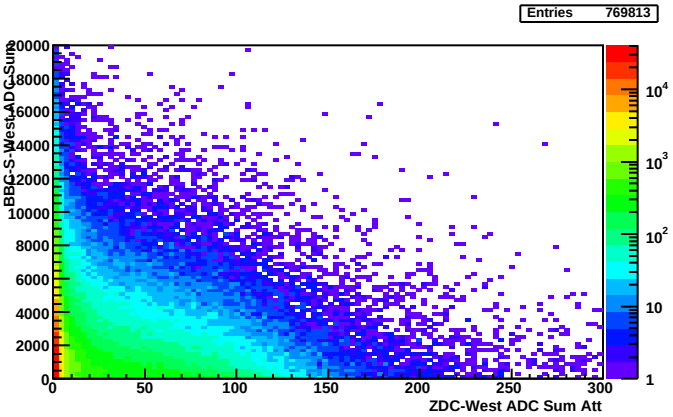

Entries 769813
BBC-L-East ADC Sum
BBC-S-East ADC Sum

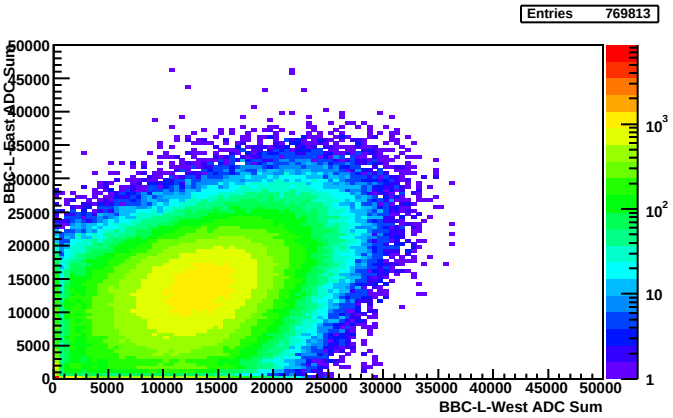

Entries 769813
BBC-L-West ADC Sum
BBC-S-West ADC Sum

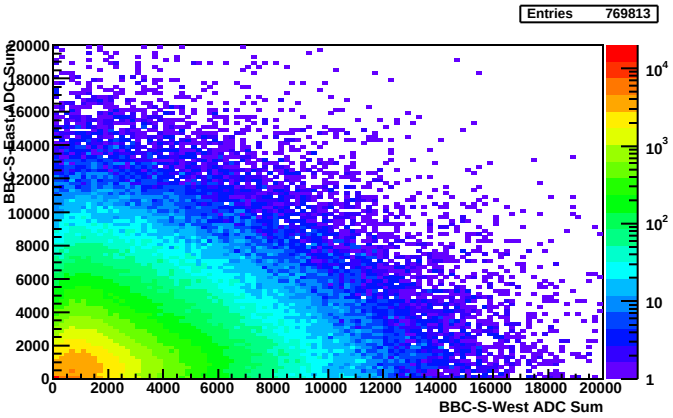

Entries 769813
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

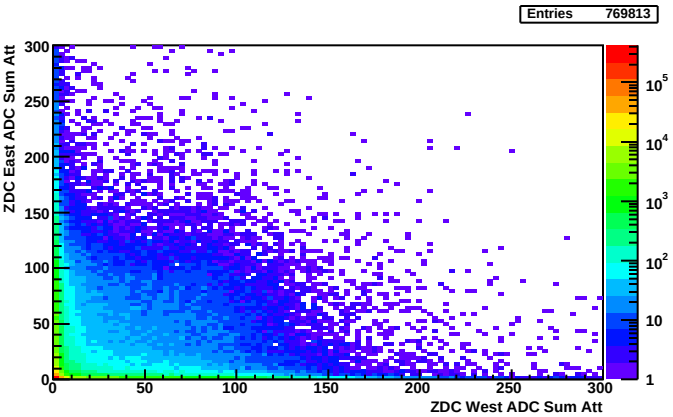

Entries 769813
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

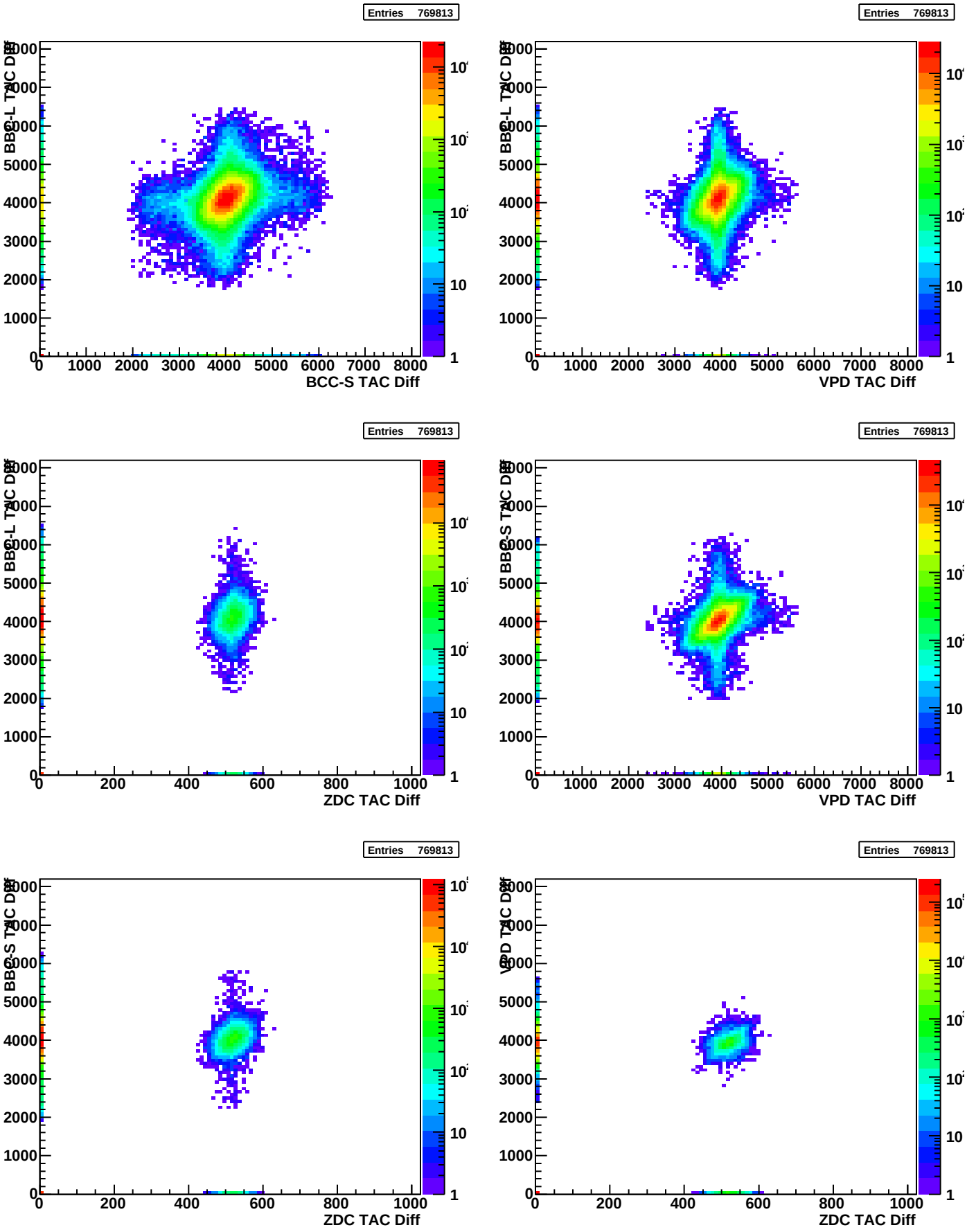

Entries 769813
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

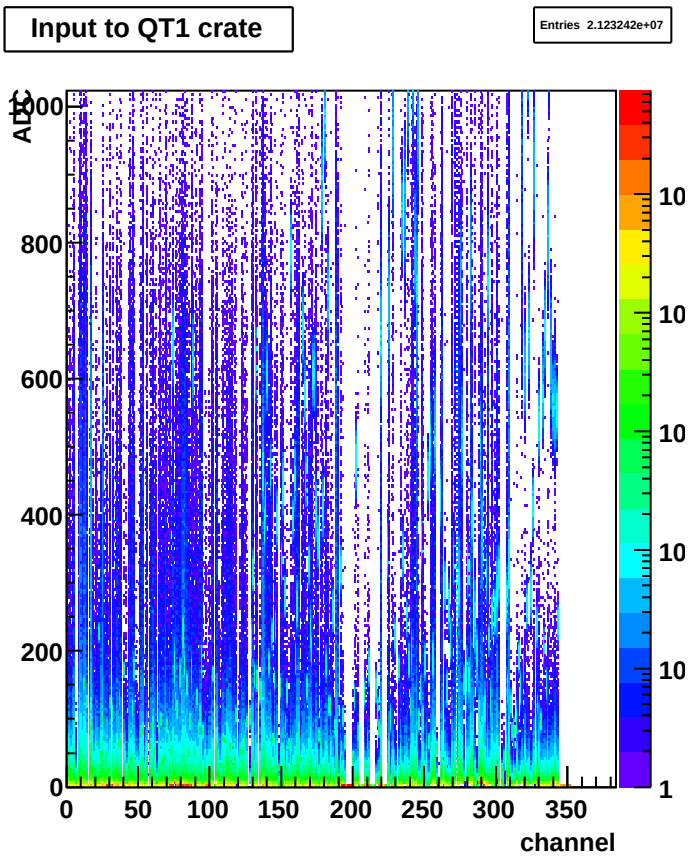

Entries 769813
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

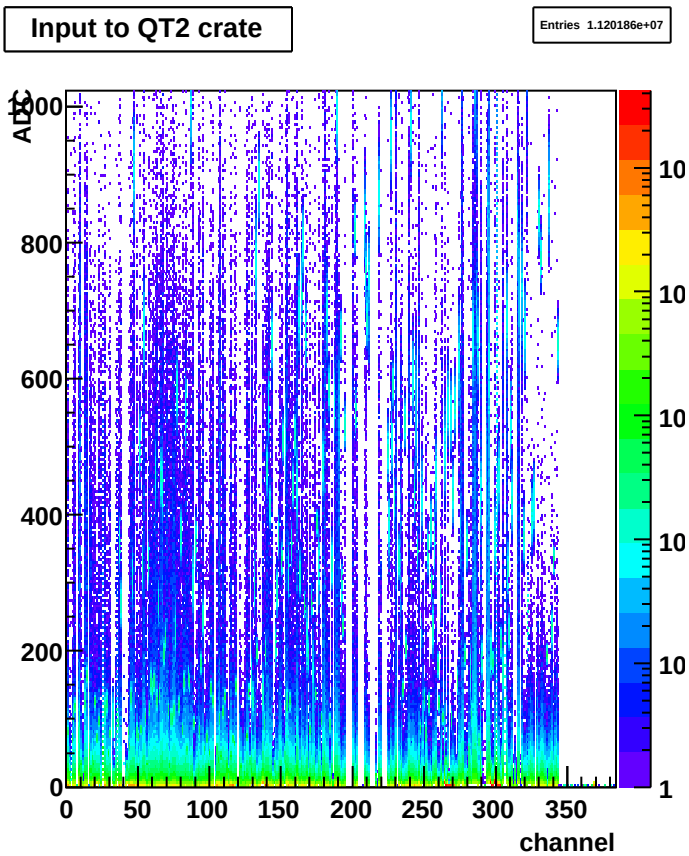

Entries 769813
BBC-L-East ADC Sum
BBC-L-West ADC Sum

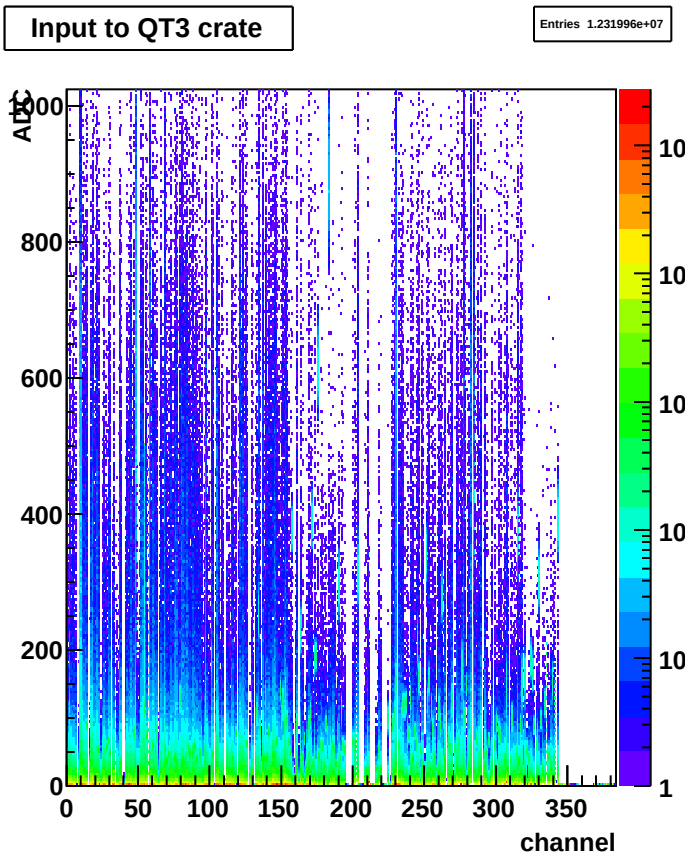

Entries 769813
BBC-S-East ADC Sum
BBC-S-West ADC Sum

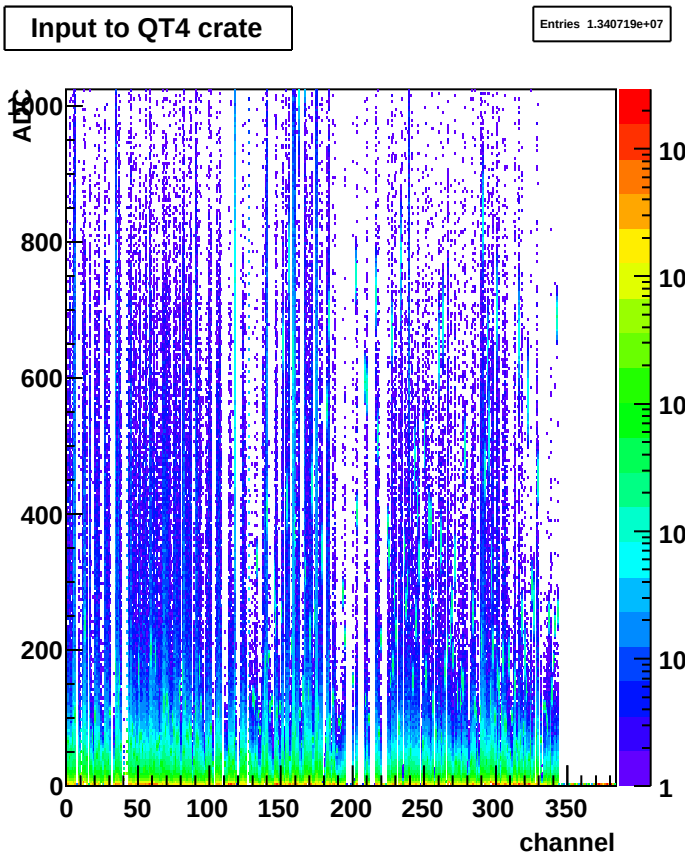

Entries 769813
ZDC East ADC Sum Att
ZDC West ADC Sum Att

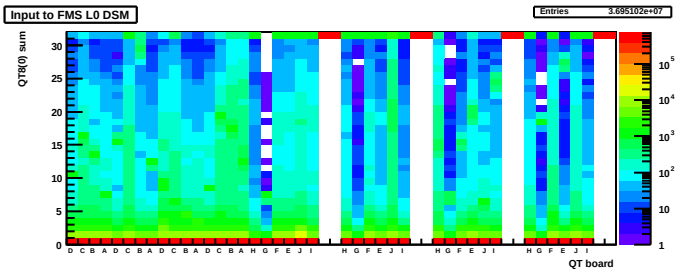

Entries 769813
BBC-L TAC Diff
BCC-S TAC Diff
Entries 769813
BBC-L TAC Diff
VPD TAC Diff
Entries 769813
BBC-L TAC Diff
ZDC TAC Diff
Entries 769813
BBC-S TAC Diff
VPD TAC Diff
Entries 769813
BBC-S TAC Diff
ZDC TAC Diff
Entries 769813
VPD TAC Diff
ZDC TAC Diff

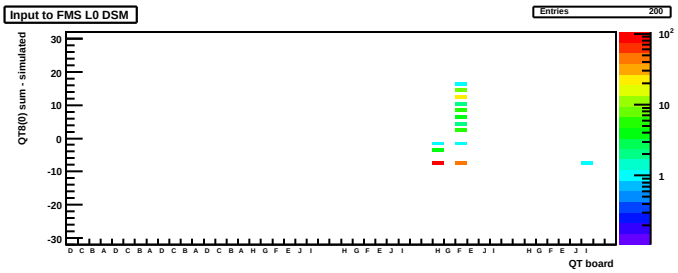

Input to QT1 crate
Entries 2.123242e+07
ADC
channel

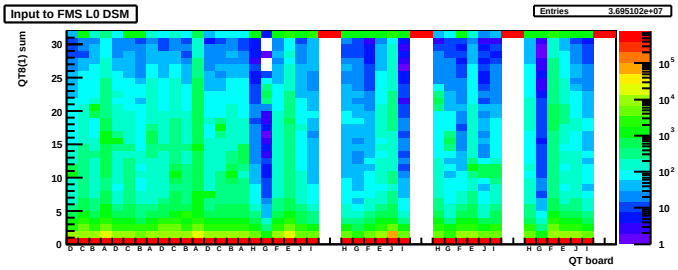

Input to QT2 crate
Entries 1.120186e+07
ADC
channel

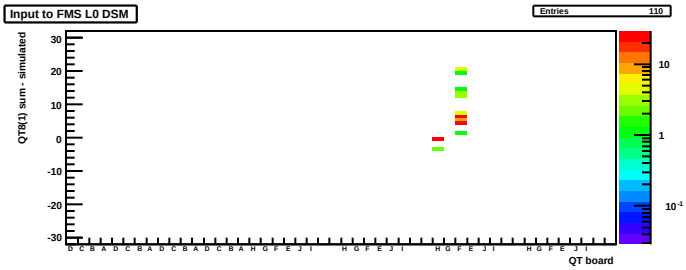

Input to QT3 crate
Entries 1.231996e+07
ADC
channel

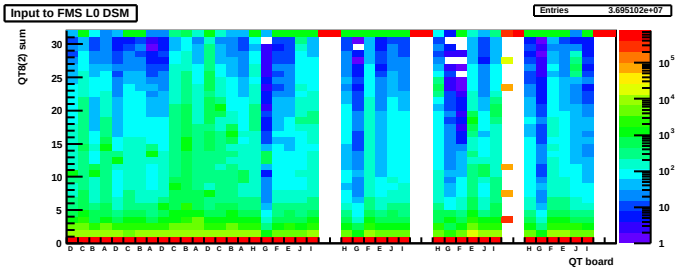

Input to QT4 crate
Entries 1.340719e+07
ADC
channel

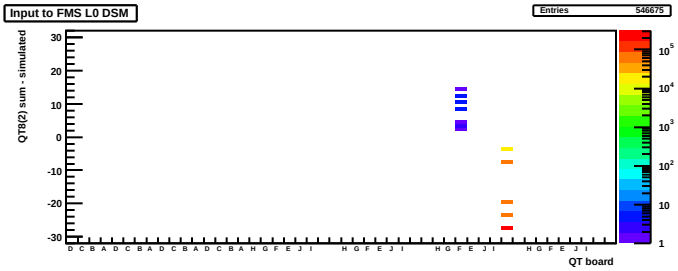

Input to FMS L0 DSM
Entries 3.695102e+07
QT8(0) sum
QT board

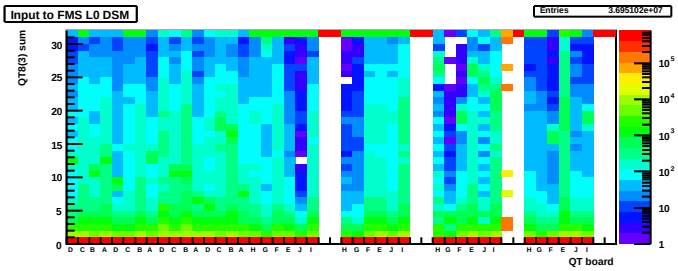

Input to FMS L0 DSM
Entries 200
QT8(0) sum - simulated
QT board

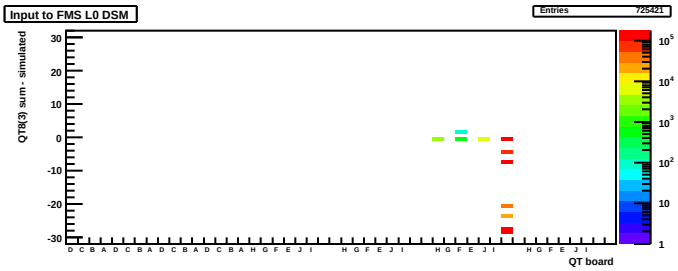

Input to FMS L0 DSM
Entries 3.695102e+07
QT8(1) sum
QT board

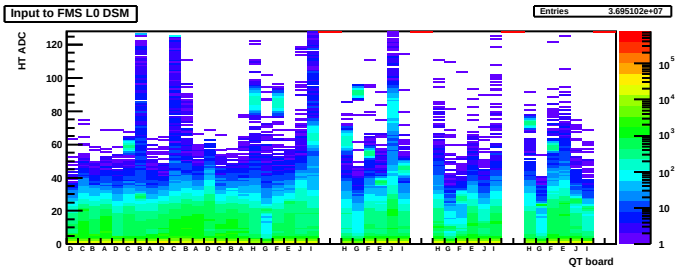

Input to FMS L0 DSM
Entries 110
QT8(1) sum - simulated
QT board

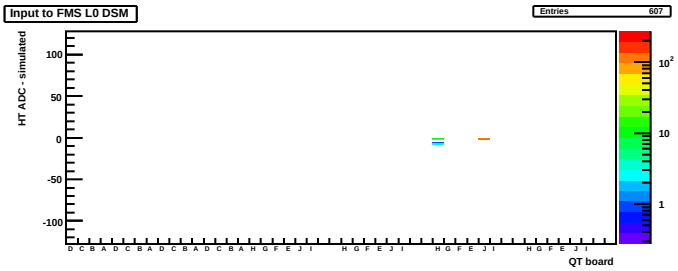

Input to FMS L0 DSM
Entries 3.695102e+07
QT8(2) sum
QT board

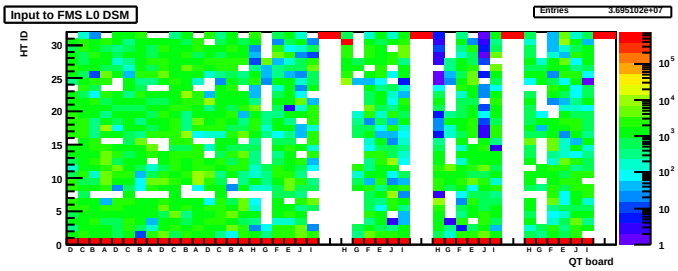

Input to FMS L0 DSM
Entries 546675
QT8(2) sum - simulated
QT board

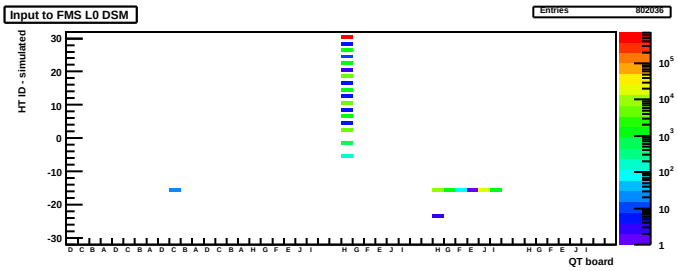

Input to FMS L0 DSM
Entries 3.695102e+07
QT8(3) sum
QT board

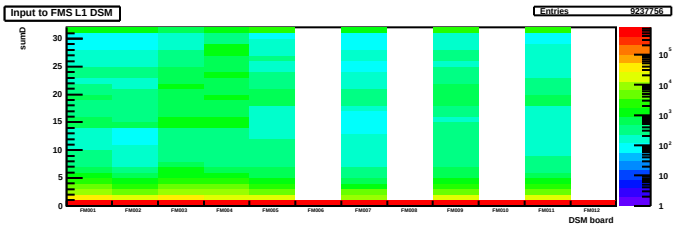

Input to FMS L0 DSM
Entries 725421
QT8(3) sum - simulated
QT board

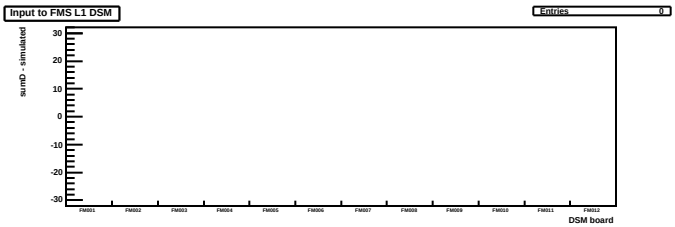

Input to FMS L0 DSM
Entries 3.695102e+07
HT ADC
QT board

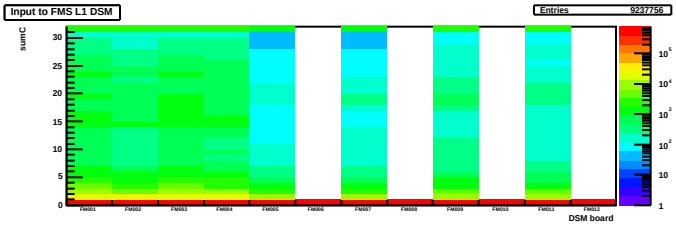

Input to FMS L0 DSM
Entries 607
HT ADC - simulated
QT board

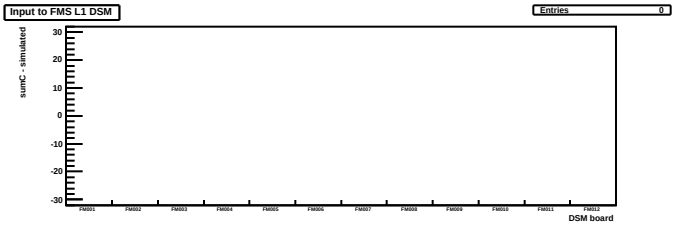

Input to FMS L0 DSM
Entries 3.695102e+07
HT ID
QT board

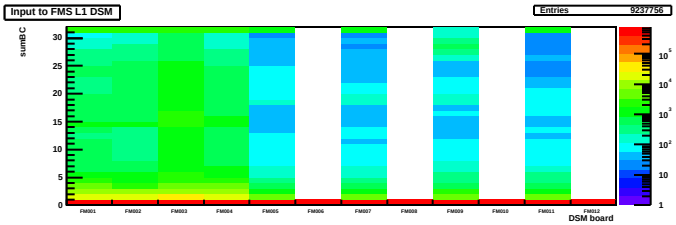

Input to FMS L0 DSM
Entries 802036
HT ID - simulated
QT board

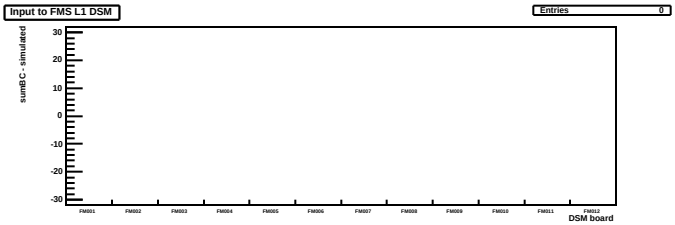

Input to FMS L1 DSM
Entries 9237756
sumD
DSM board

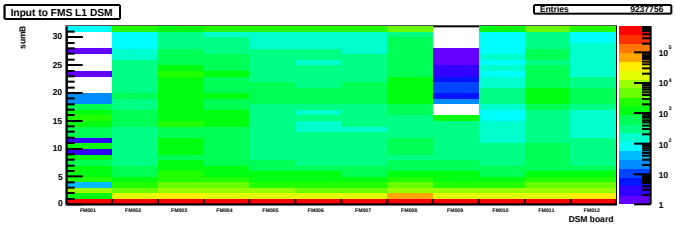

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

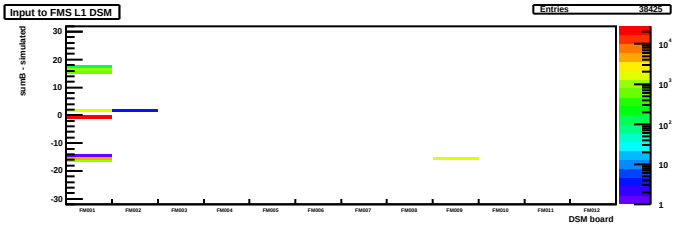

Input to FMS L1 DSM
Entries 9237756
sumC
DSM board

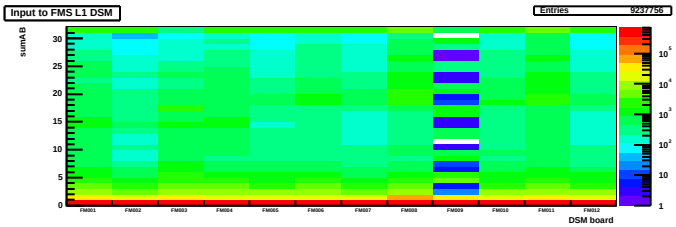

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

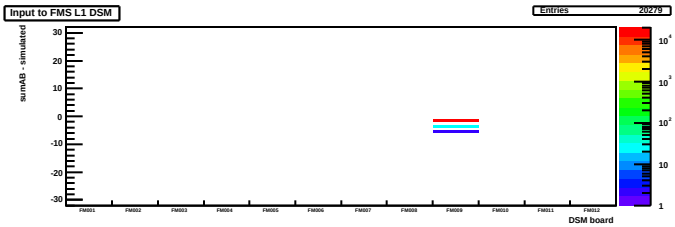

Input to FMS L1 DSM
Entries 9237756
sumBC
DSM board

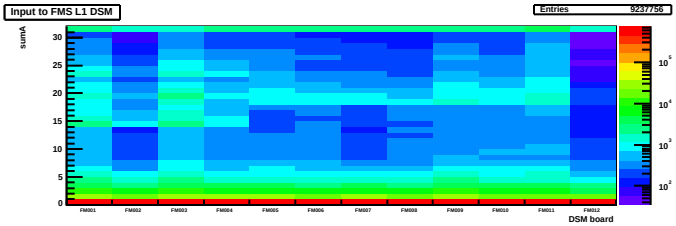

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

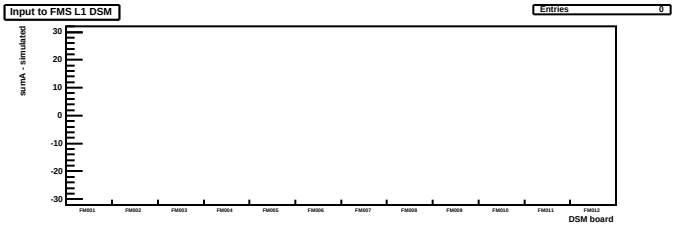

Input to FMS L1 DSM
Entries 9237756
sumB
DSM board

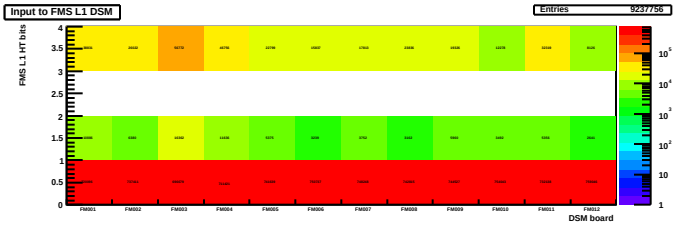

Input to FMS L1 DSM
Entries 38425
sumB - simulated
DSM board

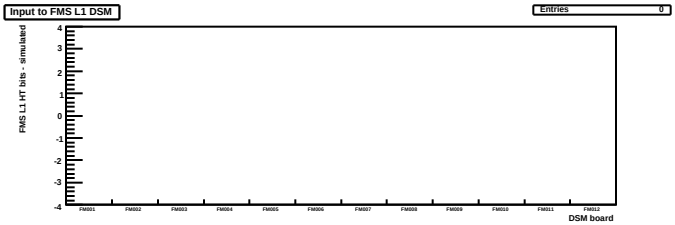

Input to FMS L1 DSM
Entries 9237756
sumAB
DSM board

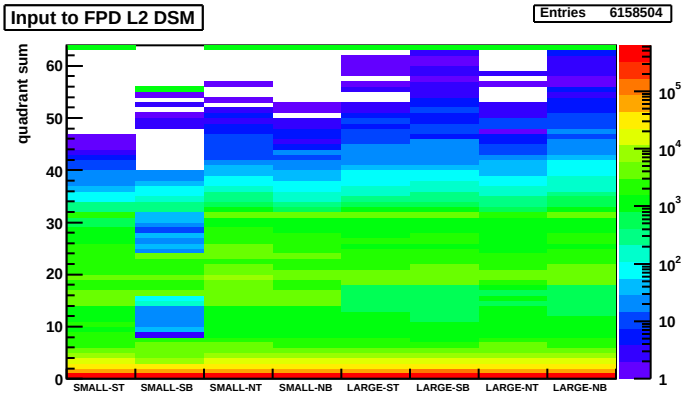

Input to FMS L1 DSM
Entries 20279
sumAB - simulated
DSM board

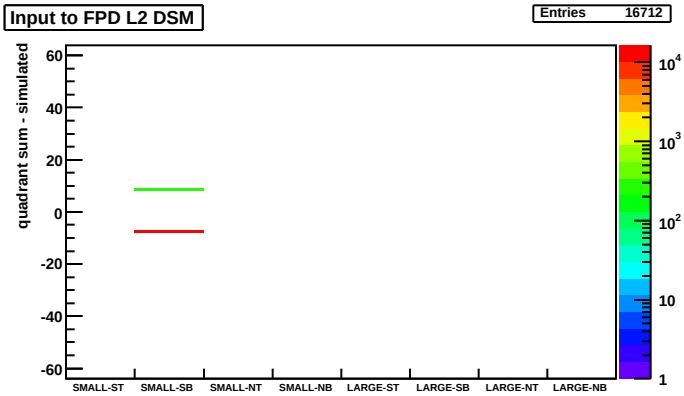

Input to FMS L1 DSM
Entries 9237756
sumA
DSM board

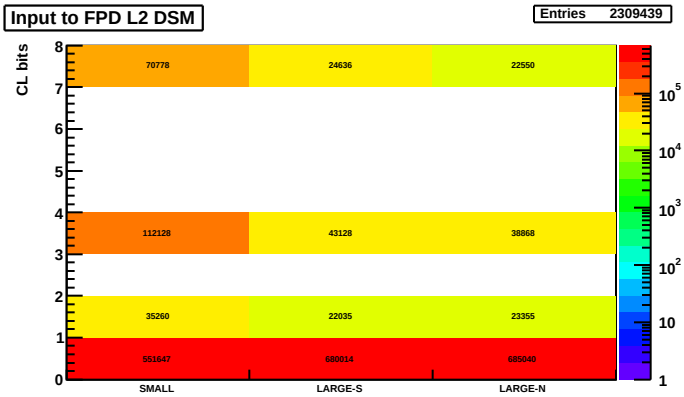

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

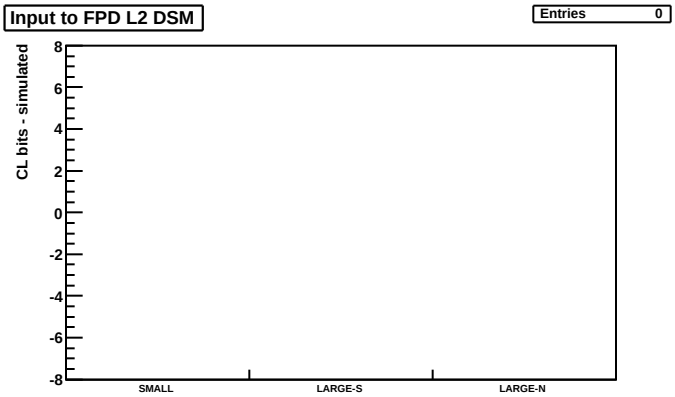

Input to FMS L1 DSM
Entries 9237756
FMS L1 HT bits
DSM board

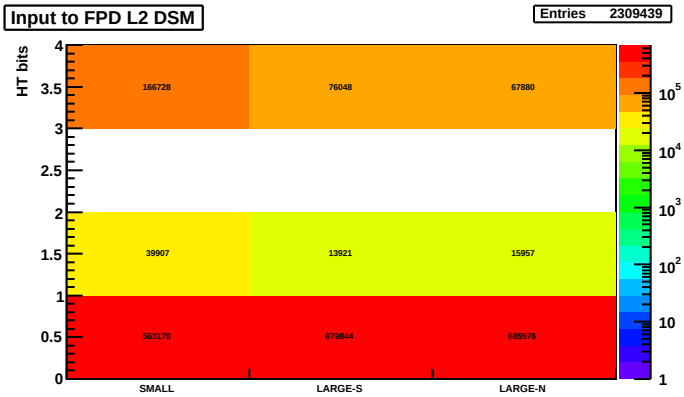

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

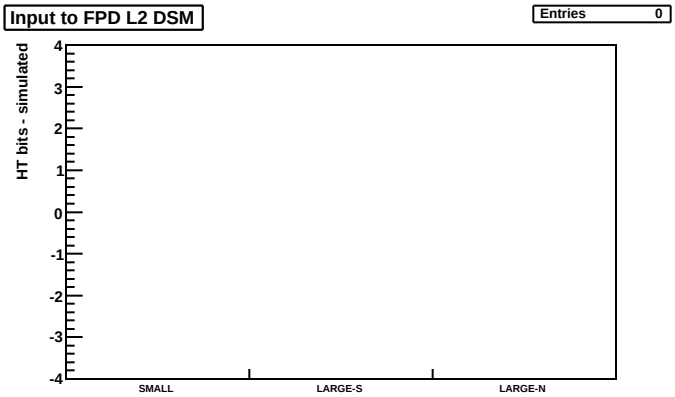

Input to FPD L2 DSM
Entries 6158504
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

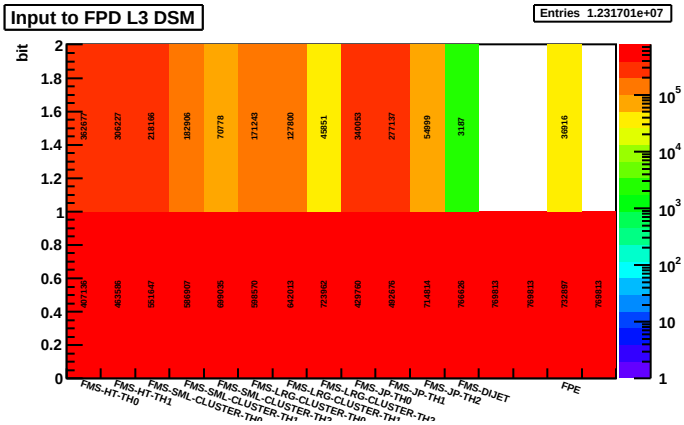

Input to FPD L2 DSM
Entries 16712
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

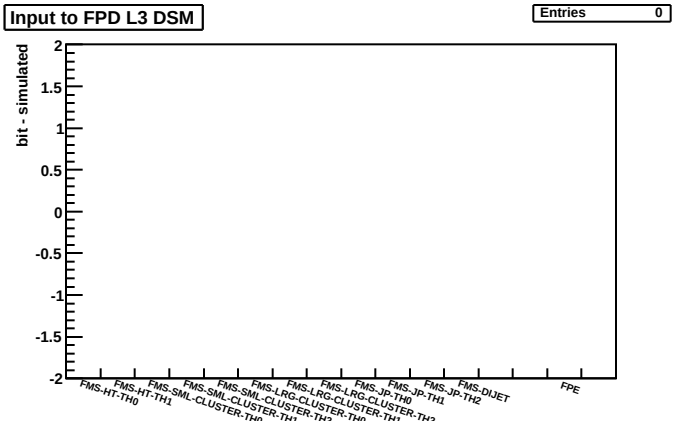

Input to FPD L2 DSM
Entries 2309439
CL bits
70778
24636
22550
112128
43128
38868
35260
22035
23355
551647
680014
685040
SMALL
LARGE-S
LARGE-N

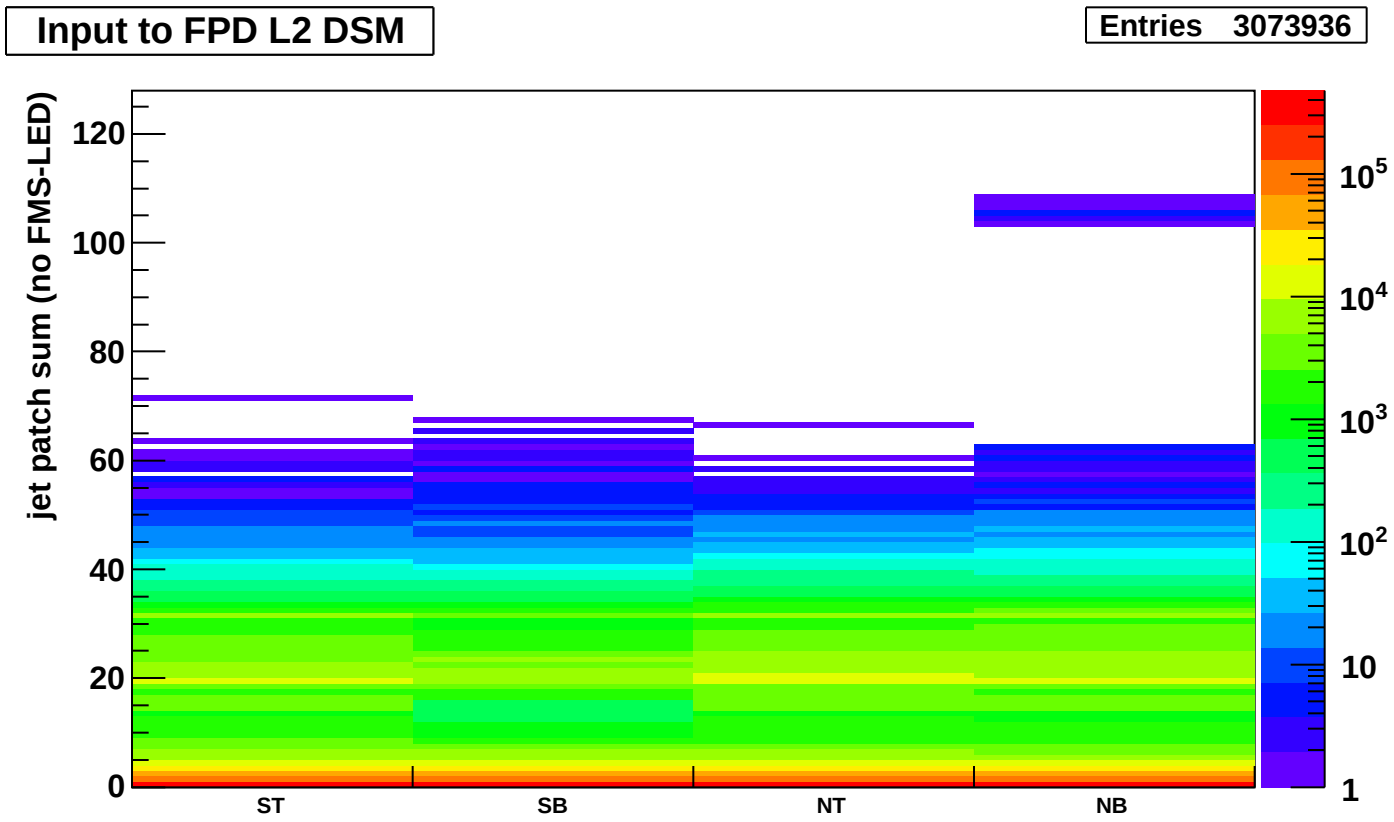

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

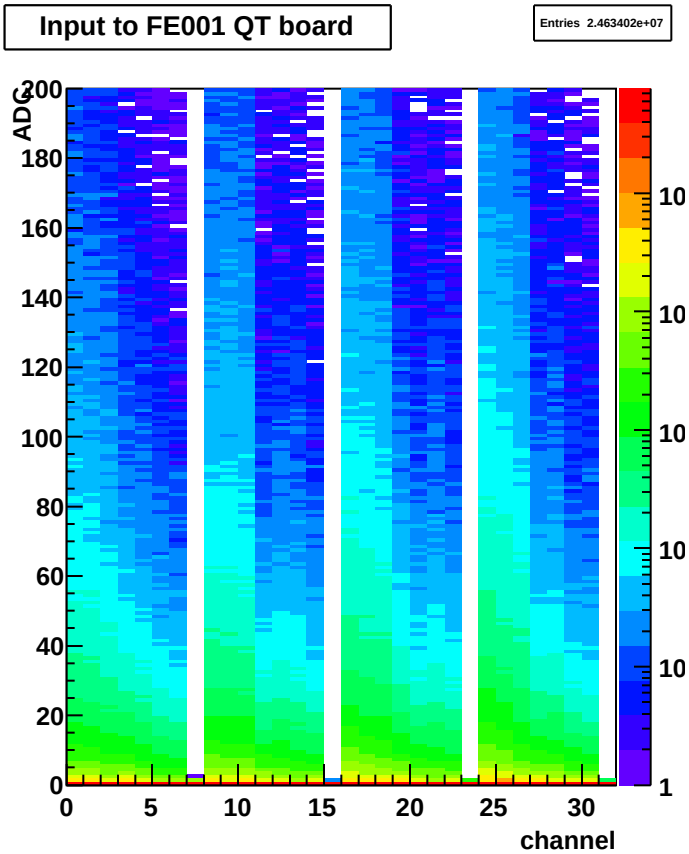

Input to FPD L2 DSM
Entries 2309439
HT bits
166728
76048
67880
39907
13921
15957
563178
679844
685976
SMALL
LARGE-S
LARGE-N

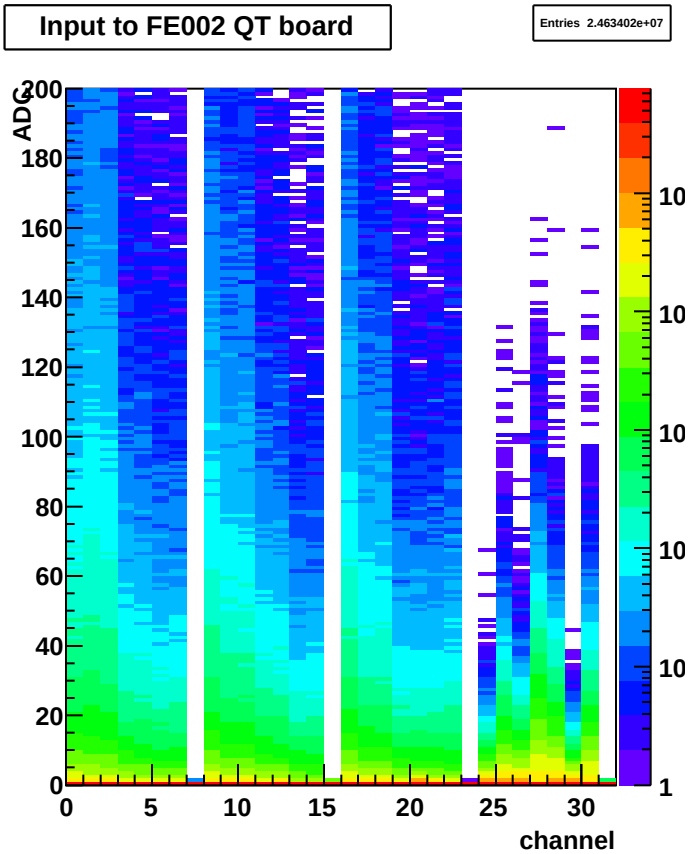

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

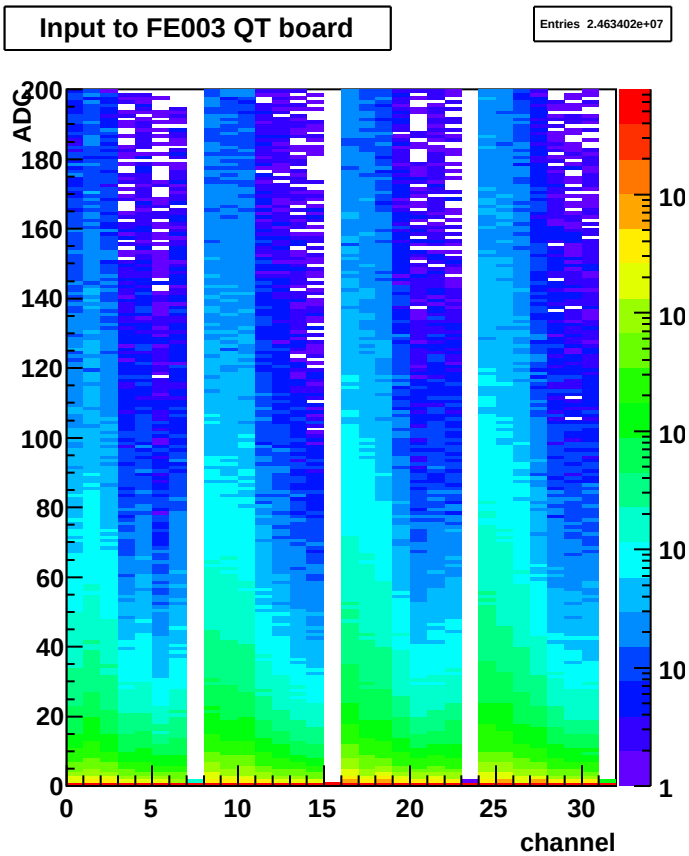

Input to FPD L3 DSM
Entries 1.231701e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

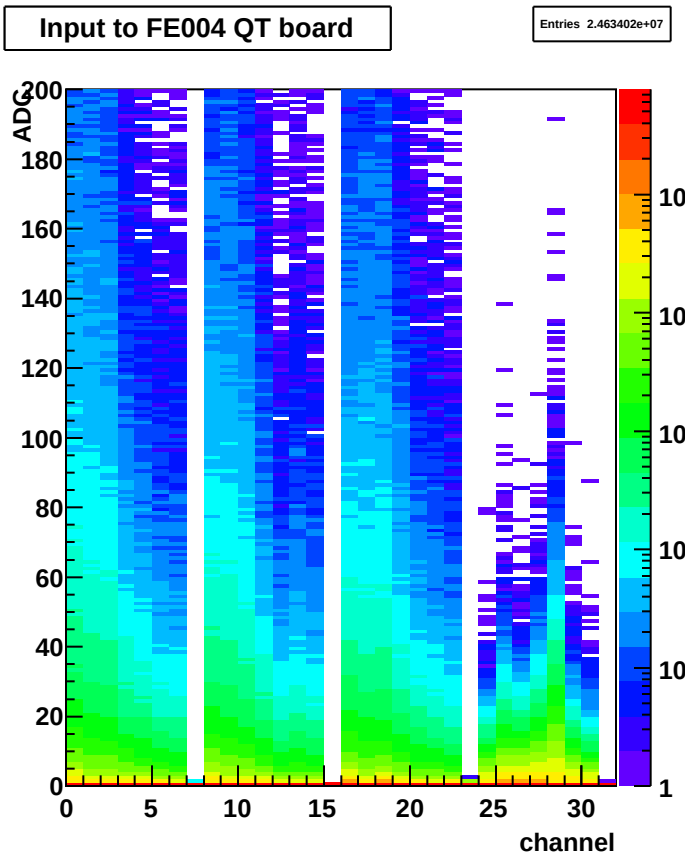

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

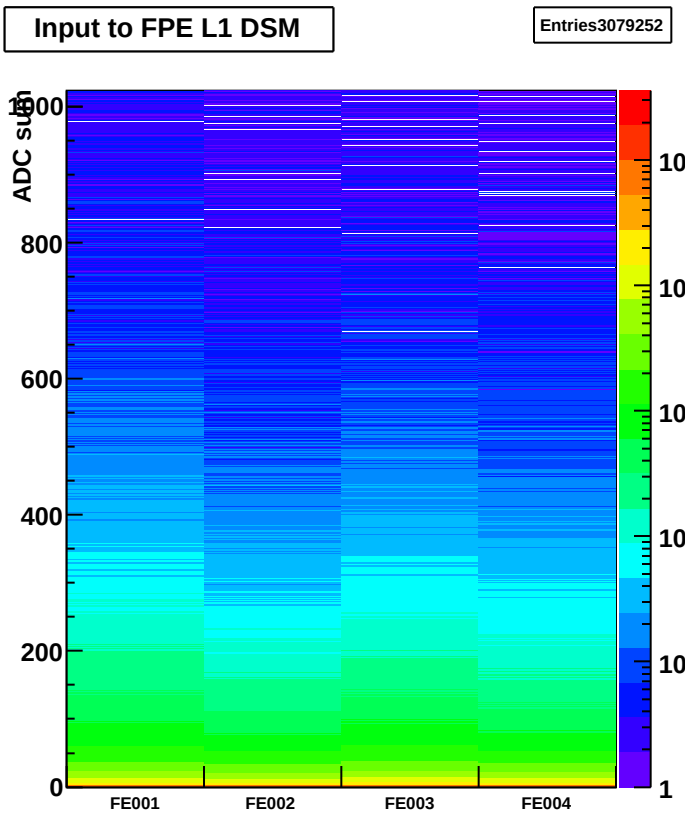

Input to FPD L2 DSM
Entries 3073936
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

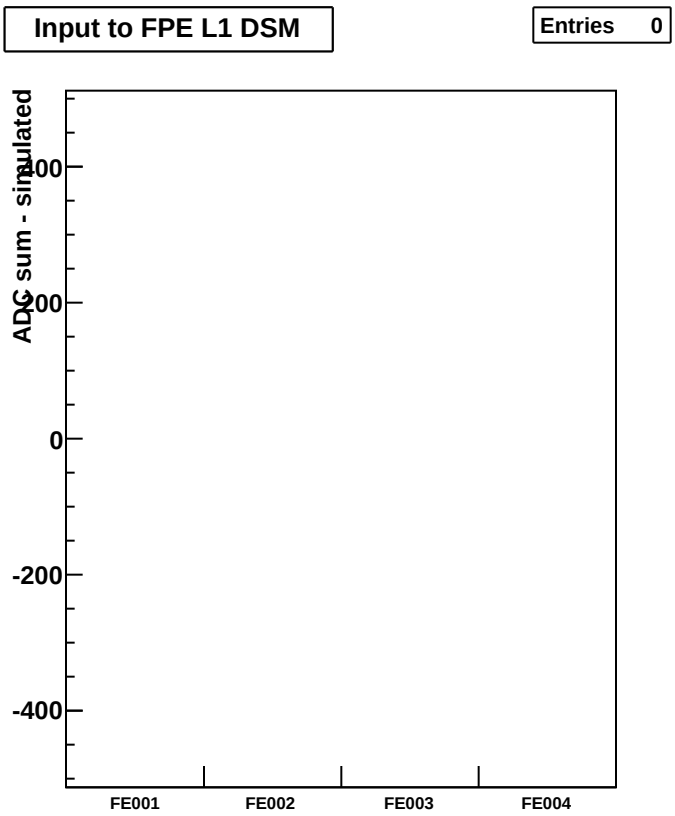

Input to FE001 QT board
Entries 2.463402e+07
ADC
channel

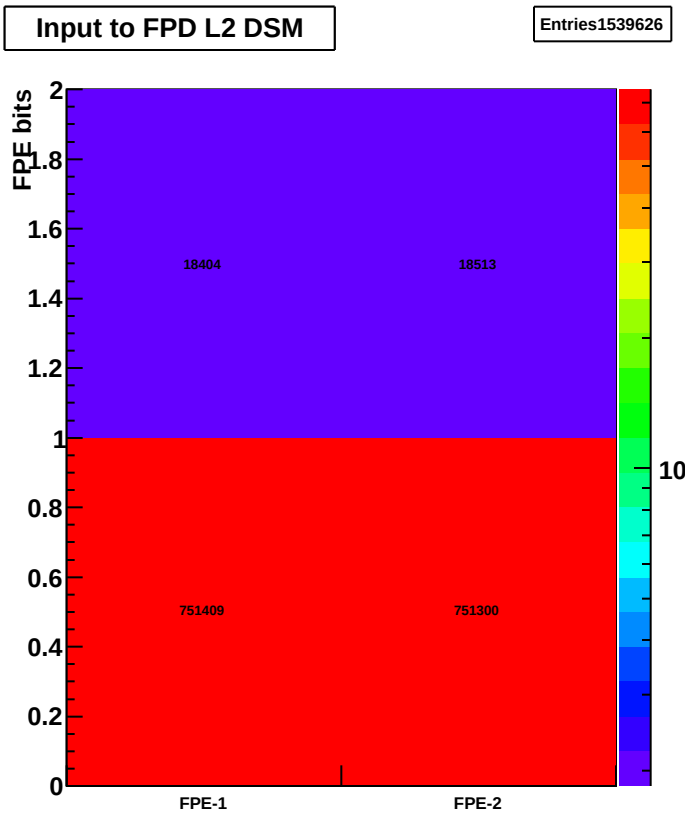

Input to FE002 QT board
Entries 2.463402e+07
ADC
channel

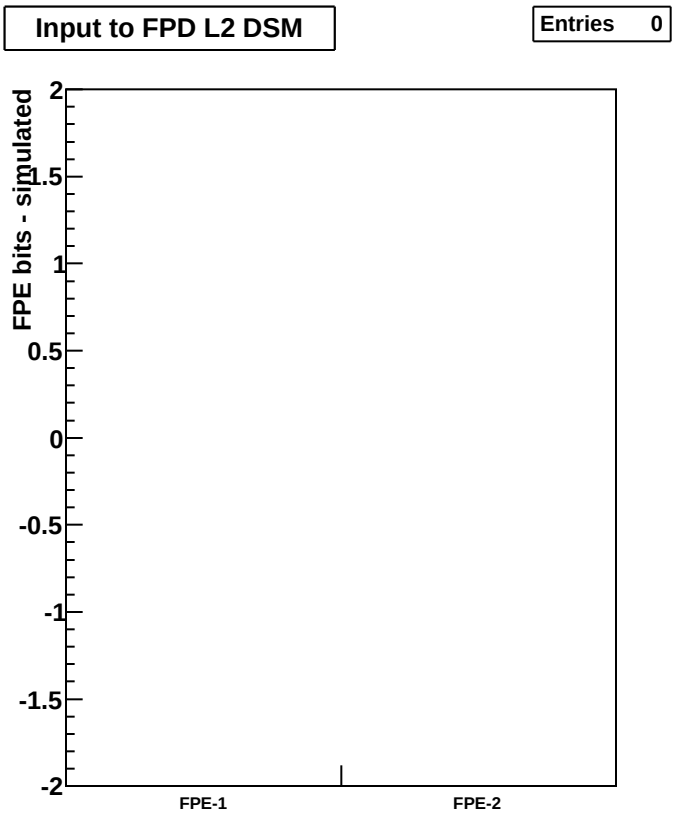

Input to FE003 QT board
Entries 2.463402e+07
ADC
channel


Input to FE004 QT board
Entries 2.463402e+07
ADC
channel


Input to FPE L1 DSM
Entries3079252
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1539626
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
18404
18513
751409
751300
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2