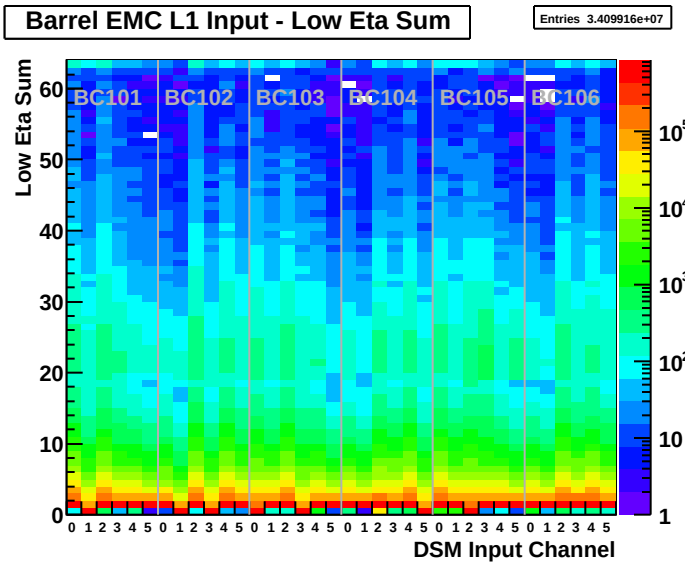
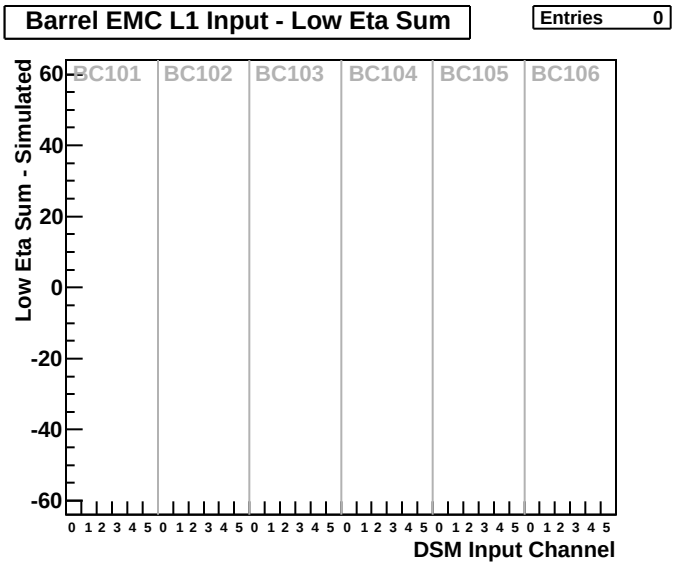
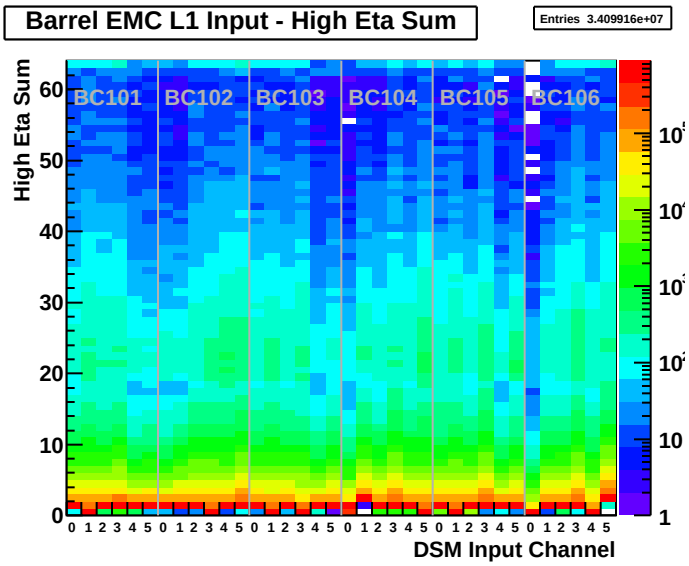
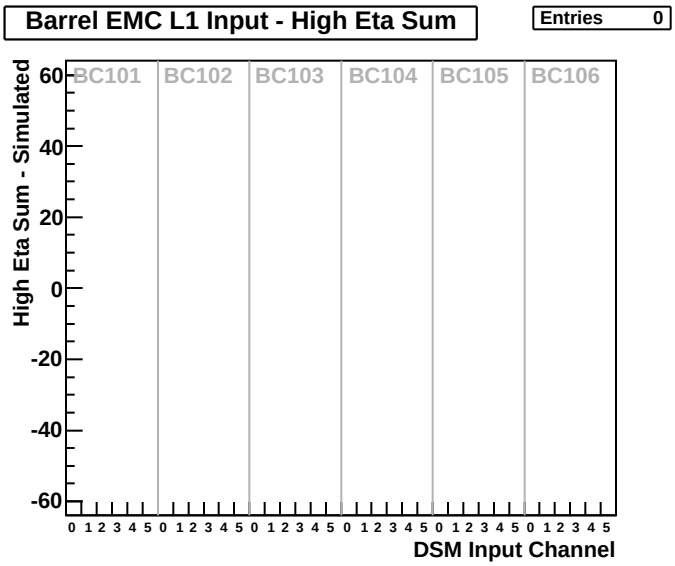
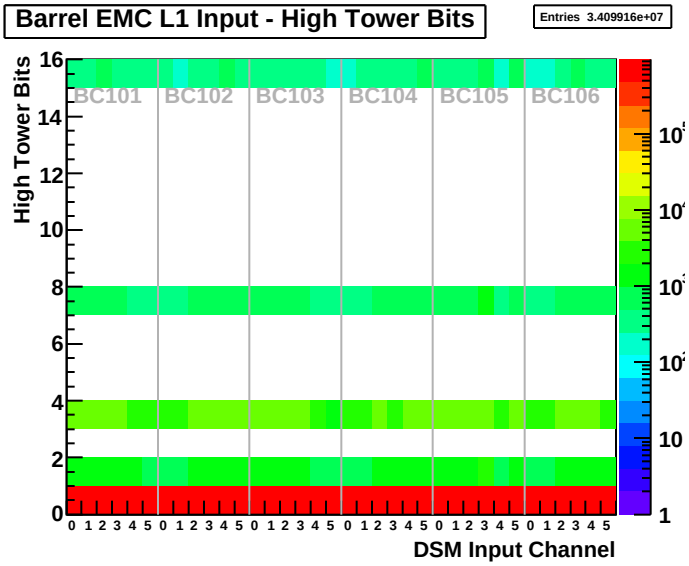
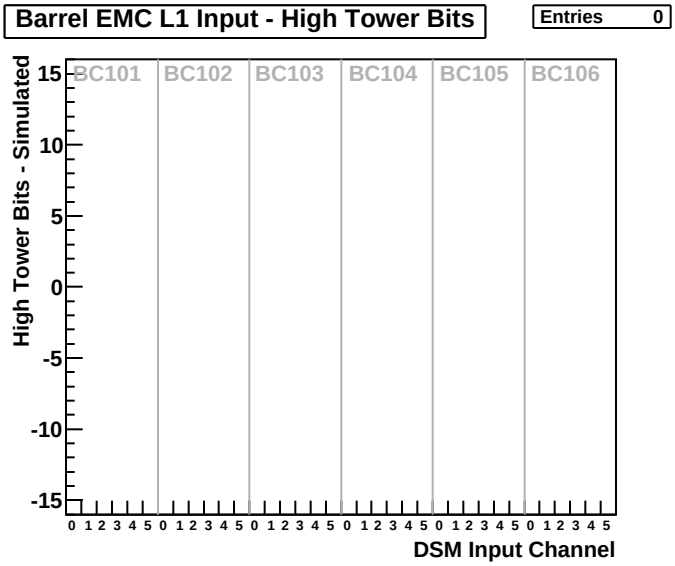

Barrel EMC L1 Input - Low Eta Sum
Entries 3.409916e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

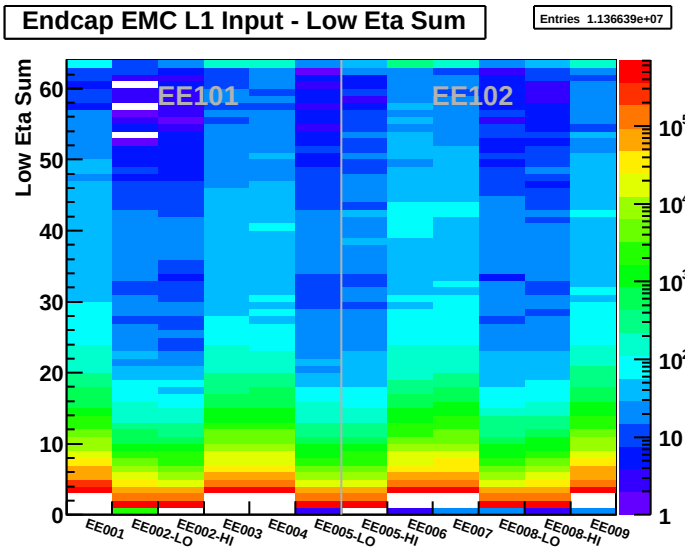

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

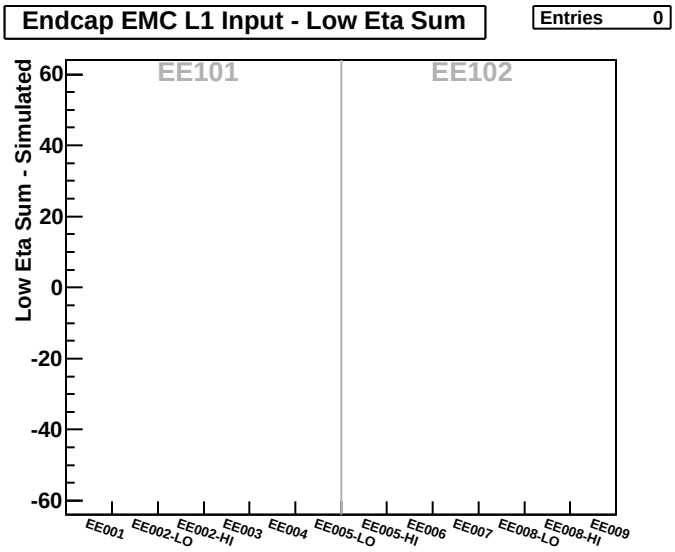

Barrel EMC L1 Input - High Eta Sum
Entries 3.409916e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

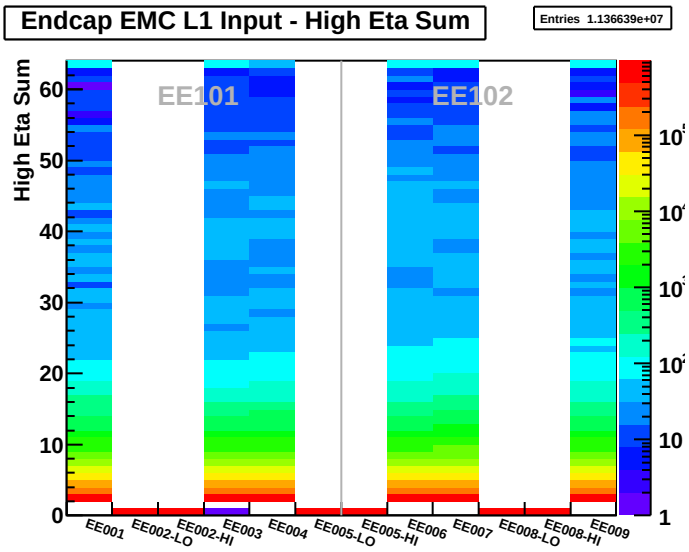

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

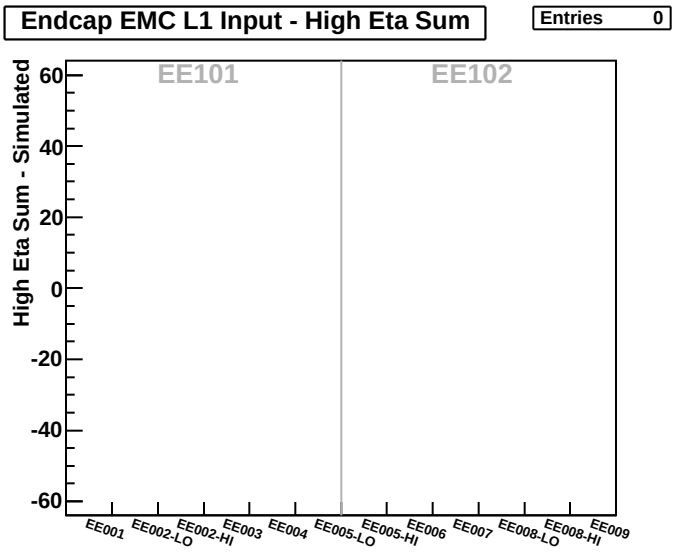

Barrel EMC L1 Input - High Tower Bits
Entries 3.409916e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

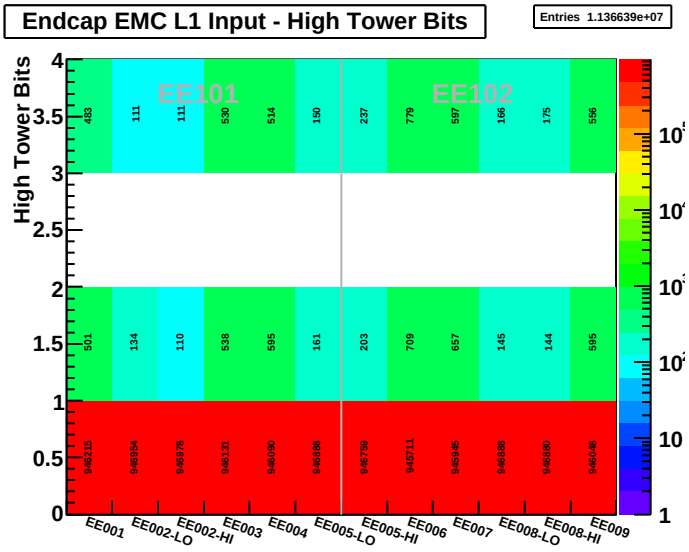

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

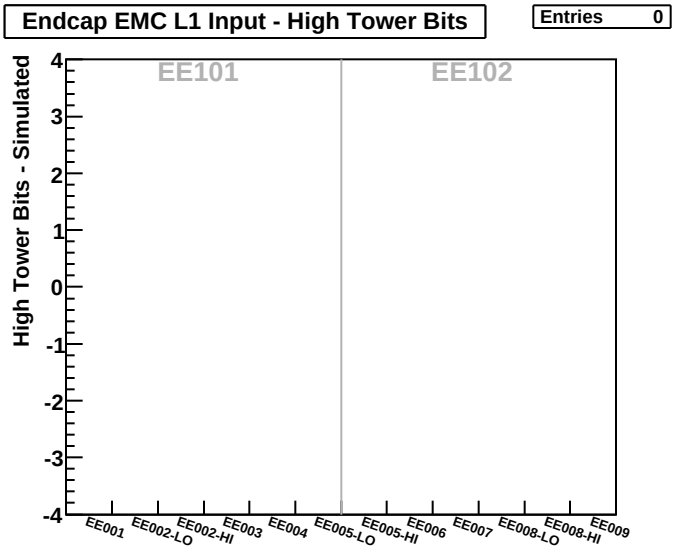

Endcap EMC L1 Input - Low Eta Sum
Entries 1.136639e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

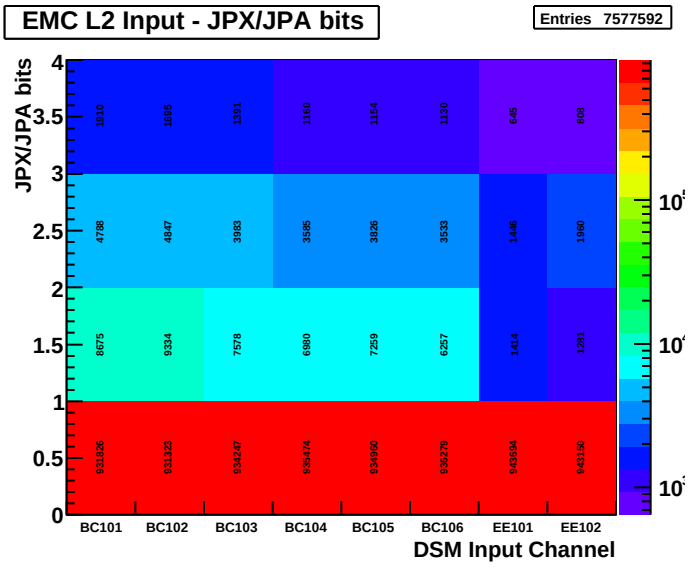

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

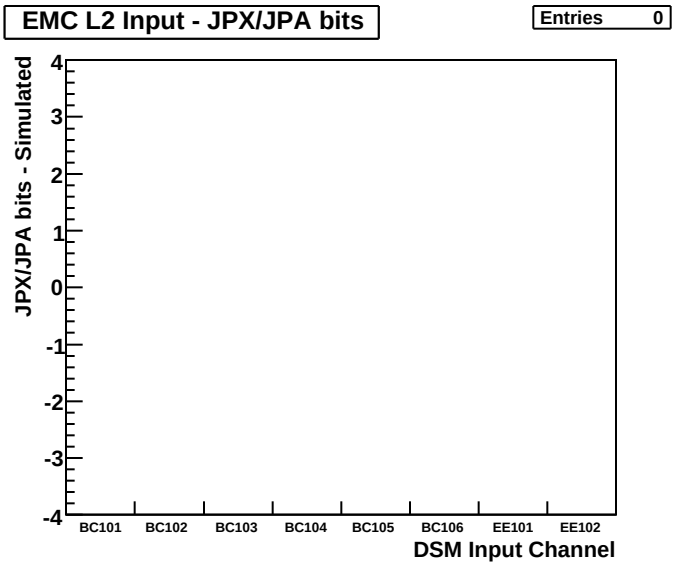

Endcap EMC L1 Input - High Eta Sum
Entries 1.136639e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

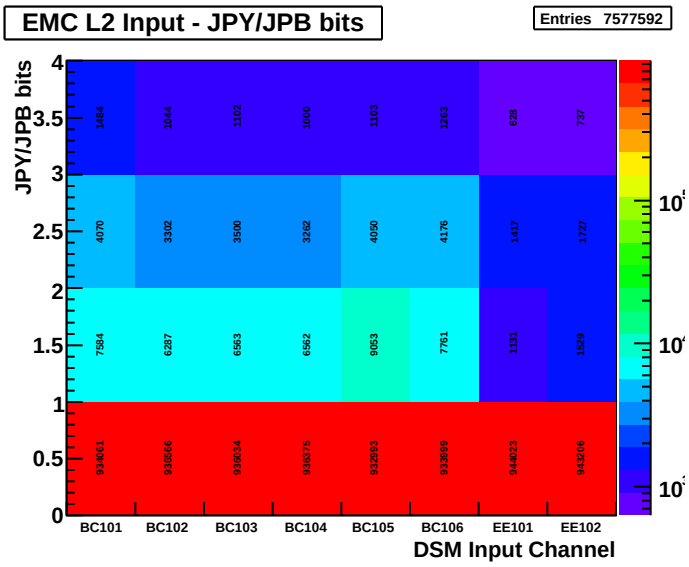

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

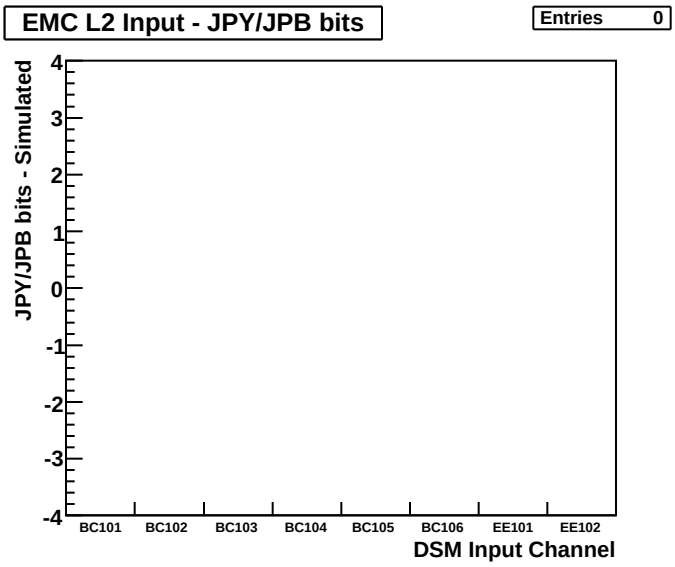

Endcap EMC L1 Input - High Tower Bits
Entries 1.136639e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

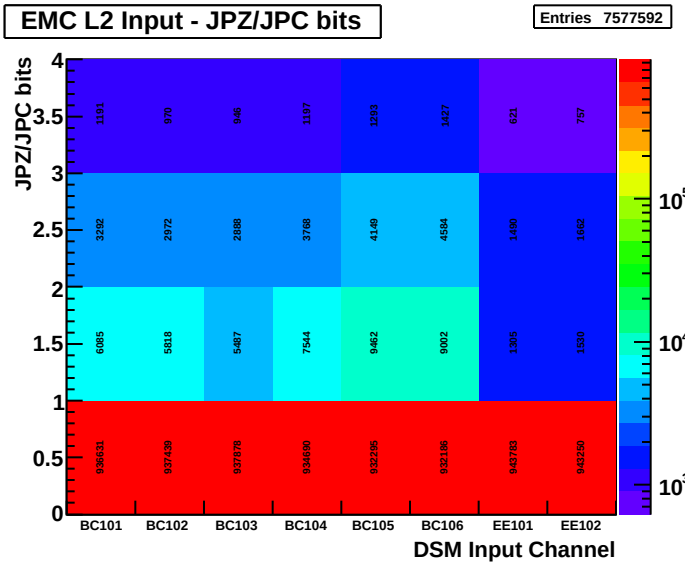

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

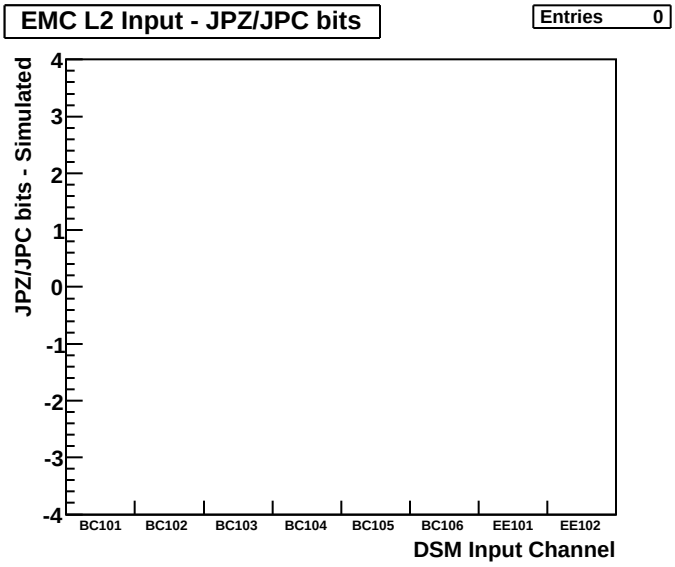

EMC L2 Input - JPX/JPA bits
Entries 7577592
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1910 1695 1391 1160 1154 1130 645 808
4788 4847 3983 3585 3826 3533 1446 1960
8675 9334 7578 6980 7259 6257 1414 1281
931826 931323 934247 935474 934960 935279 943694 943150

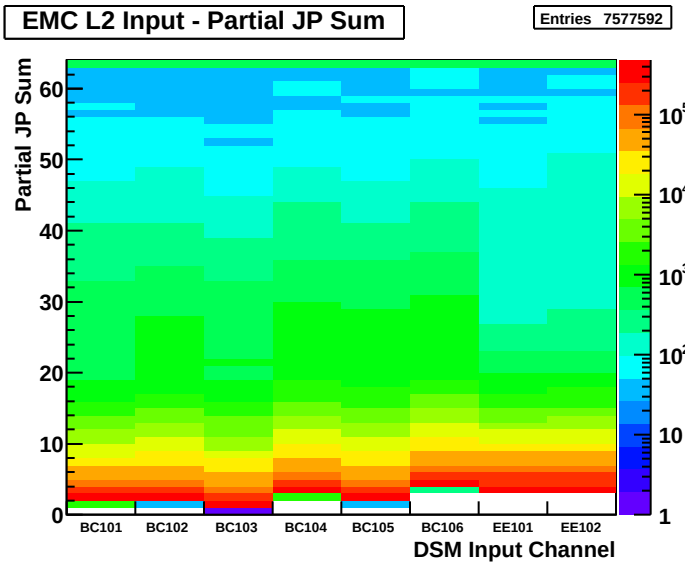

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

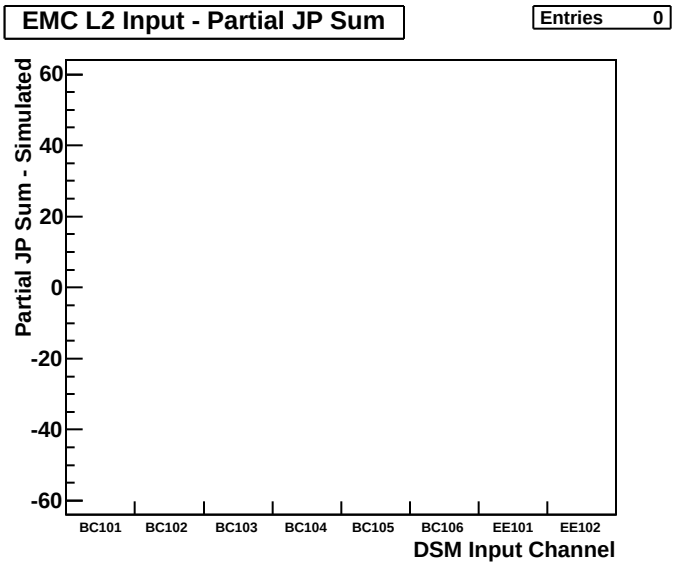

EMC L2 Input - JPY/JPB bits
Entries 7577592
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1484 1044 1102 1000 1103 1263 628 737
4070 3302 3500 3262 4050 4176 1417 1727
7384 6287 6563 6562 9053 7761 1131 1529
934061 936566 936034 936375 933993 933999 944023 942206

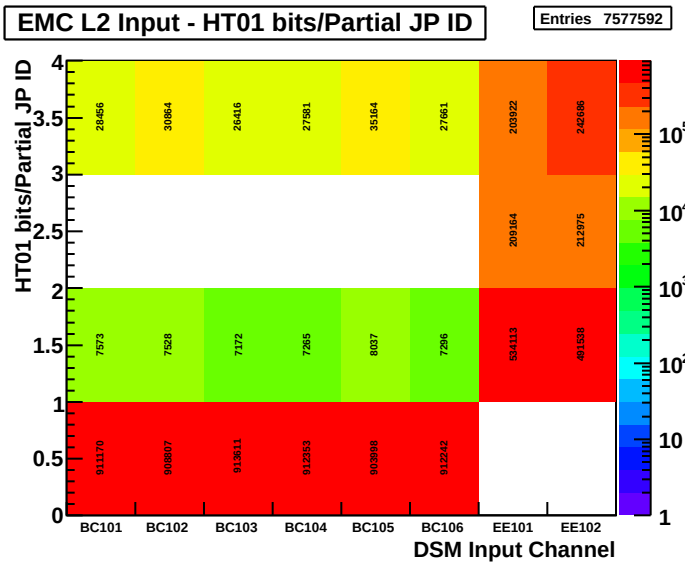

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

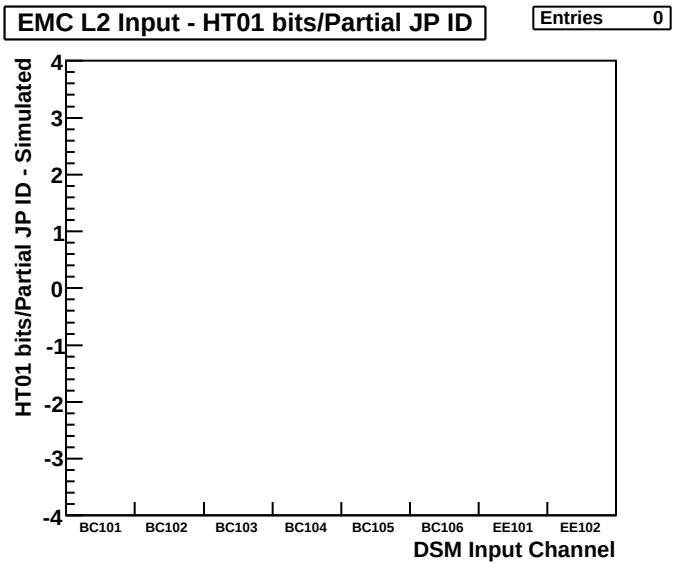

EMC L2 Input - JPZ/JPC bits
Entries 7577592
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1191 970 946 1197 1293 1427 621 757
3292 2972 2880 3768 4149 4584 1490 1682
6085 5818 5487 7544 9462 9002 1305 1530
936531 937439 937878 934690 932295 932186 943783 943250

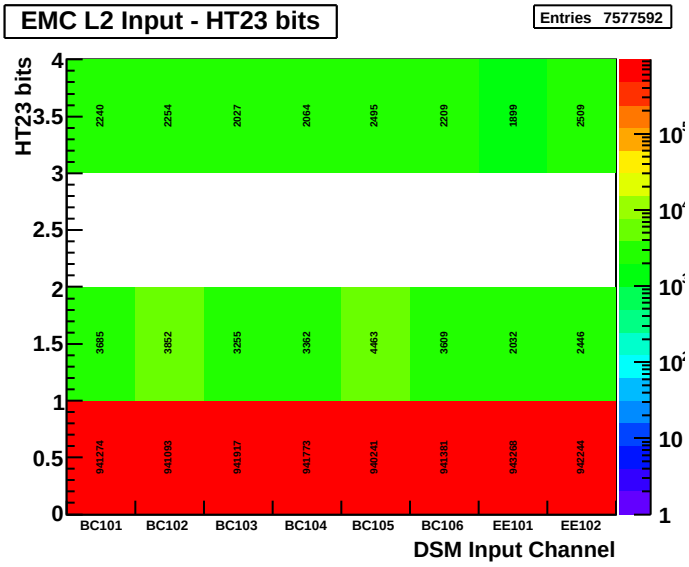

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

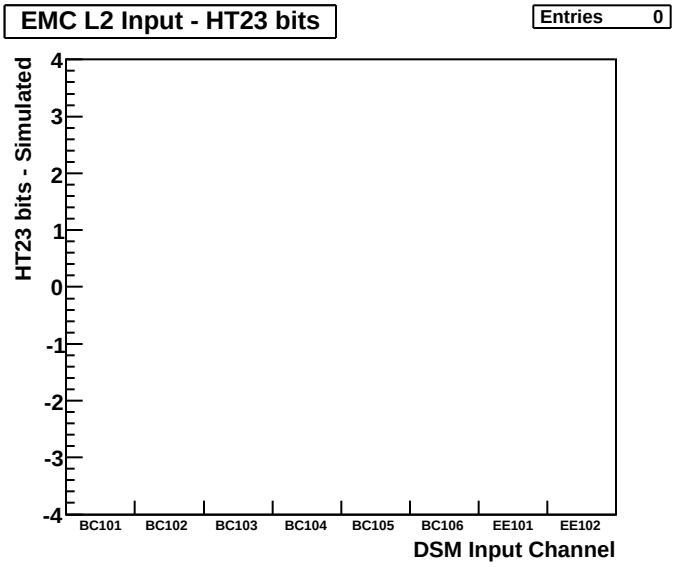

EMC L2 Input - Partial JP Sum
Entries 7577592
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

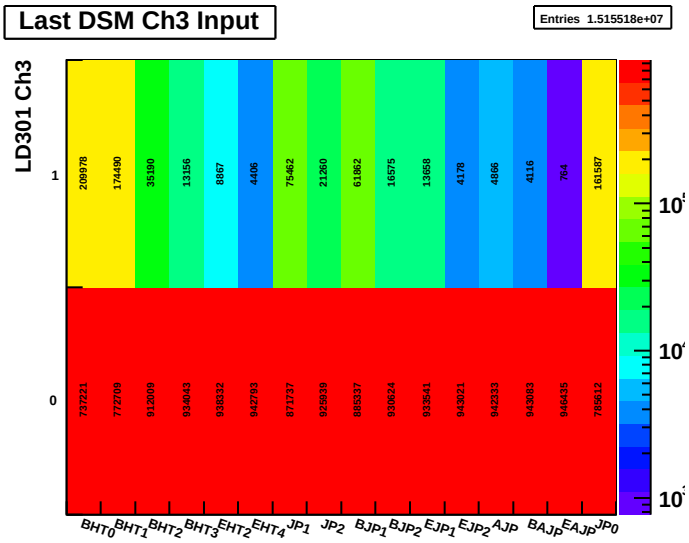

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

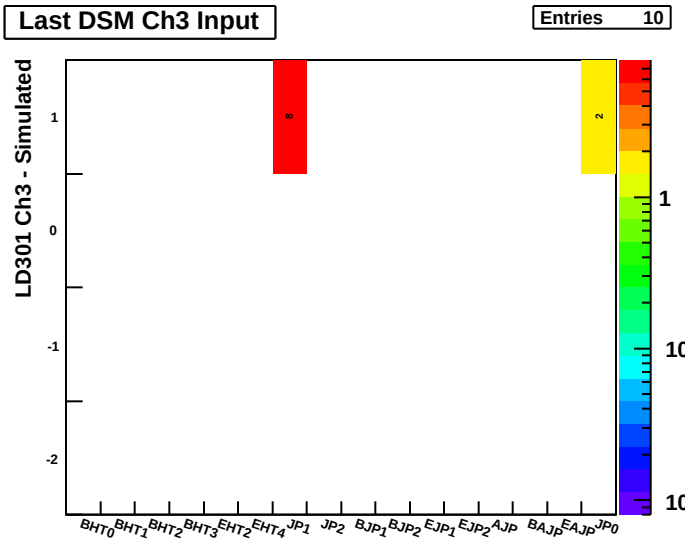

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7577592
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

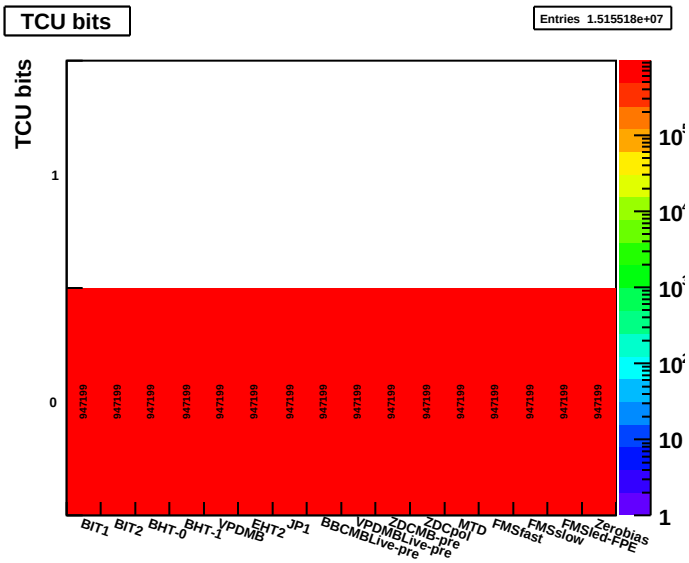

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

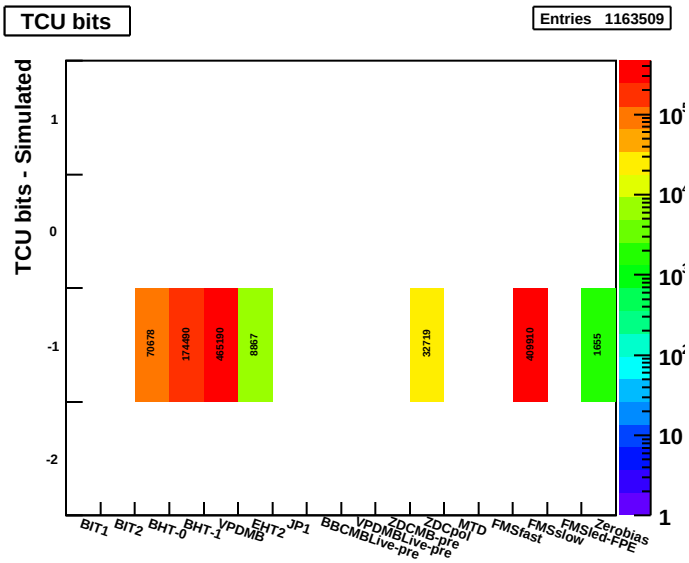

EMC L2 Input - HT23 bits
Entries 7577592
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

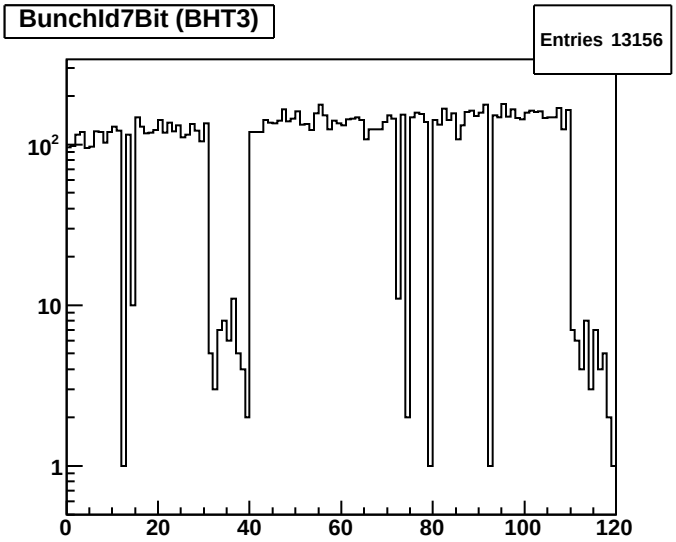

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

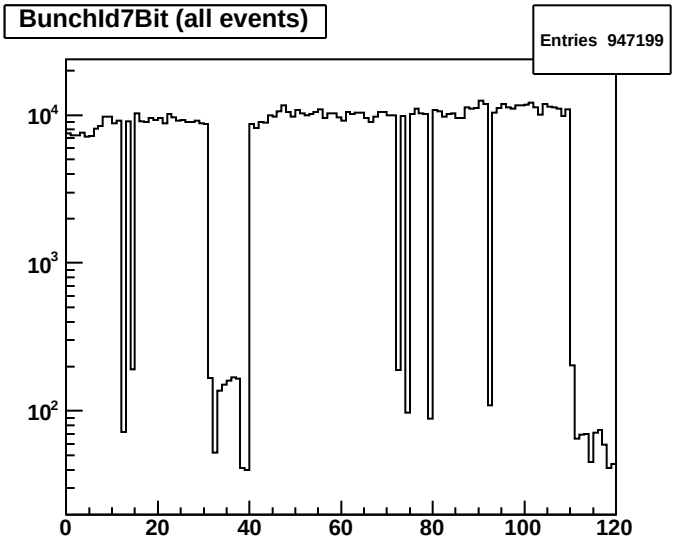

Last DSM Ch3 Input
Entries 1.515518e+07
LD301 Ch3
200978 174490 35190 13156 8867 4406 75462 21260 61862 16575 13658 4178 4866 4116 764 161587
737221 772709 912009 934043 938332 942793 871737 925939 885337 930624 933541 943021 942333 943083 946435 785612
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

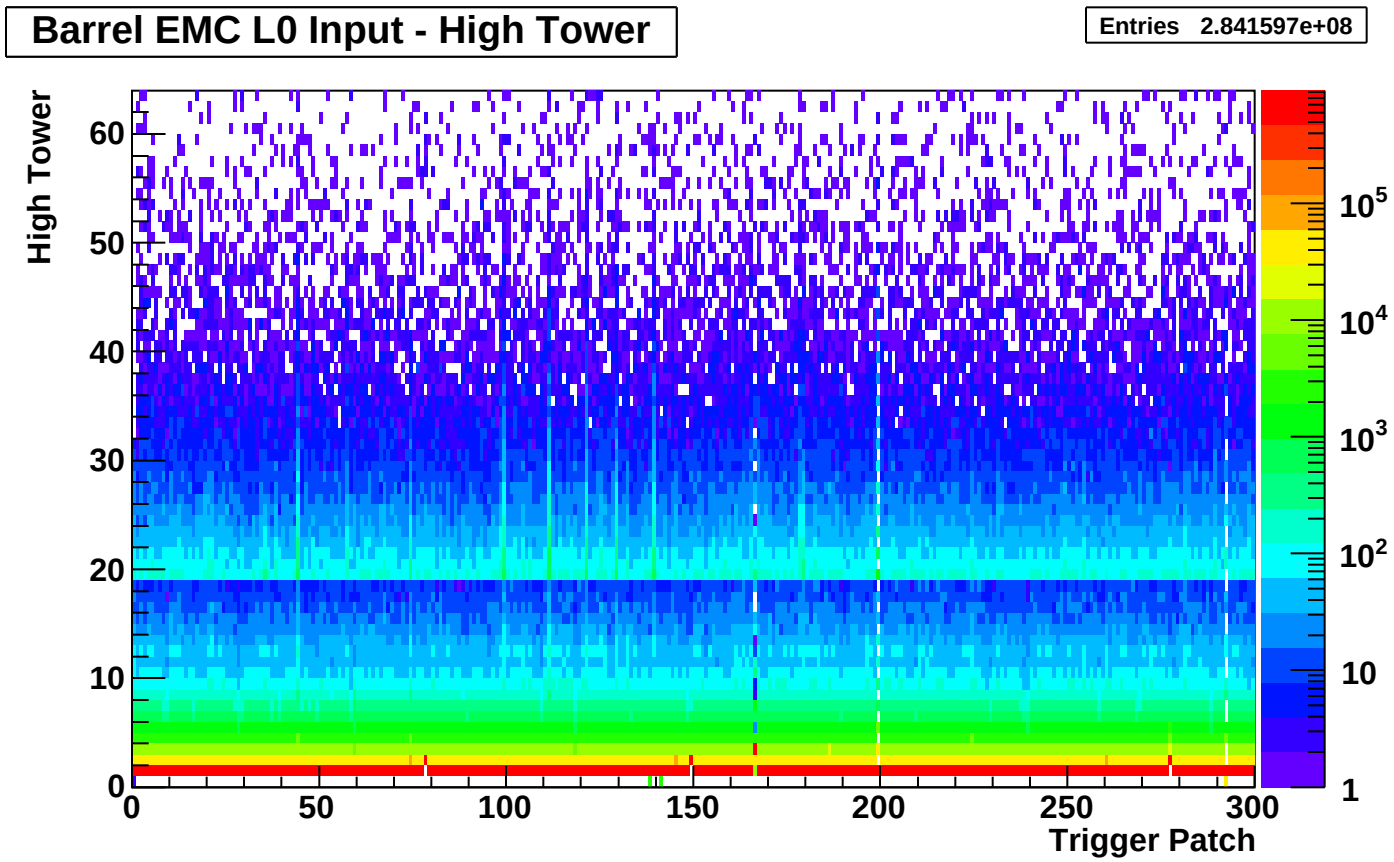

Last DSM Ch3 Input
Entries 10
LD301 Ch3 - Simulated
8
2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

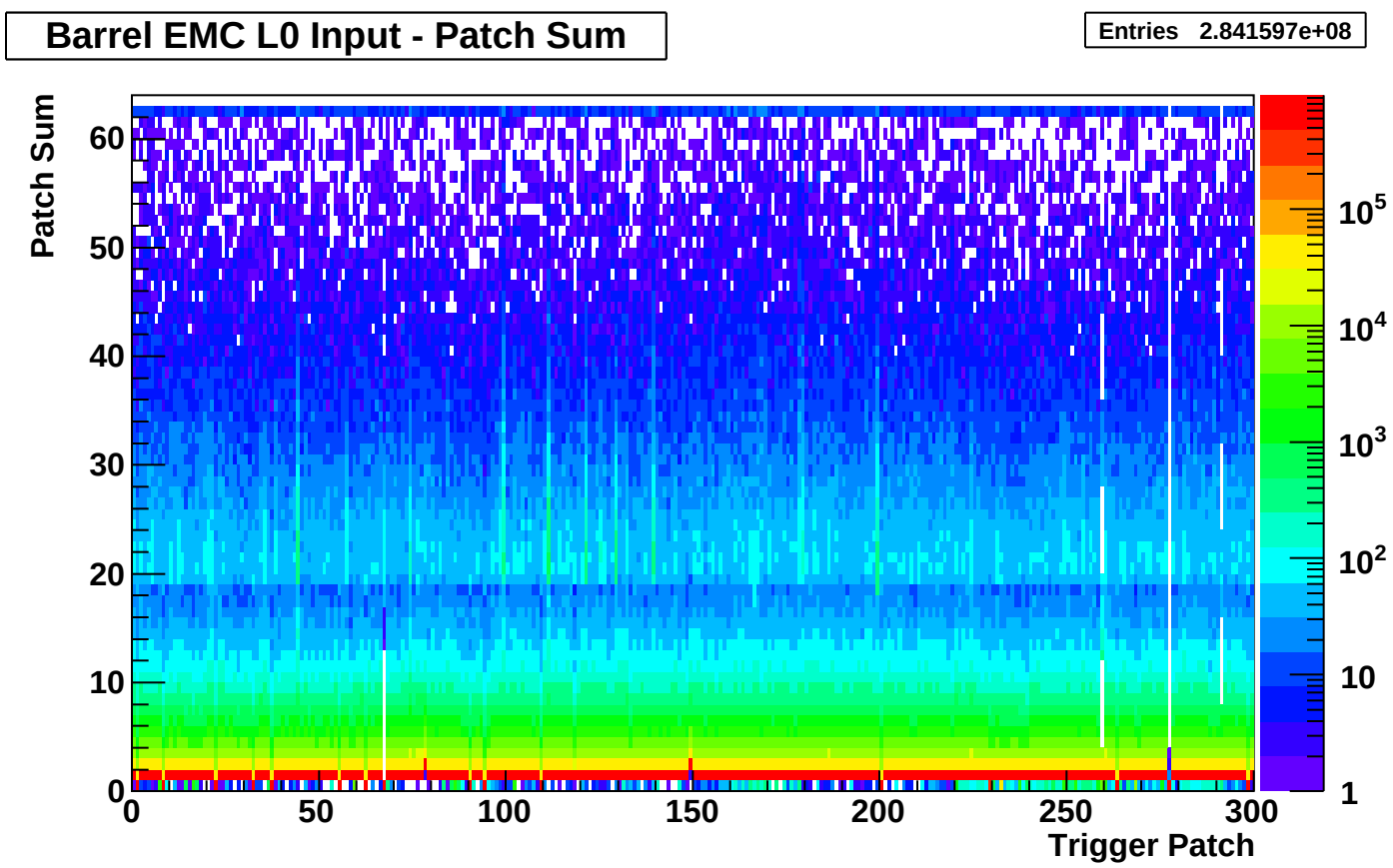

TCU bits
Entries 1.515518e+07
TCU bits
947199
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

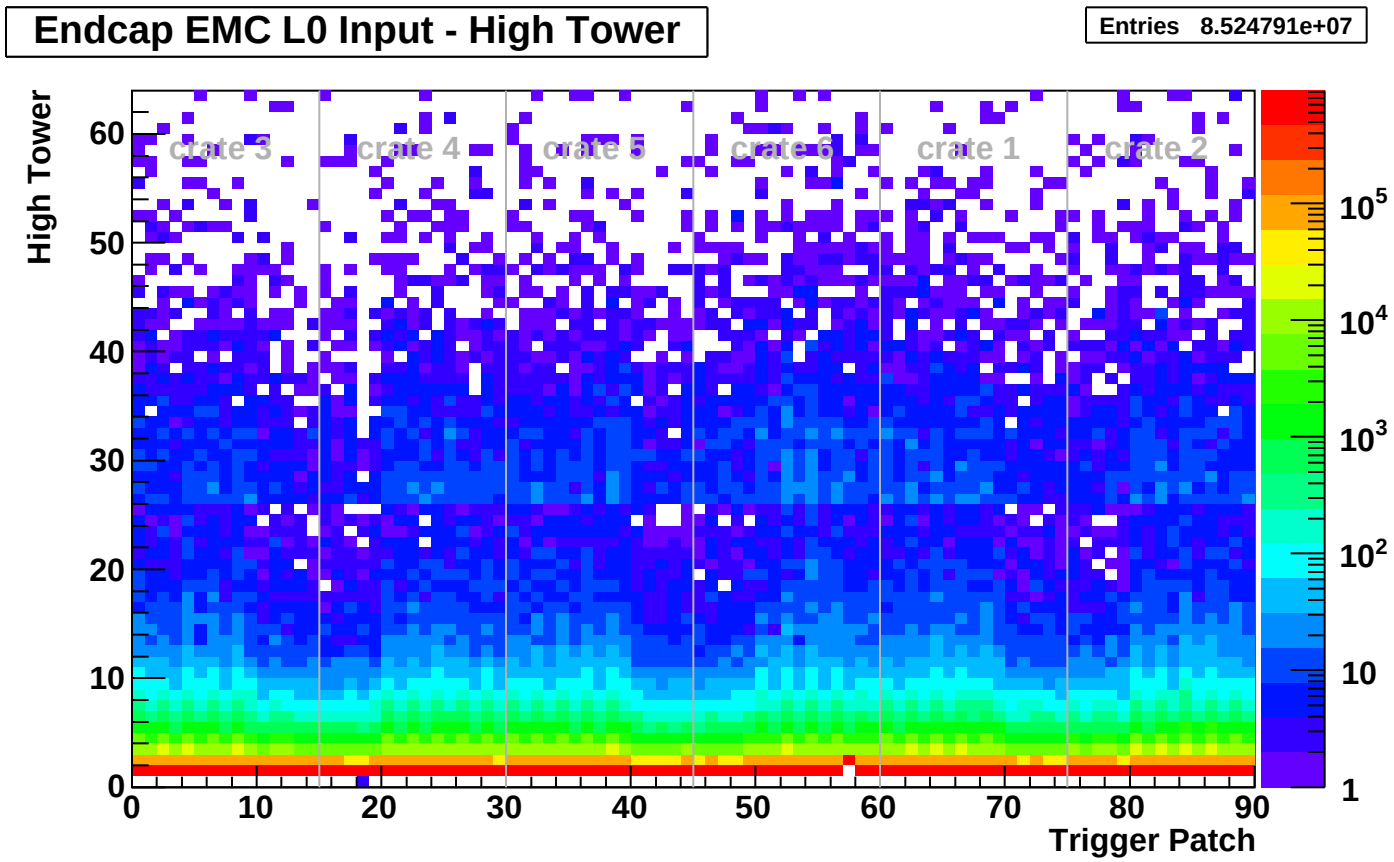

TCU bits
Entries 1163509
TCU bits - Simulated
70678 174480 465190 8867 32719 409910 1655
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

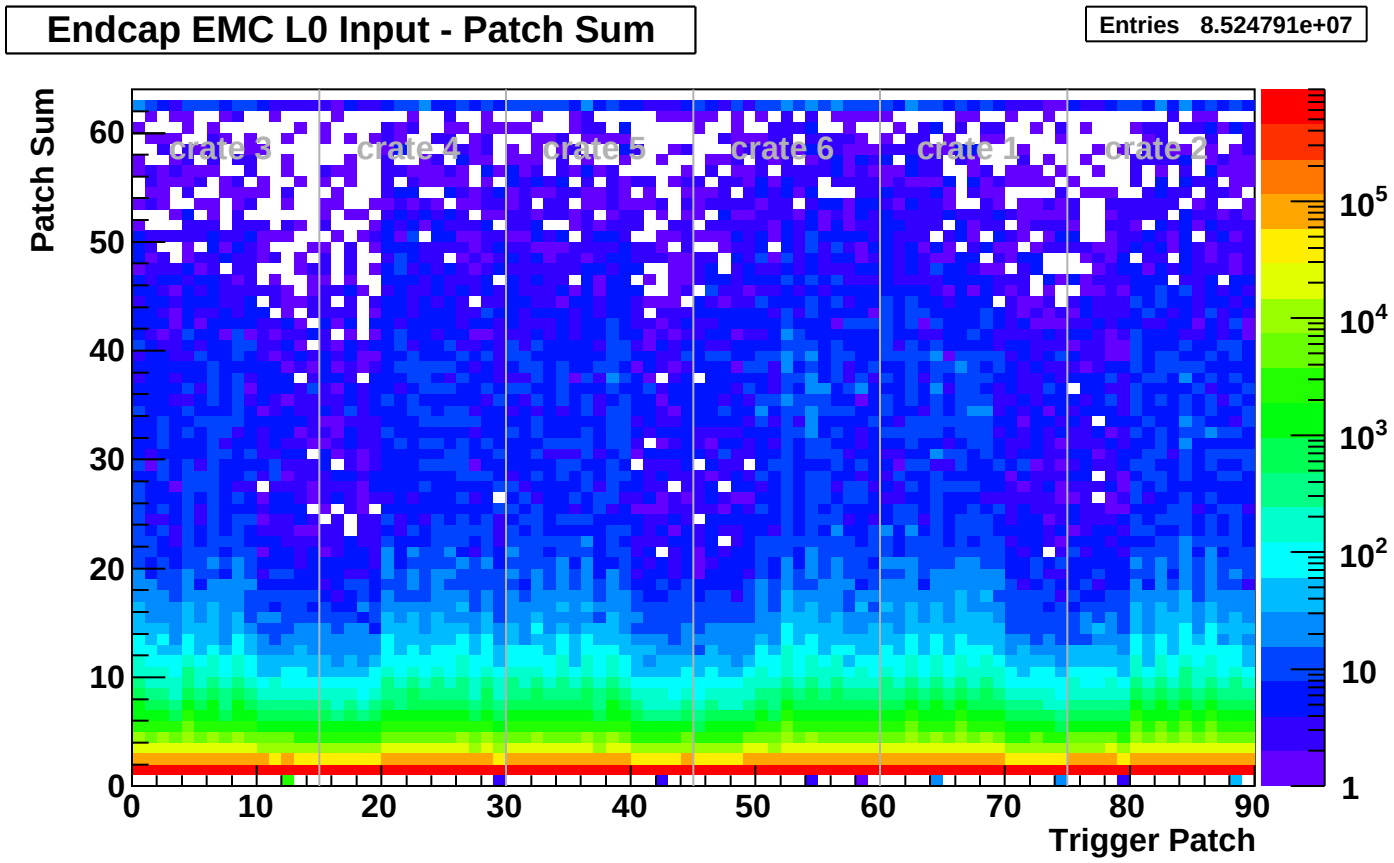

BunchId7Bit (BHT3)
Entries 13156

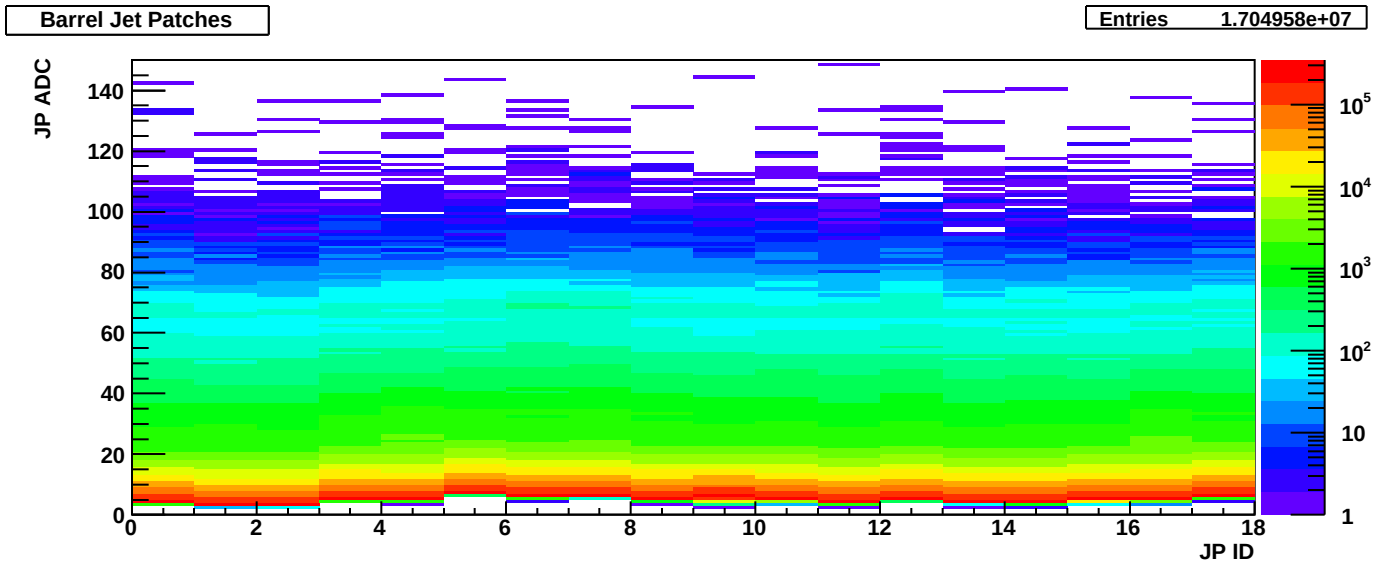

BunchId7Bit (all events)
Entries 947199

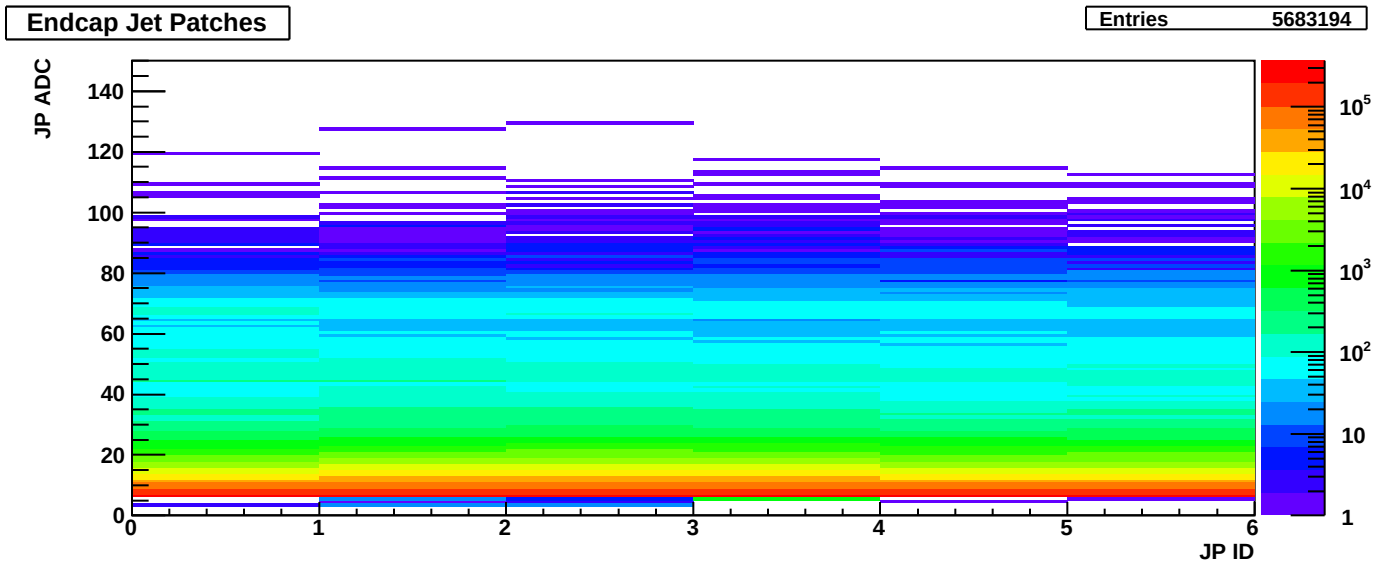

Barrel EMC L0 Input - High Tower
Entries 2.841597e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

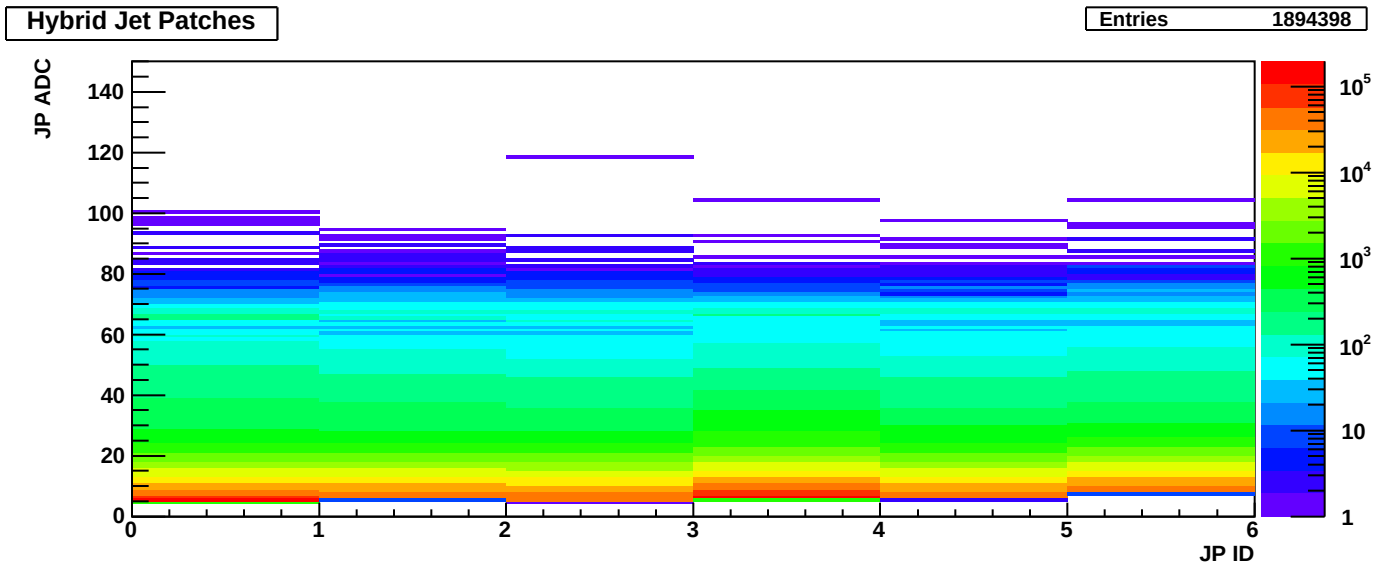

Barrel EMC L0 Input - Patch Sum
Entries 2.841597e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

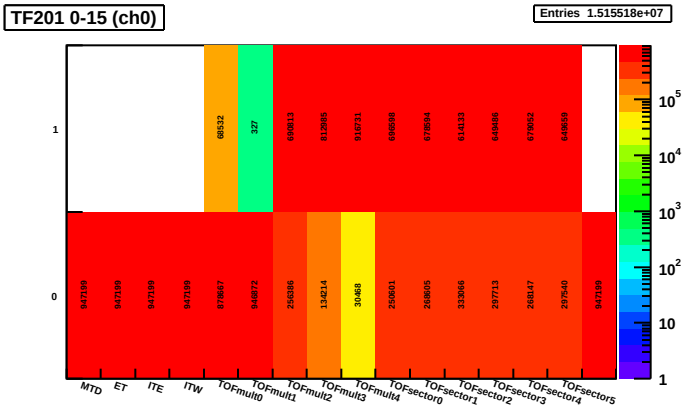

Endcap EMC L0 Input - High Tower
Entries 8.524791e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

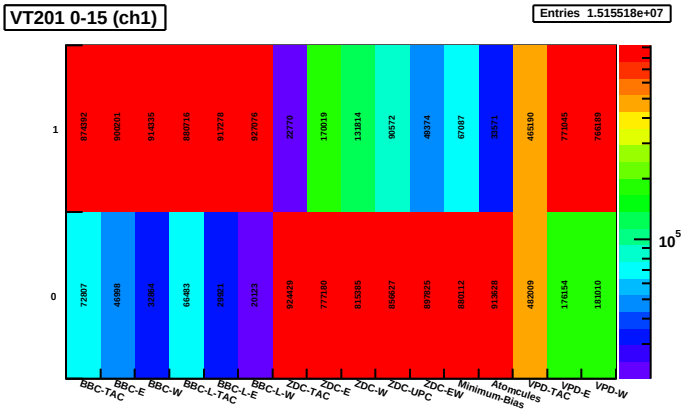

Endcap EMC L0 Input - Patch Sum
Entries 8.524791e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

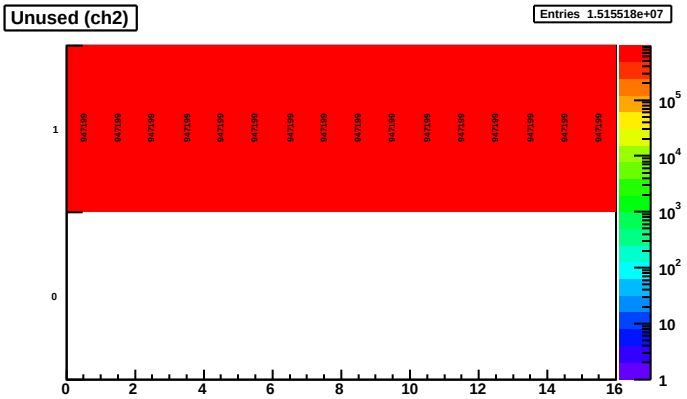

Barrel Jet Patches
Entries 1.704958e+07
JP ADC
JP ID

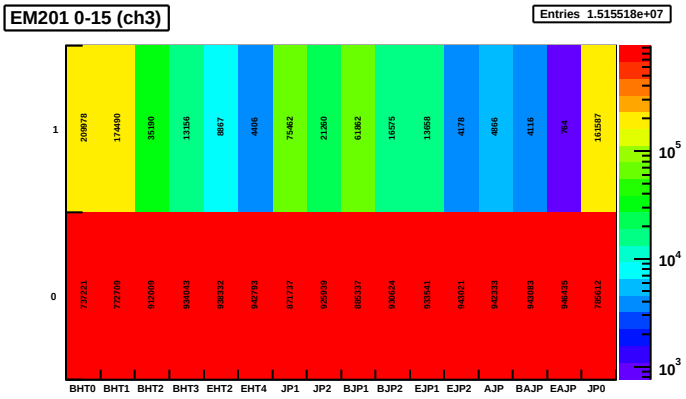

Endcap Jet Patches
Entries 5683194
JP ADC
JP ID

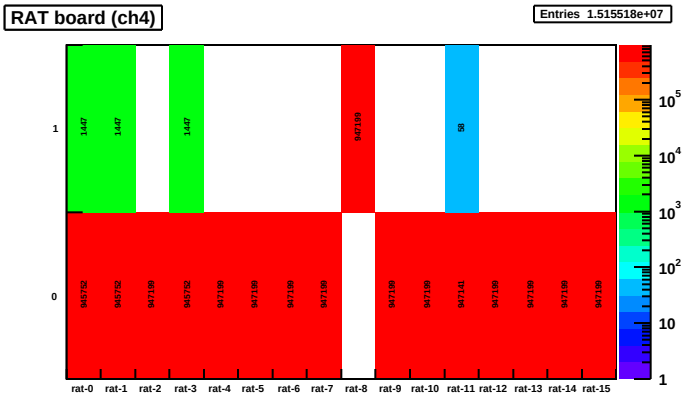

Hybrid Jet Patches
Entries 1894398
JP ADC
JP ID

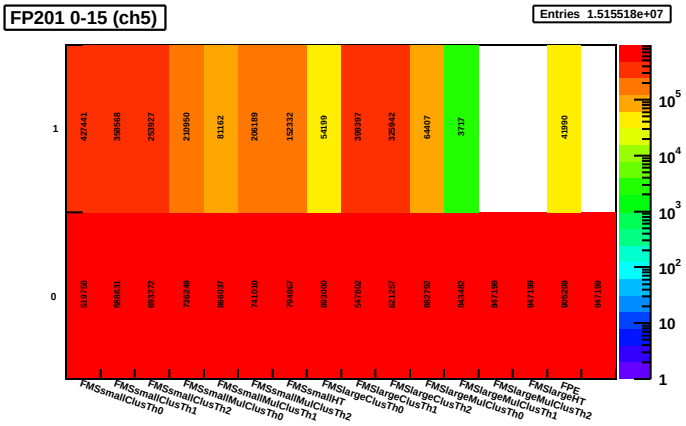

TF201 0-15 (ch0)
Entries 1.515518e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

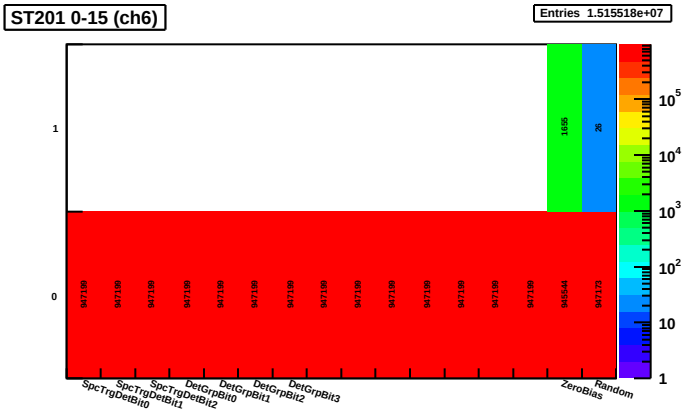

VT201 0-15 (ch1)
Entries 1.515518e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

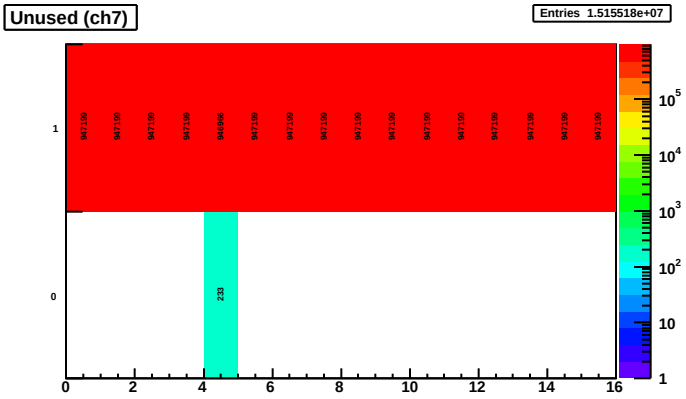

Unused (ch2)
Entries 1.515518e+07

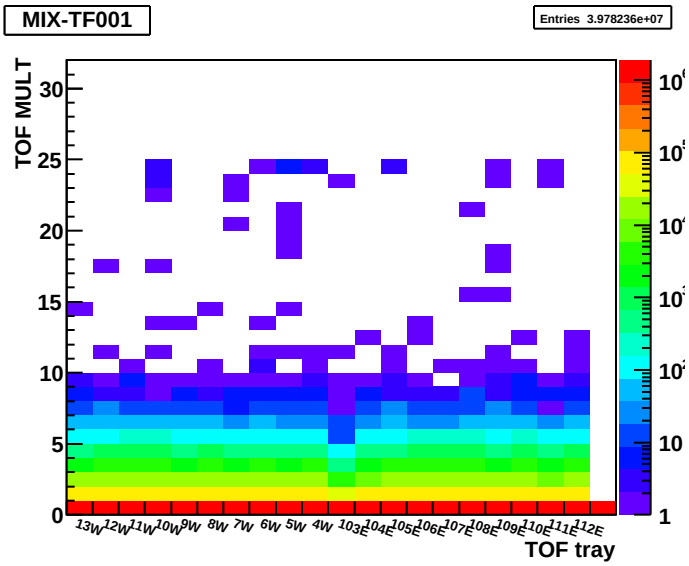

EM201 0-15 (ch3)
Entries 1.515518e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

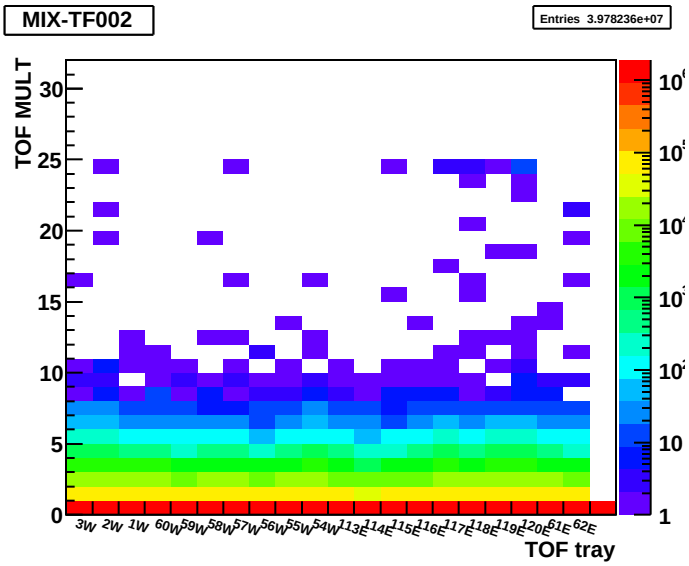

RAT board (ch4)
Entries 1.515518e+07
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

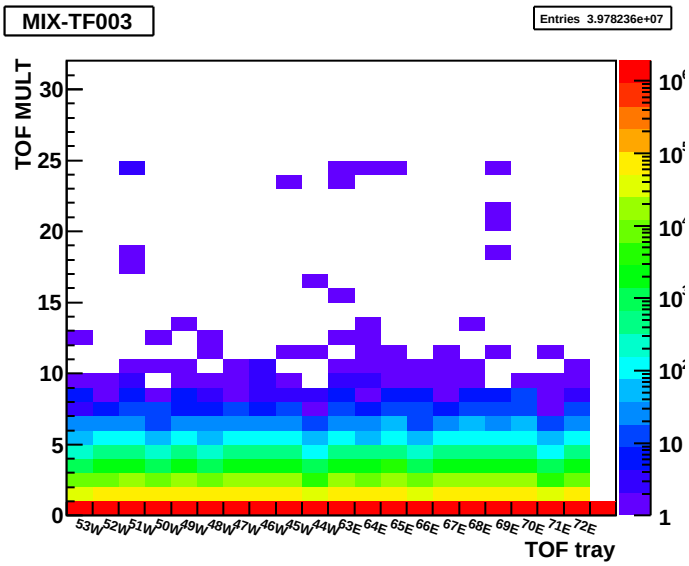

FP201 0-15 (ch5)
Entries 1.515518e+07
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

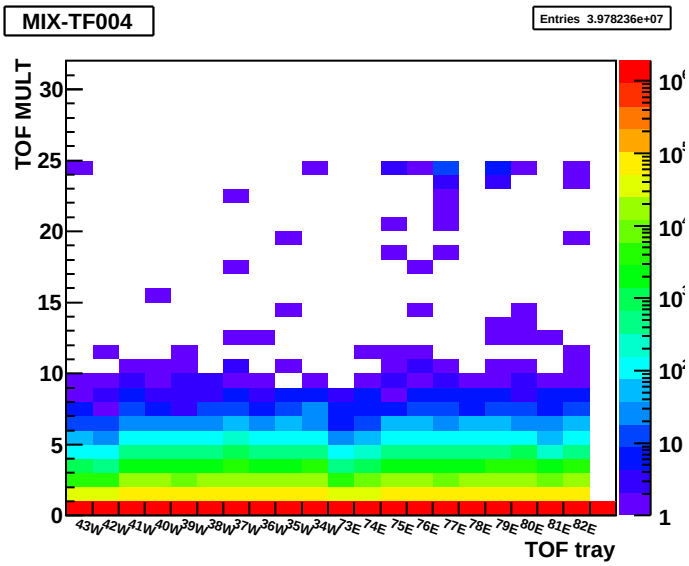

ST201 0-15 (ch6)
Entries 1.515518e+07
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

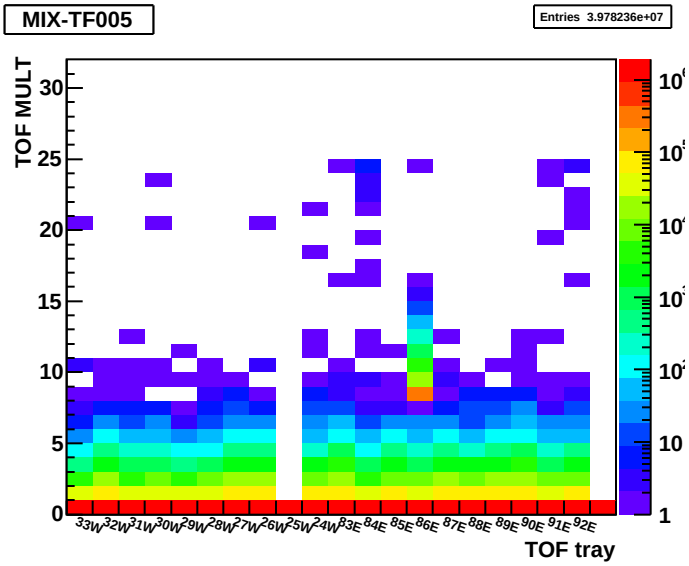

Unused (ch7)
Entries 1.515518e+07

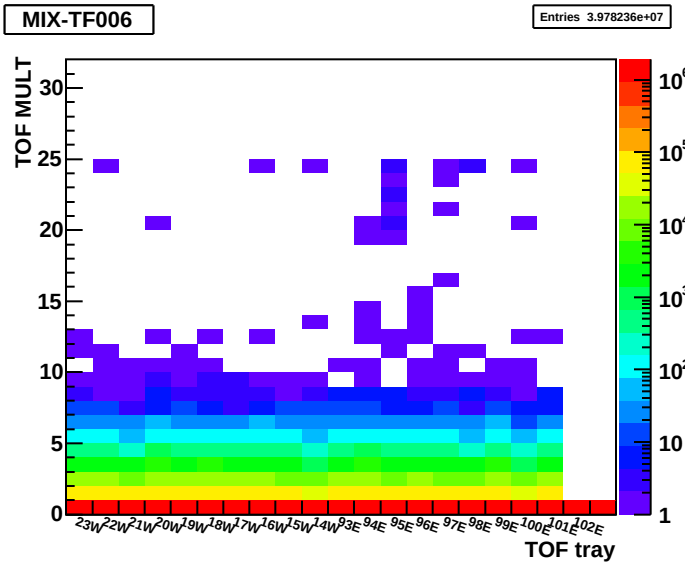

MIX-TF001
Entries 3.978236e+07
TOF MULT
TOF tray

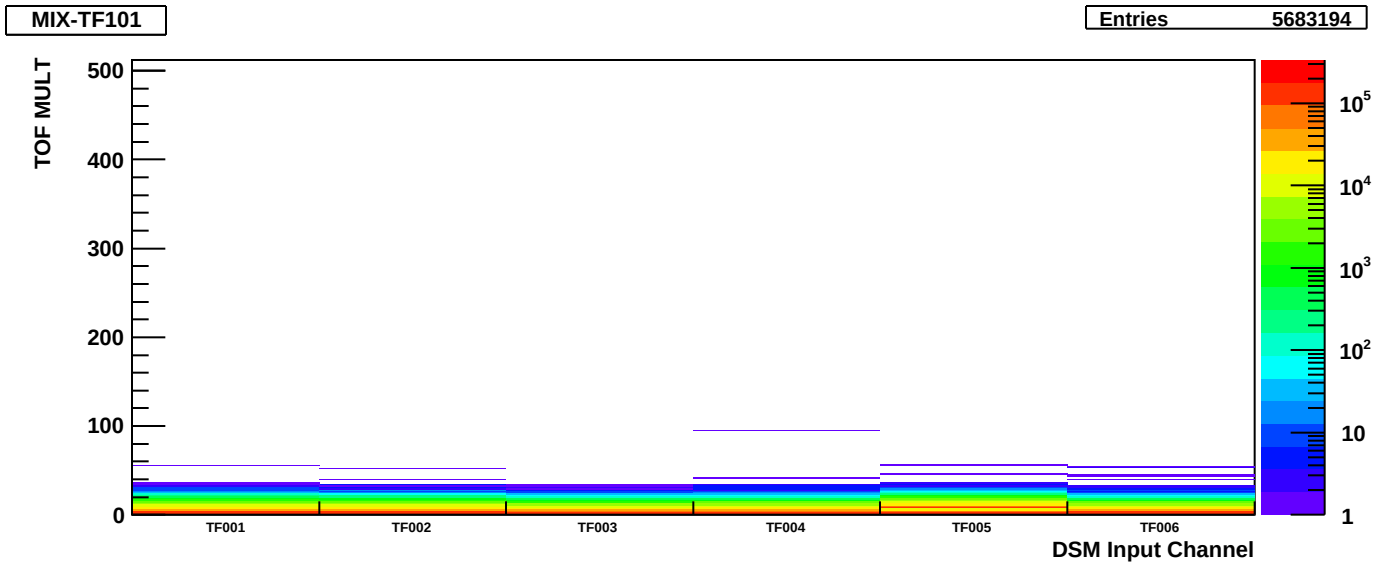

MIX-TF002
Entries 3.978236e+07
TOF MULT
TOF tray

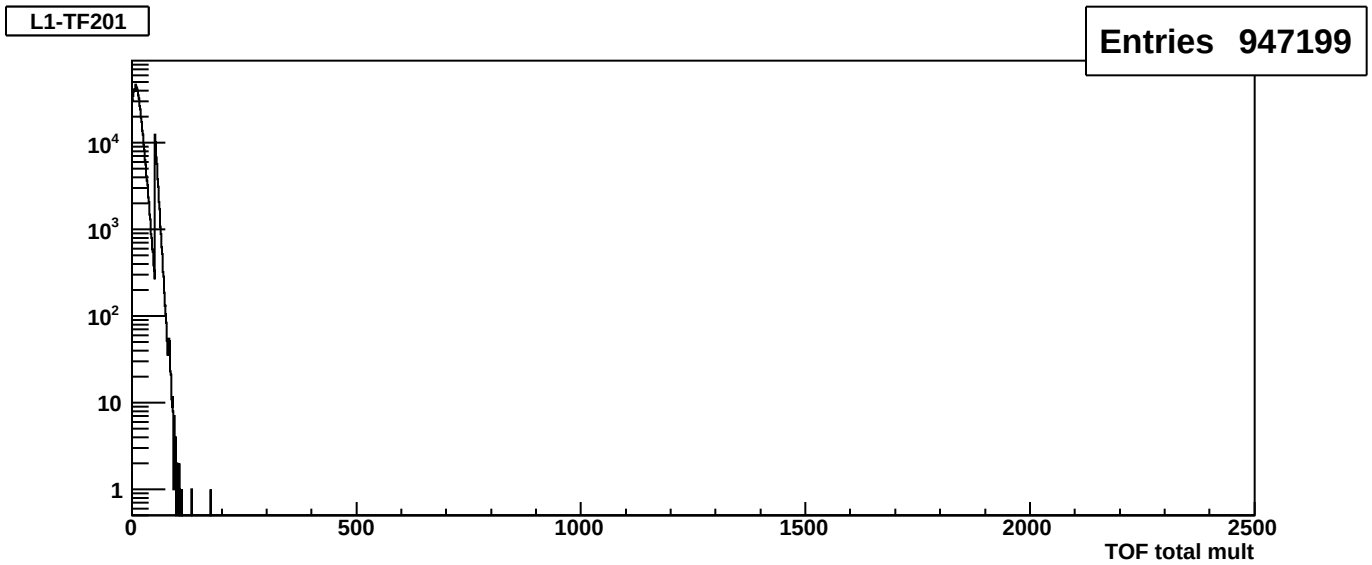

MIX-TF003
Entries 3.978236e+07
TOF MULT
TOF tray

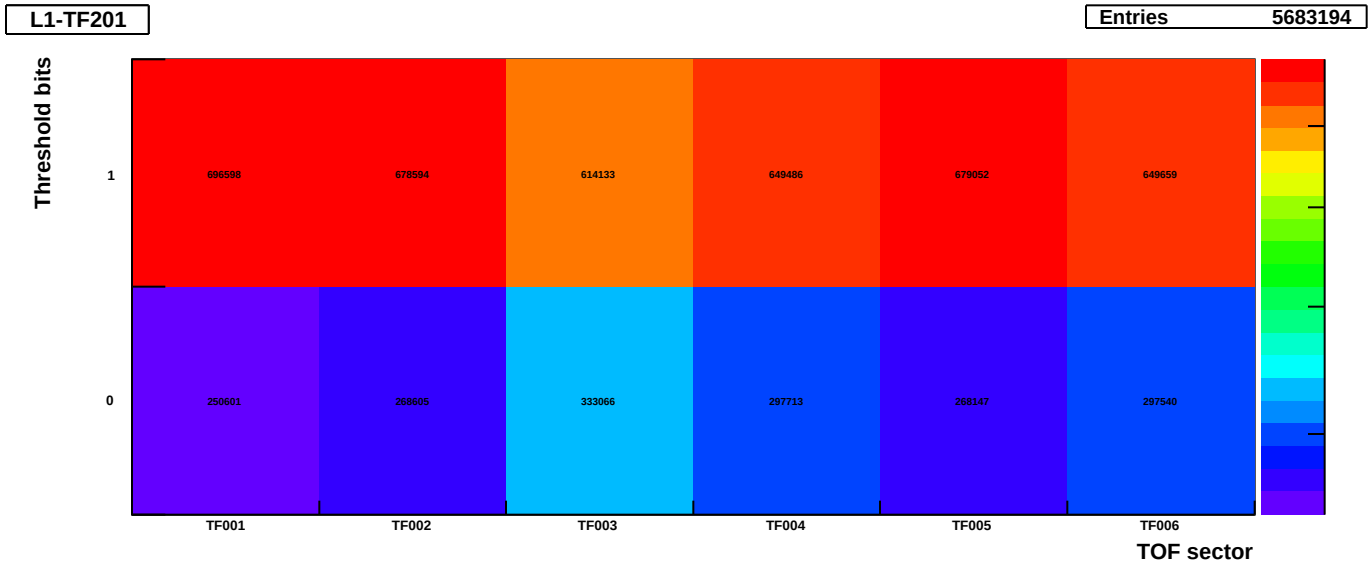

MIX-TF004
Entries 3.978236e+07
TOF MULT
TOF tray

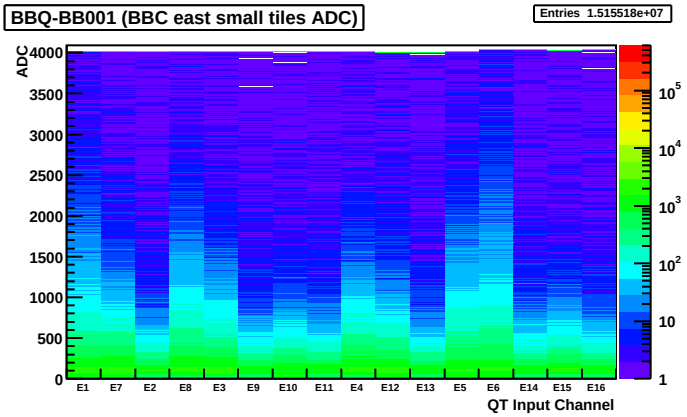

MIX-TF005
Entries 3.978236e+07
TOF MULT
TOF tray

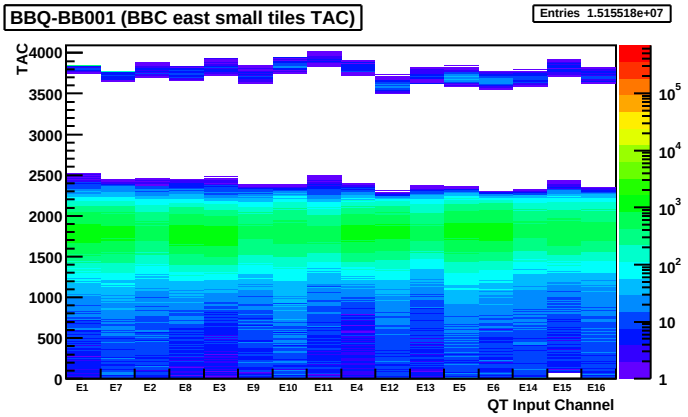

MIX-TF006
Entries 3.978236e+07
TOF MULT
TOF tray

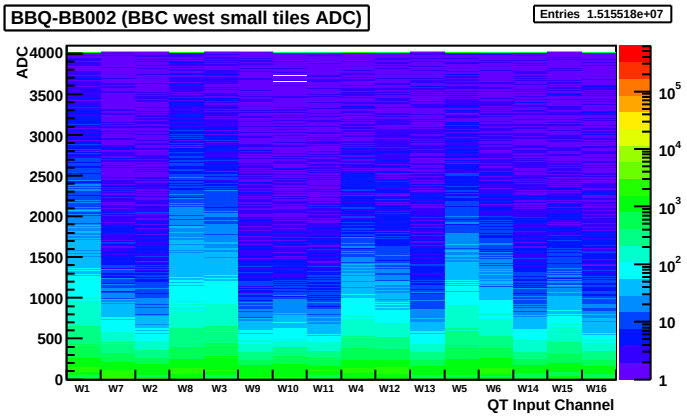

MIX-TF101
Entries 5683194
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

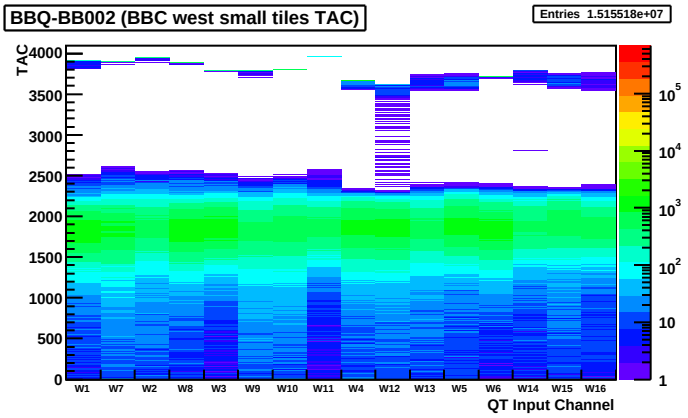

L1-TF201
Entries 947199
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

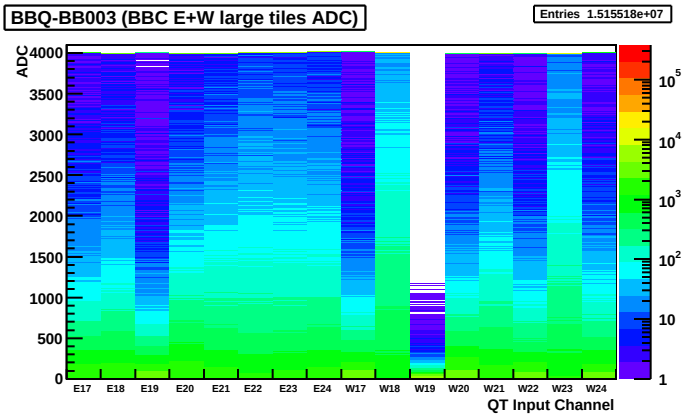

L1-TF201
Entries 5683194
Threshold bits
1
0
696598
678594
614133
649486
679052
649659
250601
268605
333066
297713
268147
297540
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

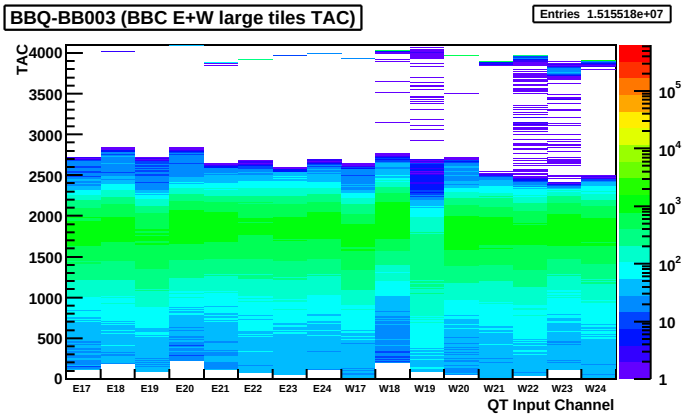

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.515518e+07
ADC
QT Input Channel

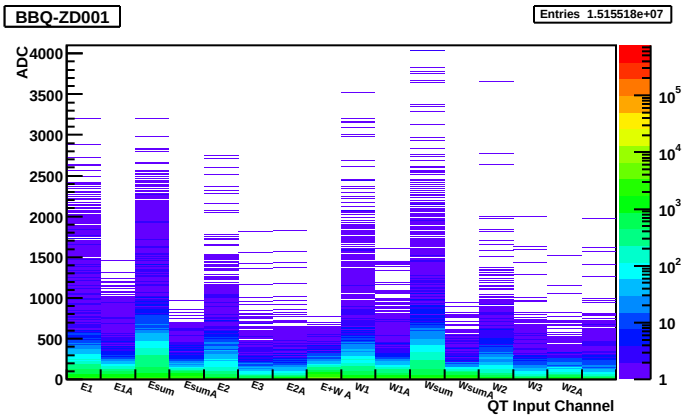

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.515518e+07
TAC
QT Input Channel

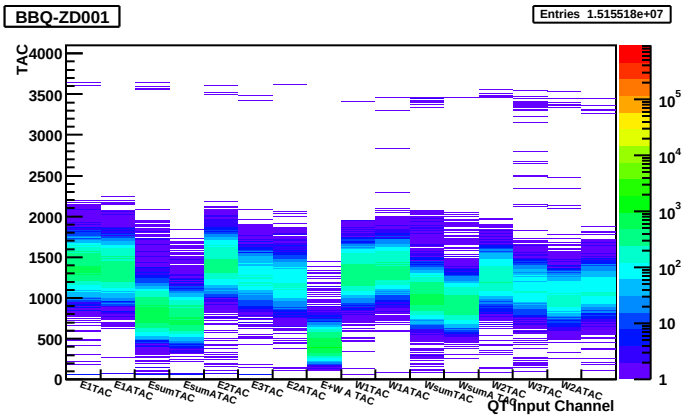

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.515518e+07
ADC
QT Input Channel

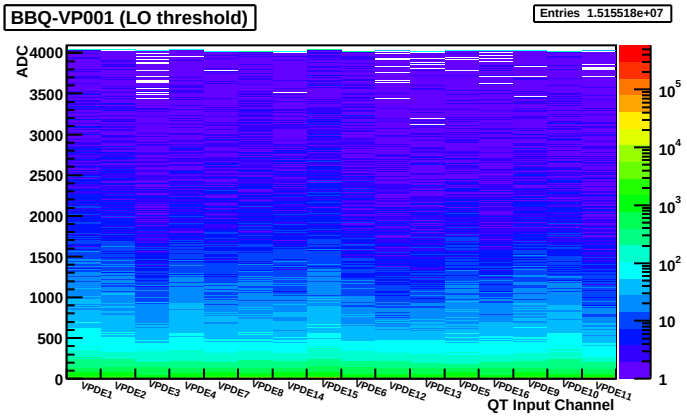

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.515518e+07
TAC
QT Input Channel

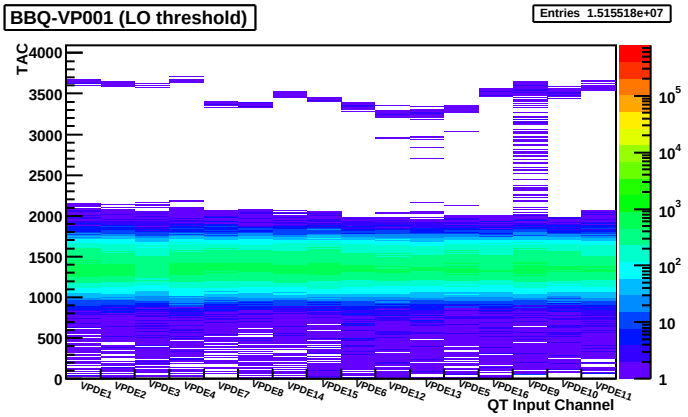

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.515518e+07
ADC
QT Input Channel

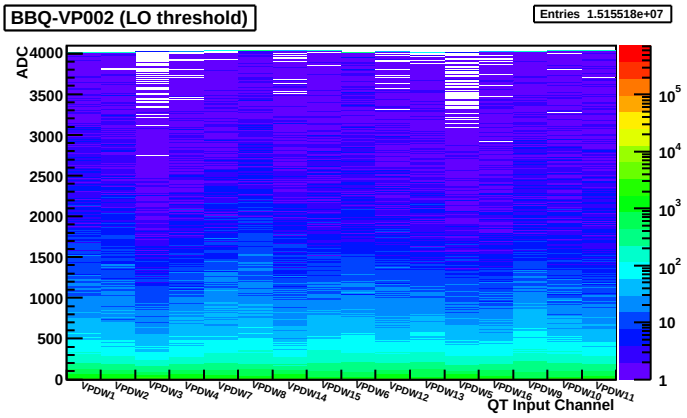

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.515518e+07
TAC
QT Input Channel

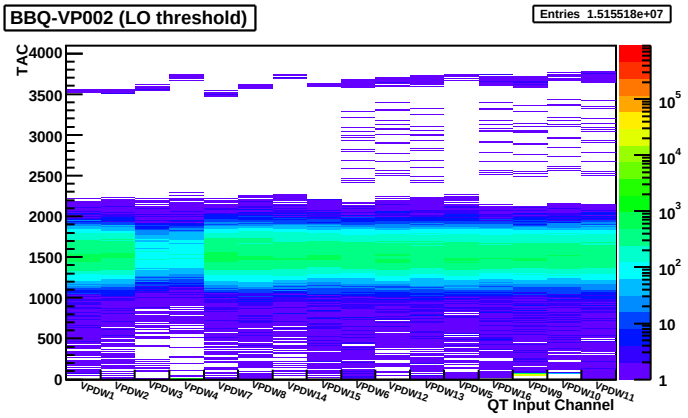

BBQ-ZD001
Entries 1.515518e+07
ADC
QT Input Channel

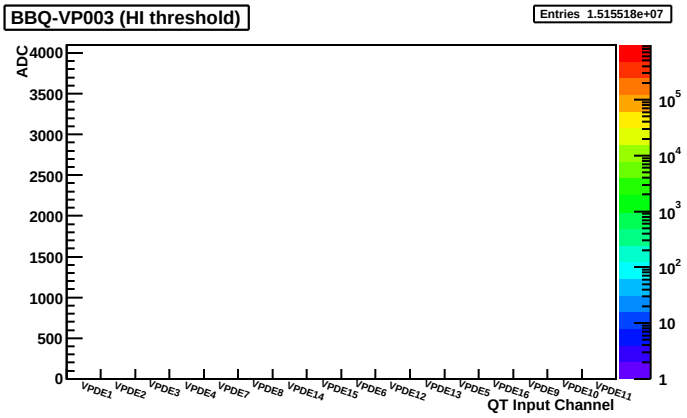

BBQ-ZD001
Entries 1.515518e+07
TAC
QT Input Channel

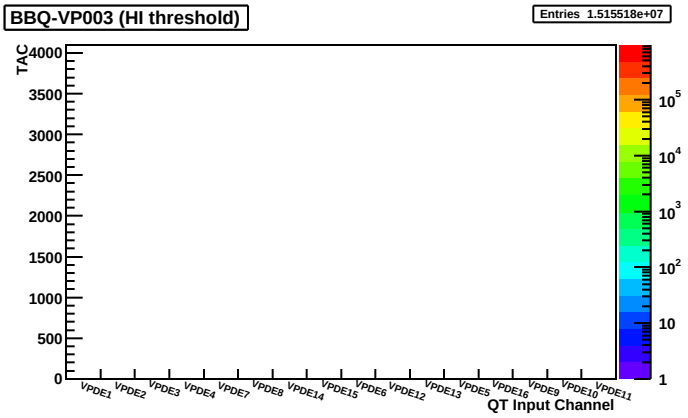

BBQ-VP001 (LO threshold)
Entries 1.515518e+07
ADC
QT Input Channel

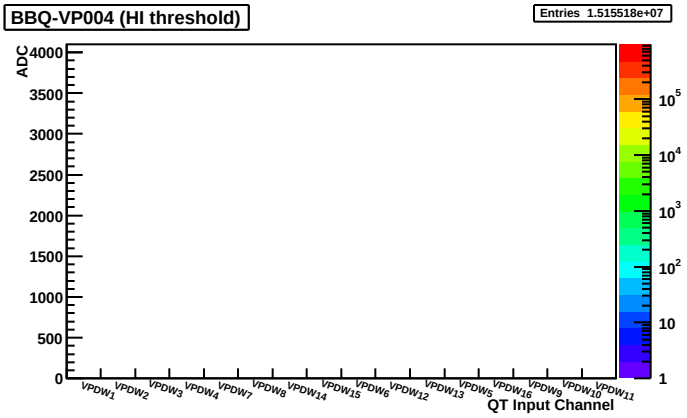

BBQ-VP001 (LO threshold)
Entries 1.515518e+07
TAC
QT Input Channel

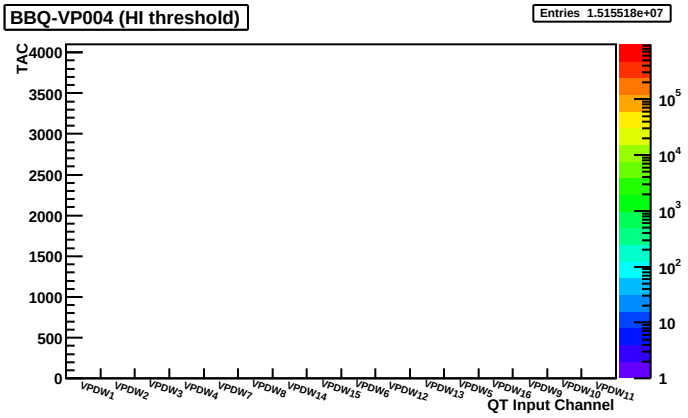

BBQ-VP002 (LO threshold)
Entries 1.515518e+07
ADC
QT Input Channel

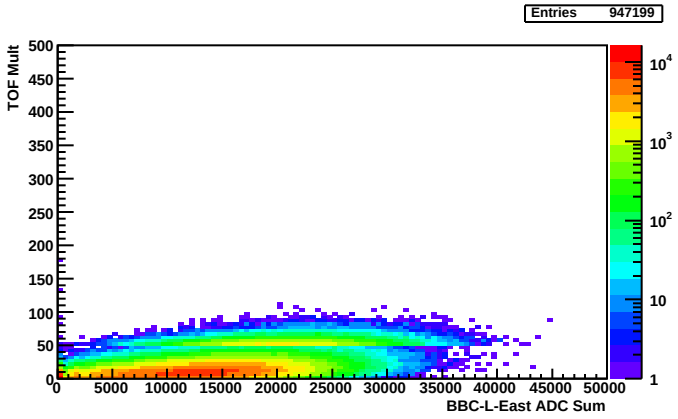

BBQ-VP002 (LO threshold)
Entries 1.515518e+07
TAC
QT Input Channel

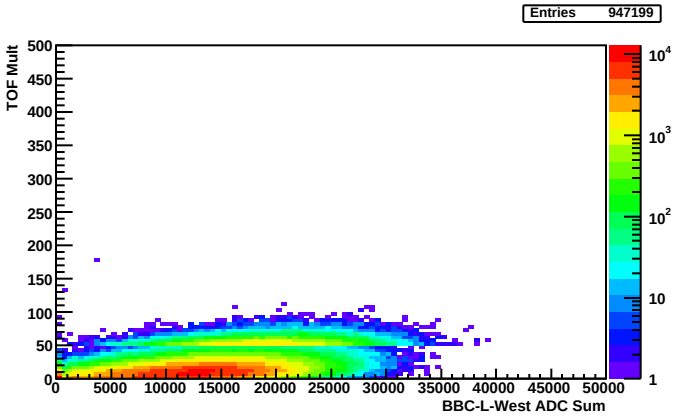

BBQ-VP003 (HI threshold)
Entries 1.515518e+07
ADC
QT Input Channel

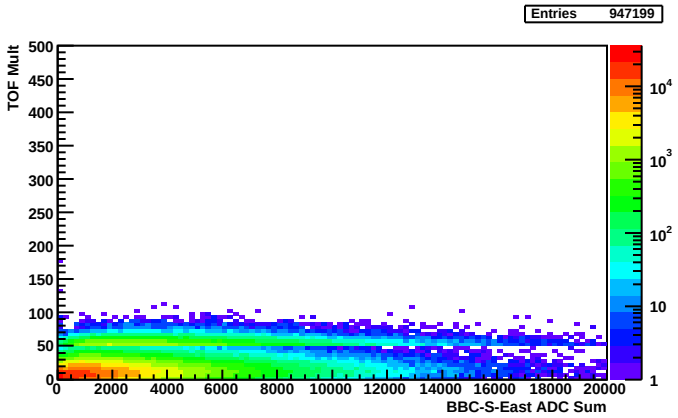

BBQ-VP003 (HI threshold)
Entries 1.515518e+07
TAC
QT Input Channel

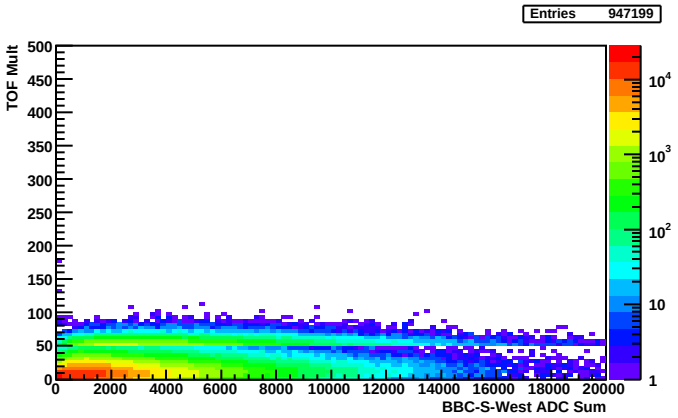

BBQ-VP004 (HI threshold)
Entries 1.515518e+07
ADC
QT Input Channel

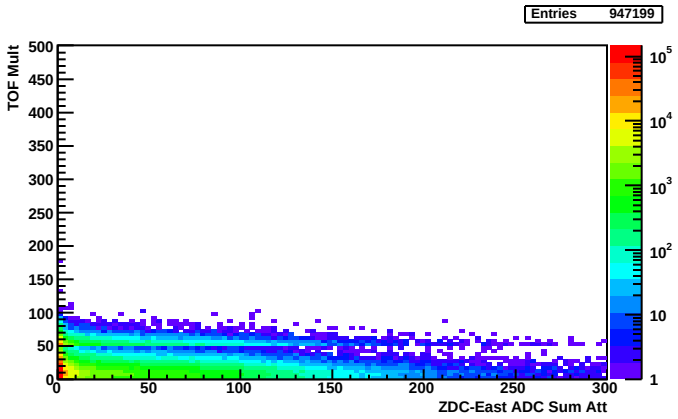

BBQ-VP004 (HI threshold)
Entries 1.515518e+07
TAC
QT Input Channel

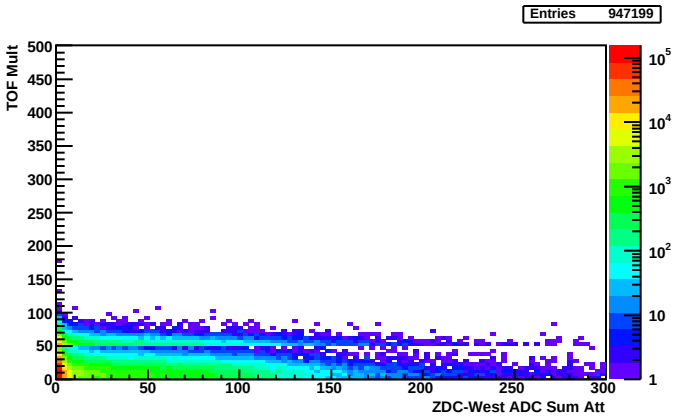

Entries 947199
TOF Mult
BBC-L-East ADC Sum

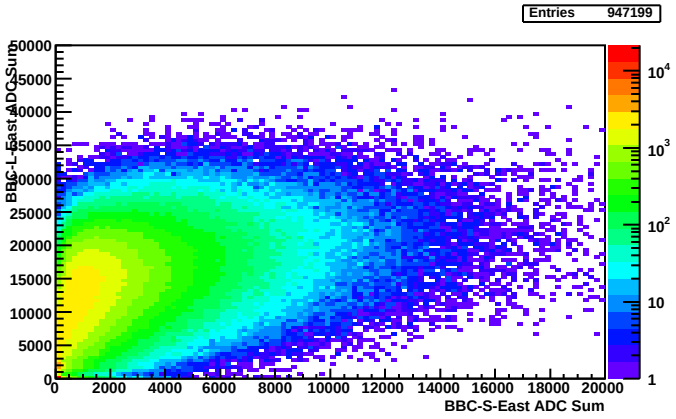

Entries 947199
TOF Mult
BBC-L-West ADC Sum

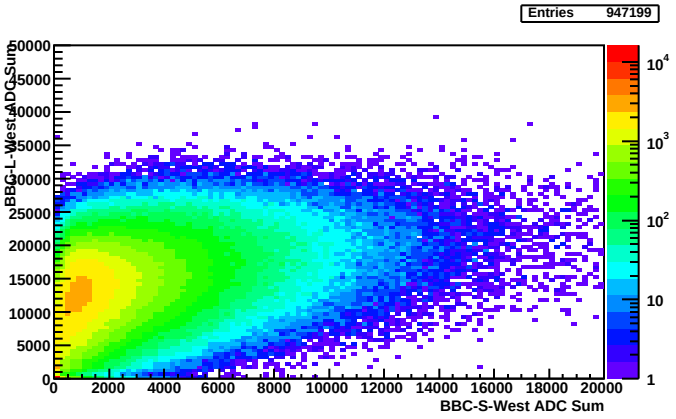

Entries 947199
TOF Mult
BBC-S-East ADC Sum

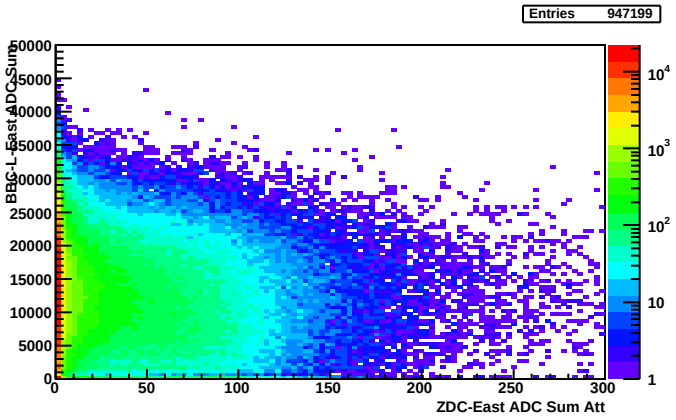

Entries 947199
TOF Mult
BBC-S-West ADC Sum

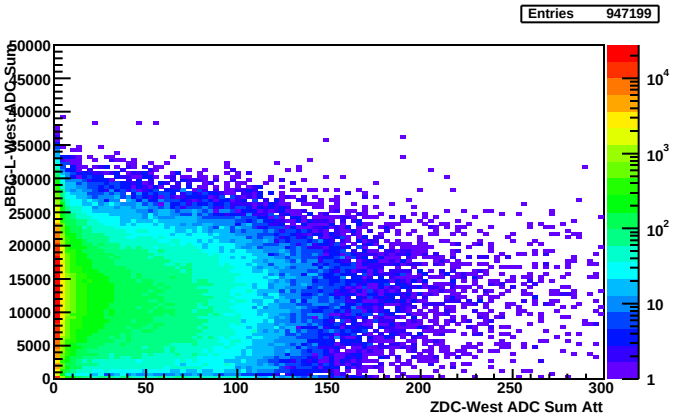

Entries 947199
TOF Mult
ZDC-East ADC Sum Att

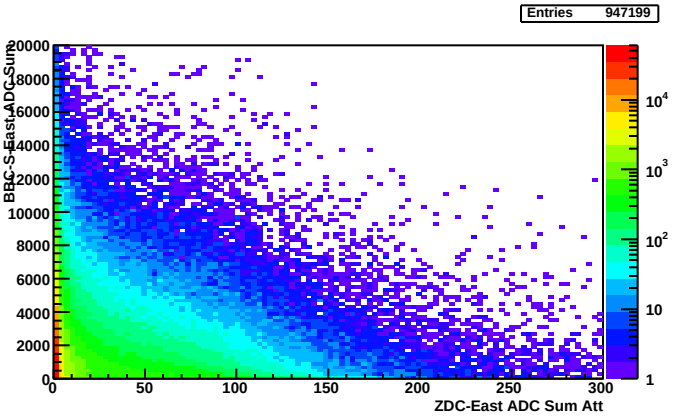

Entries 947199
TOF Mult
ZDC-West ADC Sum Att

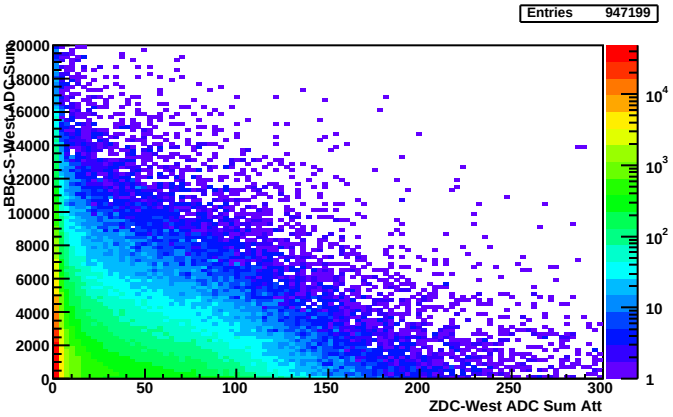

Entries 947199
BBC-L-East ADC Sum
BBC-S-East ADC Sum

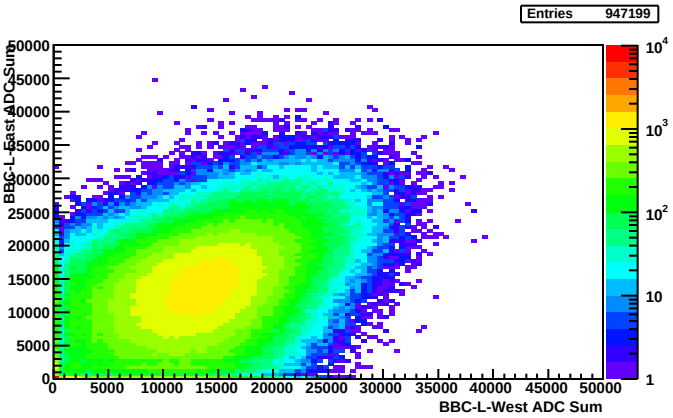

Entries 947199
BBC-L-West ADC Sum
BBC-S-West ADC Sum

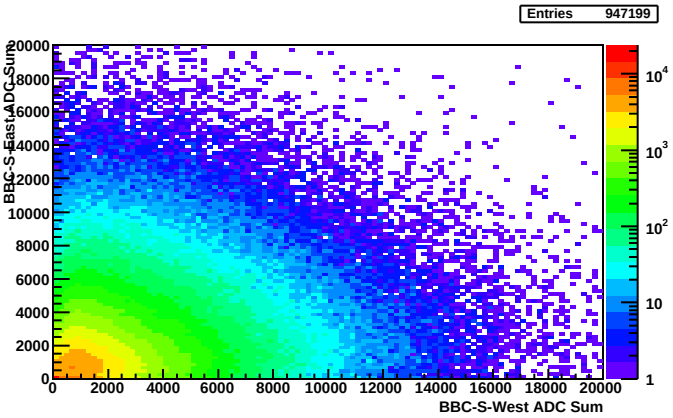

Entries 947199
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

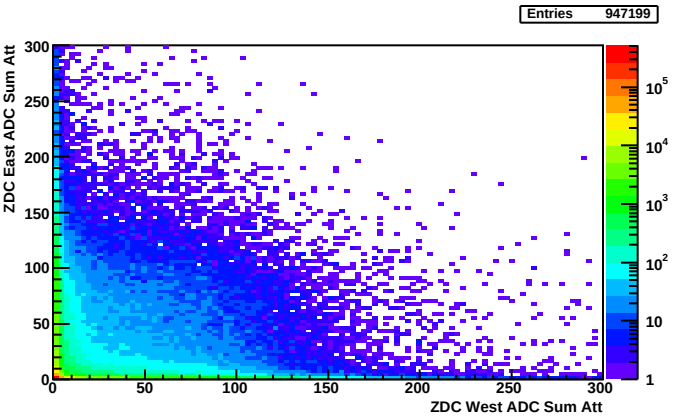

Entries 947199
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

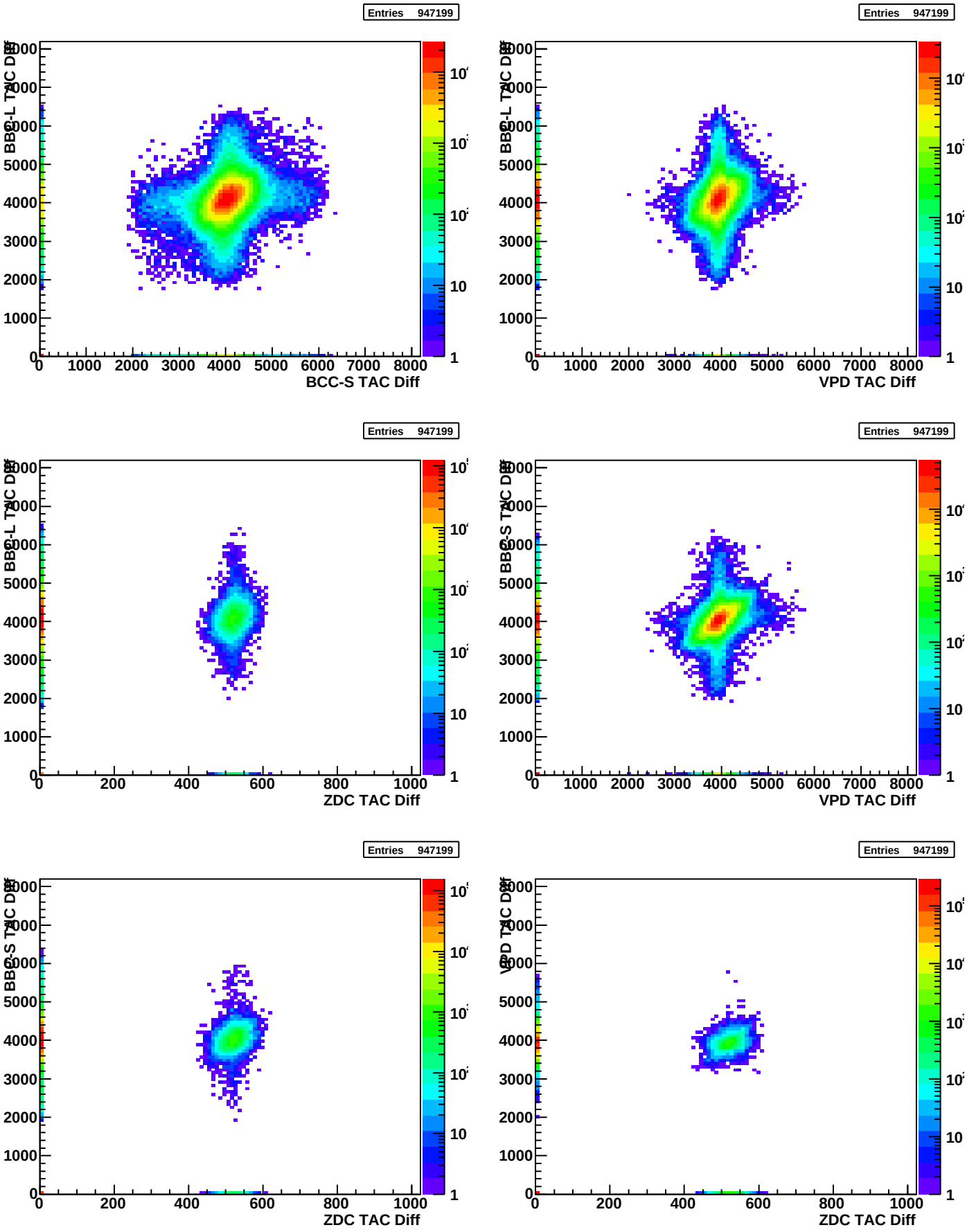

Entries 947199
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

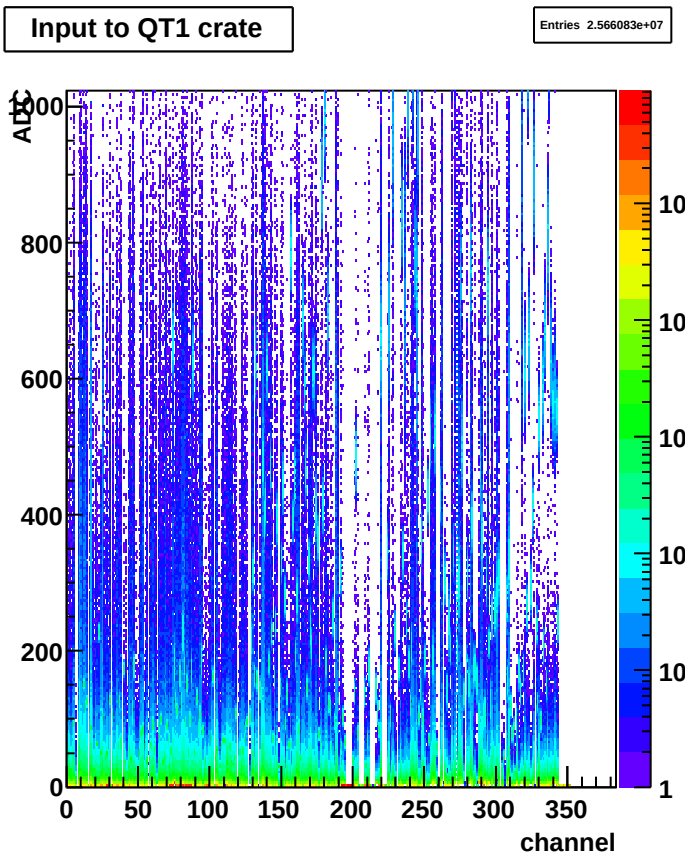

Entries 947199
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

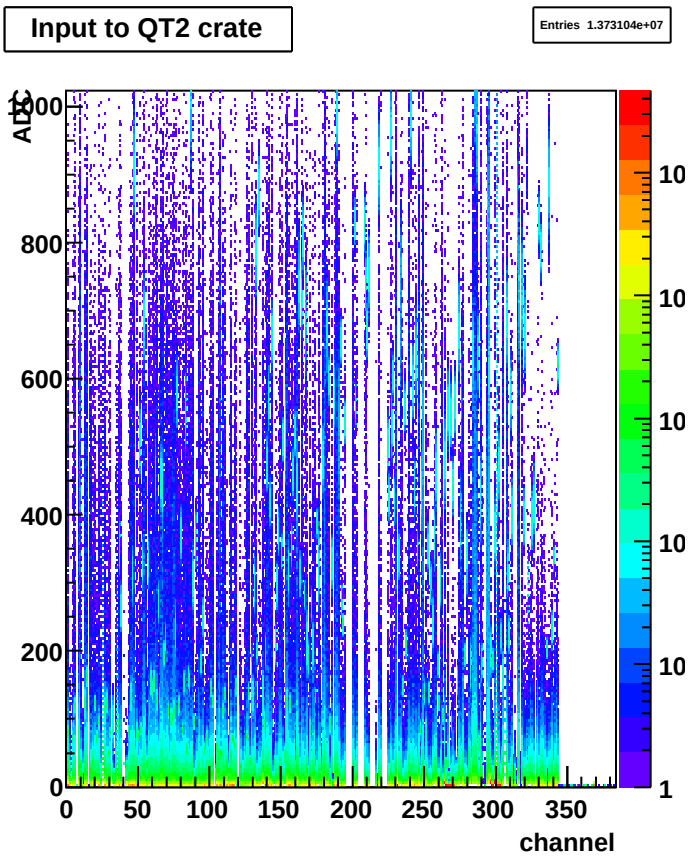

Entries 947199
BBC-L-East ADC Sum
BBC-L-West ADC Sum

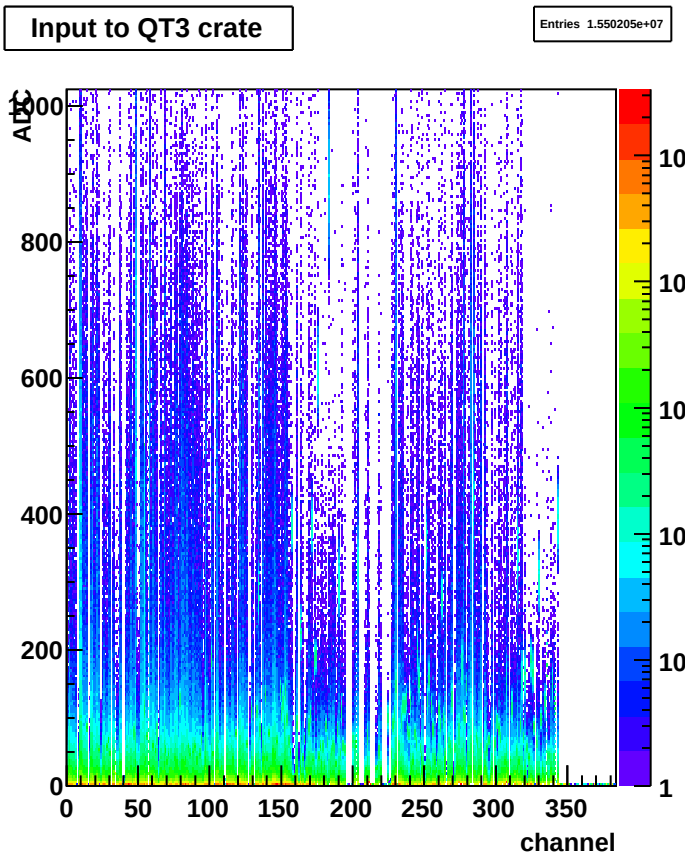

Entries 947199
BBC-S-East ADC Sum
BBC-S-West ADC Sum

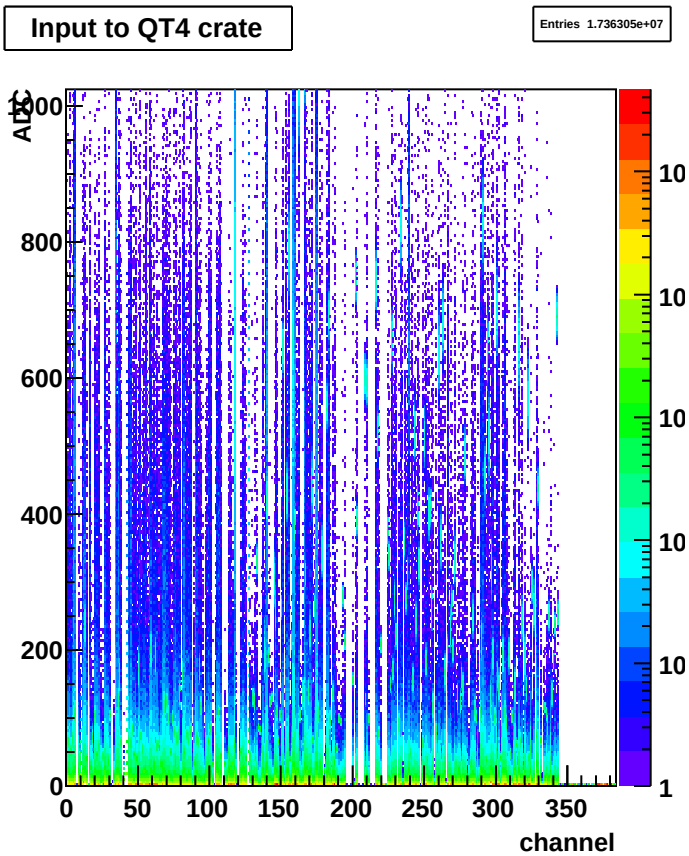

Entries 947199
ZDC East ADC Sum Att
ZDC West ADC Sum Att

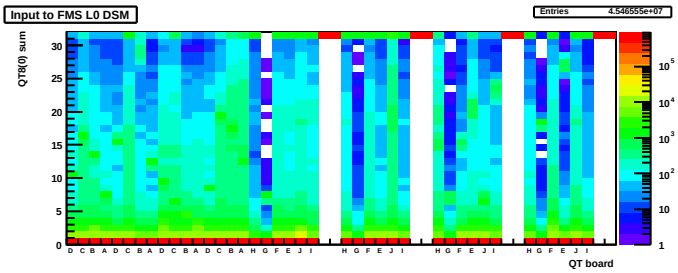

Entries 947199
BBC-L TAC Diff
BCC-S TAC Diff
Entries 947199
BBC-L TAC Diff
VPD TAC Diff
Entries 947199
BBC-L TAC Diff
ZDC TAC Diff
Entries 947199
BBC-S TAC Diff
VPD TAC Diff
Entries 947199
BBC-S TAC Diff
ZDC TAC Diff
Entries 947199
VPD TAC Diff
ZDC TAC Diff

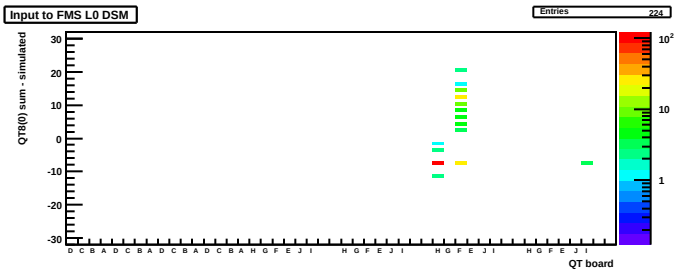

Input to QT1 crate
Entries 2.566083e+07
ADC
channel

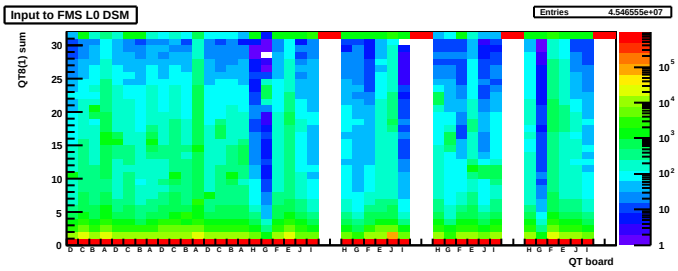

Input to QT2 crate
Entries 1.373104e+07
ADC
channel

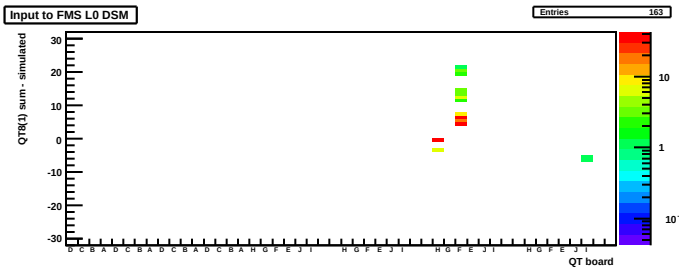

Input to QT3 crate
Entries 1.550205e+07
ADC
channel

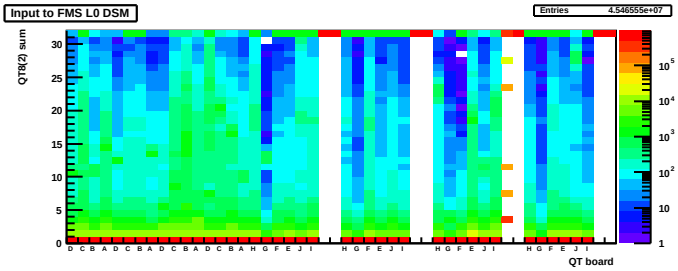

Input to QT4 crate
Entries 1.736305e+07
ADC
channel

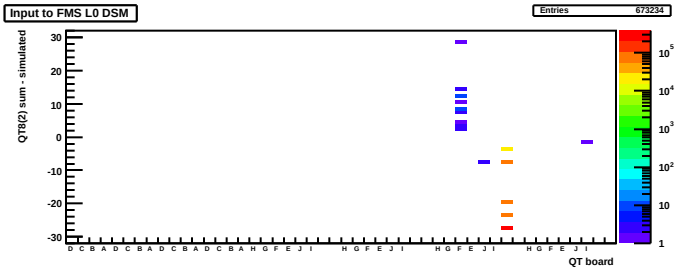

Input to FMS L0 DSM
Entries 4.546555e+07
QT8(0) sum
QT board

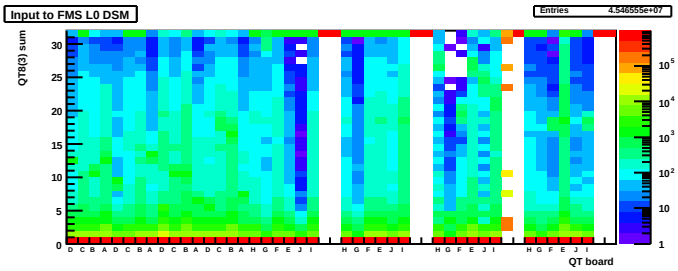

Input to FMS L0 DSM
Entries 224
QT8(0) sum - simulated
QT board

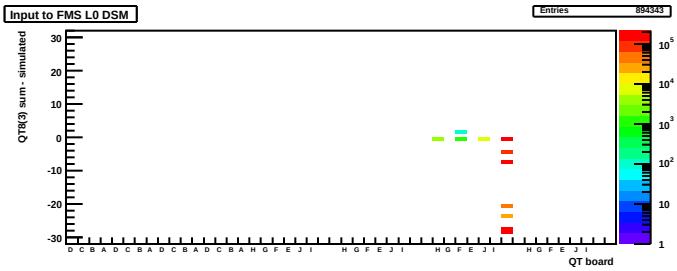

Input to FMS L0 DSM
Entries 4.546555e+07
QT8(1) sum
QT board

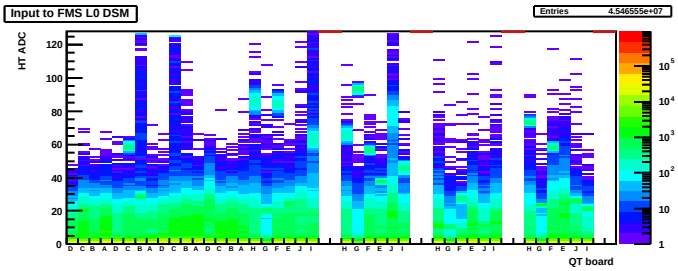

Input to FMS L0 DSM
Entries 163
QT8(1) sum - simulated
QT board

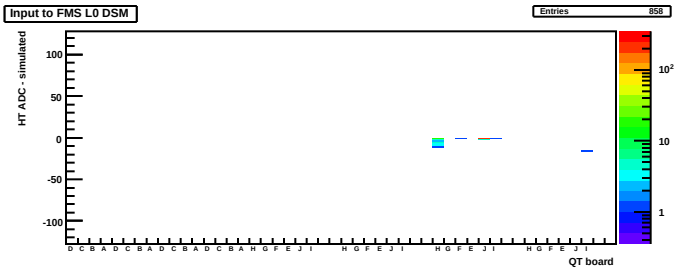

Input to FMS L0 DSM
Entries 4.546555e+07
QT8(2) sum
QT board

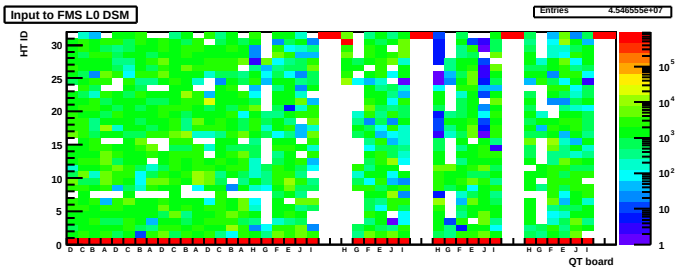

Input to FMS L0 DSM
Entries 673234
QT8(2) sum - simulated
QT board

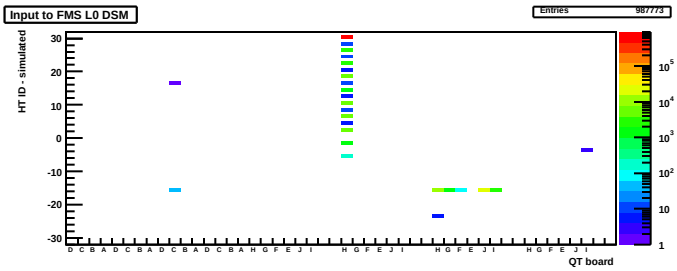

Input to FMS L0 DSM
Entries 4.546555e+07
QT8(3) sum
QT board

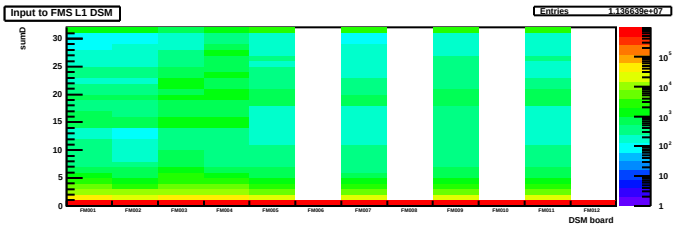

Input to FMS L0 DSM
Entries 894343
QT8(3) sum - simulated
QT board

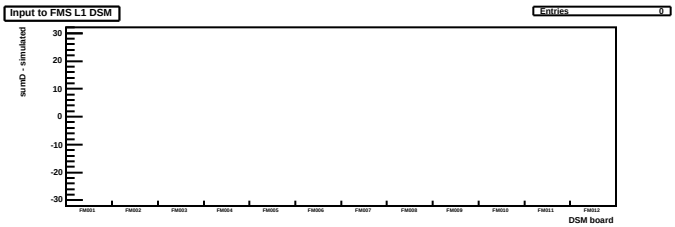

Input to FMS L0 DSM
Entries 4.546555e+07
HT ADC
QT board

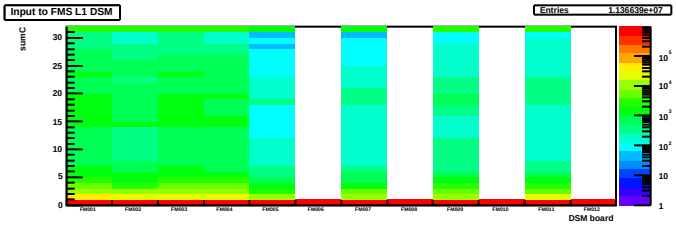

Input to FMS L0 DSM
Entries 858
HT ADC - simulated
QT board

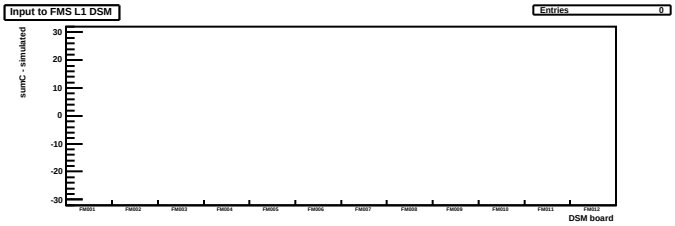

Input to FMS L0 DSM
Entries 4.546555e+07
HT ID
QT board

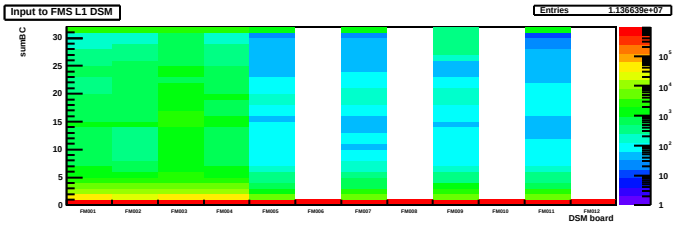

Input to FMS L0 DSM
Entries 987773
HT ID - simulated
QT board

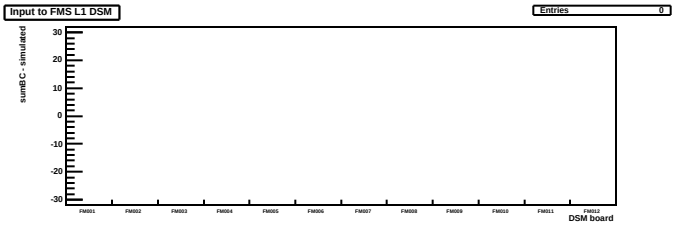

Input to FMS L1 DSM
Entries 1.136639e+07
sumD
DSM board

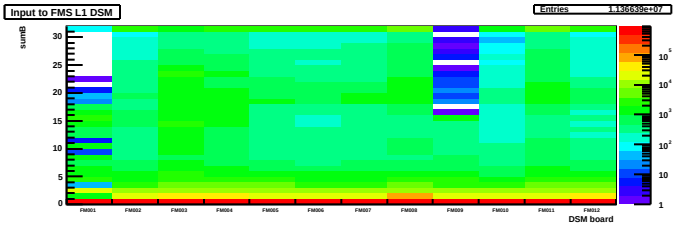

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

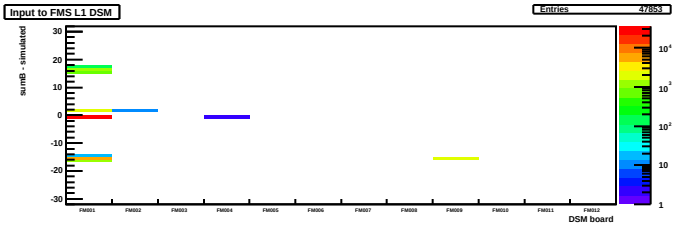

Input to FMS L1 DSM
Entries 1.136639e+07
sumC
DSM board

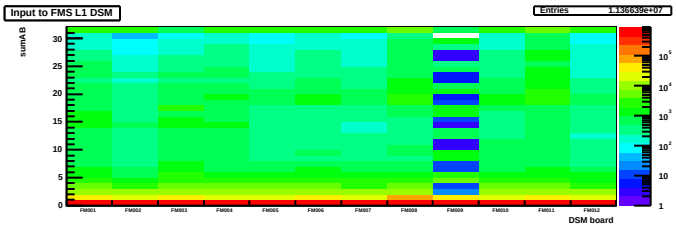

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

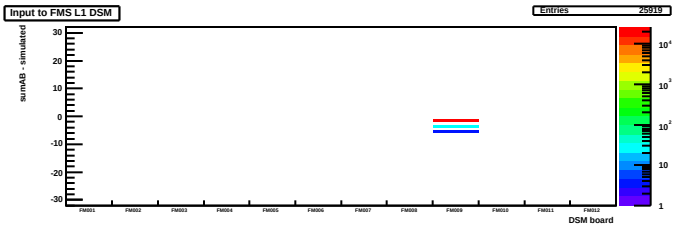

Input to FMS L1 DSM
Entries 1.136639e+07
sumBC
DSM board

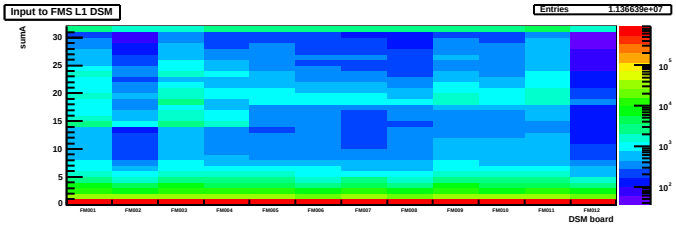

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

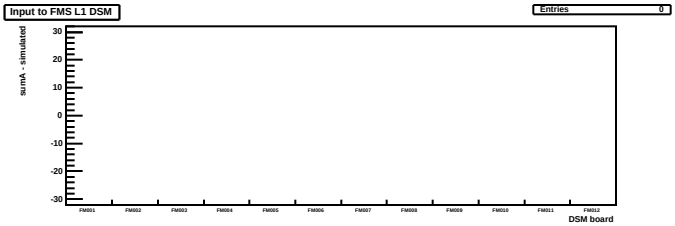

Input to FMS L1 DSM
Entries 1.136639e+07
sumB
DSM board

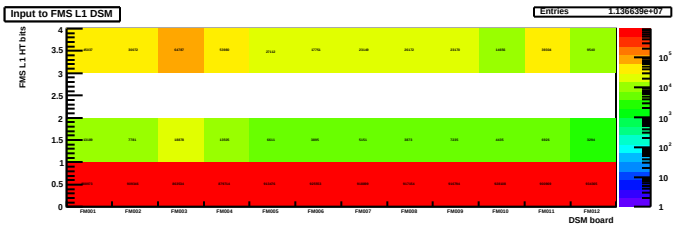

Input to FMS L1 DSM
Entries 47853
sumB - simulated
DSM board

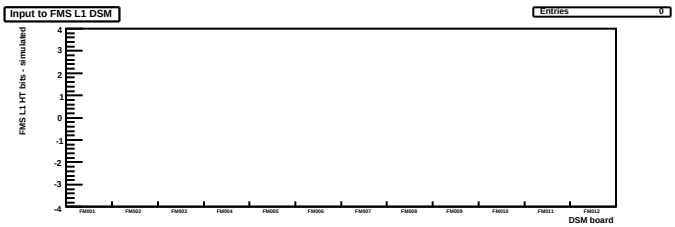

Input to FMS L1 DSM
Entries 1.136639e+07
sumAB
DSM board

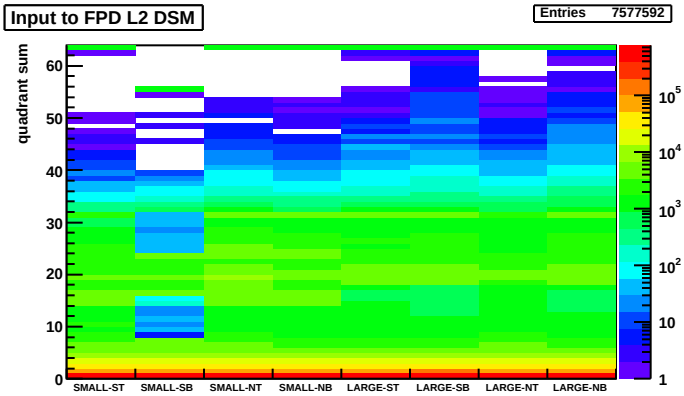

Input to FMS L1 DSM
Entries 25919
sumAB - simulated
DSM board

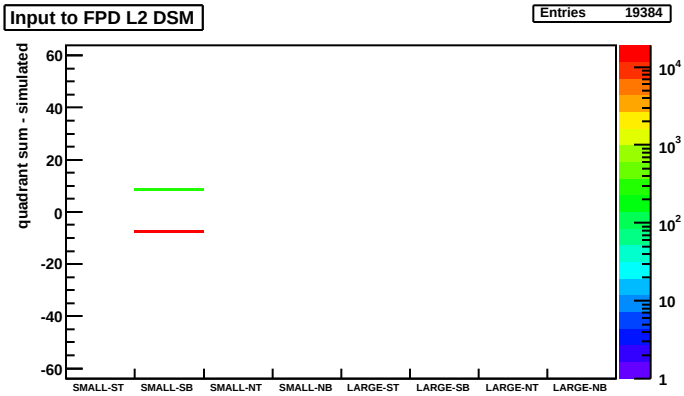

Input to FMS L1 DSM
Entries 1.136639e+07
sumA
DSM board

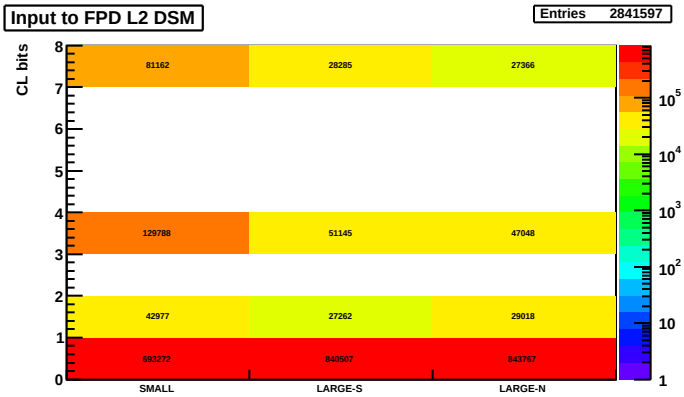

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

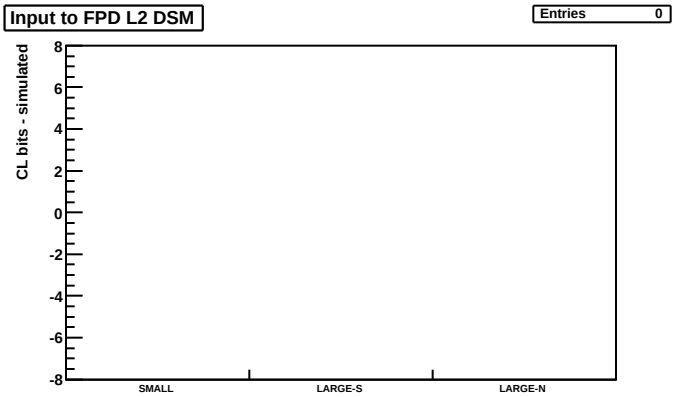

Input to FMS L1 DSM
Entries 1.136639e+07
FMS L1 HT bits
DSM board

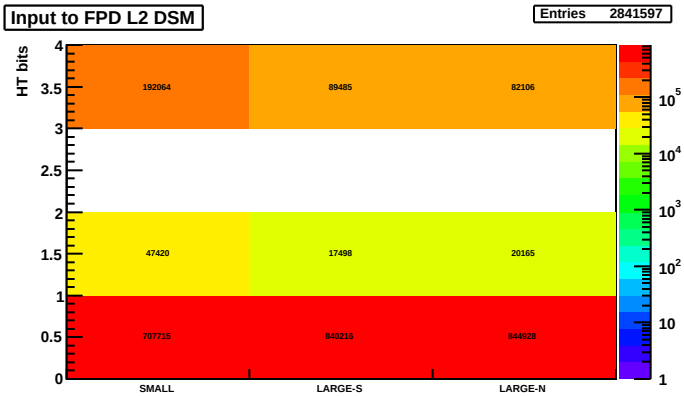

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

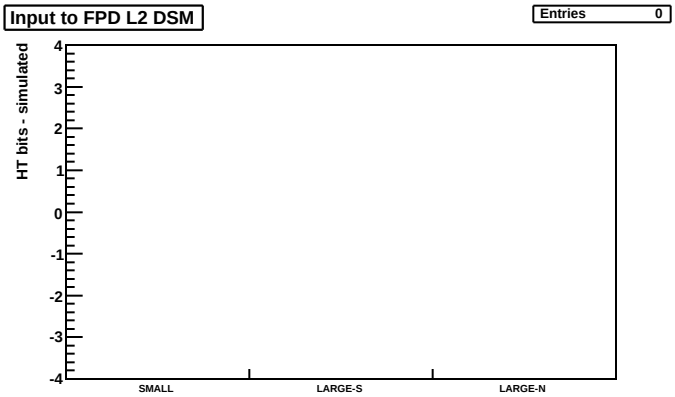

Input to FPD L2 DSM
Entries 7577592
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

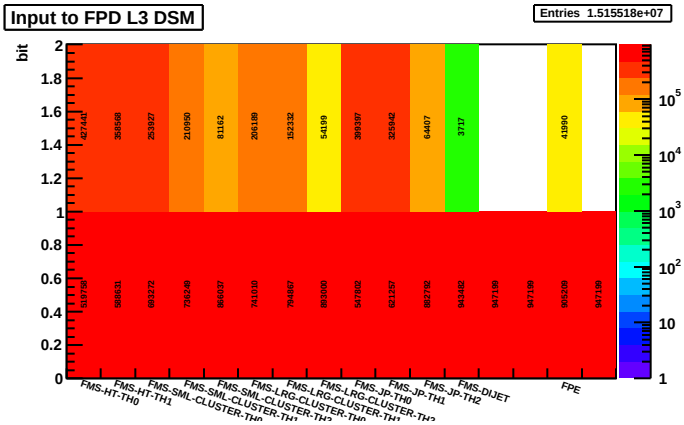

Input to FPD L2 DSM
Entries 19384
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

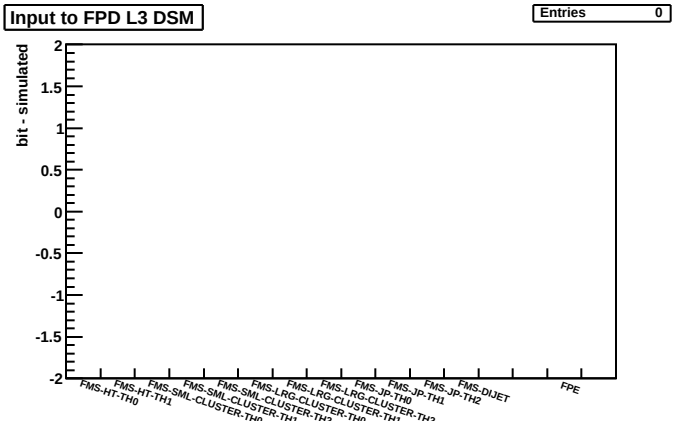

Input to FPD L2 DSM
Entries 2841597
CL bits
81162
28285
27366
129788
51145
47048
42977
27262
29018
693272
840507
843767
SMALL
LARGE-S
LARGE-N

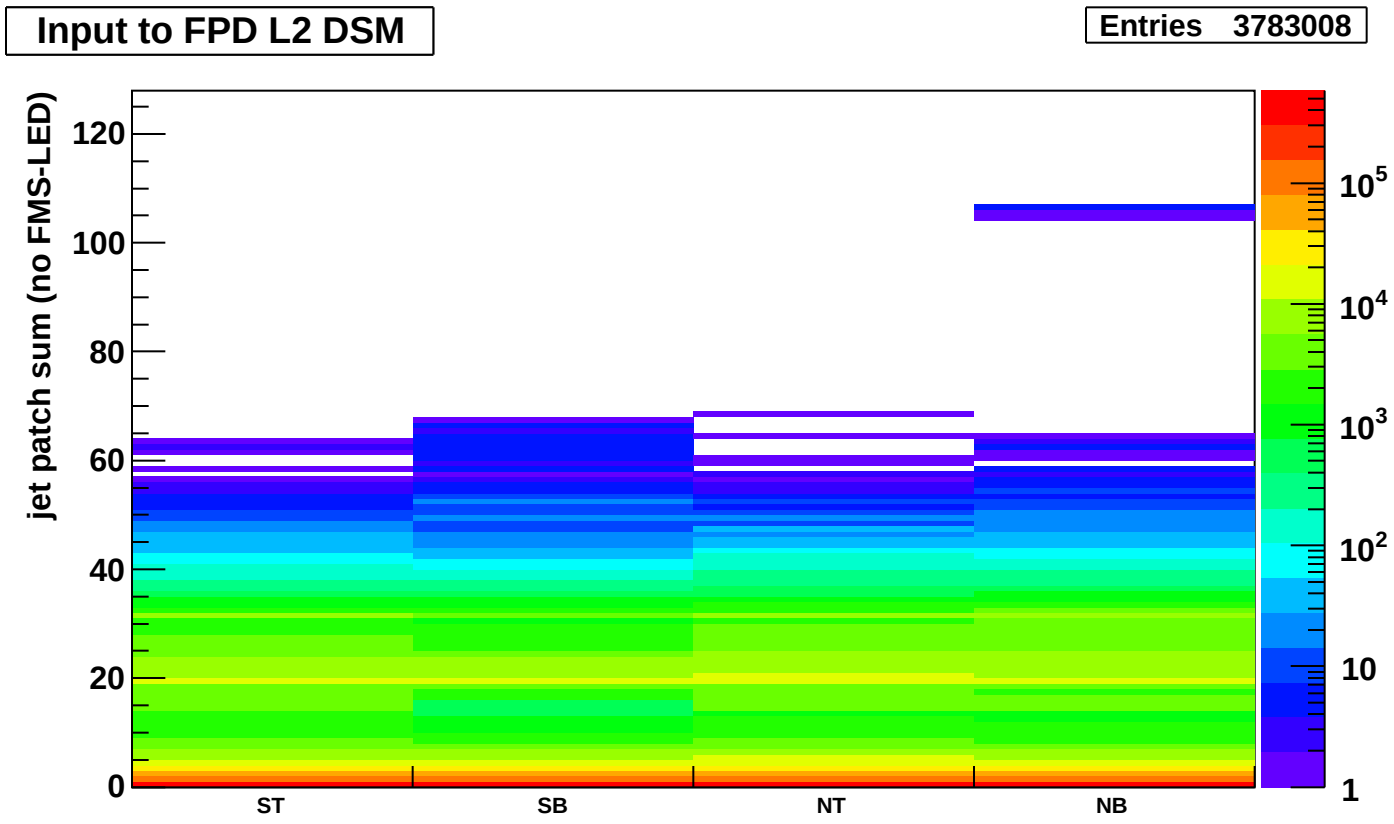

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

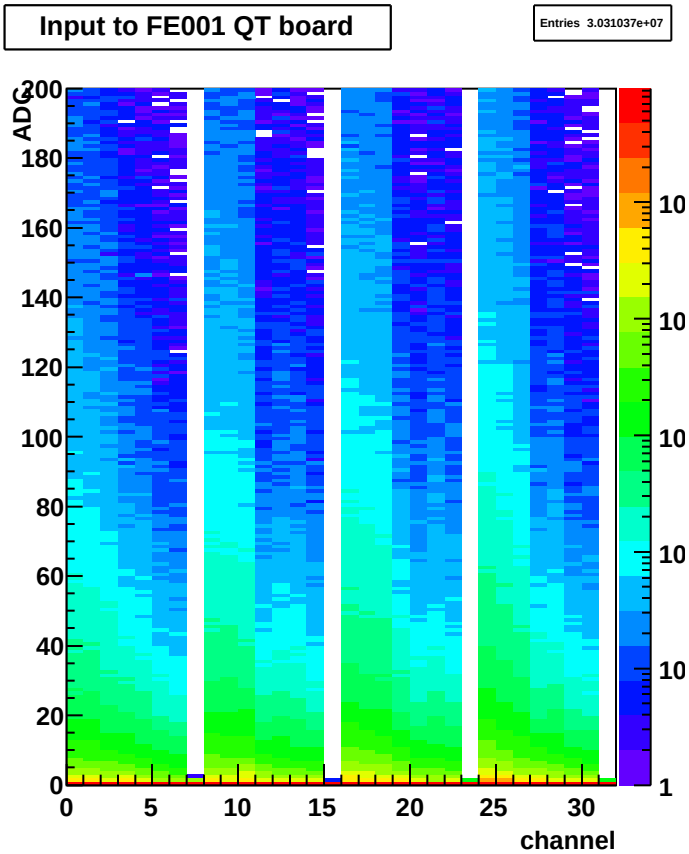

Input to FPD L2 DSM
Entries 2841597
HT bits
192064
89485
82106
47420
17498
20165
707715
840216
844928
SMALL
LARGE-S
LARGE-N

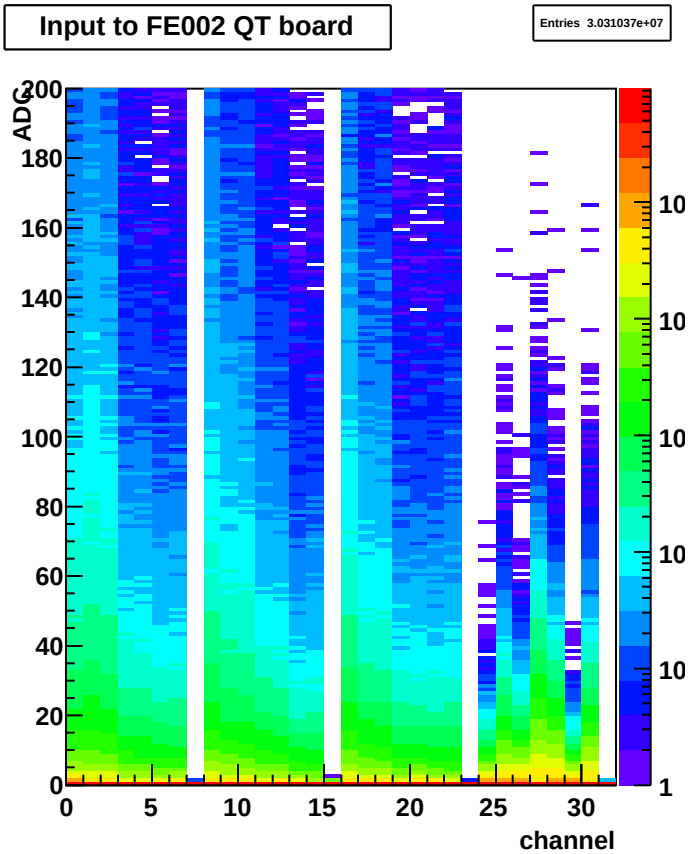

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

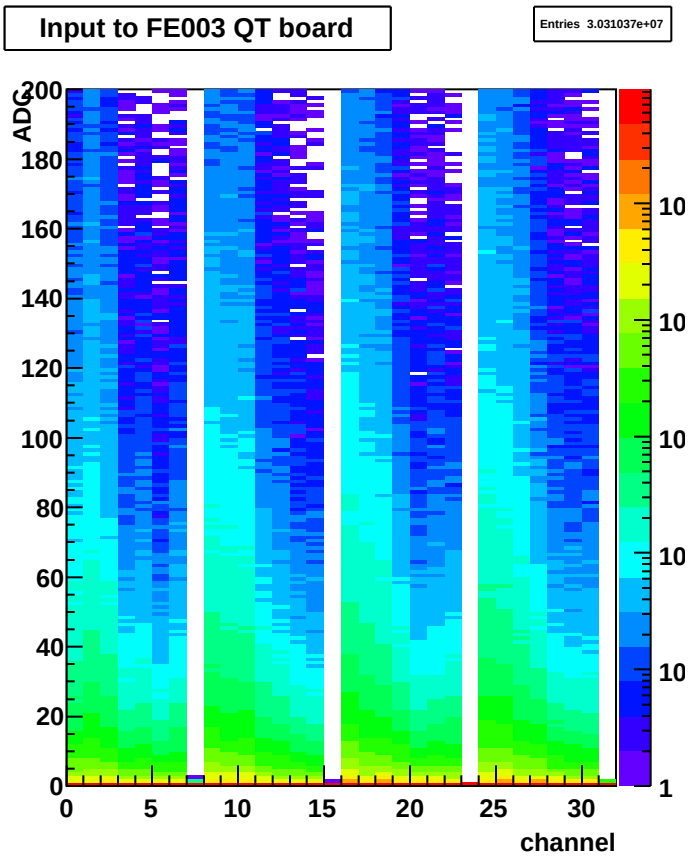

Input to FPD L3 DSM
Entries 1.515518e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

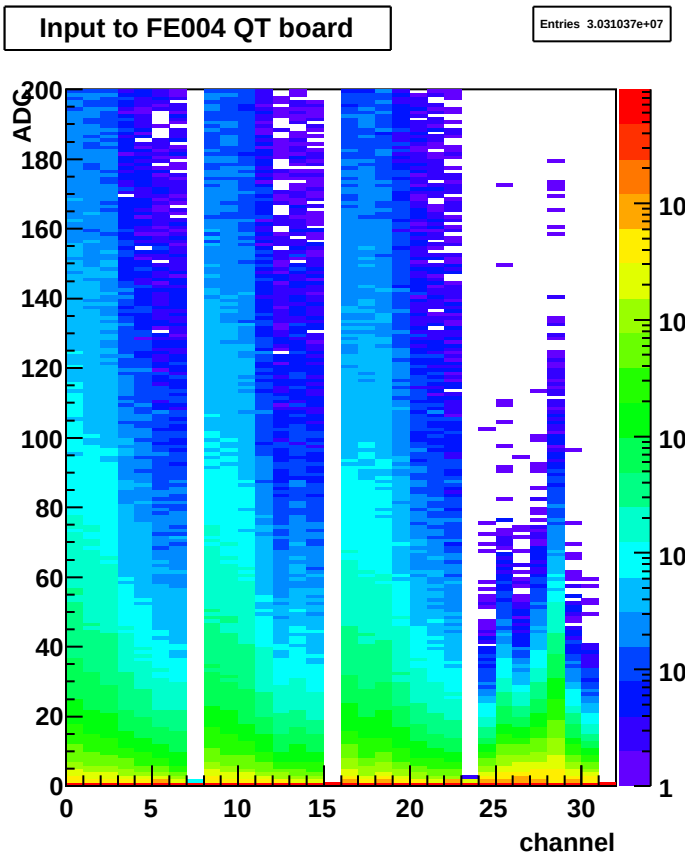

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

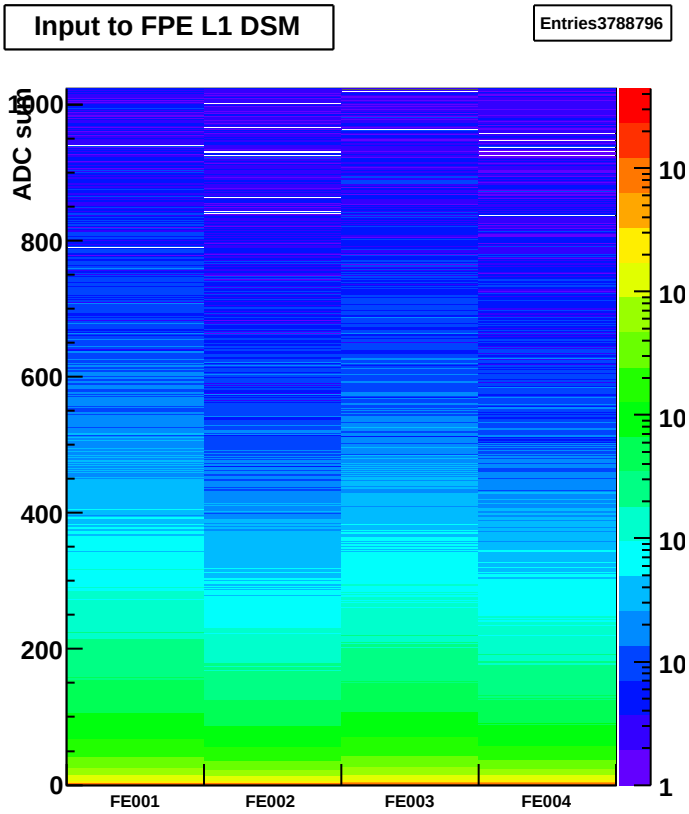

Input to FPD L2 DSM
Entries 3783008
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

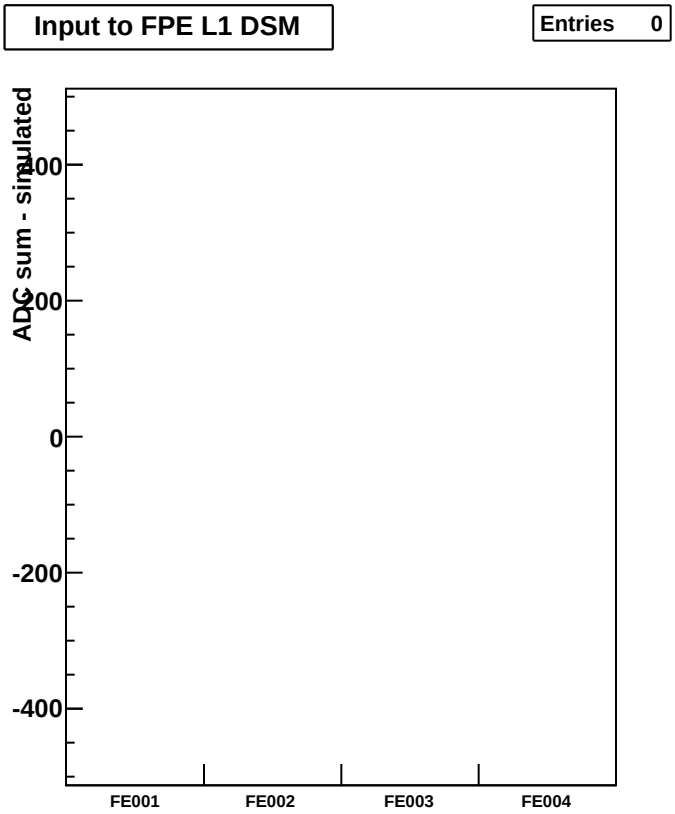

Input to FE001 QT board
Entries 3.031037e+07
ADC
channel

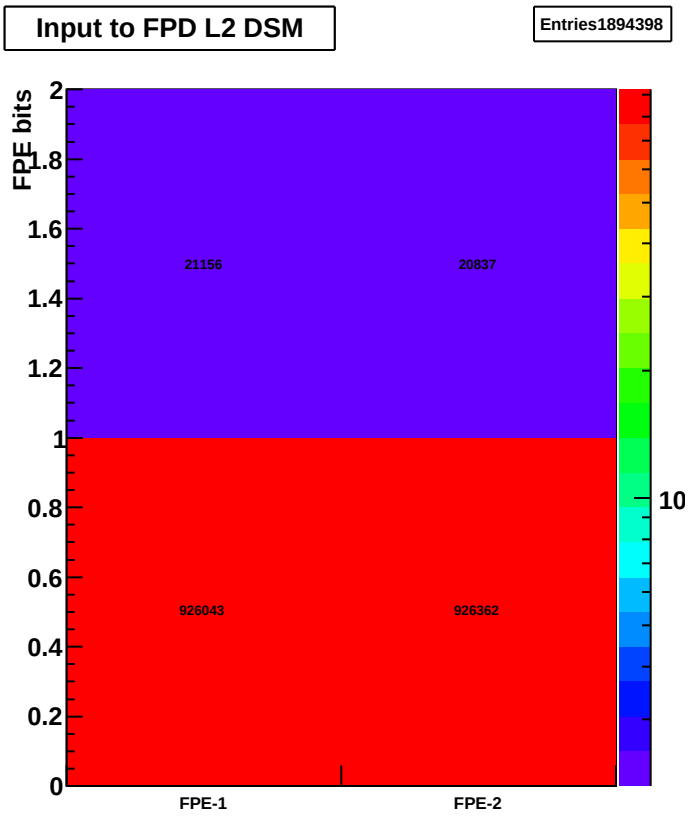

Input to FE002 QT board
Entries 3.031037e+07
ADC
channel

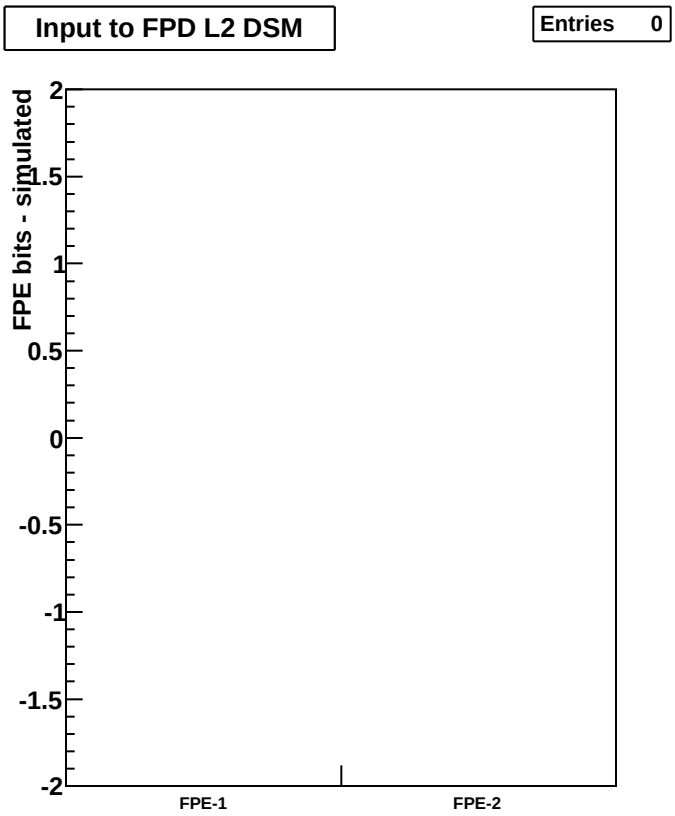

Input to FE003 QT board
Entries 3.031037e+07
ADC
channel


Input to FE004 QT board
Entries 3.031037e+07
ADC
channel


Input to FPE L1 DSM
Entries3788796
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1894398
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
21156
20837
926043
926362
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2