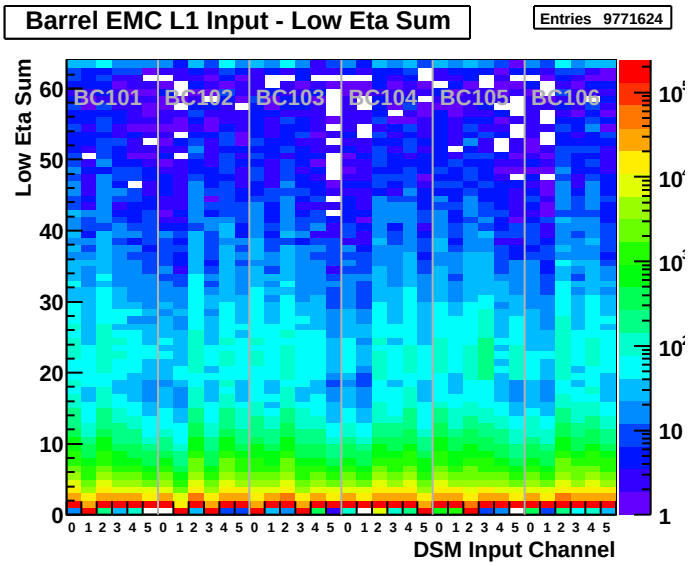
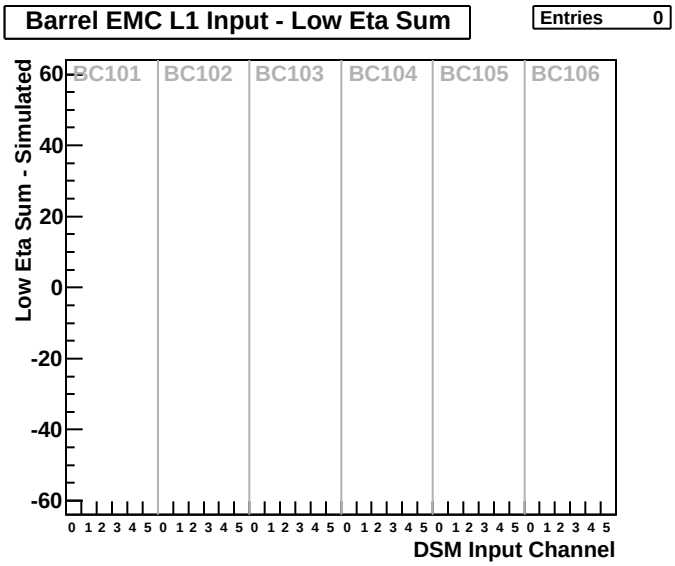
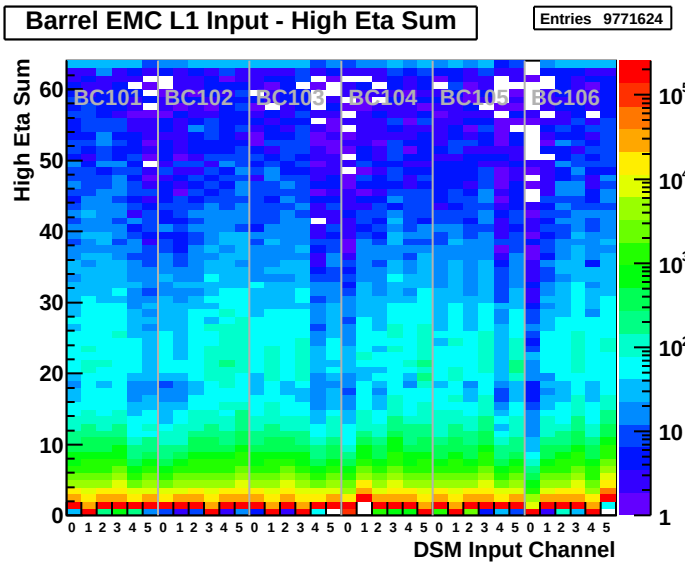
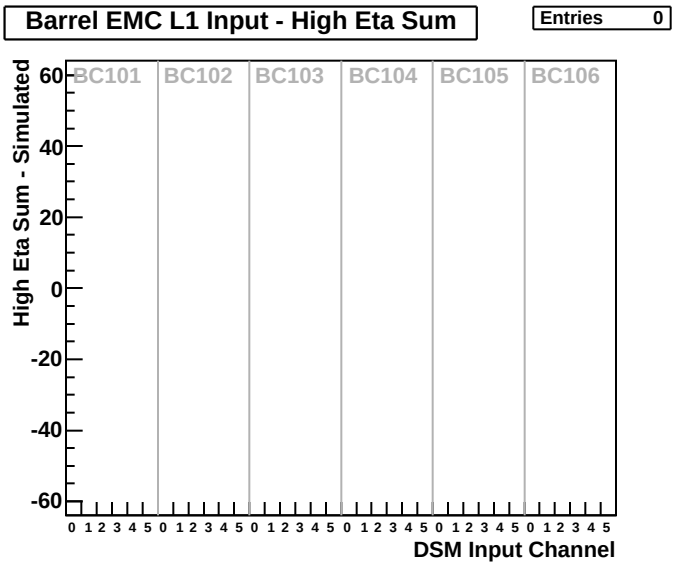
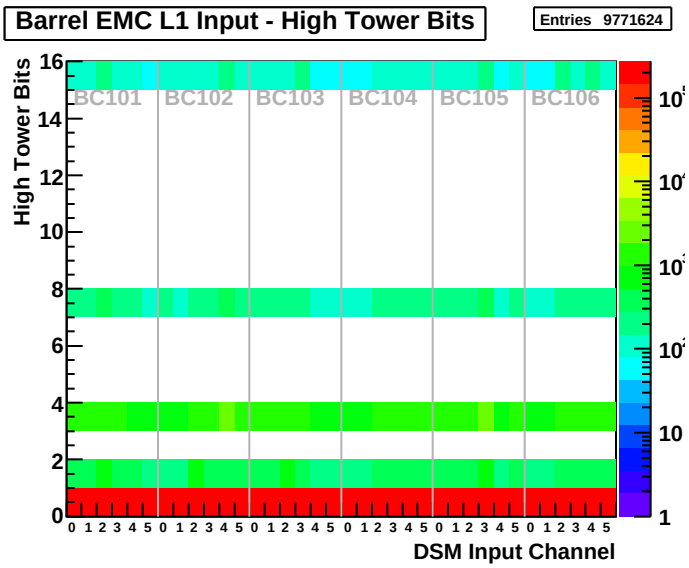
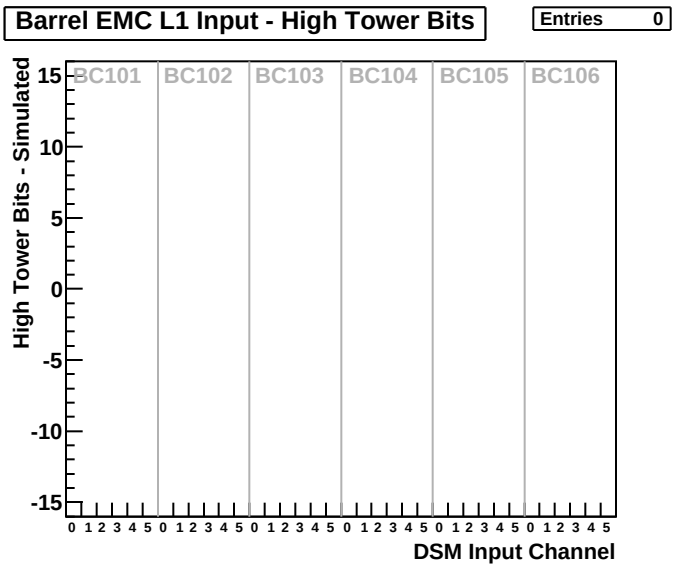

Barrel EMC L1 Input - Low Eta Sum
Entries 9771624
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

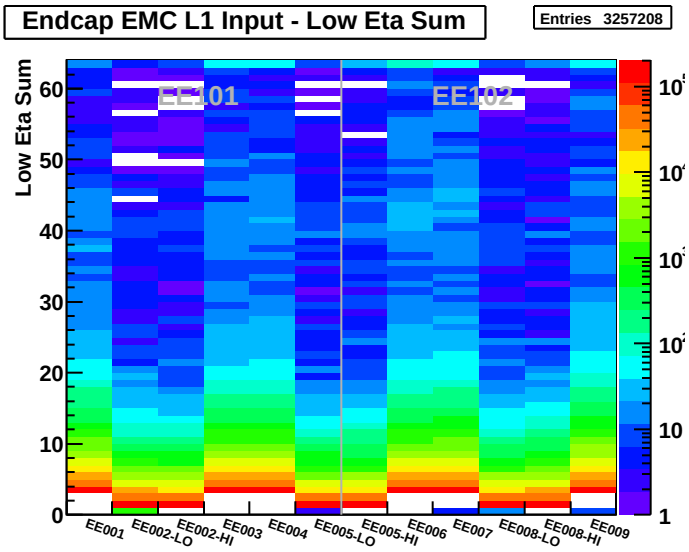

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

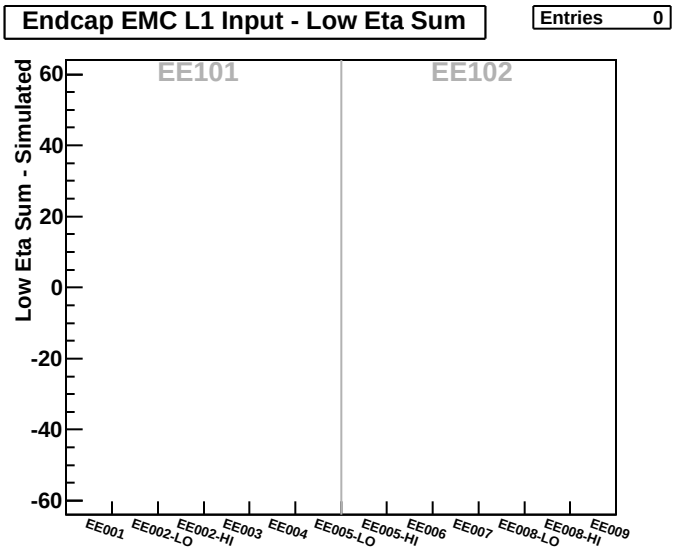

Barrel EMC L1 Input - High Eta Sum
Entries 9771624
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

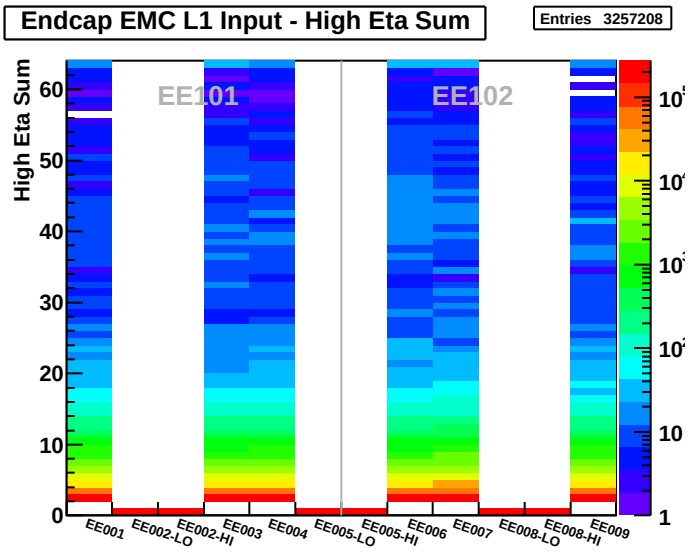

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

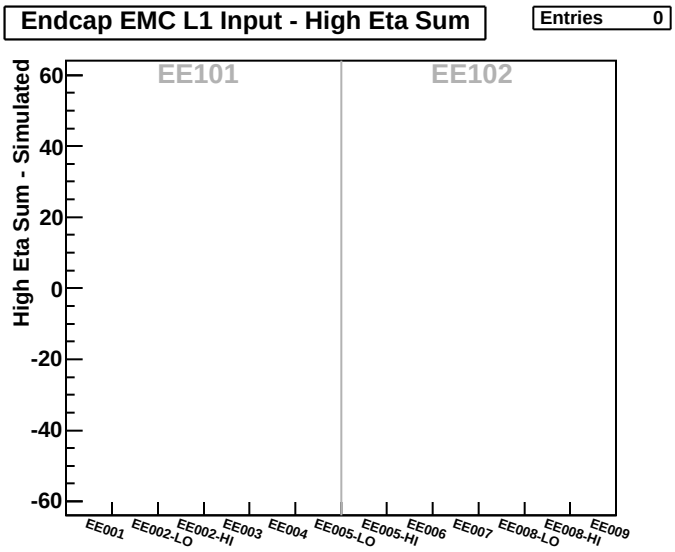

Barrel EMC L1 Input - High Tower Bits
Entries 9771624
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

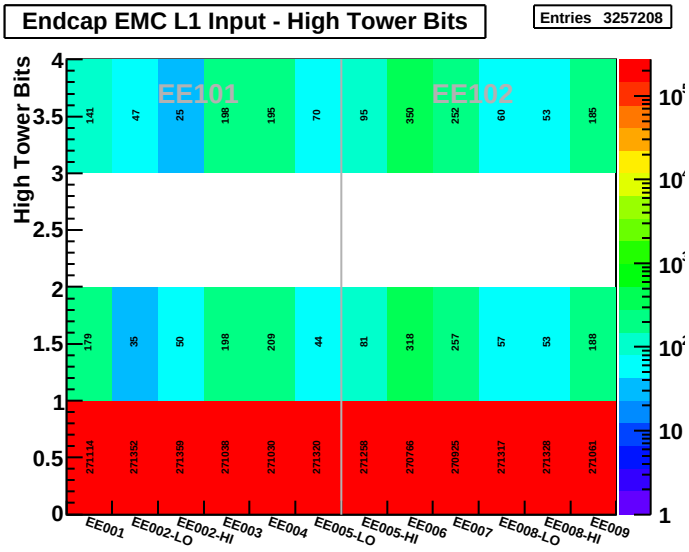

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

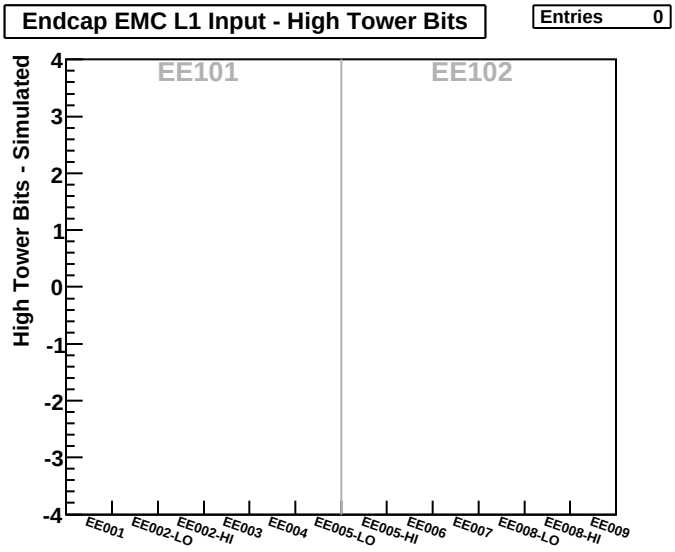

Endcap EMC L1 Input - Low Eta Sum
Entries 3257208
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

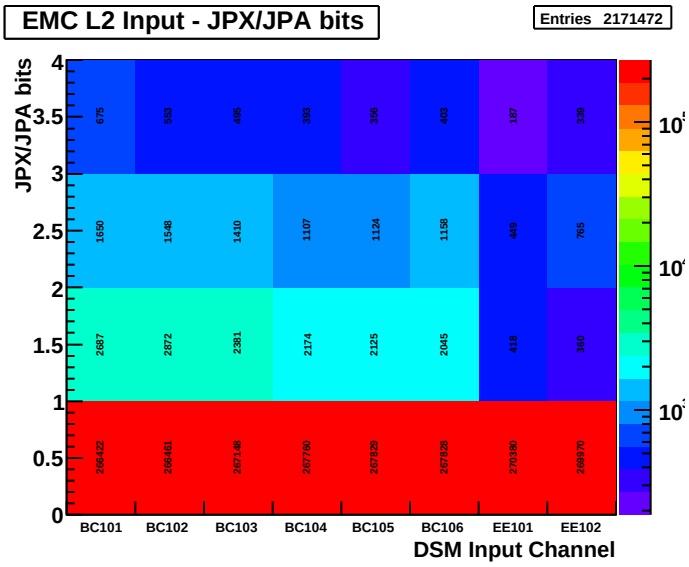

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

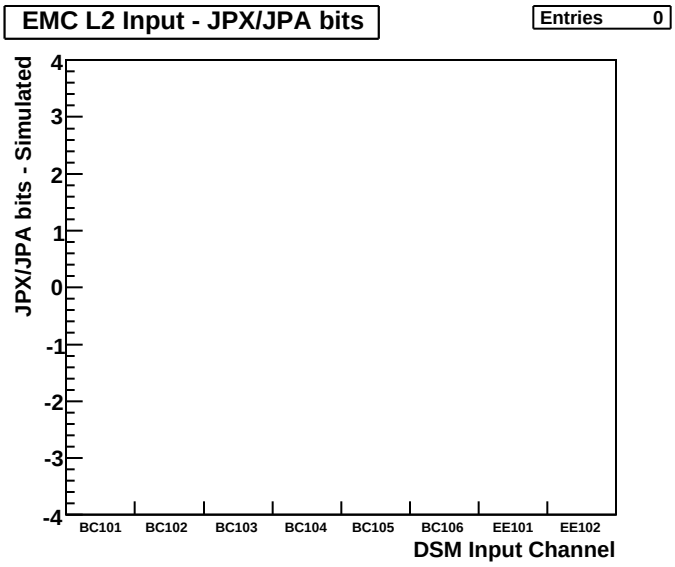

Endcap EMC L1 Input - High Eta Sum
Entries 3257208
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

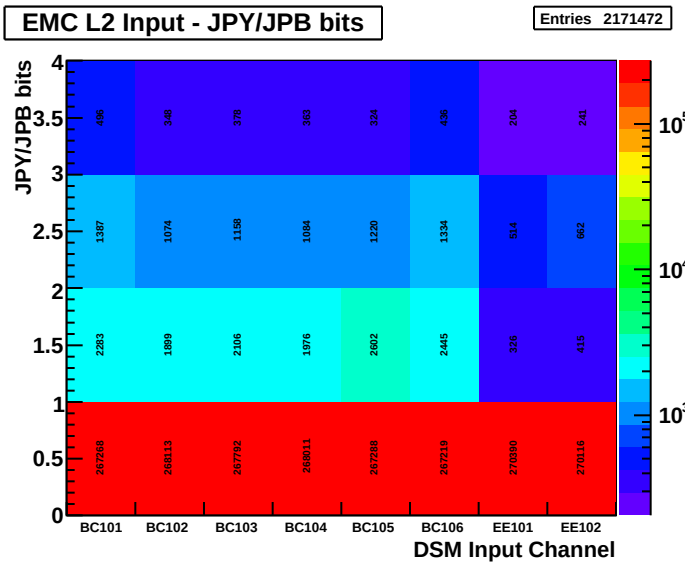

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

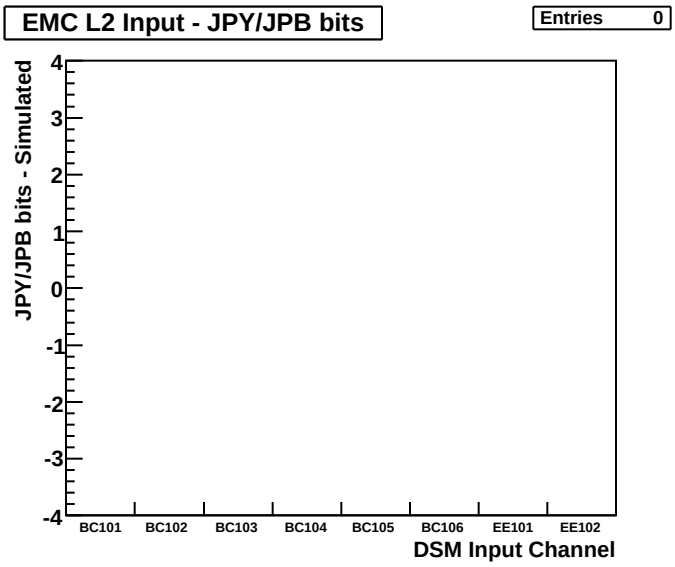

Endcap EMC L1 Input - High Tower Bits
Entries 3257208
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

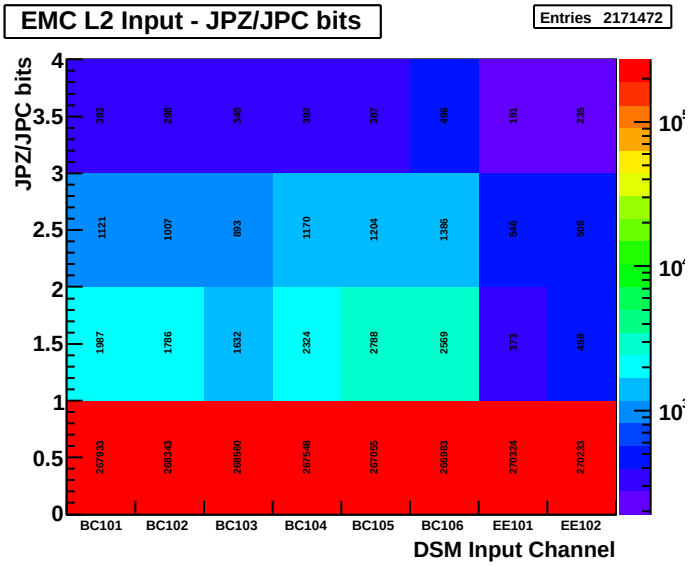

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

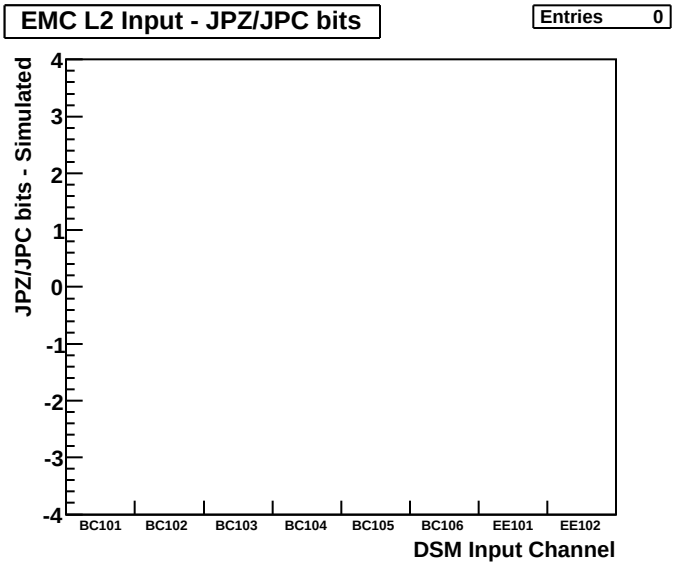

EMC L2 Input - JPX/JPA bits
Entries 2171472
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
675 553 495 393 356 403 187 339
1650 1548 1410 1107 1124 1158 449 765
2687 2872 2381 2174 2125 2045 418 360
266422 266461 267148 267760 267829 267828 270380 269970

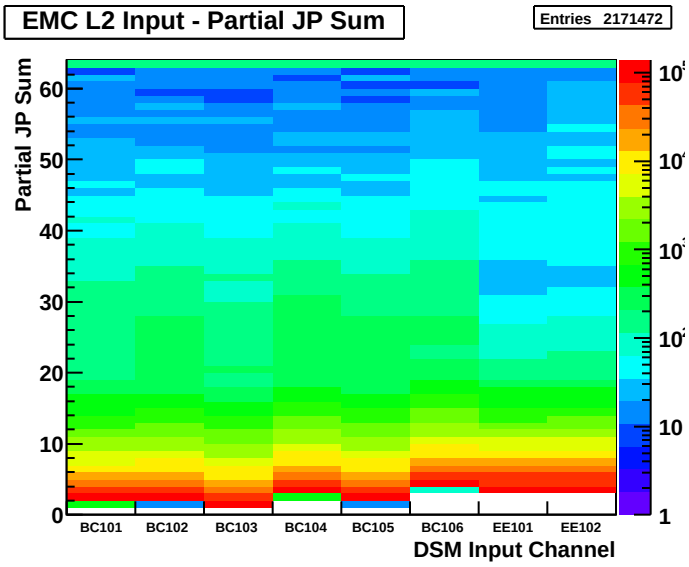

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

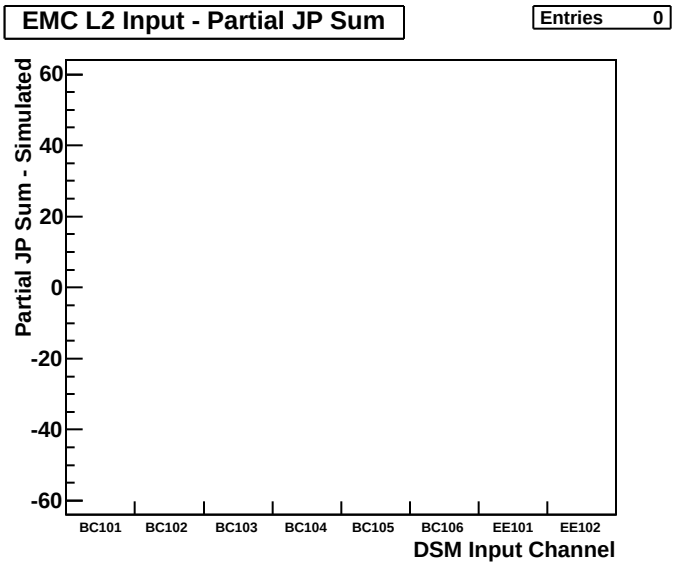

EMC L2 Input - JPY/JPB bits
Entries 2171472
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
486 348 378 363 324 436 204 241
1387 1074 1158 1084 1220 1334 514 662
2283 1899 2106 1976 2602 2445 326 415
267268 268113 267792 268011 267288 267219 270390 270116

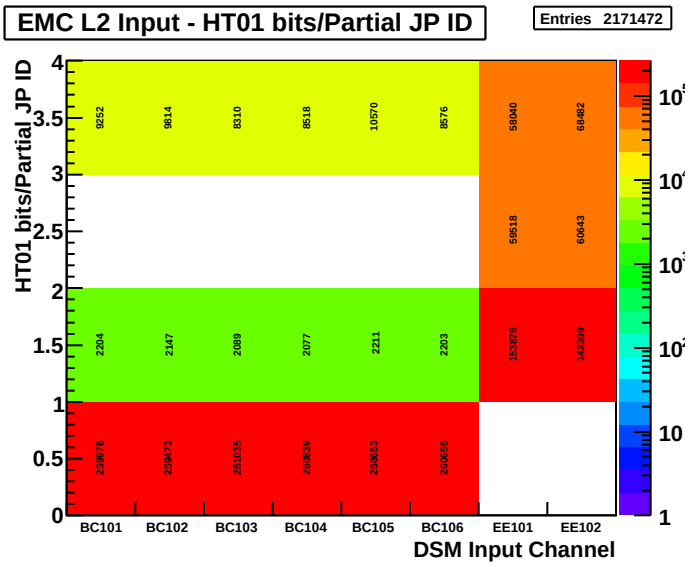

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

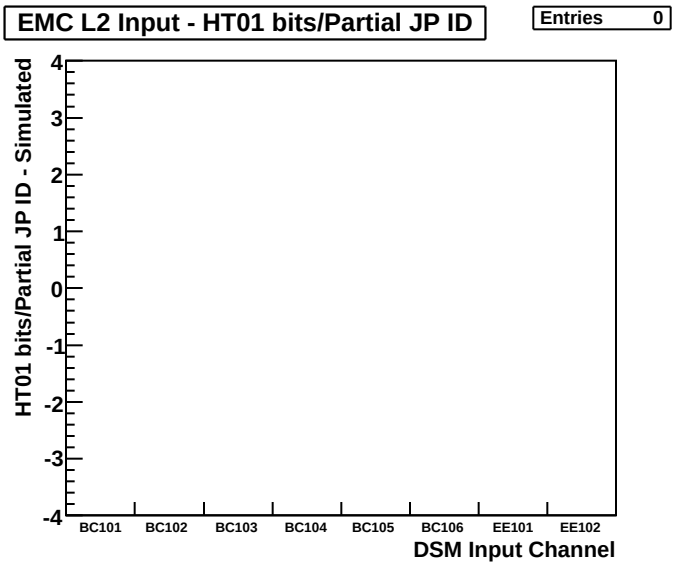

EMC L2 Input - JPZ/JPC bits
Entries 2171472
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
339 298 349 392 387 496 191 235
1121 1007 893 1170 1204 1386 546 508
1987 1786 1632 2324 2788 2569 373 458
267933 268343 268560 267548 267055 266983 270324 270233

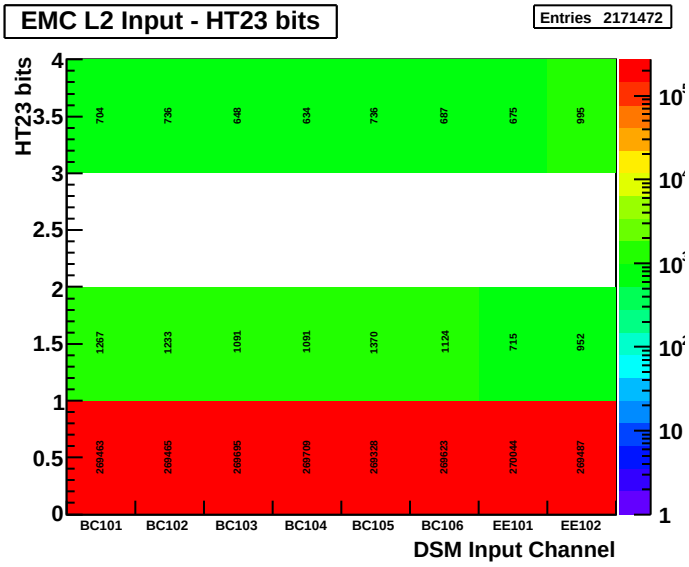

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

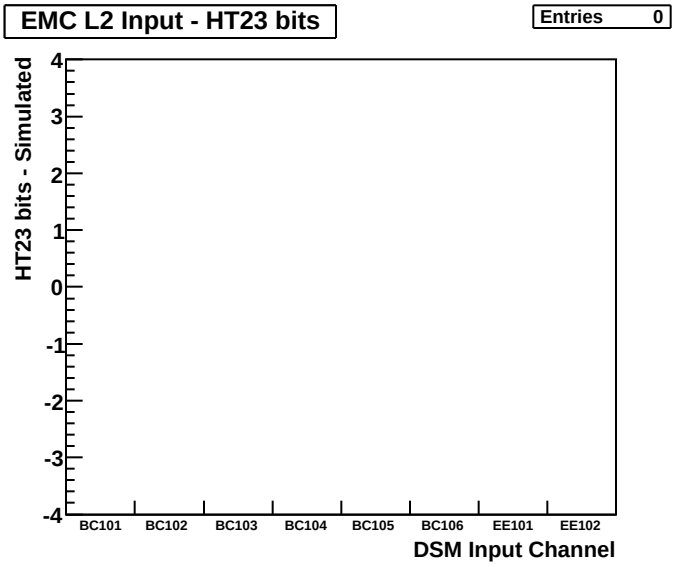

EMC L2 Input - Partial JP Sum
Entries 2171472
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

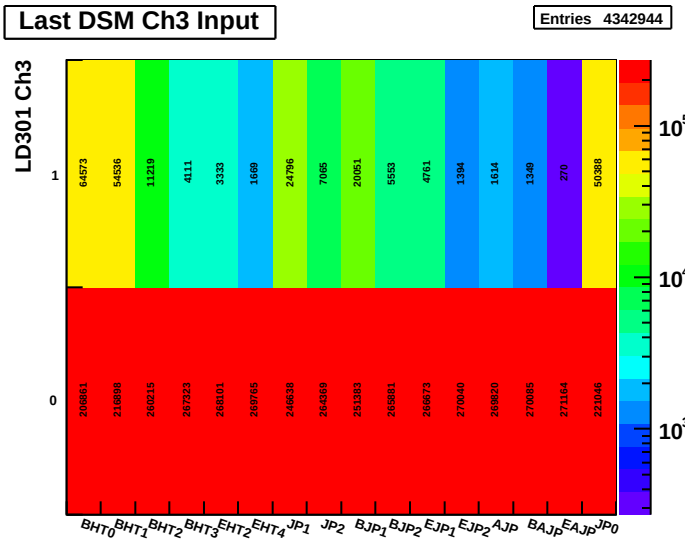

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

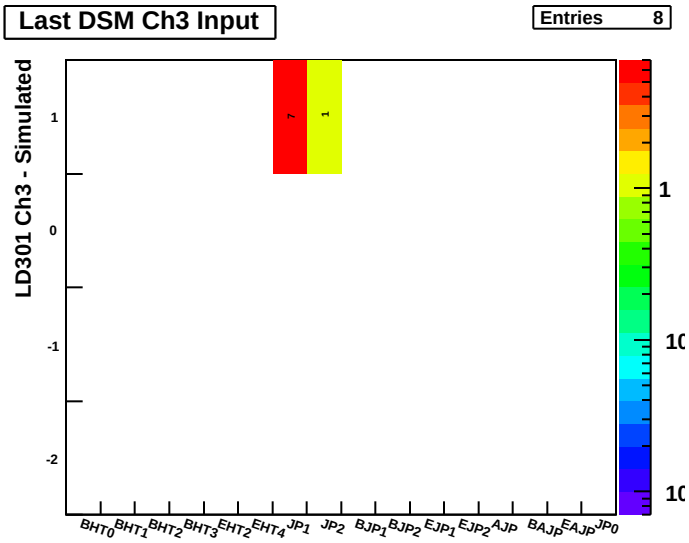

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2171472
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

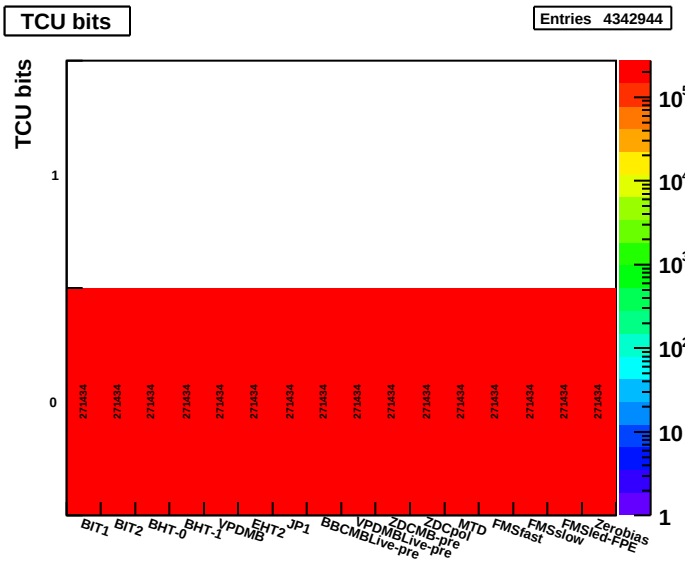

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

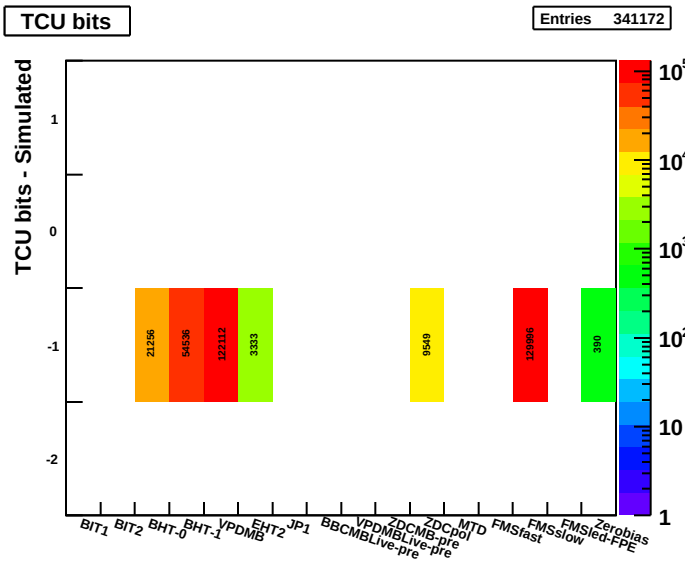

EMC L2 Input - HT23 bits
Entries 2171472
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

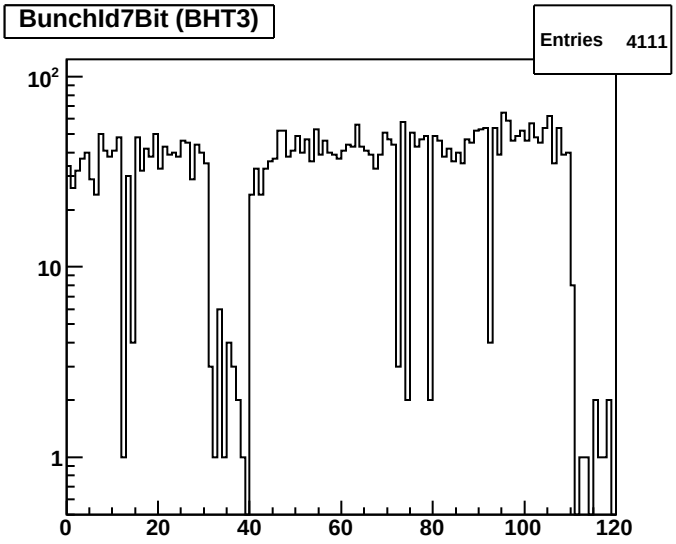

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

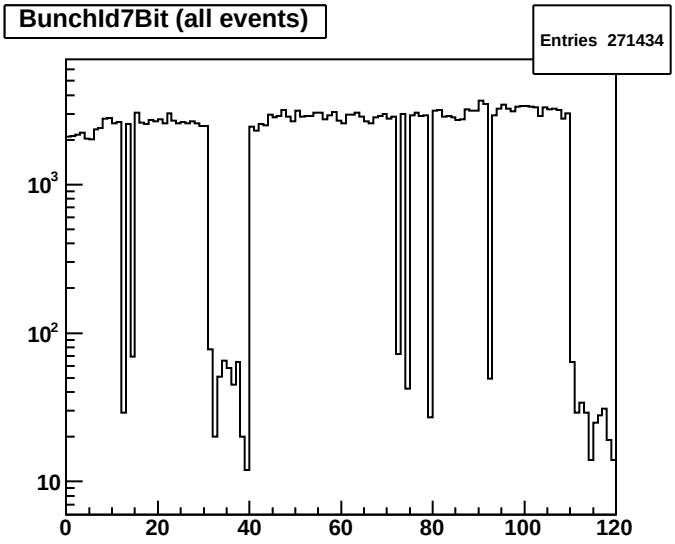

Last DSM Ch3 Input
Entries 4342944
LD301 Ch3
64573 54536 11219 4111 3333 1669 24796 7065 20051 5553 4761 1394 1614 1349 270 50388
206861 216898 260215 267323 268101 269765 246638 264369 251383 265881 266673 270040 269820 270085 271164 221046
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

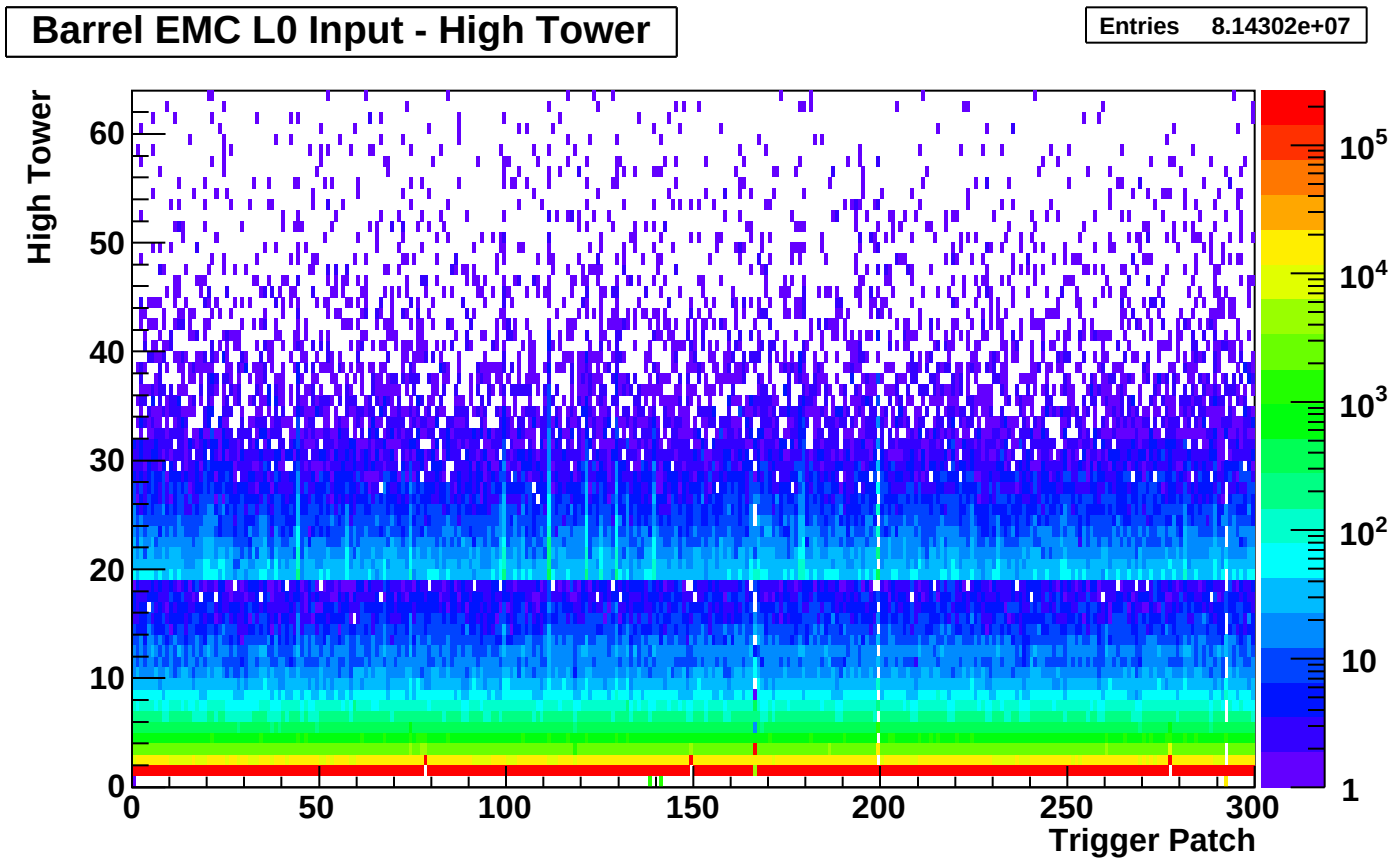

Last DSM Ch3 Input
Entries 8
LD301 Ch3 - Simulated
7 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

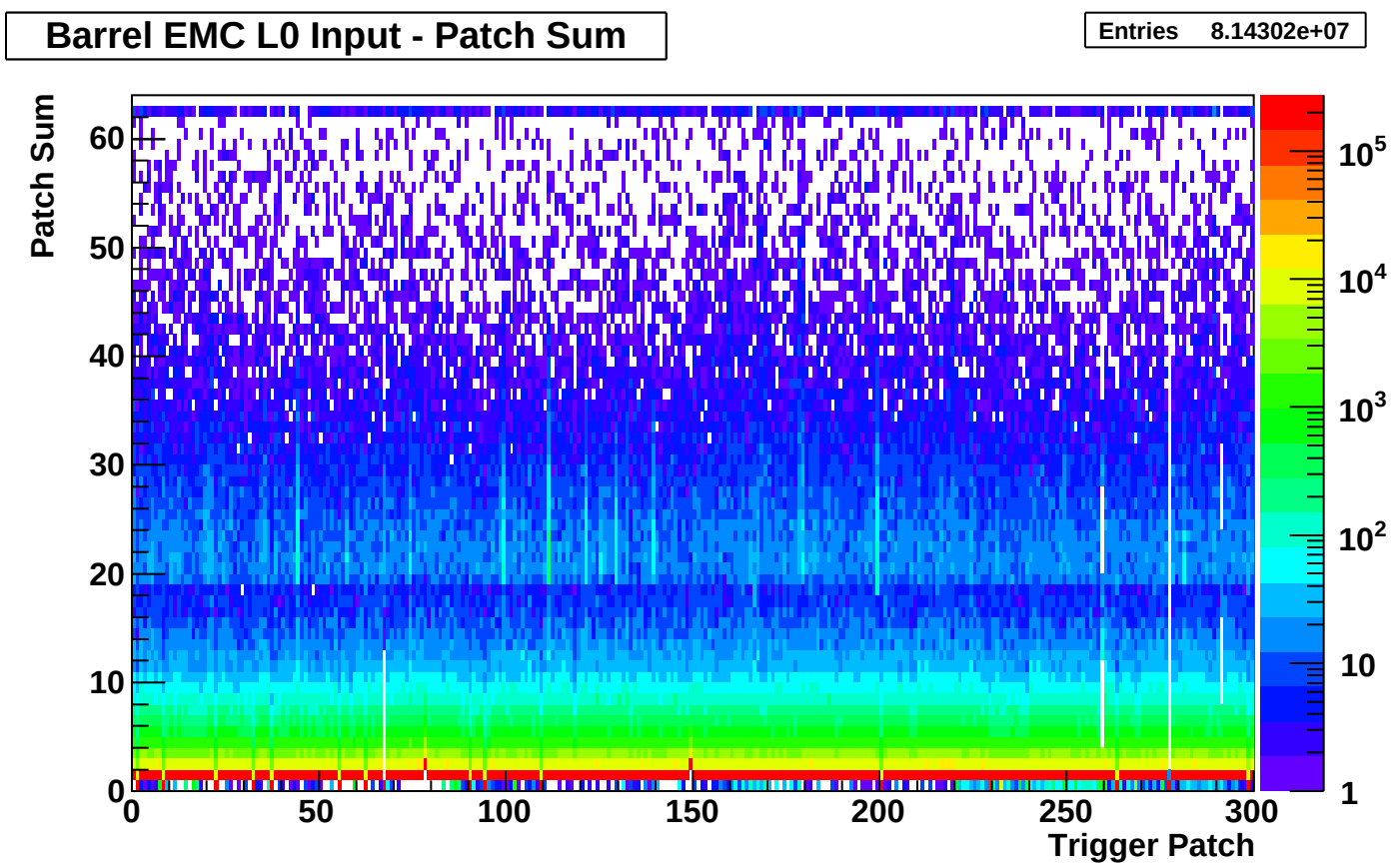

TCU bits
Entries 4342944
TCU bits
271434
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

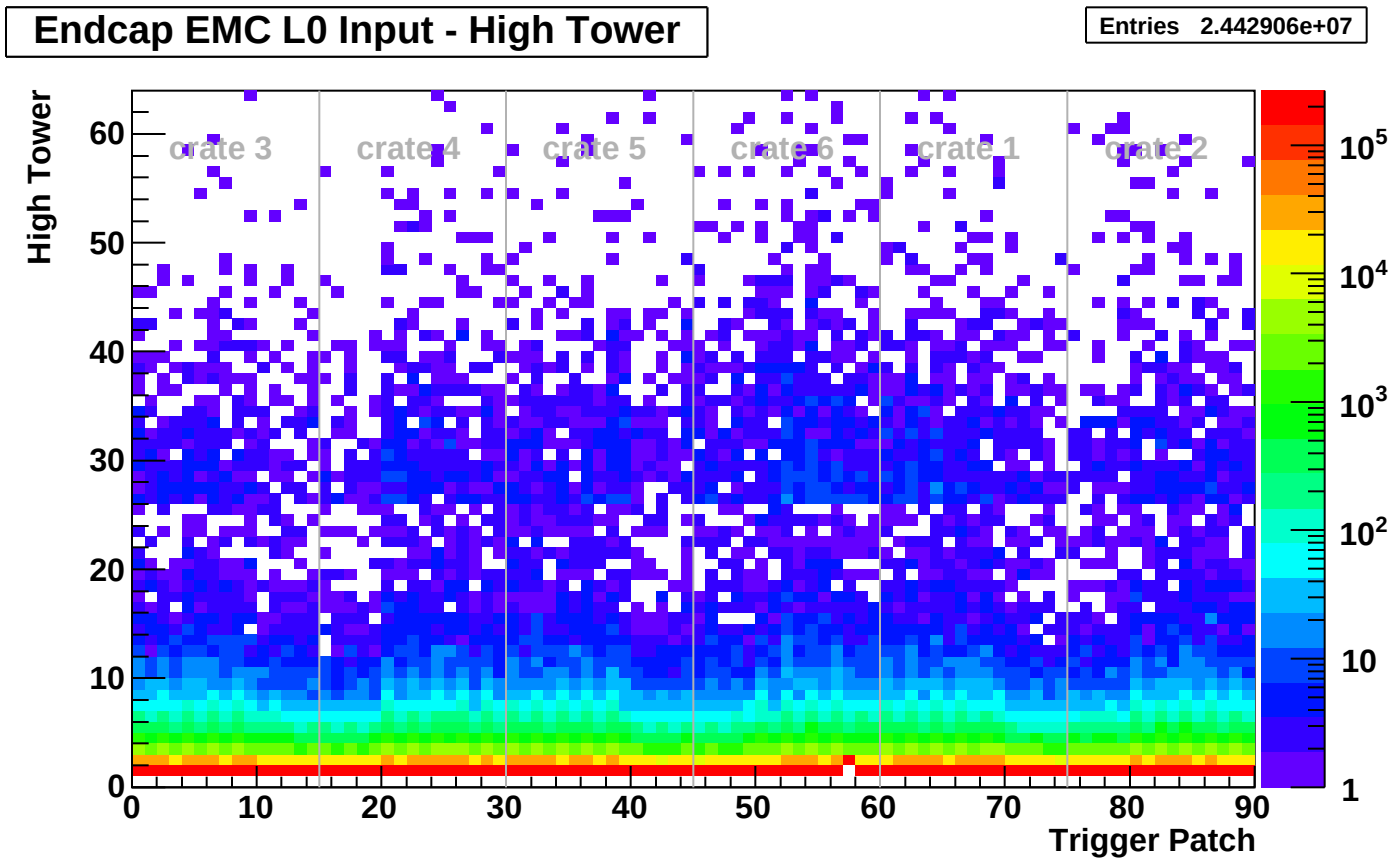

TCU bits
Entries 341172
TCU bits - Simulated
21256 54536 122112 3333 9549 129996 390
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

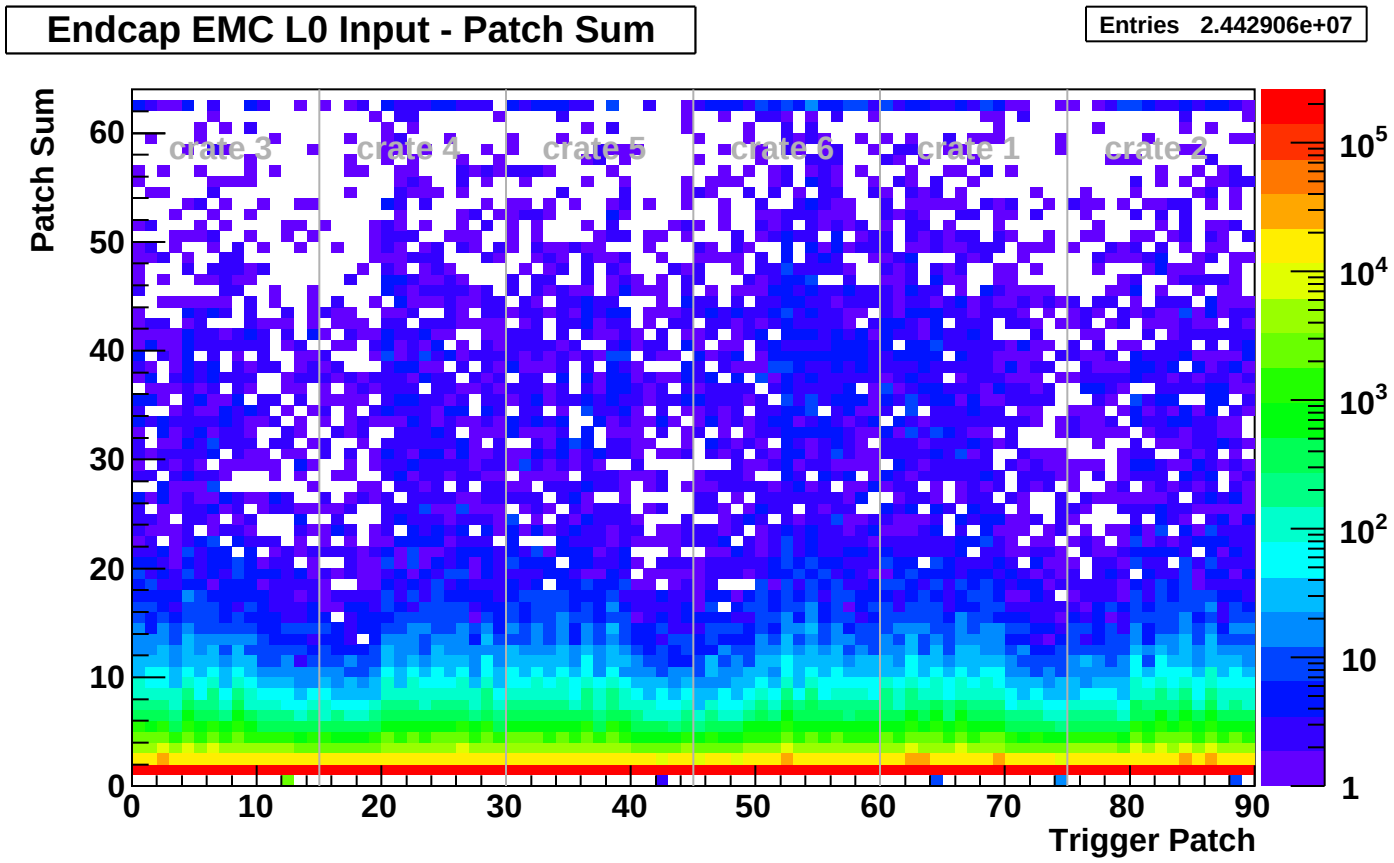

BunchId7Bit (BHT3)
Entries 4111

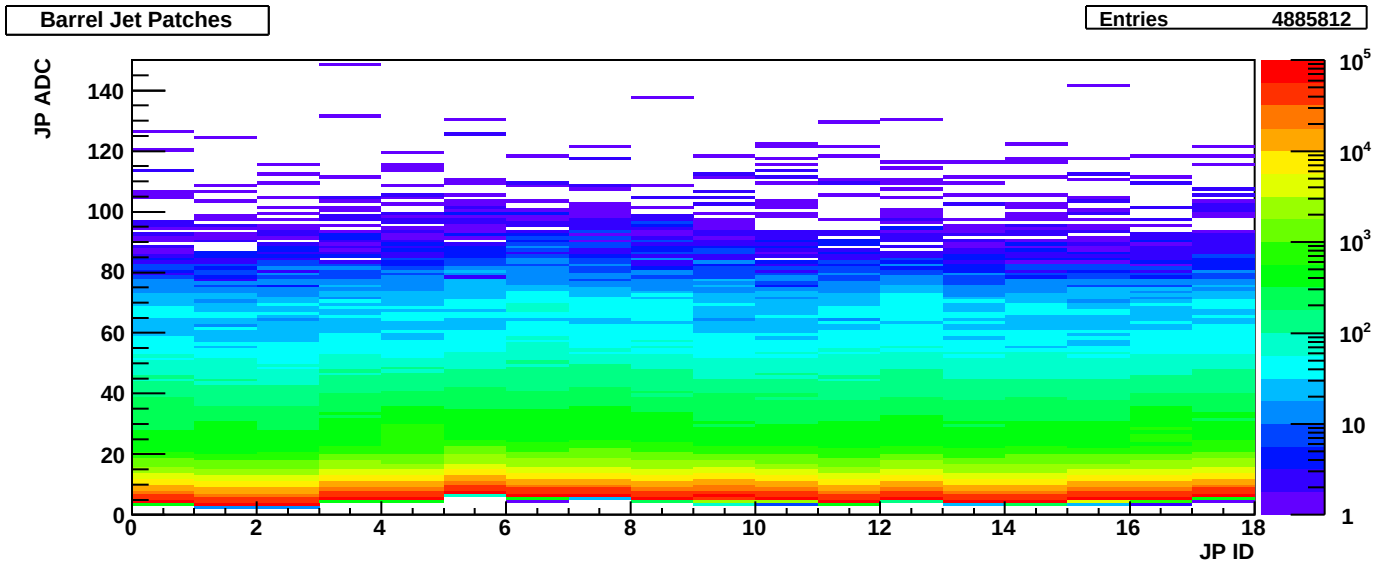

BunchId7Bit (all events)
Entries 271434

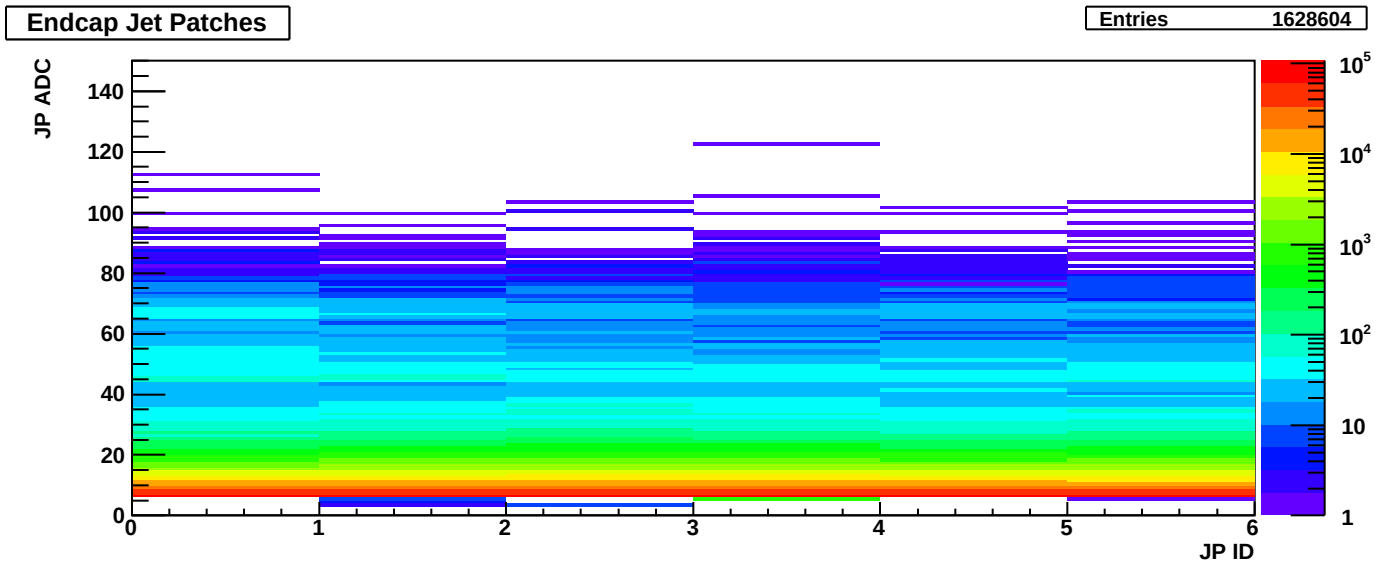

Barrel EMC L0 Input - High Tower
Entries 8.14302e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

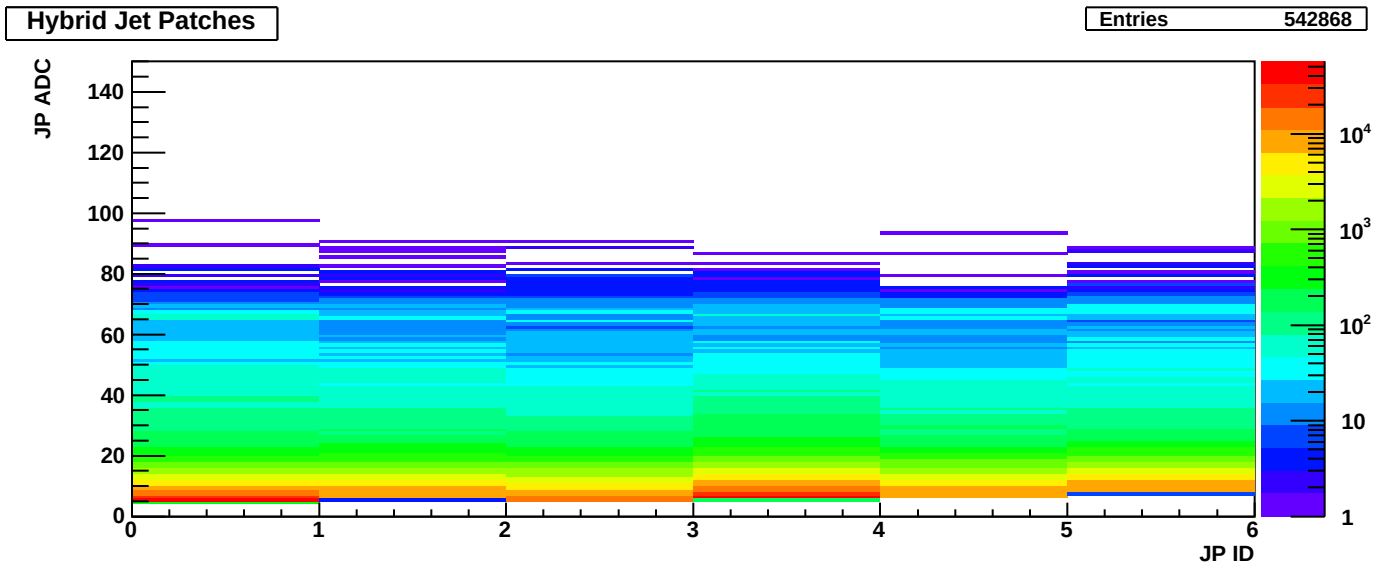

Barrel EMC L0 Input - Patch Sum
Entries 8.14302e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

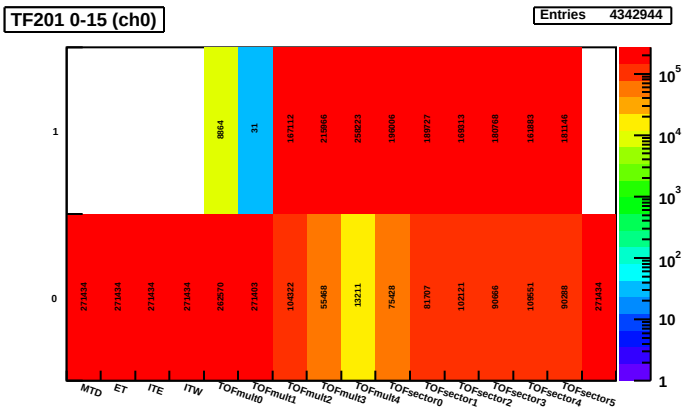

Endcap EMC L0 Input - High Tower
Entries 2.442906e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

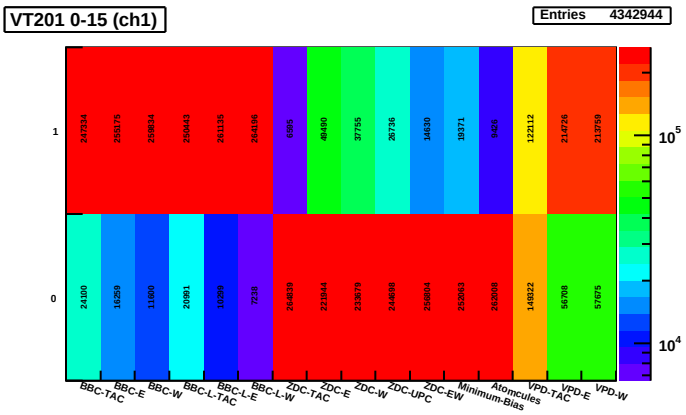

Endcap EMC L0 Input - Patch Sum
Entries 2.442906e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

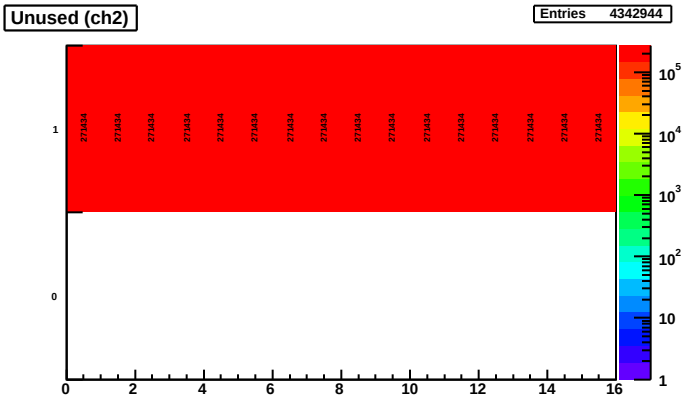

Barrel Jet Patches
Entries 4885812
JP ADC
JP ID

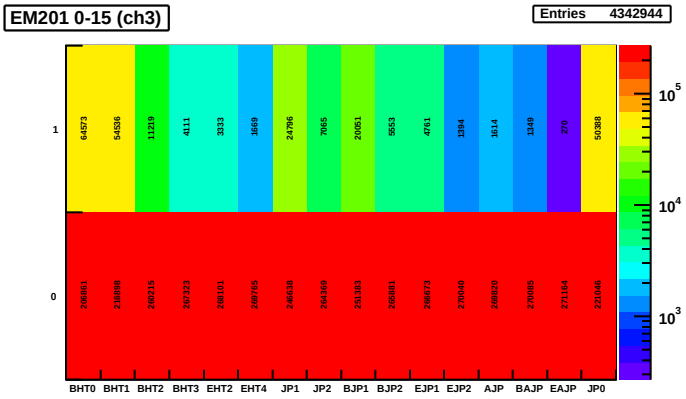

Endcap Jet Patches
Entries 1628604
JP ADC
JP ID

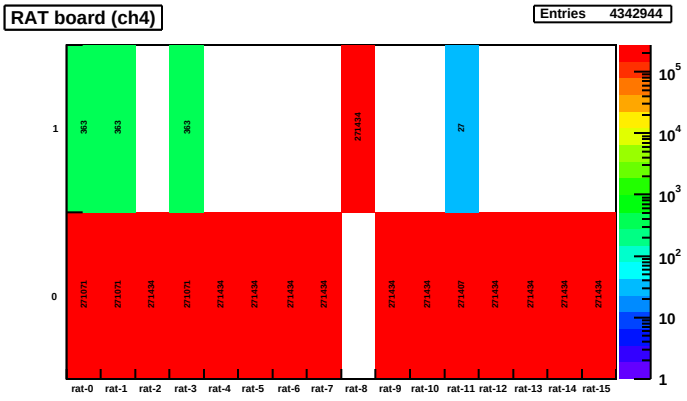

Hybrid Jet Patches
Entries 542868
JP ADC
JP ID

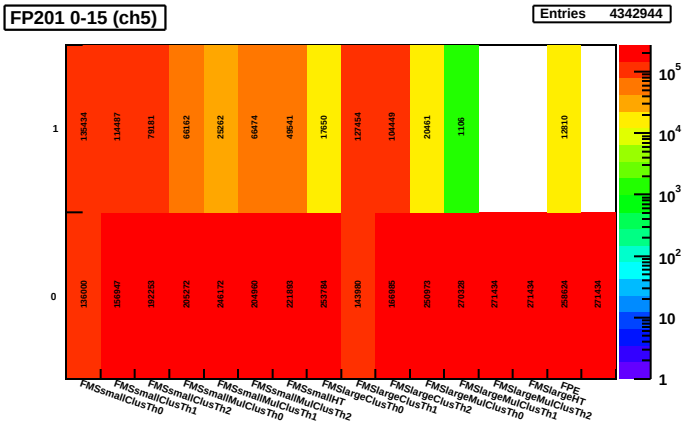

TF201 0-15 (ch0)
Entries 4342944
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

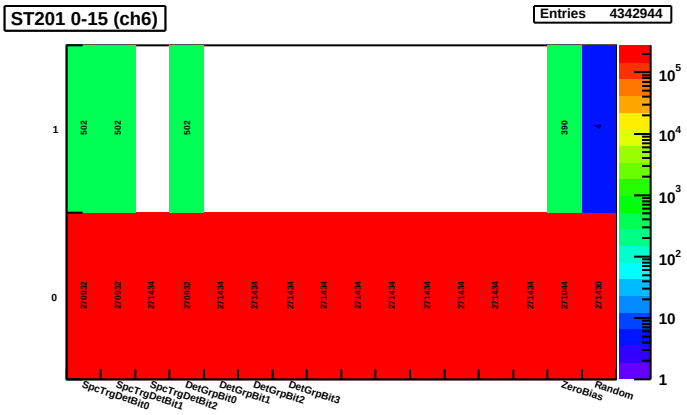

VT201 0-15 (ch1)
Entries 4342944
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

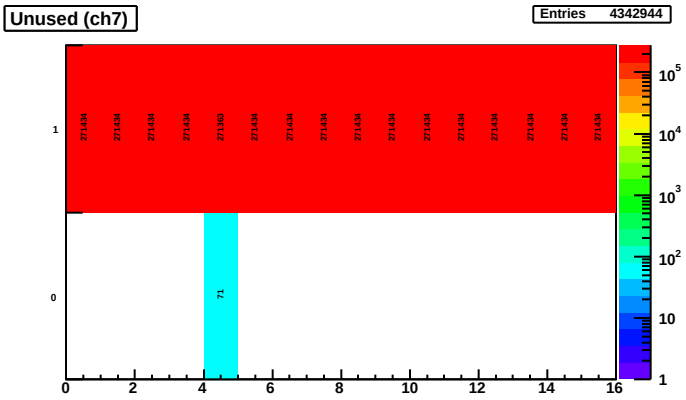

Unused (ch2)
Entries 4342944

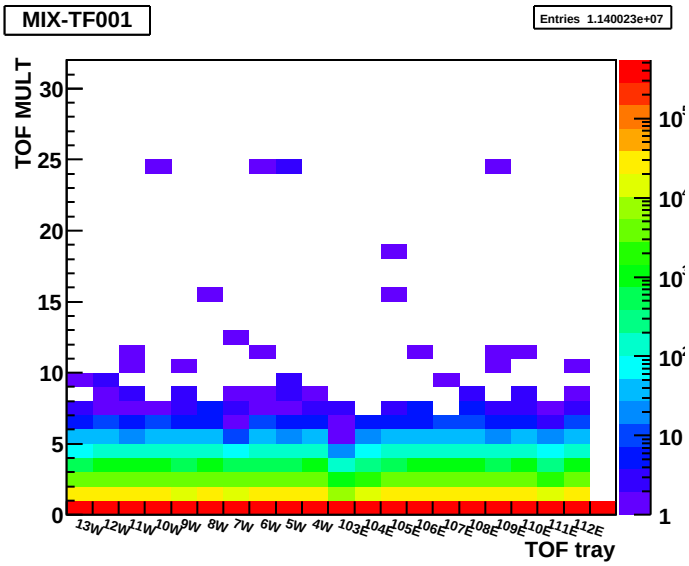

EM201 0-15 (ch3)
Entries 4342944
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

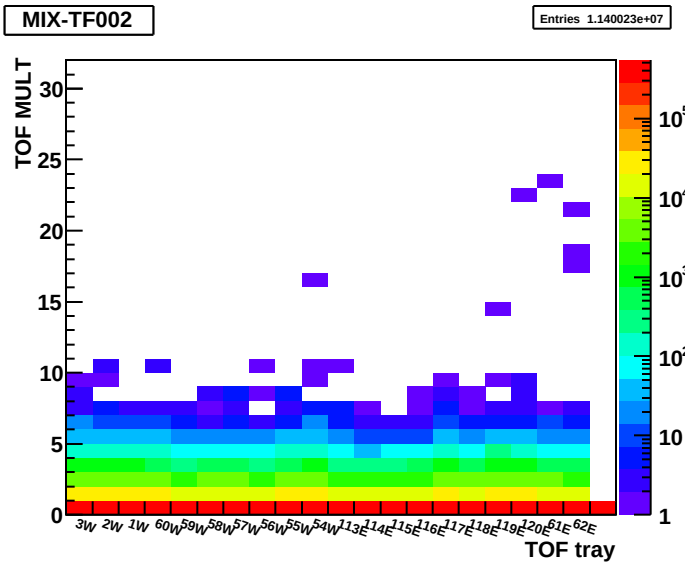

RAT board (ch4)
Entries 4342944
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

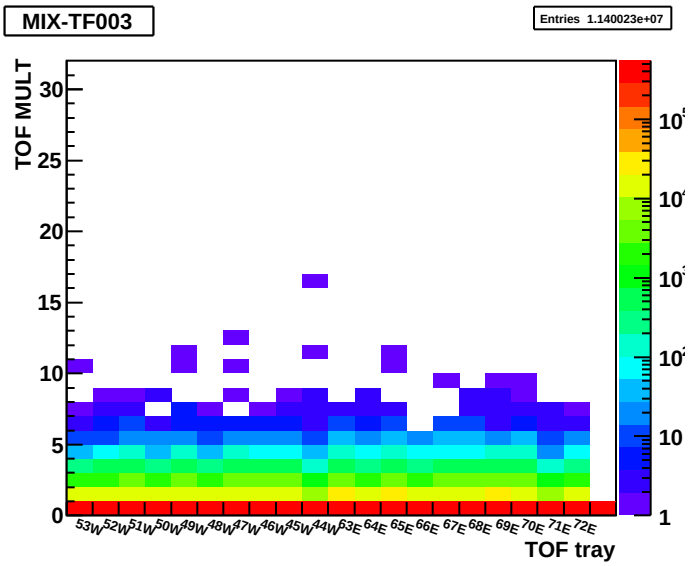

FP201 0-15 (ch5)
Entries 4342944
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

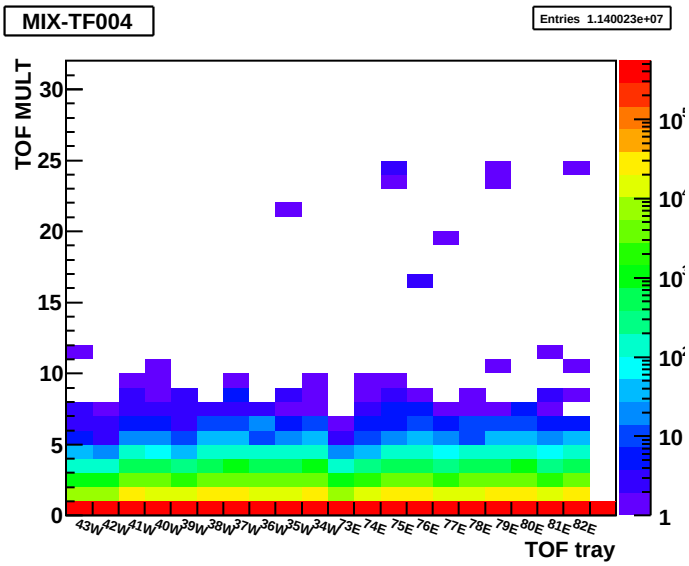

ST201 0-15 (ch6)
Entries 4342944
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

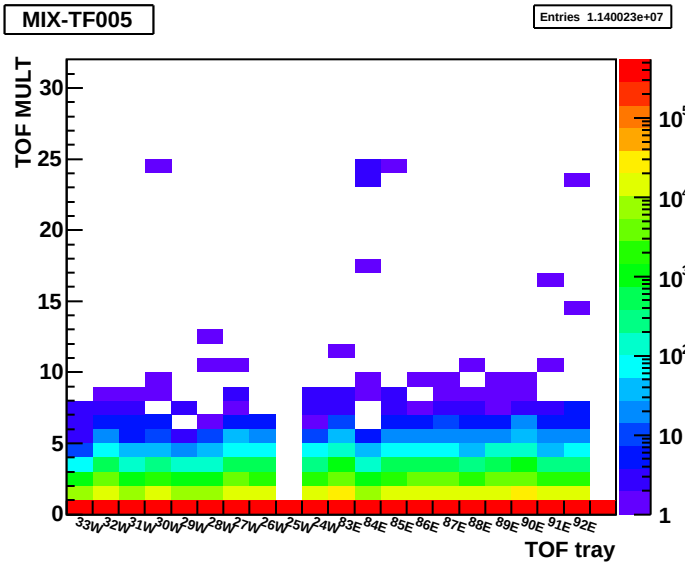

Unused (ch7)
Entries 4342944

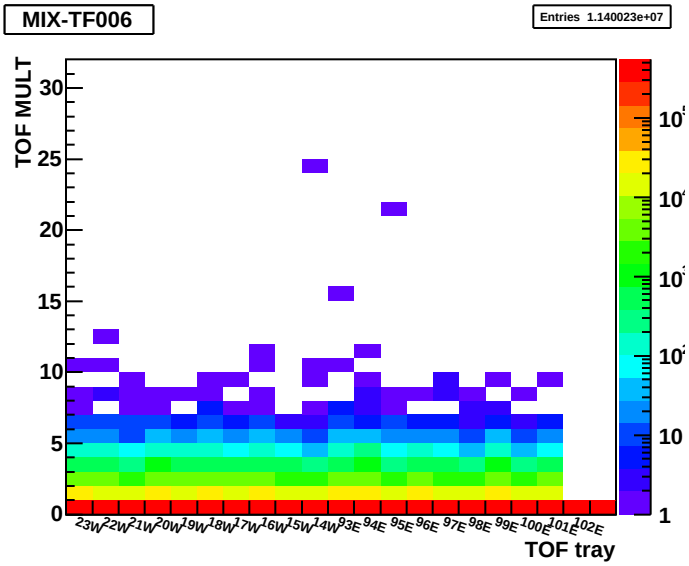

MIX-TF001
Entries 1.140023e+07
TOF MULT
TOF tray

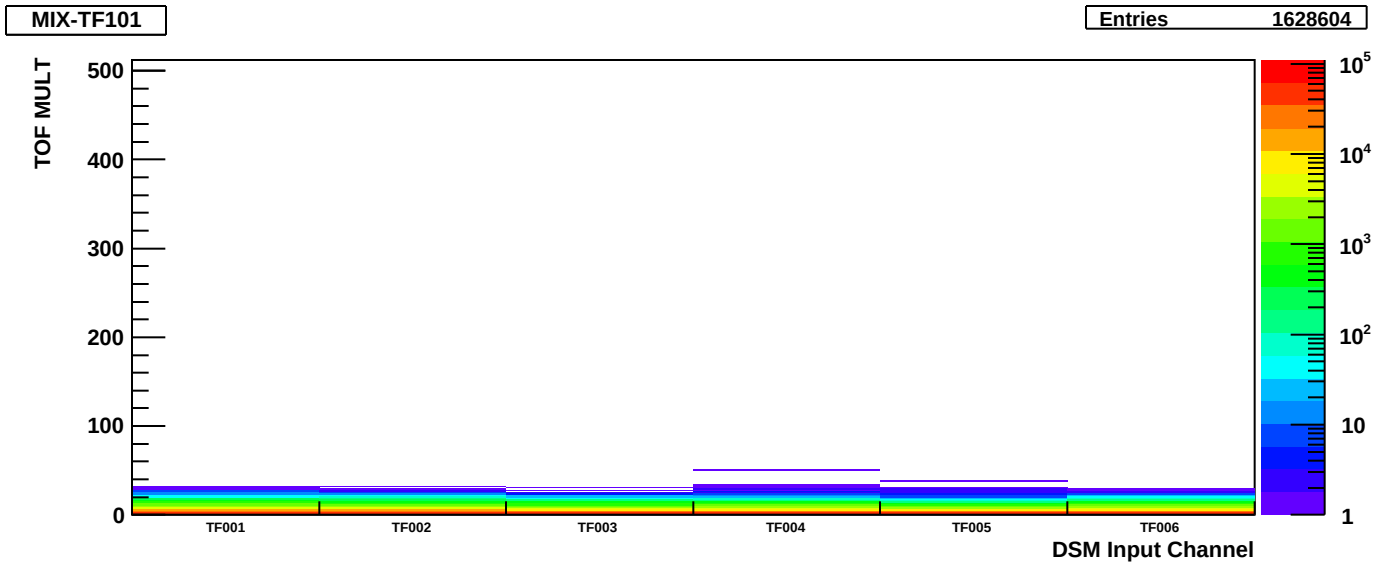

MIX-TF002
Entries 1.140023e+07
TOF MULT
TOF tray

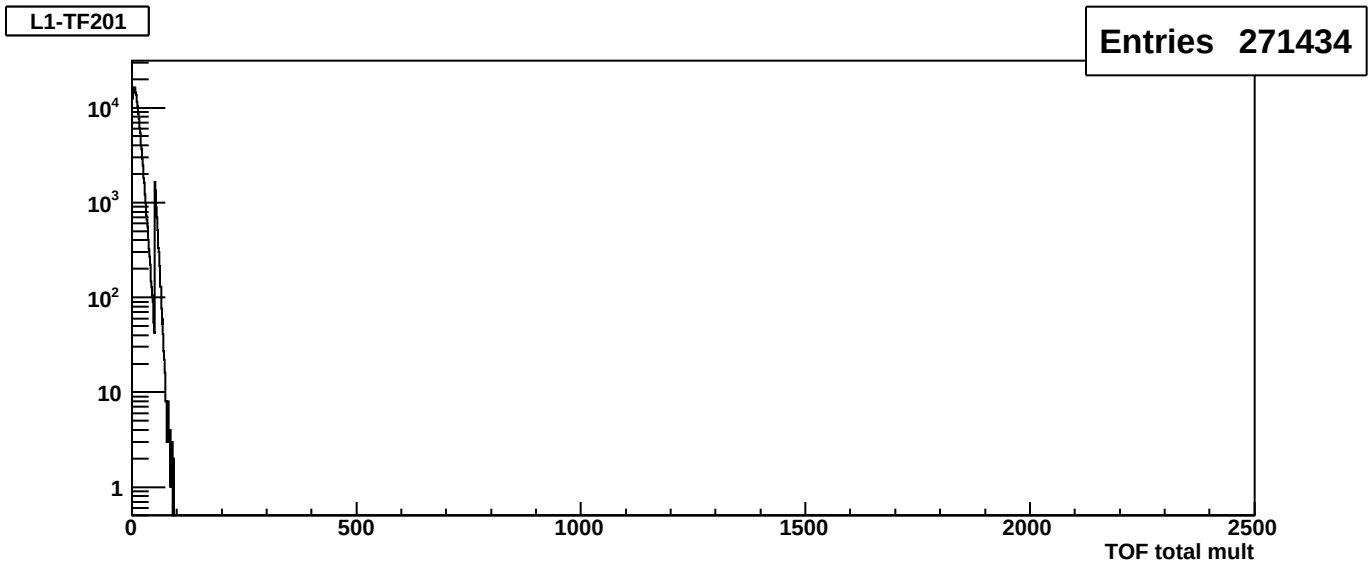

MIX-TF003
Entries 1.140023e+07
TOF MULT
TOF tray

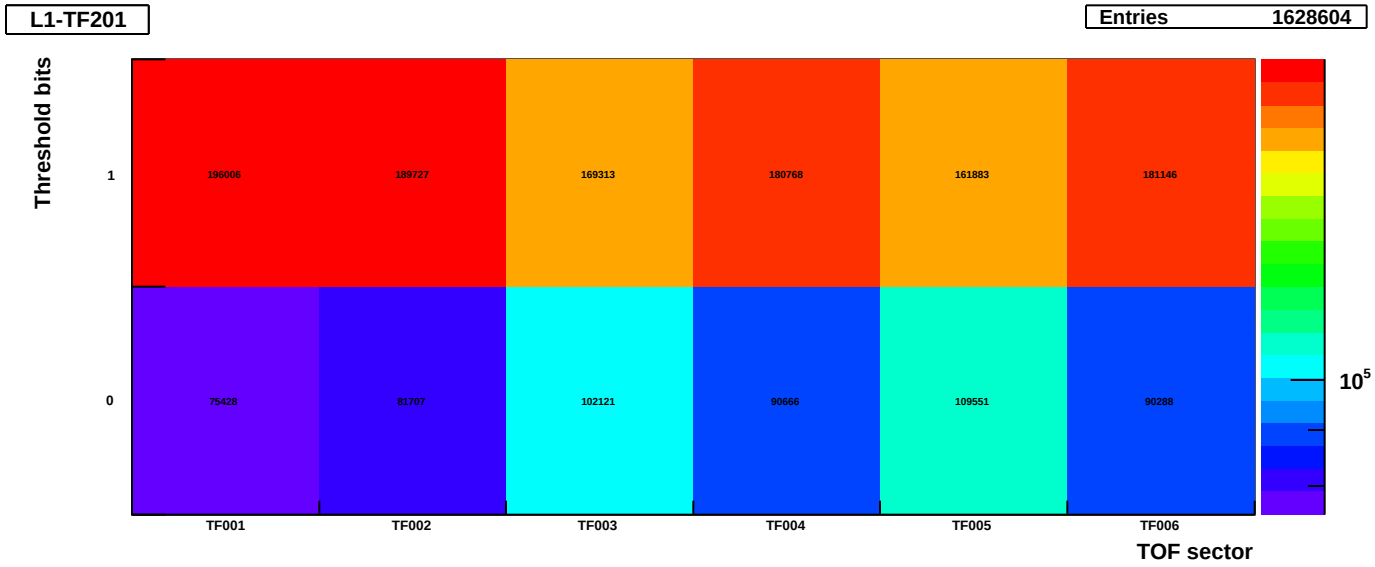

MIX-TF004
Entries 1.140023e+07
TOF MULT
TOF tray

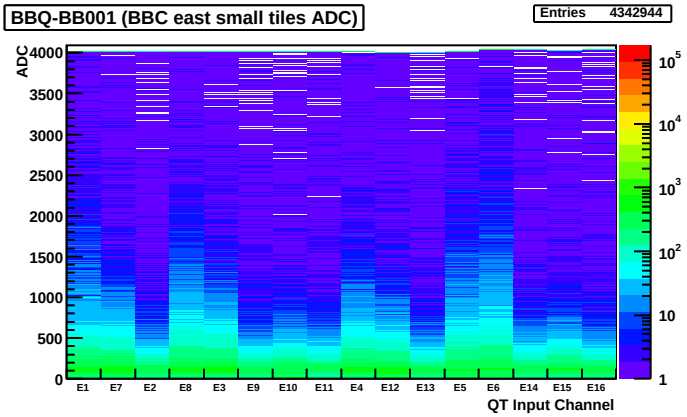

MIX-TF005
Entries 1.140023e+07
TOF MULT
TOF tray

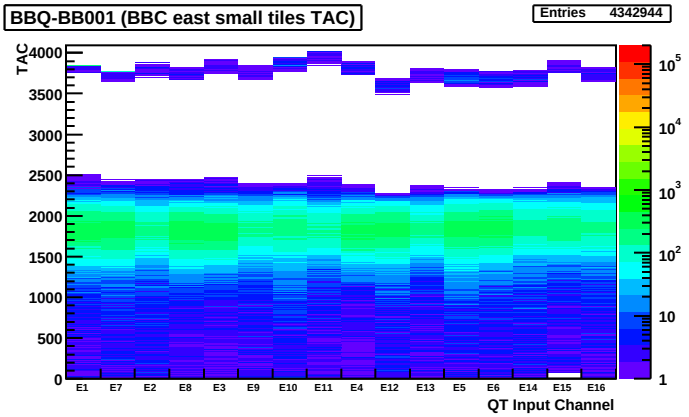

MIX-TF006
Entries 1.140023e+07
TOF MULT
TOF tray

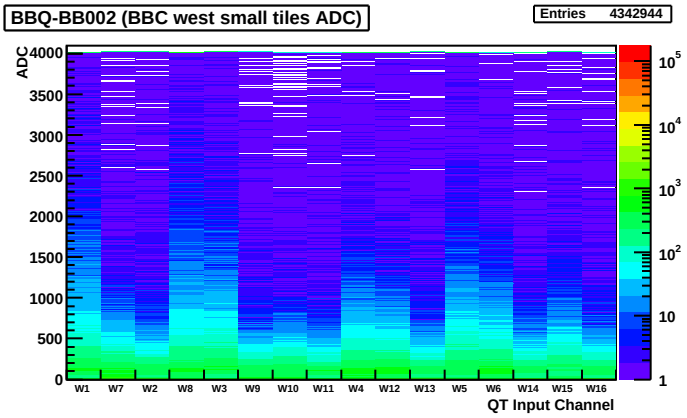

MIX-TF101
Entries 1628604
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

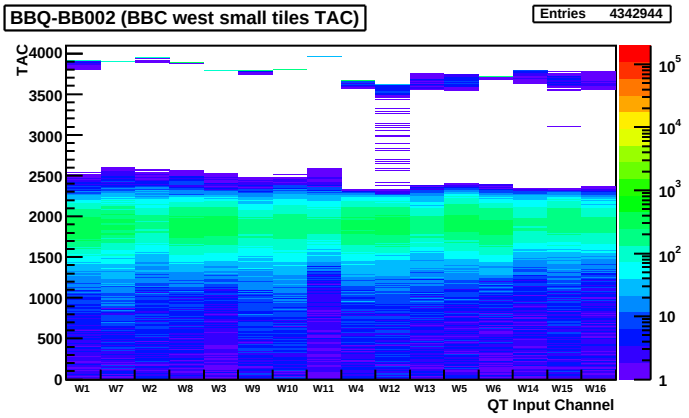

L1-TF201
Entries 271434
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

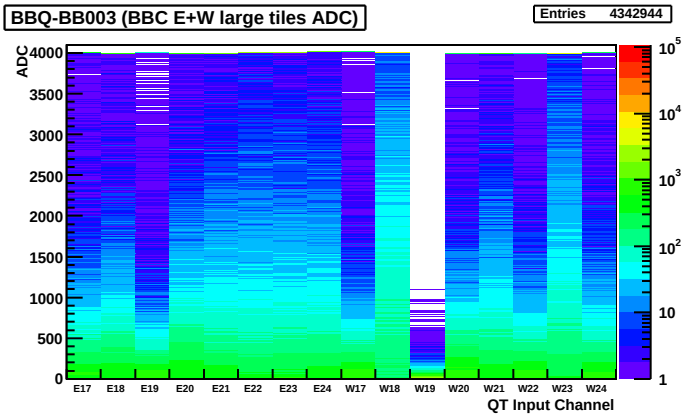

L1-TF201
Entries 1628604
Threshold bits
1
0
196006
189727
169313
180768
161883
181146
75428
81707
102121
90666
109551
90288
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

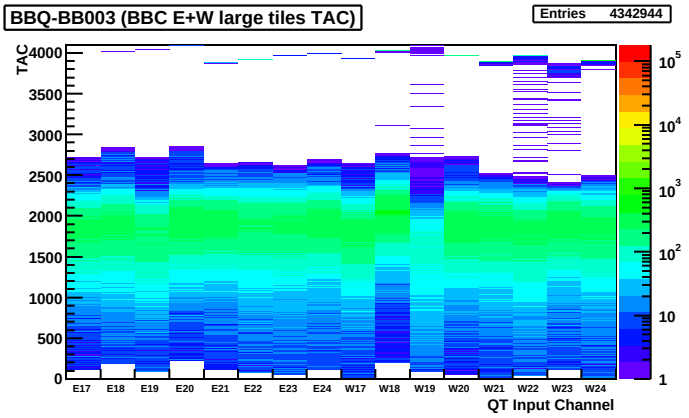

BBQ-BB001 (BBC east small tiles ADC)
Entries 4342944
ADC
QT Input Channel

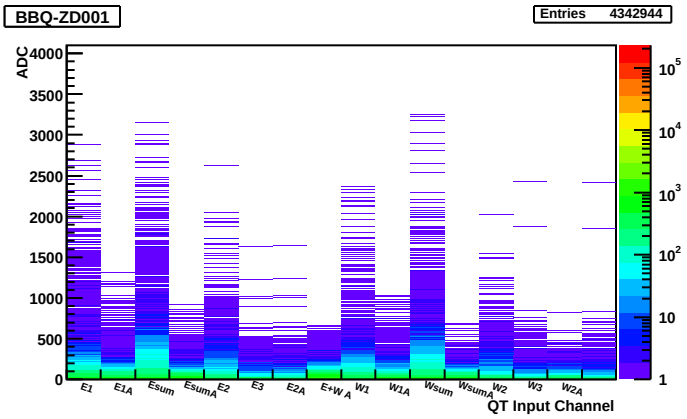

BBQ-BB001 (BBC east small tiles TAC)
Entries 4342944
TAC
QT Input Channel

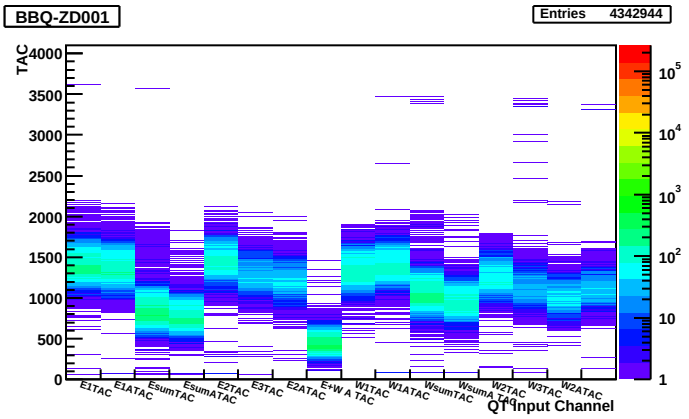

BBQ-BB002 (BBC west small tiles ADC)
Entries 4342944
ADC
QT Input Channel

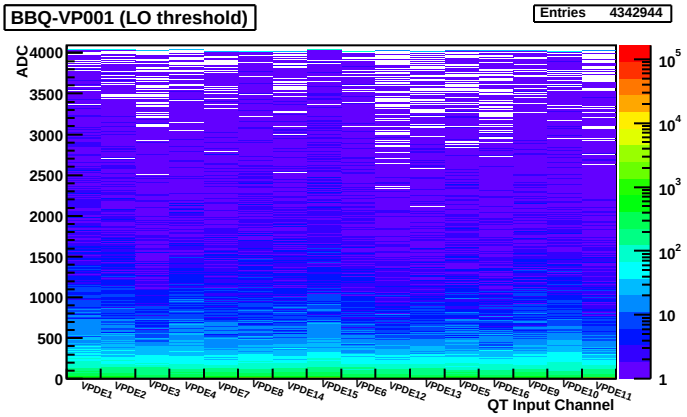

BBQ-BB002 (BBC west small tiles TAC)
Entries 4342944
TAC
QT Input Channel

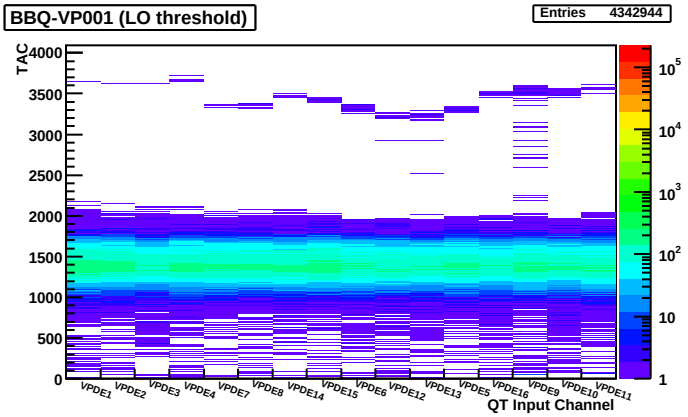

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4342944
ADC
QT Input Channel

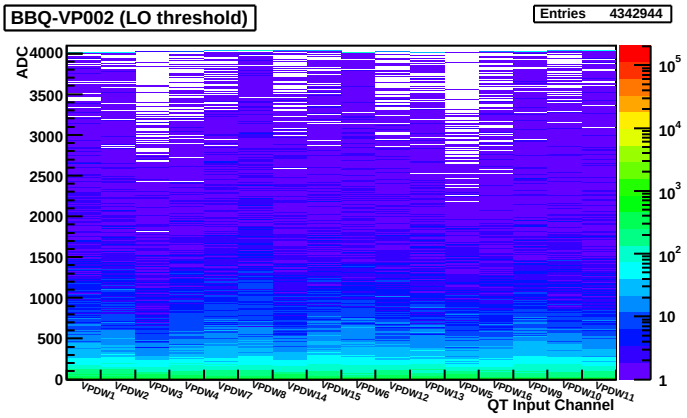

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4342944
TAC
QT Input Channel

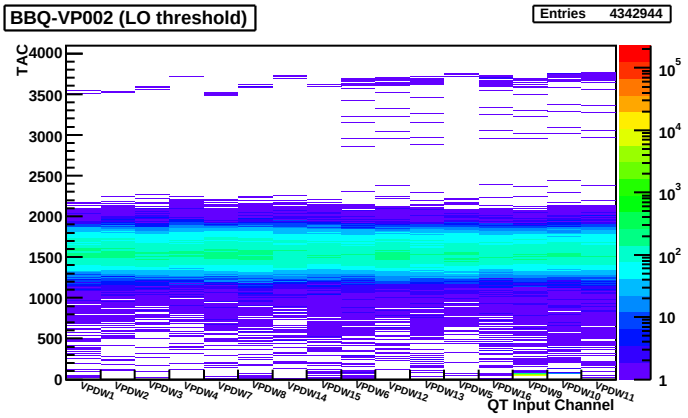

BBQ-ZD001
Entries 4342944
ADC
QT Input Channel

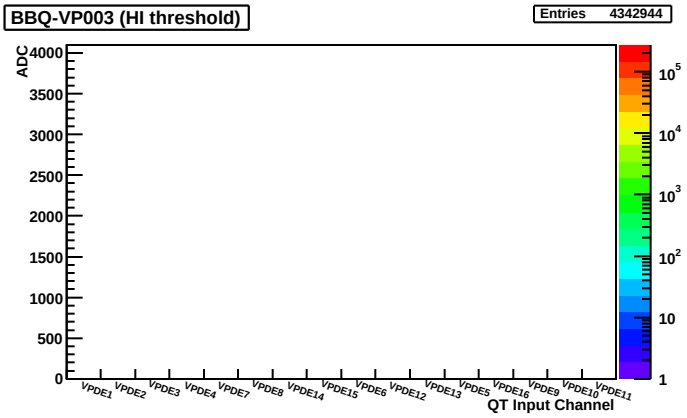

BBQ-ZD001
Entries 4342944
TAC
QT Input Channel

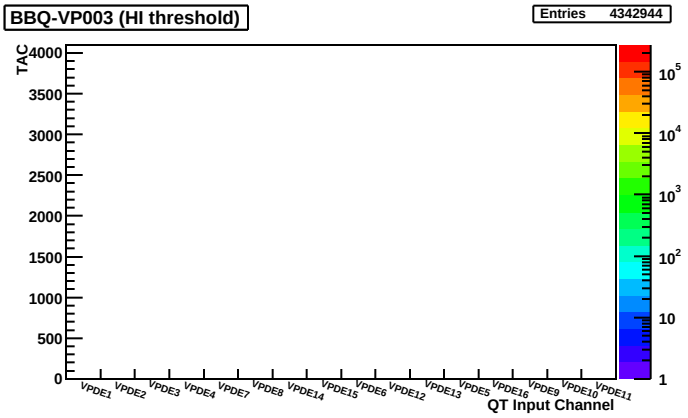

BBQ-VP001 (LO threshold)
Entries 4342944
ADC
QT Input Channel

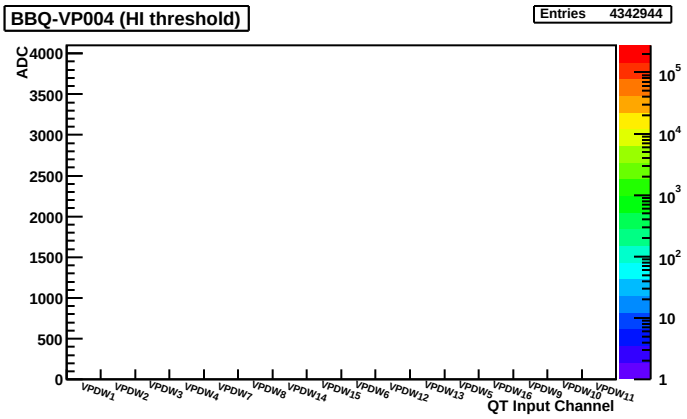

BBQ-VP001 (LO threshold)
Entries 4342944
TAC
QT Input Channel

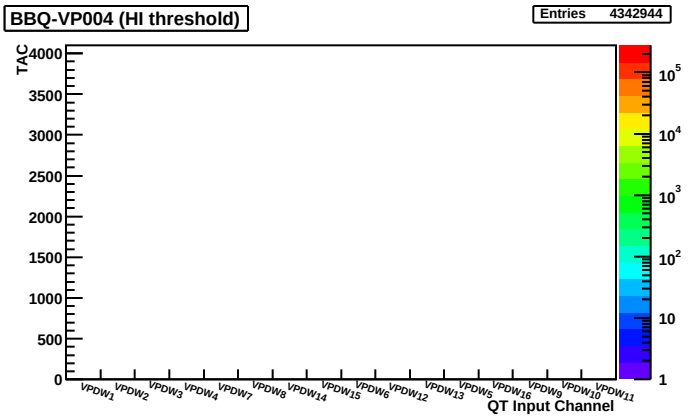

BBQ-VP002 (LO threshold)
Entries 4342944
ADC
QT Input Channel

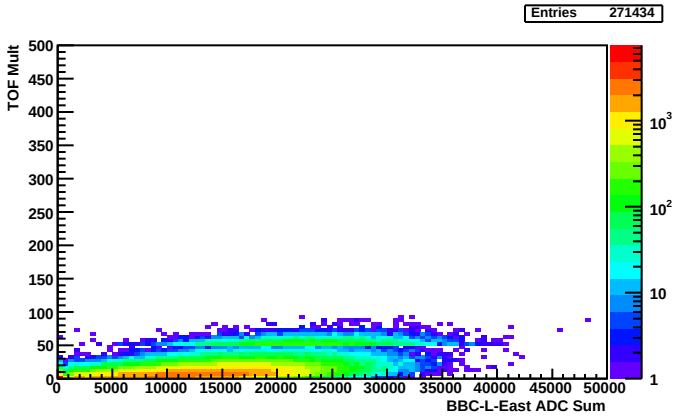

BBQ-VP002 (LO threshold)
Entries 4342944
TAC
QT Input Channel

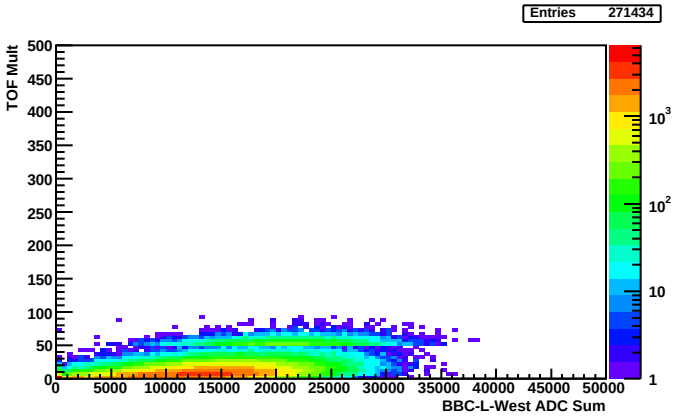

BBQ-VP003 (HI threshold)
Entries 4342944
ADC
QT Input Channel

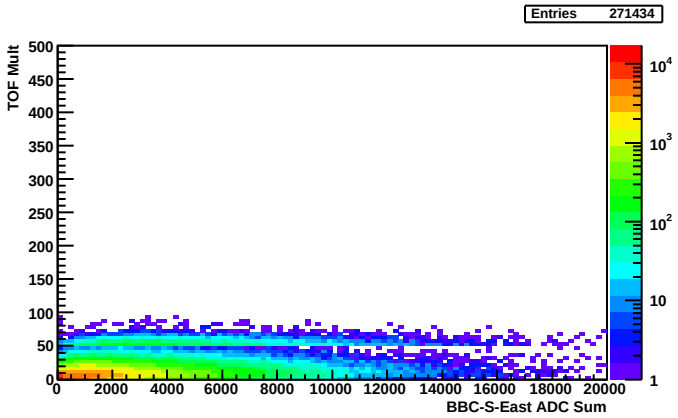

BBQ-VP003 (HI threshold)
Entries 4342944
TAC
QT Input Channel

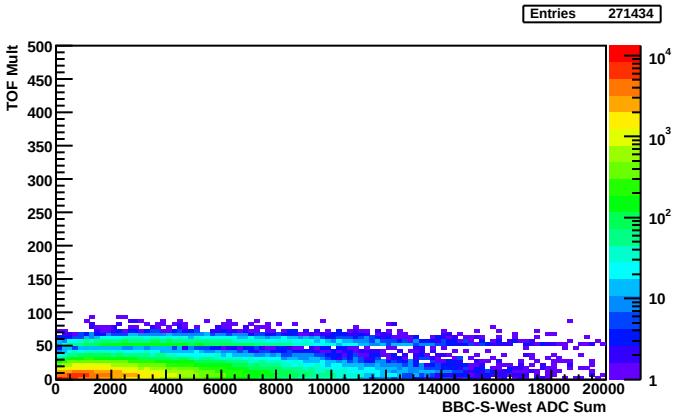

BBQ-VP004 (HI threshold)
Entries 4342944
ADC
QT Input Channel

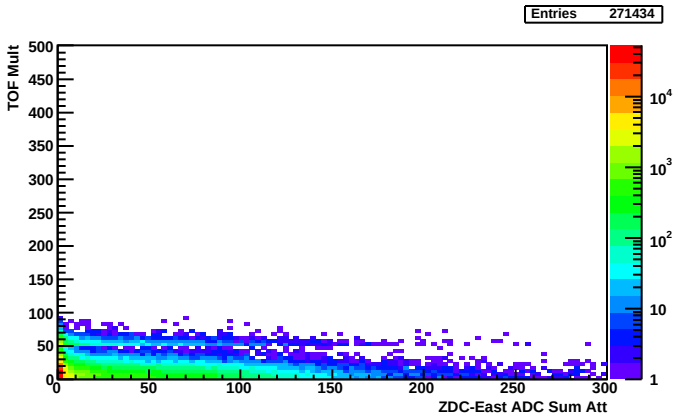

BBQ-VP004 (HI threshold)
Entries 4342944
TAC
QT Input Channel

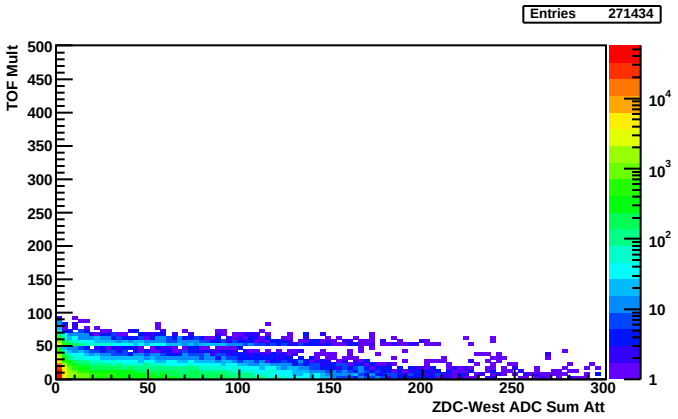

Entries 271434
TOF Mult
BBC-L-East ADC Sum

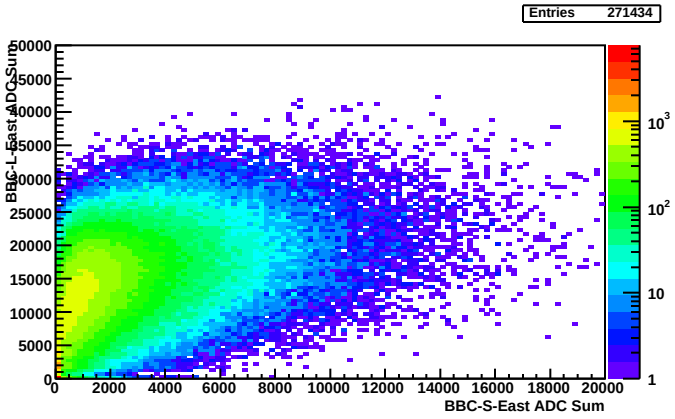

Entries 271434
TOF Mult
BBC-L-West ADC Sum

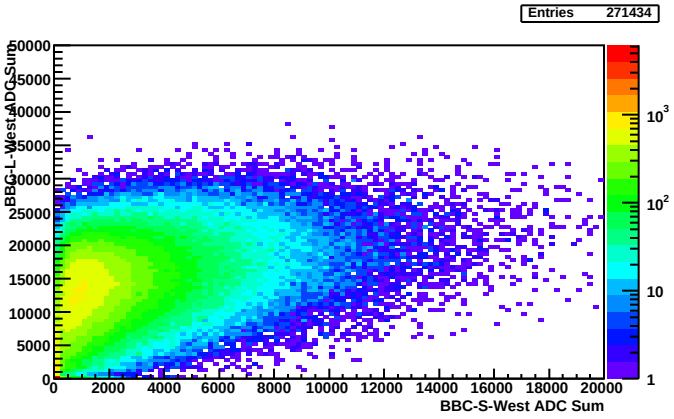

Entries 271434
TOF Mult
BBC-S-East ADC Sum

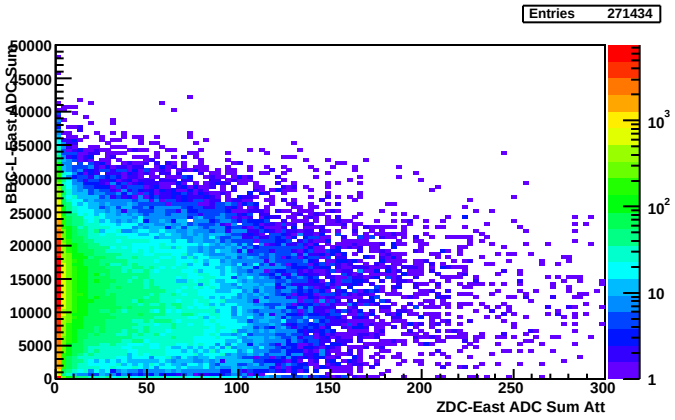

Entries 271434
TOF Mult
BBC-S-West ADC Sum

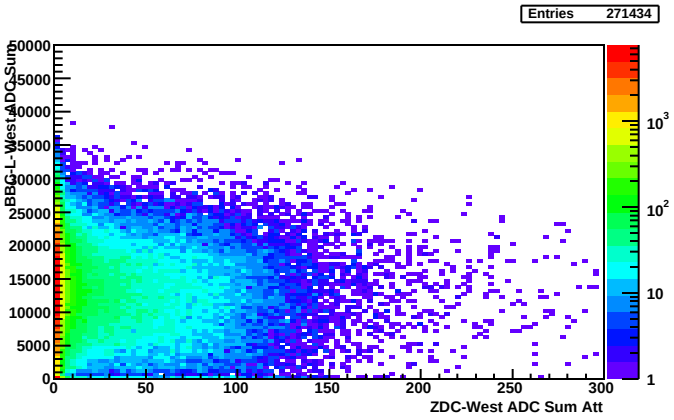

Entries 271434
TOF Mult
ZDC-East ADC Sum Att

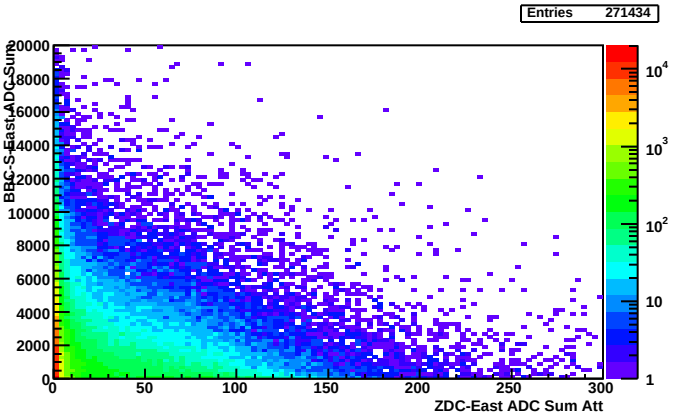

Entries 271434
TOF Mult
ZDC-West ADC Sum Att

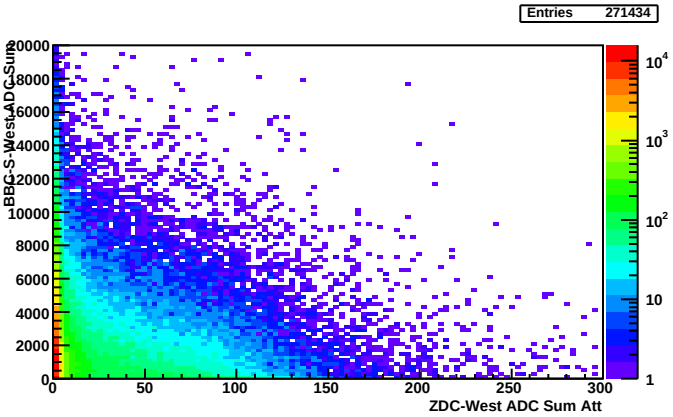

Entries 271434
BBC-L-East ADC Sum
BBC-S-East ADC Sum

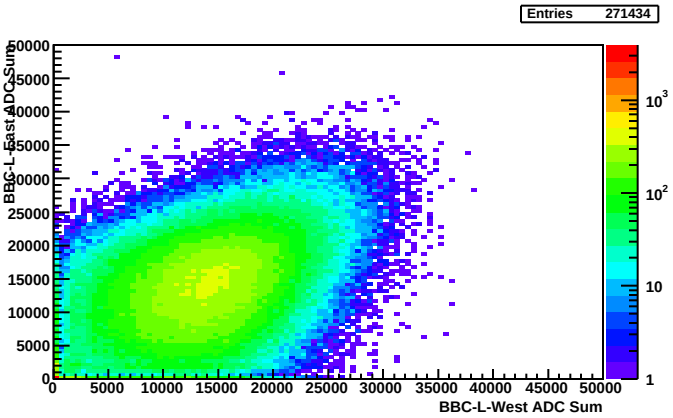

Entries 271434
BBC-L-West ADC Sum
BBC-S-West ADC Sum

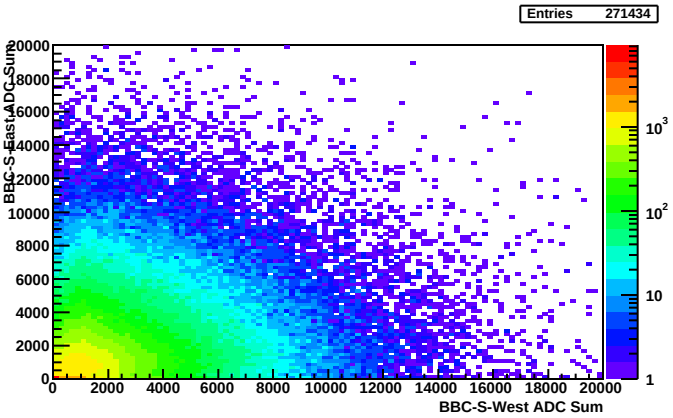

Entries 271434
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

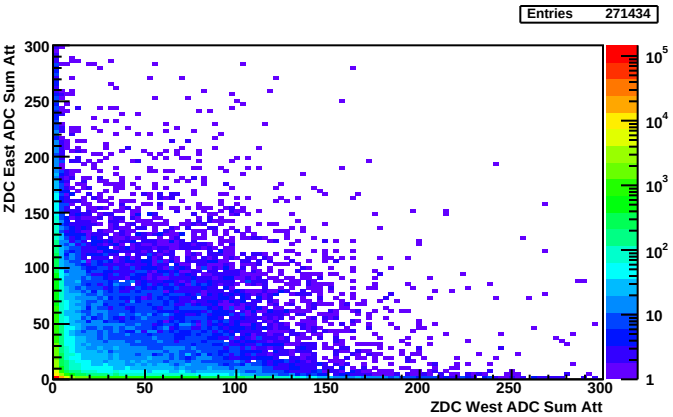

Entries 271434
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

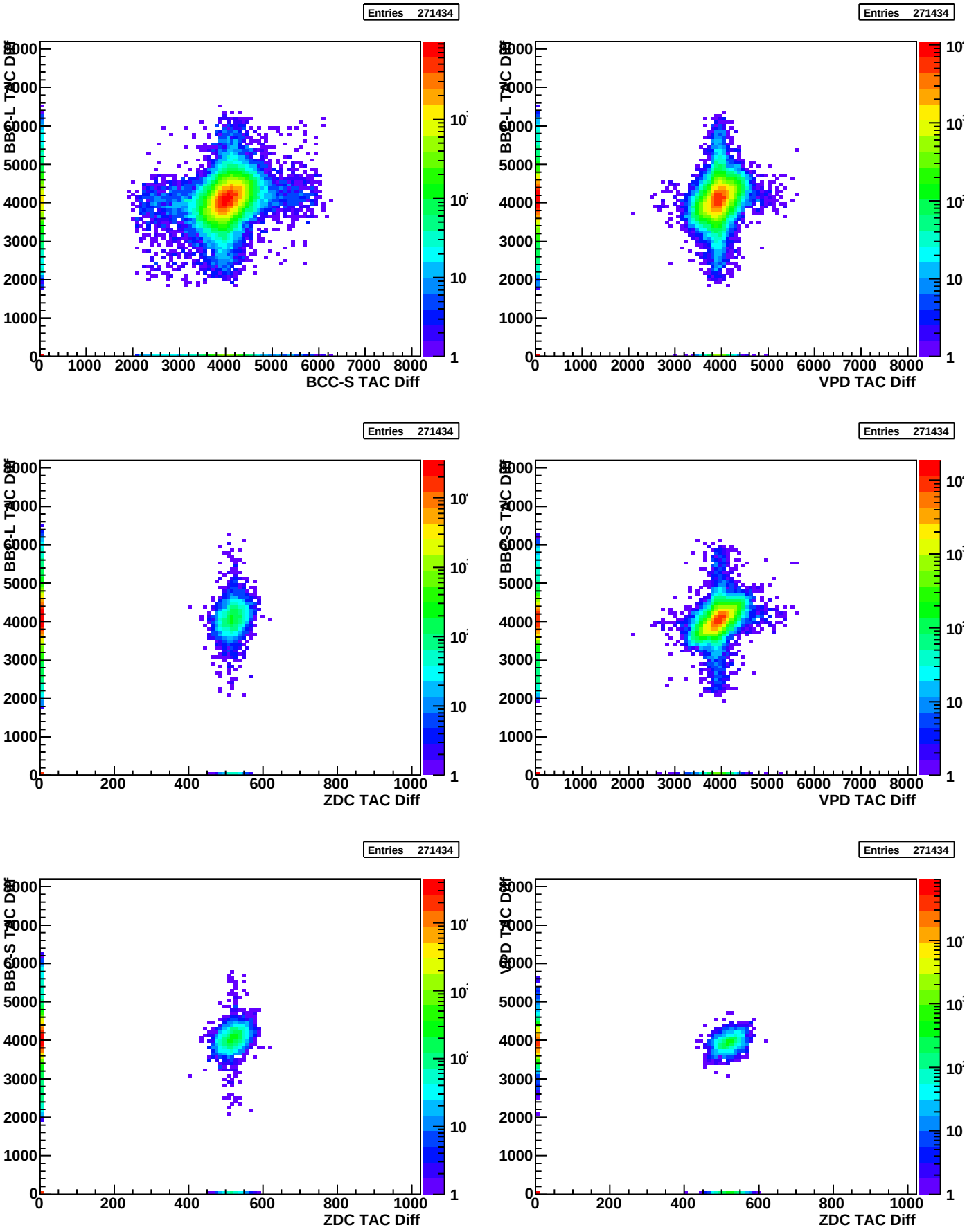

Entries 271434
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

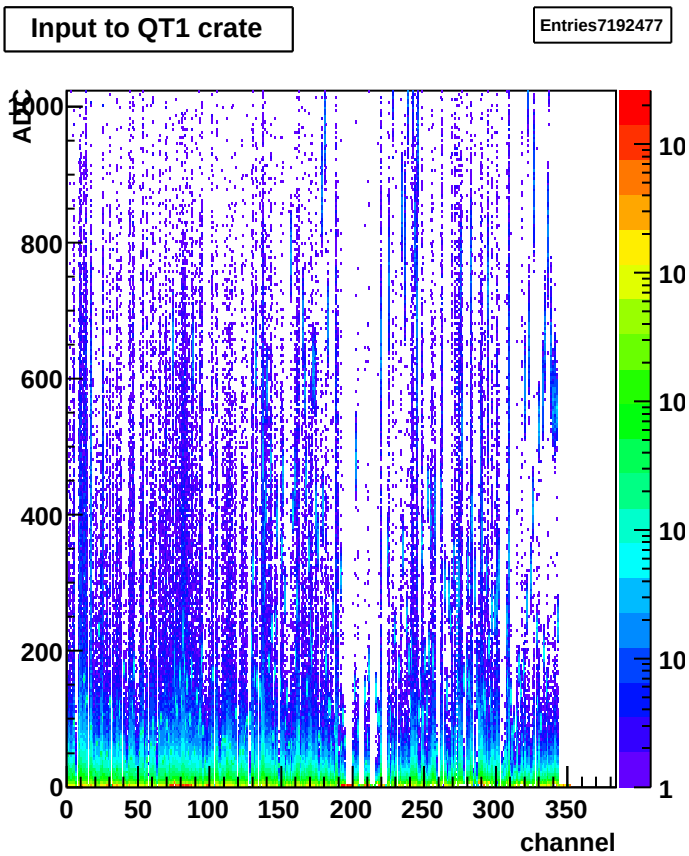

Entries 271434
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

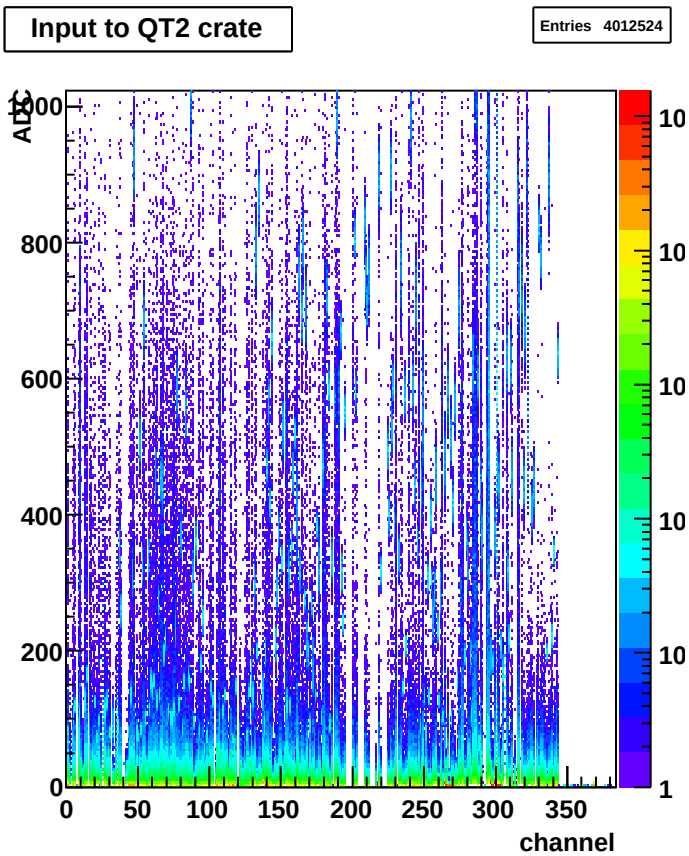

Entries 271434
BBC-L-East ADC Sum
BBC-L-West ADC Sum

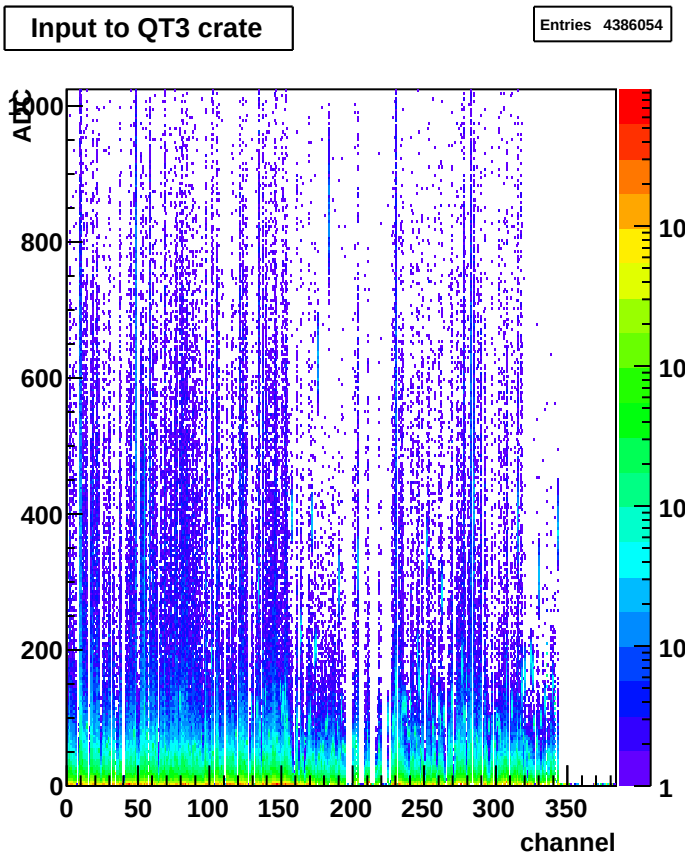

Entries 271434
BBC-S-East ADC Sum
BBC-S-West ADC Sum

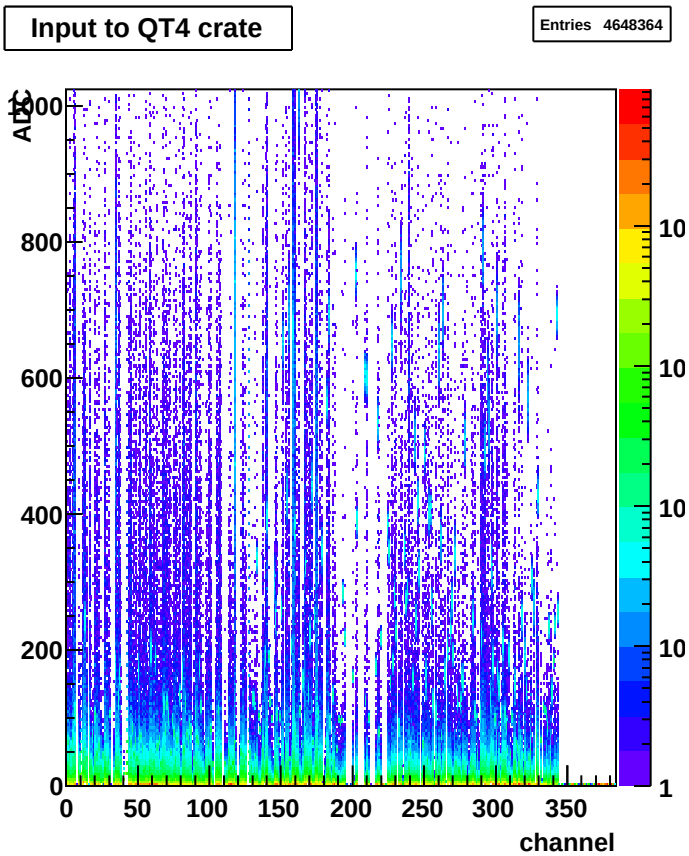

Entries 271434
ZDC East ADC Sum Att
ZDC West ADC Sum Att

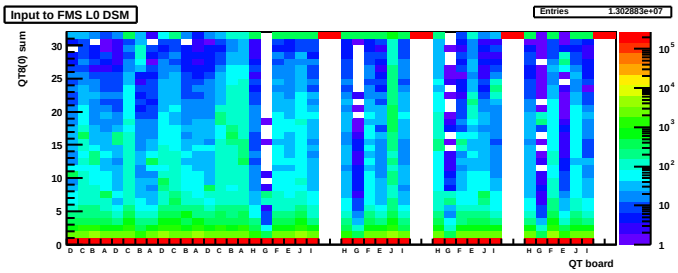

Entries 271434
BBC-L TAC Diff
BCC-S TAC Diff
Entries 271434
BBC-L TAC Diff
VPD TAC Diff
Entries 271434
BBC-L TAC Diff
ZDC TAC Diff
Entries 271434
BBC-S TAC Diff
VPD TAC Diff
Entries 271434
BBC-S TAC Diff
ZDC TAC Diff
Entries 271434
VPD TAC Diff
ZDC TAC Diff

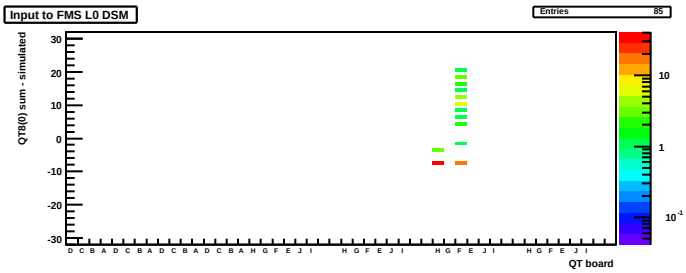

Input to QT1 crate
Entries7192477
ADC
channel

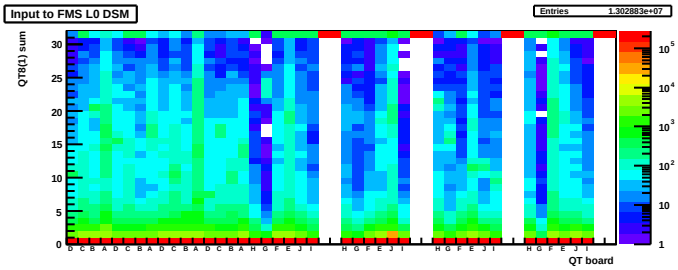

Input to QT2 crate
Entries 4012524
ADC
channel

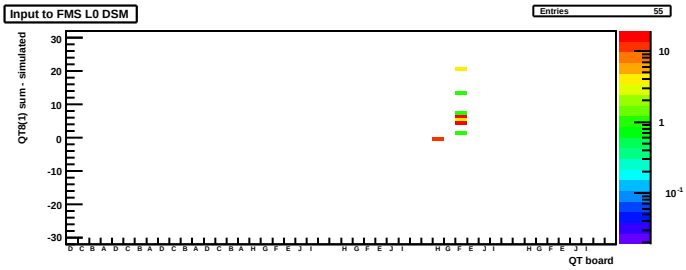

Input to QT3 crate
Entries 4386054
ADC
channel

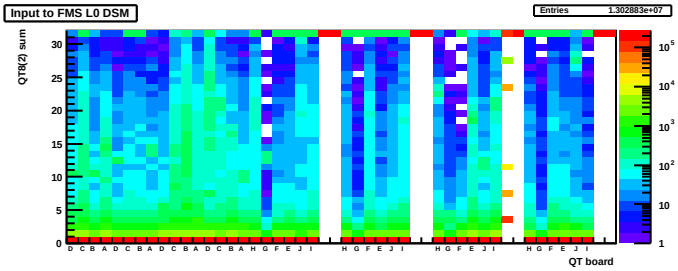

Input to QT4 crate
Entries 4648364
ADC
channel

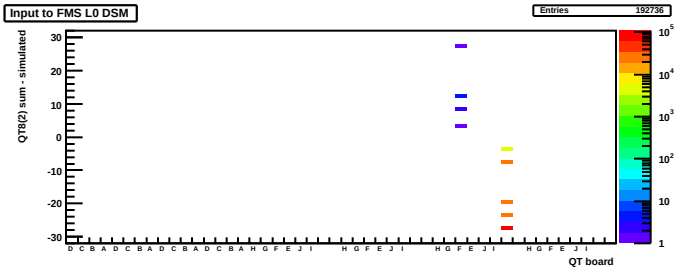

Input to FMS L0 DSM
Entries 1.302883e+07
QT8(0) sum
QT board

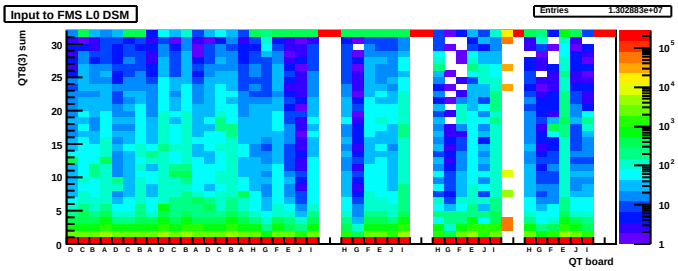

Input to FMS L0 DSM
Entries 85
QT8(0) sum - simulated
QT board

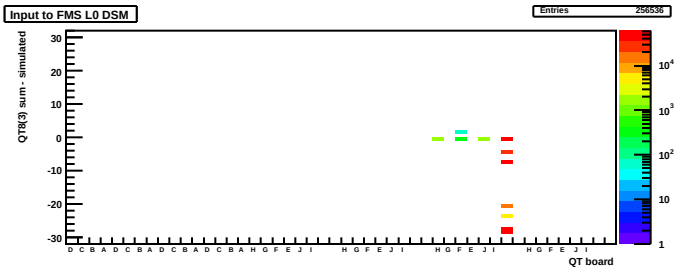

Input to FMS L0 DSM
Entries 1.302883e+07
QT8(1) sum
QT board

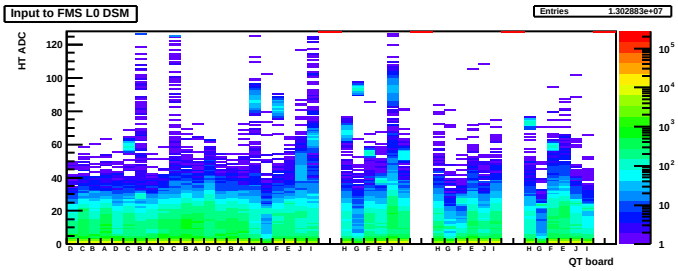

Input to FMS L0 DSM
Entries 55
QT8(1) sum - simulated
QT board

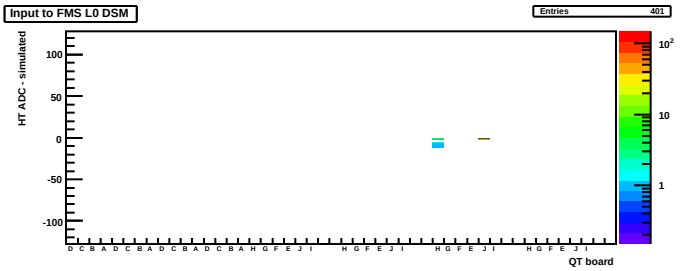

Input to FMS L0 DSM
Entries 1.302883e+07
QT8(2) sum
QT board

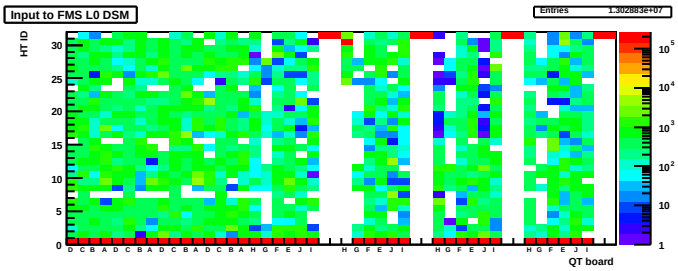

Input to FMS L0 DSM
Entries 192736
QT8(2) sum - simulated
QT board

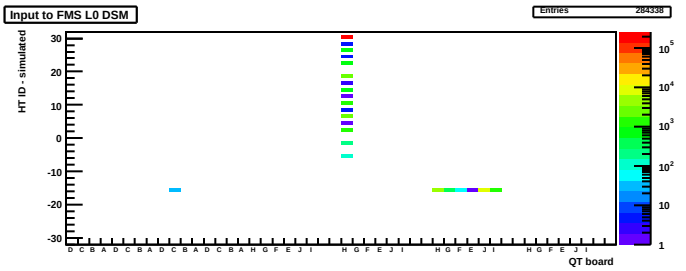

Input to FMS L0 DSM
Entries 1.302883e+07
QT8(3) sum
QT board

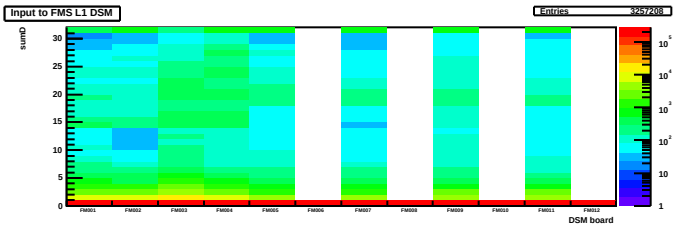

Input to FMS L0 DSM
Entries 256536
QT8(3) sum - simulated
QT board

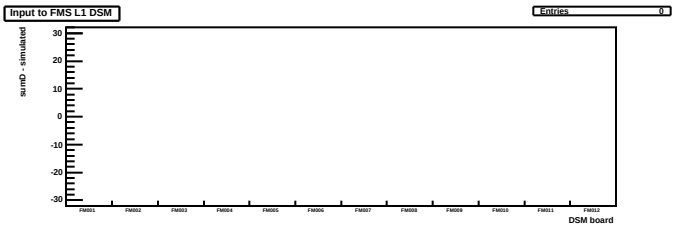

Input to FMS L0 DSM
Entries 1.302883e+07
HT ADC
QT board

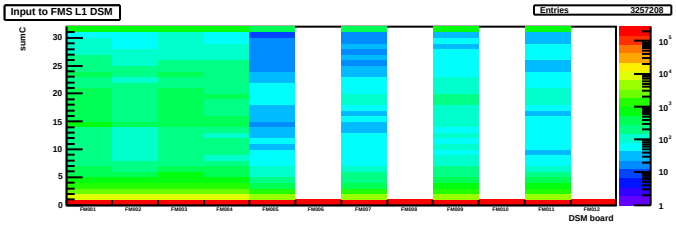

Input to FMS L0 DSM
Entries 401
HT ADC - simulated
QT board

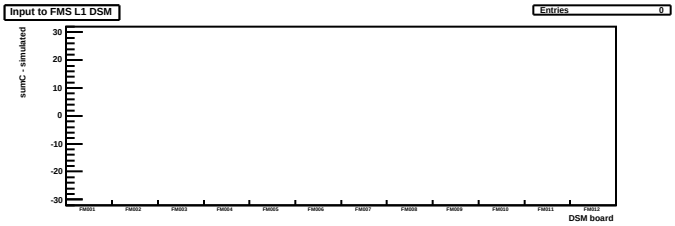

Input to FMS L0 DSM
Entries 1.302883e+07
HT ID
QT board

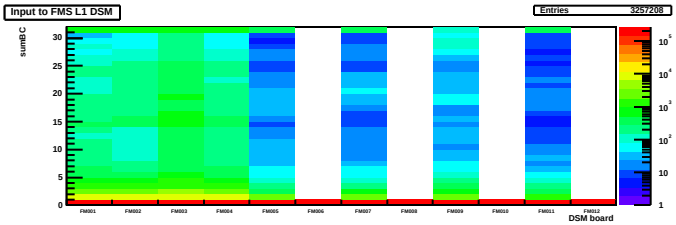

Input to FMS L0 DSM
Entries 284338
HT ID - simulated
QT board

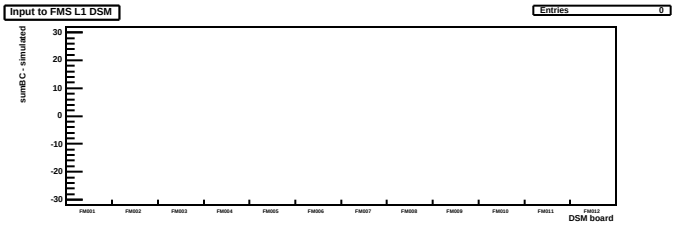

Input to FMS L1 DSM
Entries 3257208
sumD
DSM board

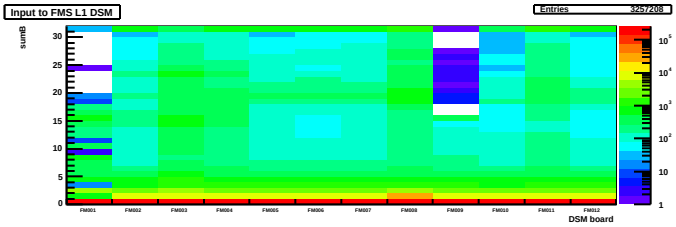

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

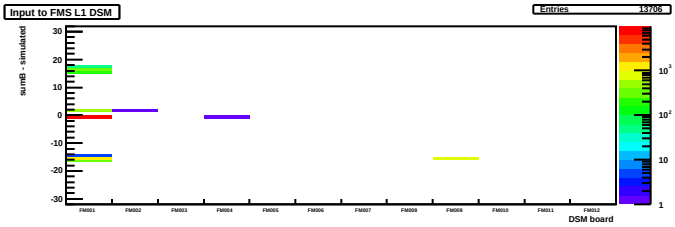

Input to FMS L1 DSM
Entries 3257208
sumC
DSM board

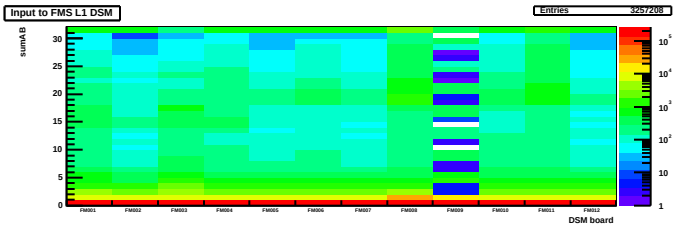

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

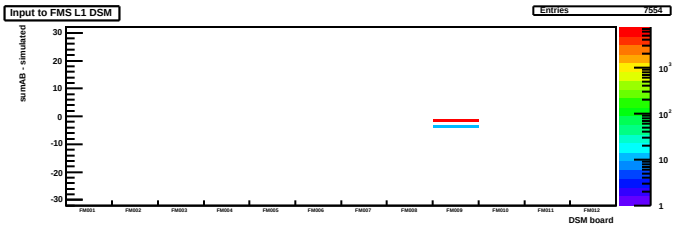

Input to FMS L1 DSM
Entries 3257208
sumBC
DSM board

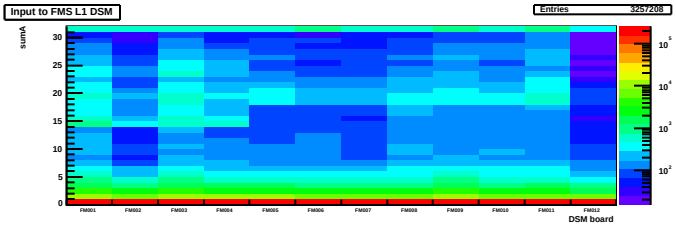

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

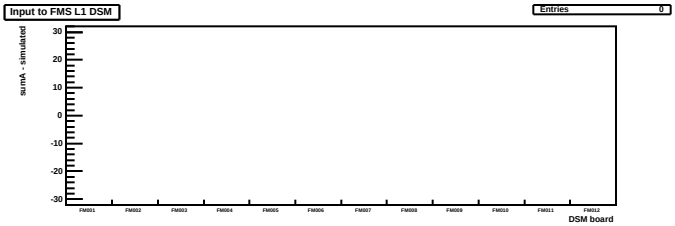

Input to FMS L1 DSM
Entries 3257208
sumB
DSM board

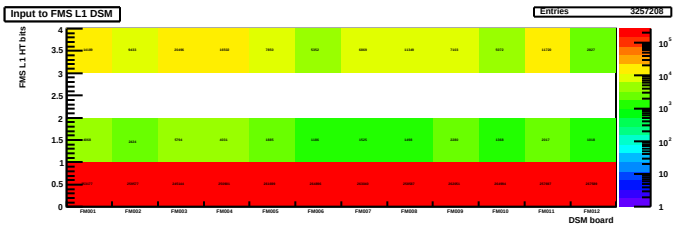

Input to FMS L1 DSM
Entries 13706
sumB - simulated
DSM board

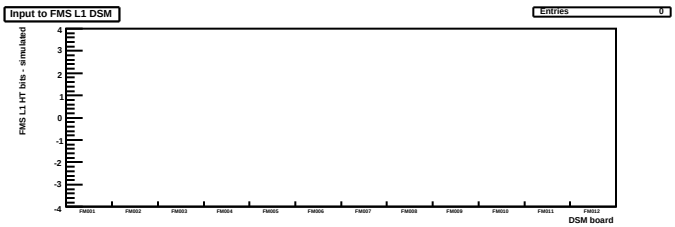

Input to FMS L1 DSM
Entries 3257208
sumAB
DSM board

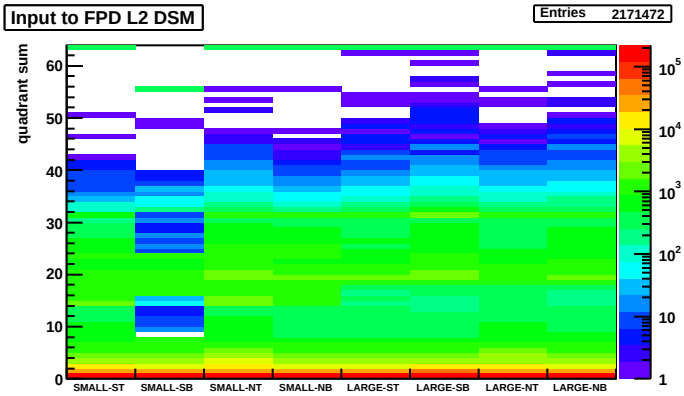

Input to FMS L1 DSM
Entries 7554
sumAB - simulated
DSM board

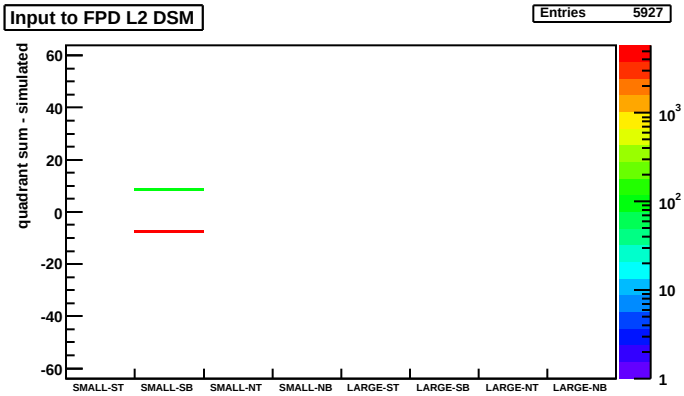

Input to FMS L1 DSM
Entries 3257208
sumA
DSM board

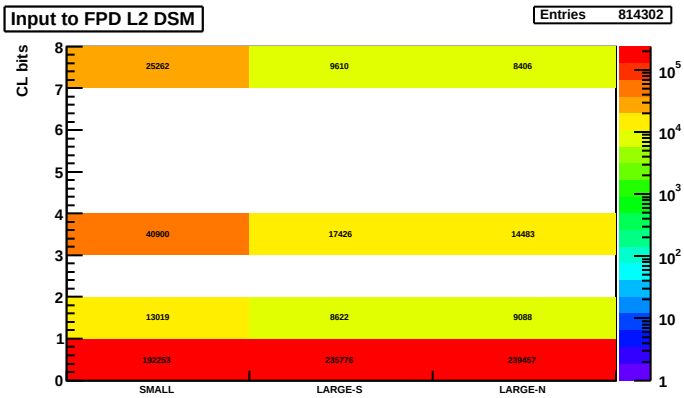

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

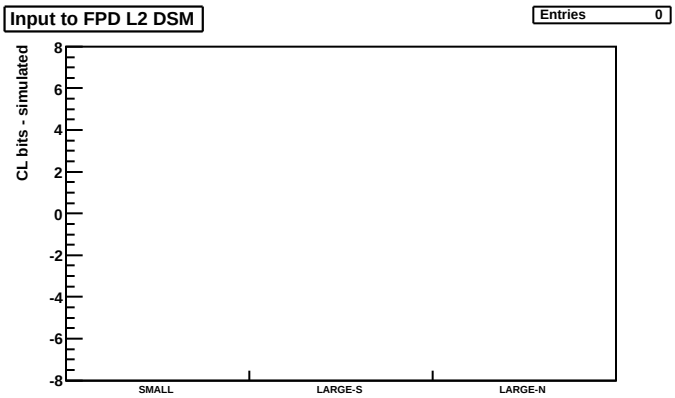

Input to FMS L1 DSM
Entries 3257208
FMS L1 HT bits
DSM board

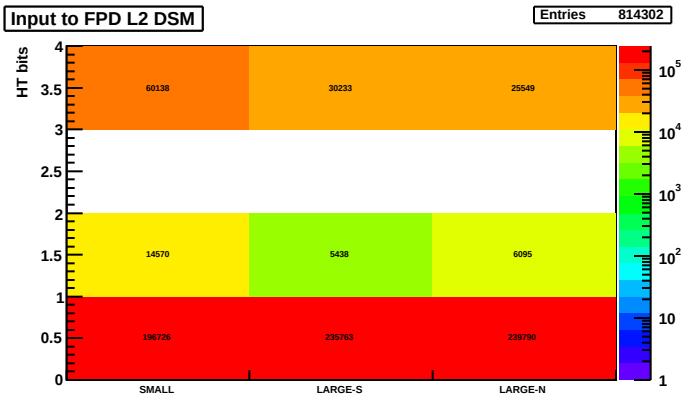

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

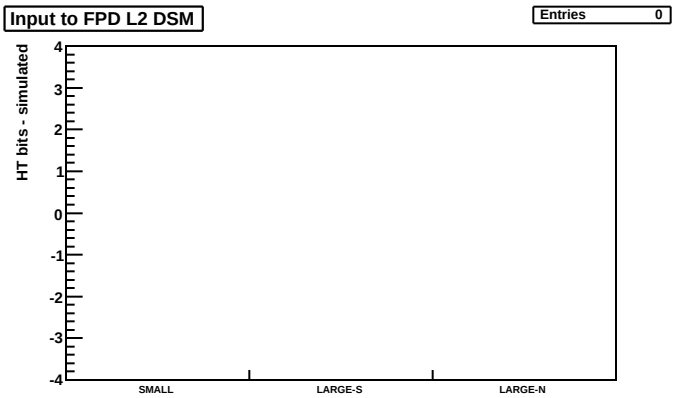

Input to FPD L2 DSM
Entries 2171472
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

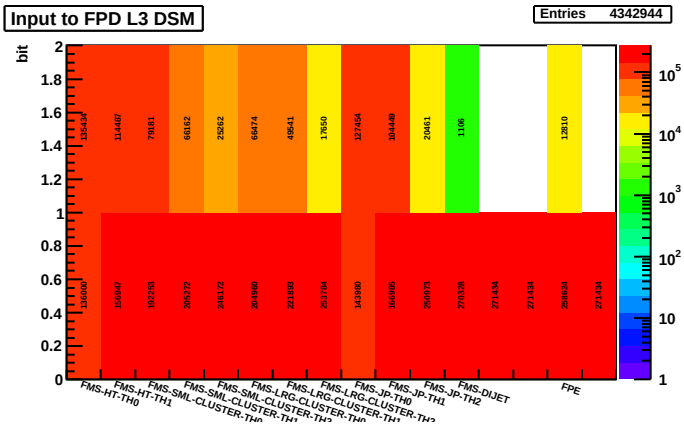

Input to FPD L2 DSM
Entries 5927
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

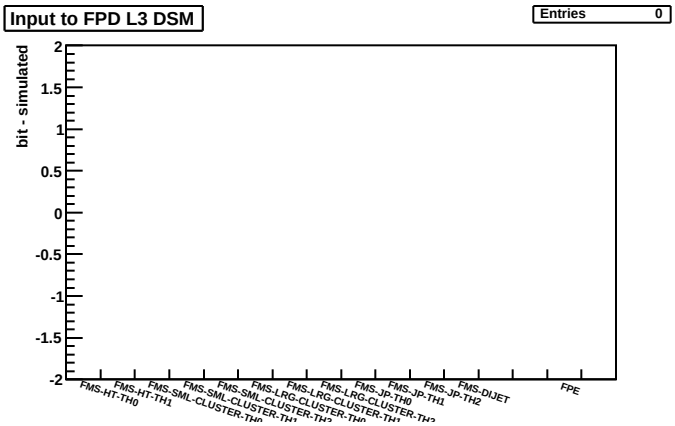

Input to FPD L2 DSM
Entries 814302
CL bits
25262 9610 8406
40900 17426 14483
13019 8622 9088
192253 235776 239457
SMALL LARGE-S LARGE-N

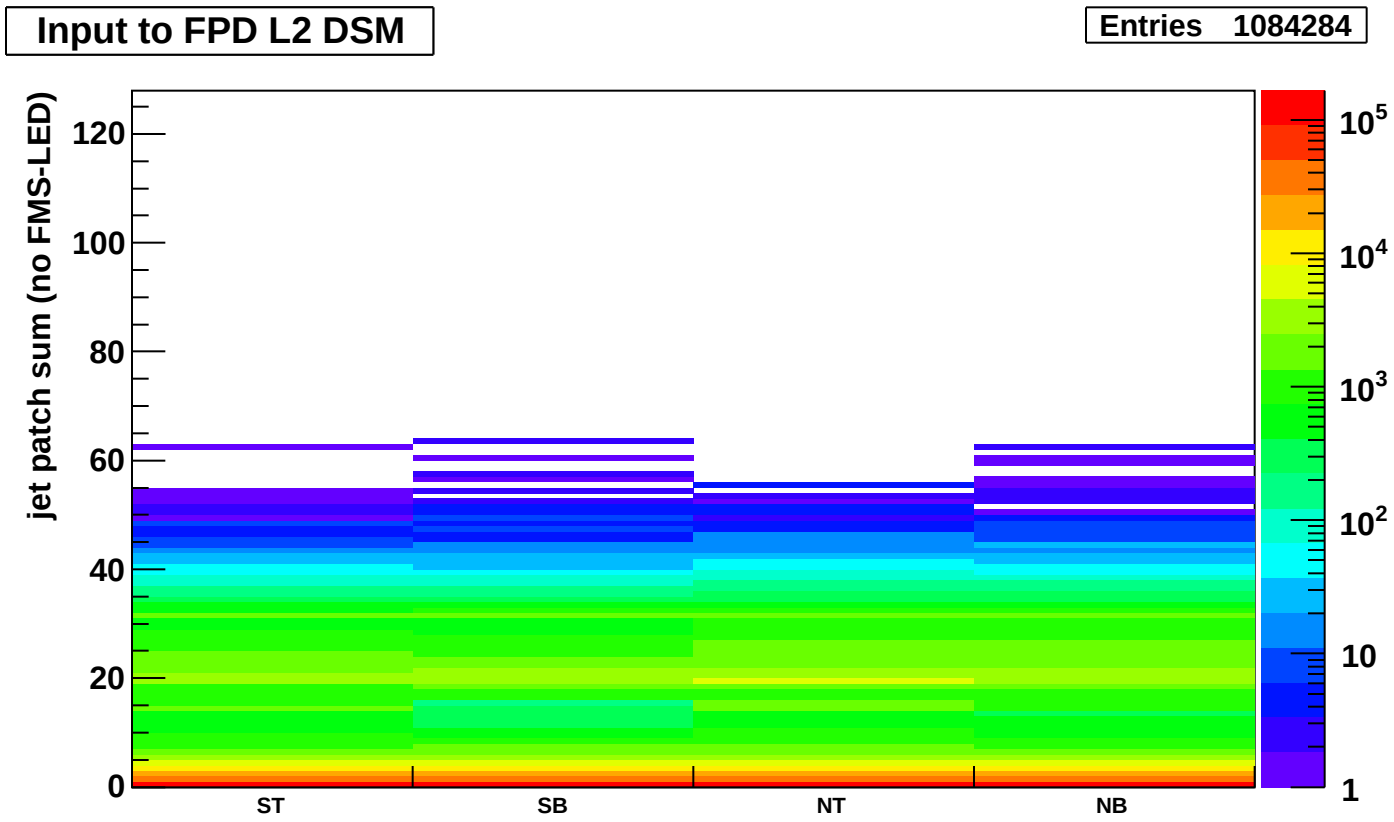

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL LARGE-S LARGE-N

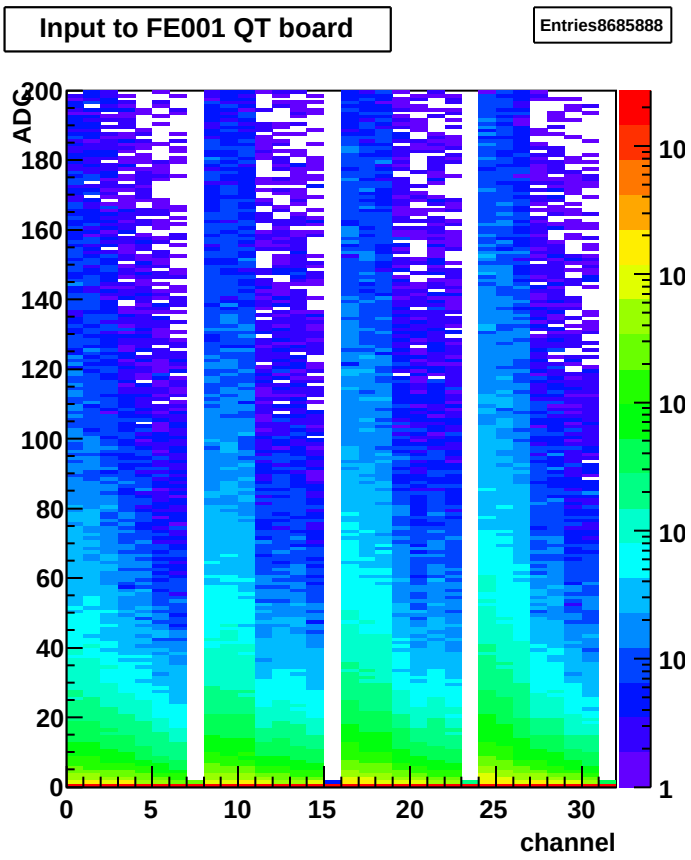

Input to FPD L2 DSM
Entries 814302
HT bits
60138 30233 25549
14570 5438 6095
196726 235763 239790
SMALL LARGE-S LARGE-N

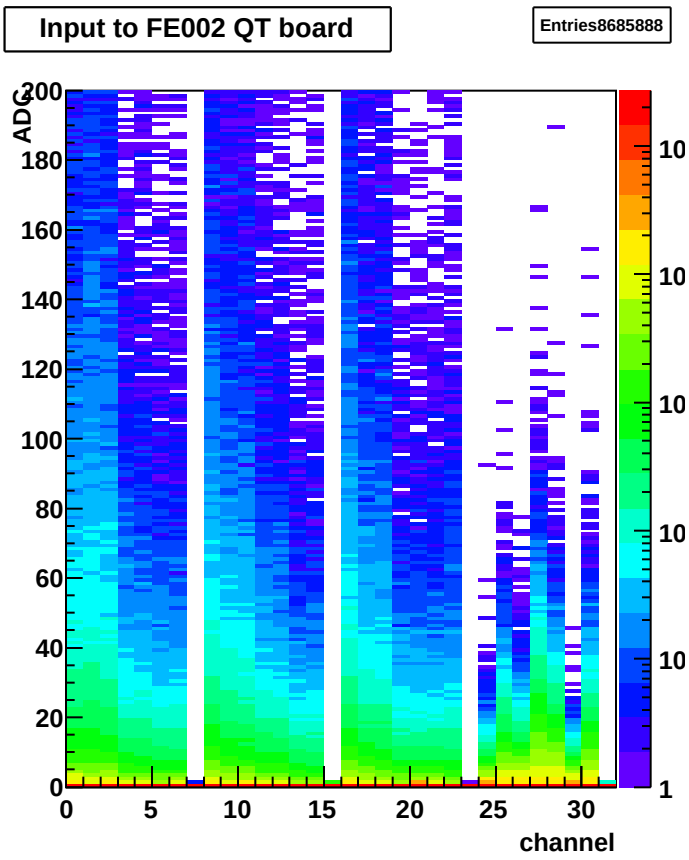

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

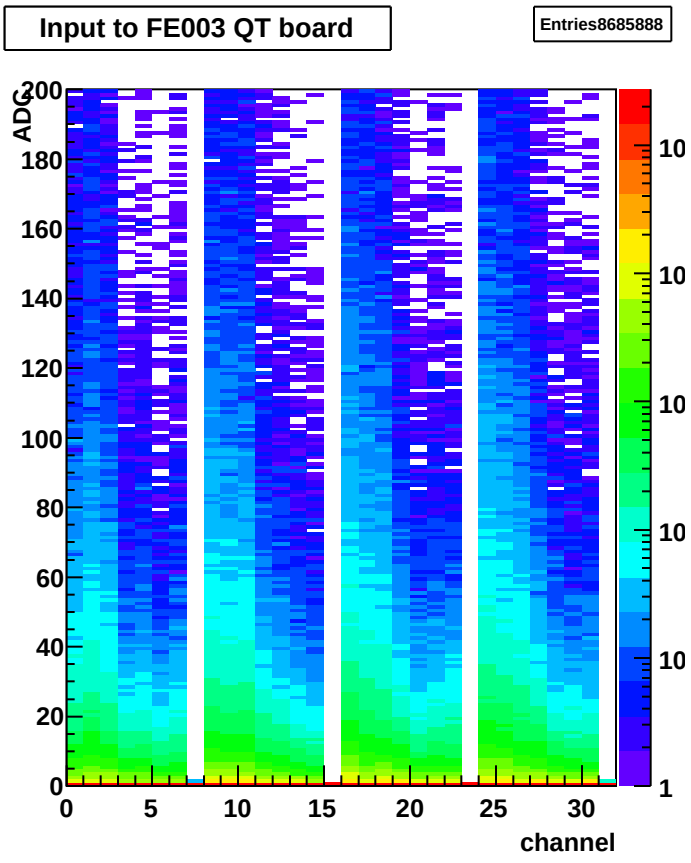

Input to FPD L3 DSM
Entries 4342944
bit
135434 114487 78131 66162 23262 66474 49541 17650 127454 104449 20461 1186 12810
136605 156947 192253 205272 246172 204960 221593 253784 143980 166985 250973 270328 271434 271434 259824 271434
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

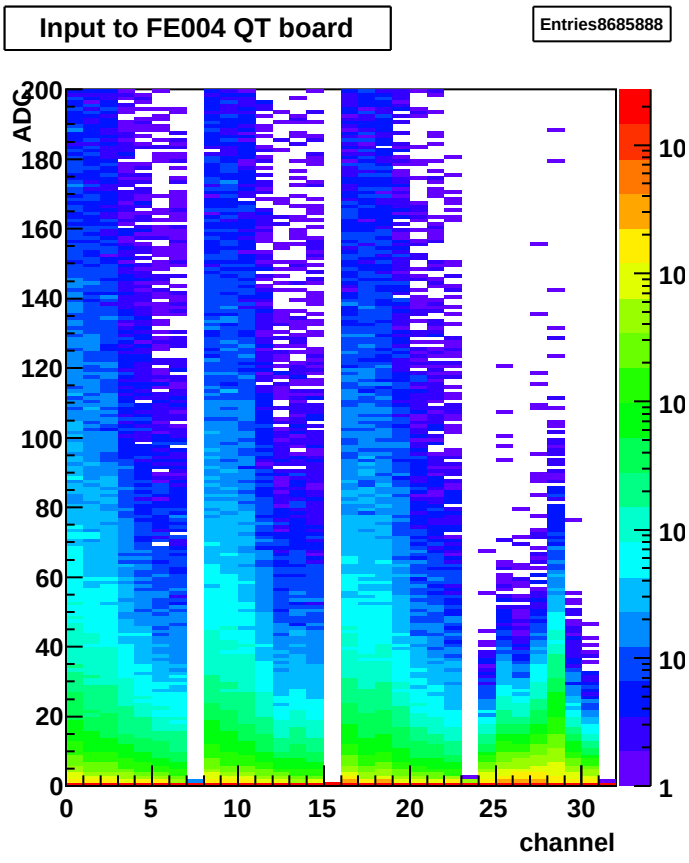

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

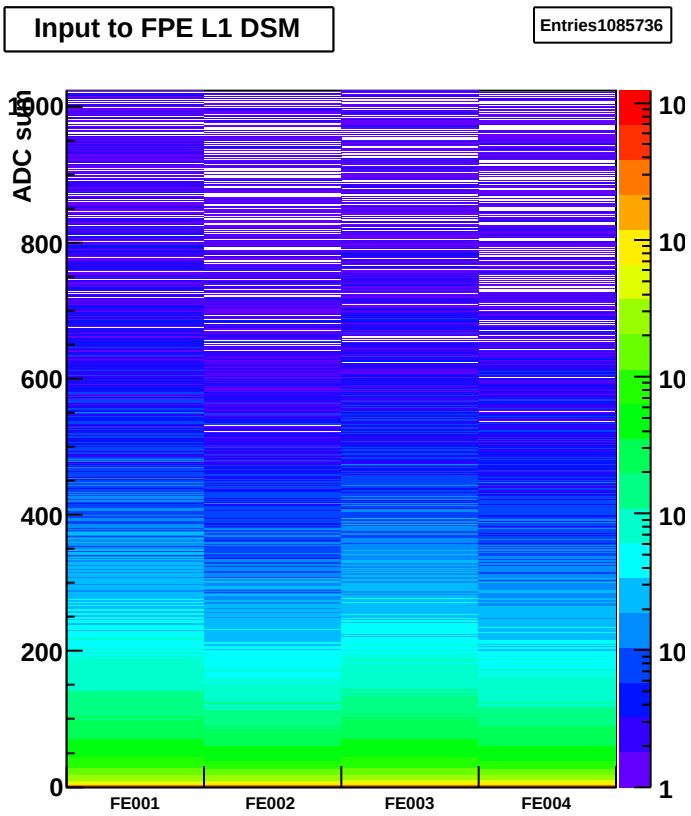

Input to FPD L2 DSM
Entries 1084284
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

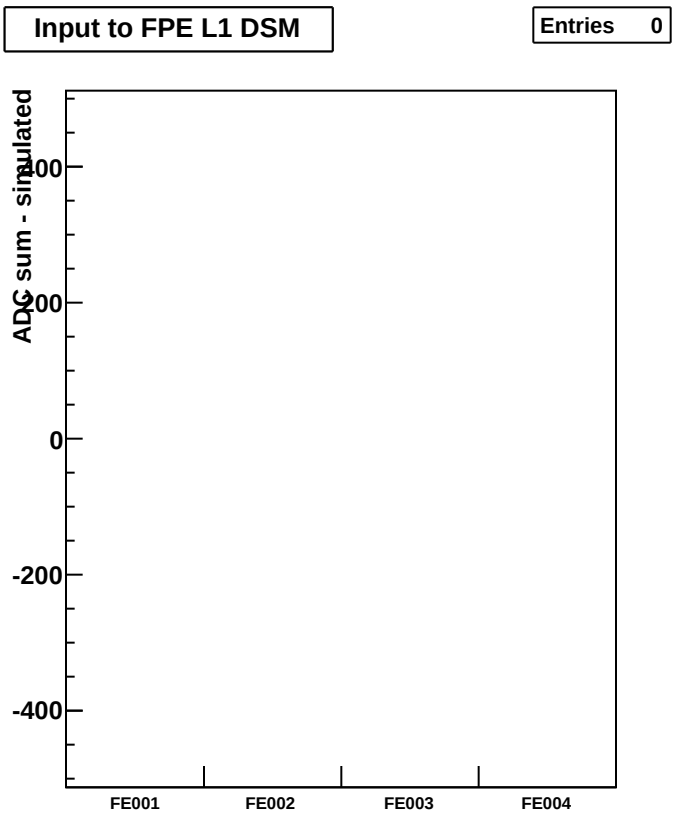

Input to FE001 QT board
Entries8685888
ADC
channel

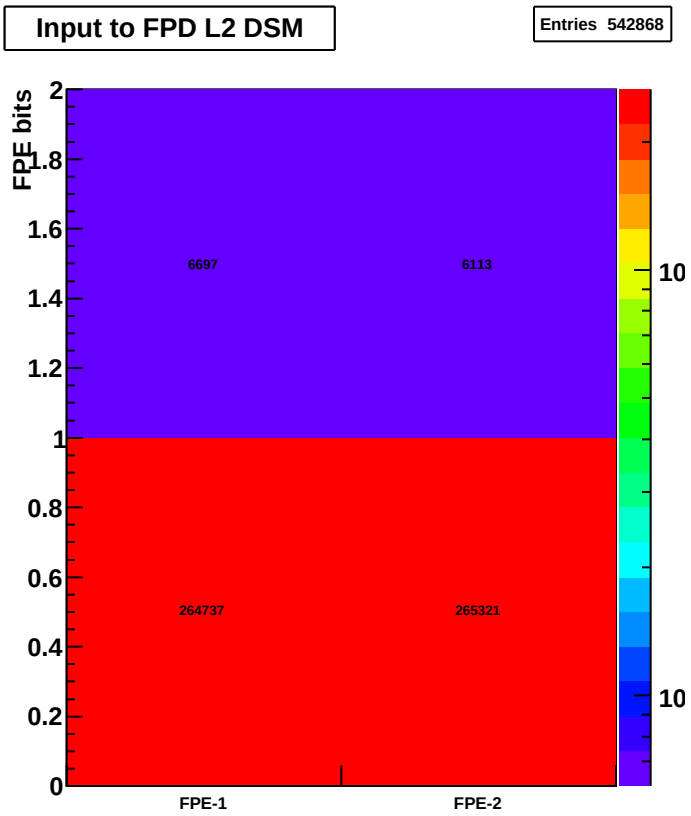

Input to FE002 QT board
Entries8685888
ADC
channel

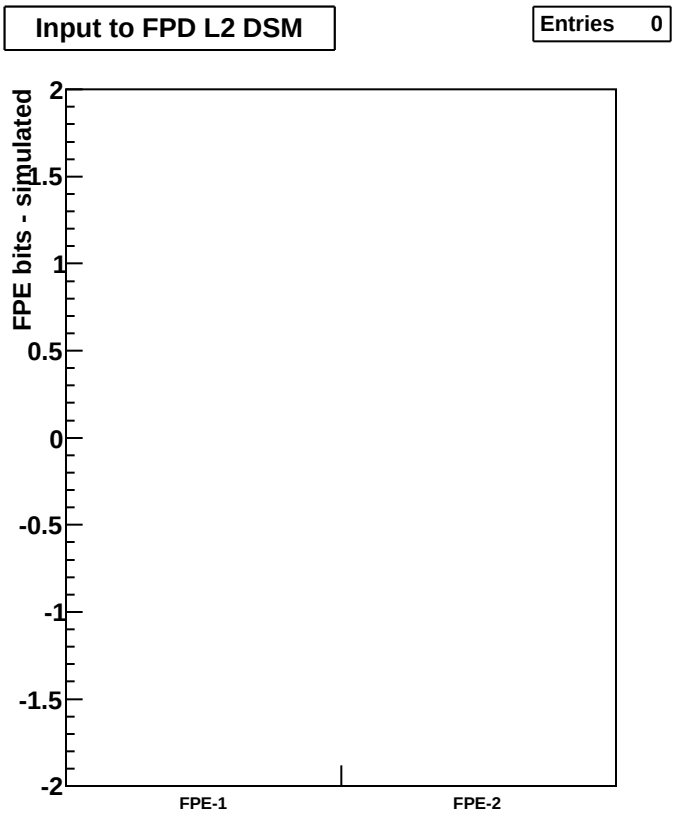

Input to FE003 QT board
Entries8685888
ADC
channel


Input to FE004 QT board
Entries8685888
ADC
channel


Input to FPE L1 DSM
Entries1085736
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 542868
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
6697
6113
264737
265321
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2