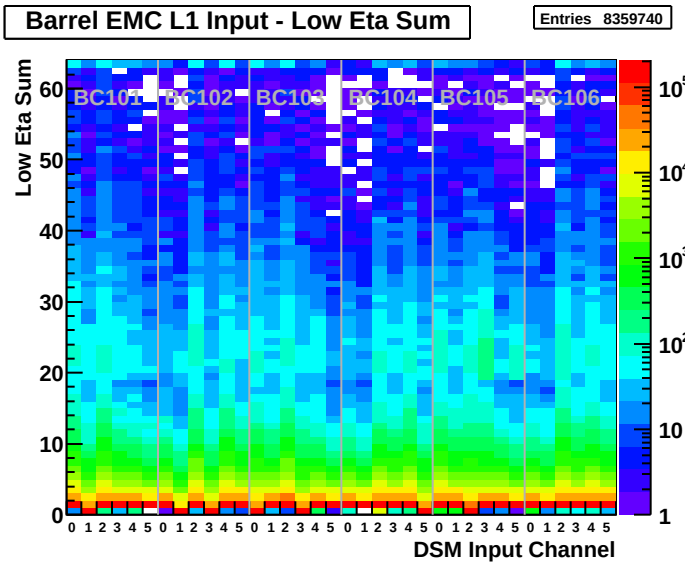
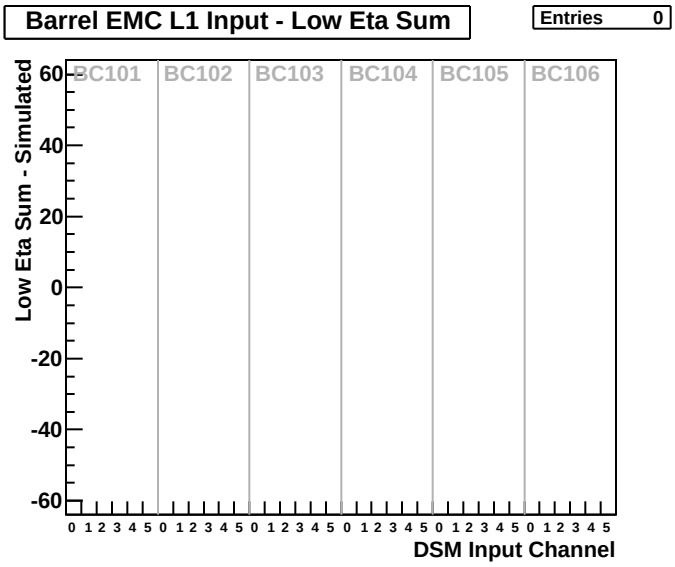
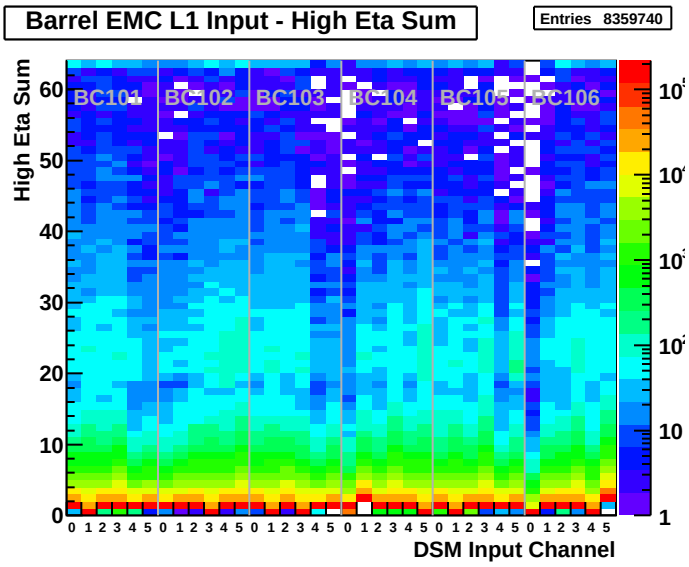
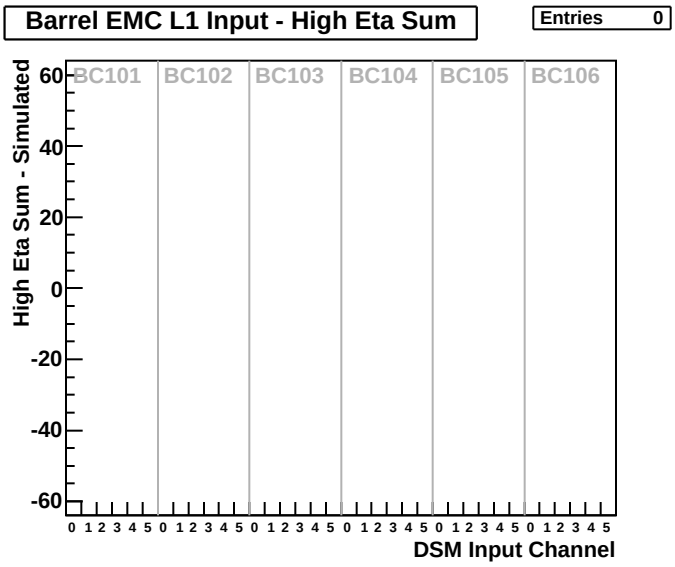
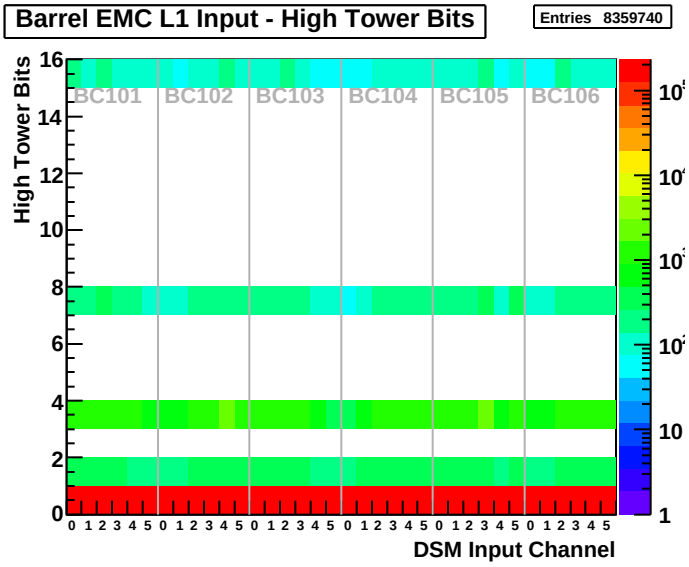
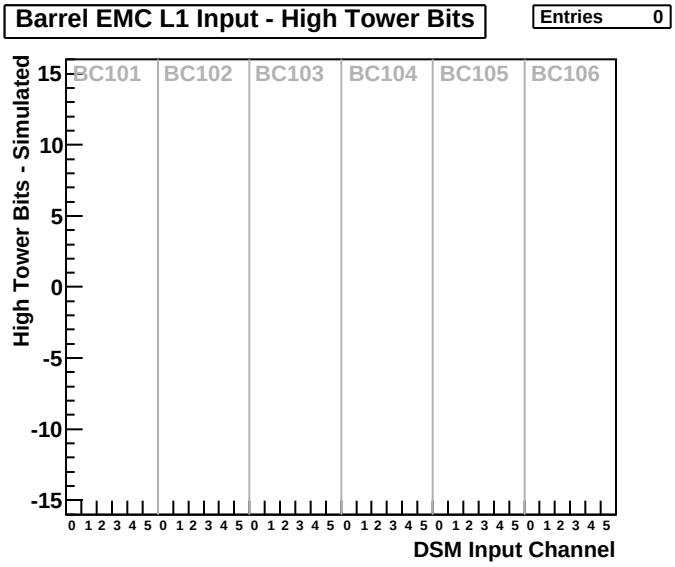

Barrel EMC L1 Input - Low Eta Sum
Entries 8359740
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

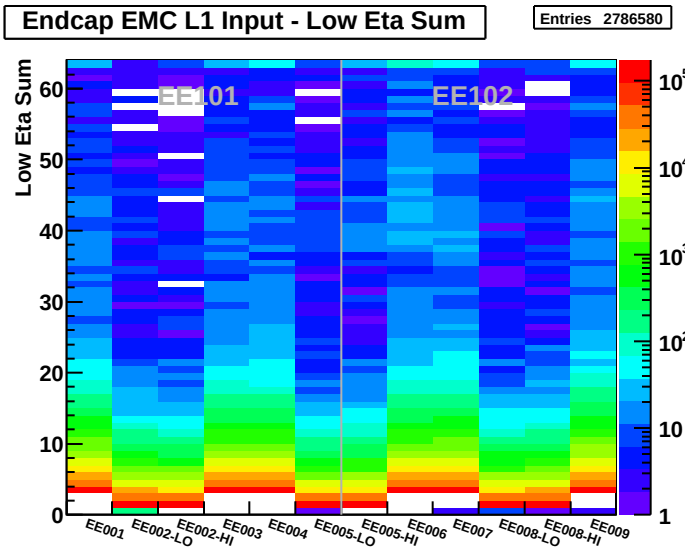

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

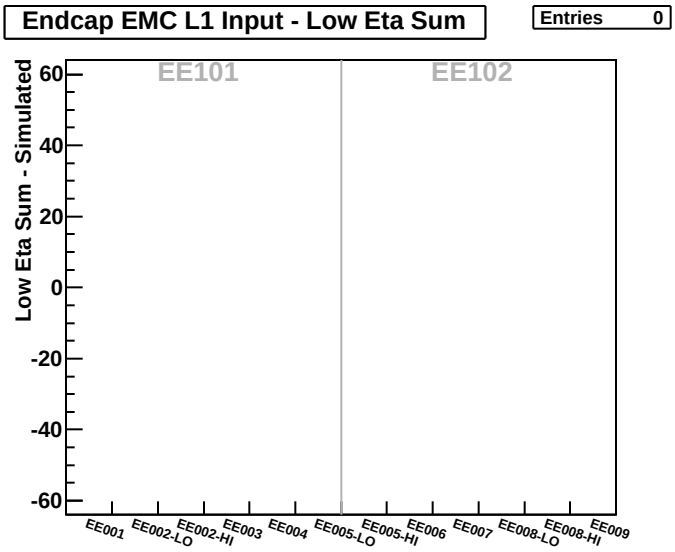

Barrel EMC L1 Input - High Eta Sum
Entries 8359740
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

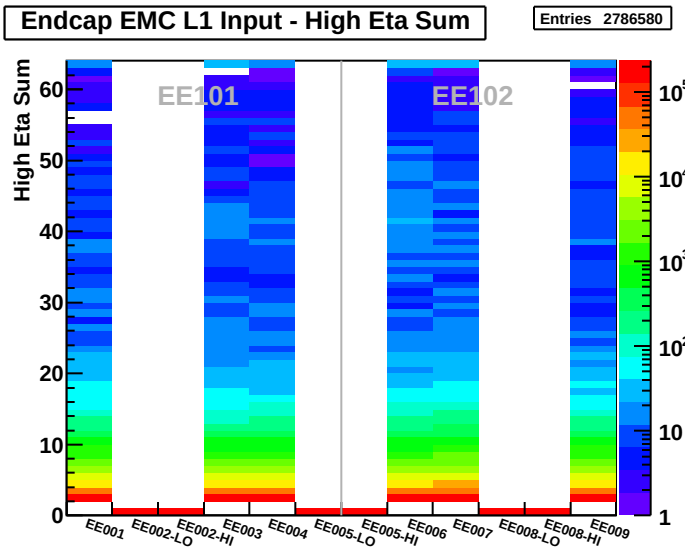

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

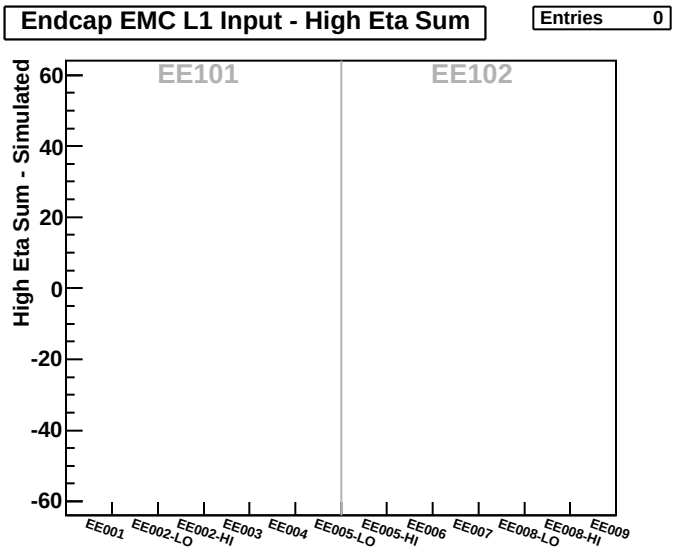

Barrel EMC L1 Input - High Tower Bits
Entries 8359740
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

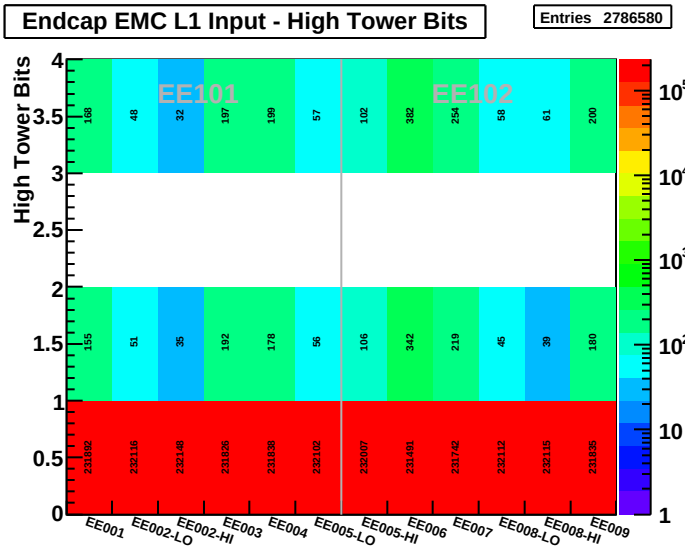

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

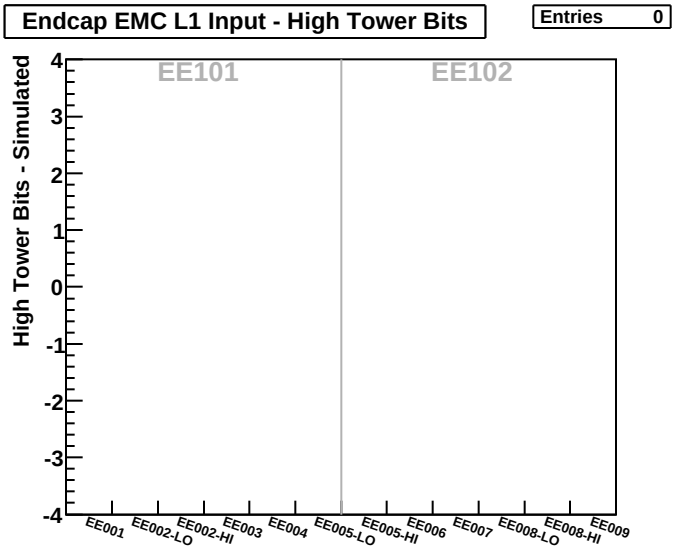

Endcap EMC L1 Input - Low Eta Sum
Entries 2786580
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

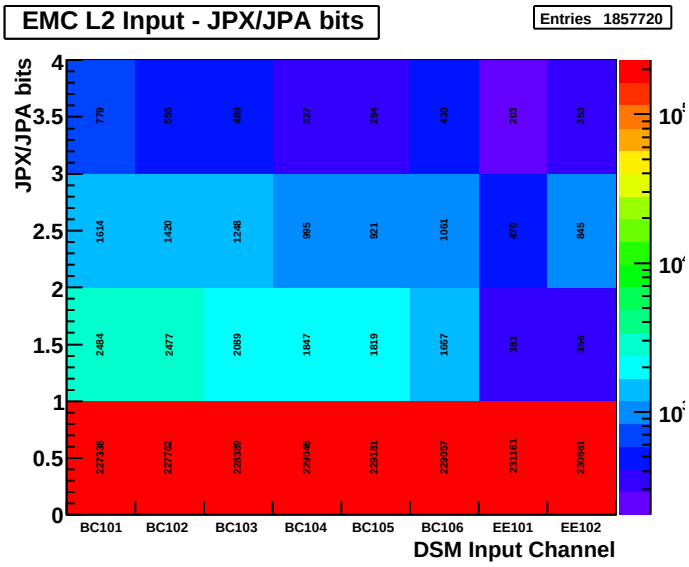

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

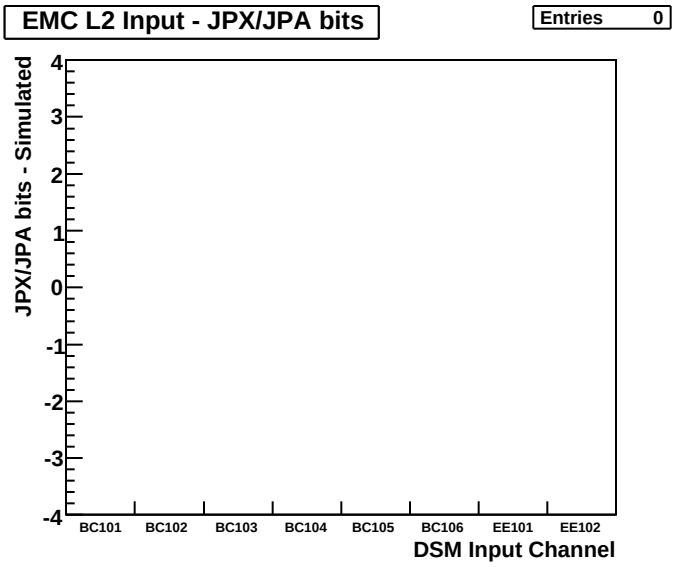

Endcap EMC L1 Input - High Eta Sum
Entries 2786580
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

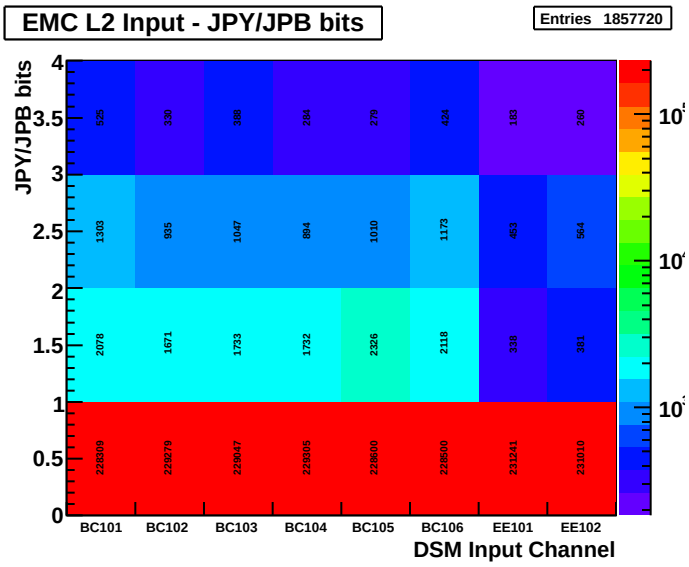

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

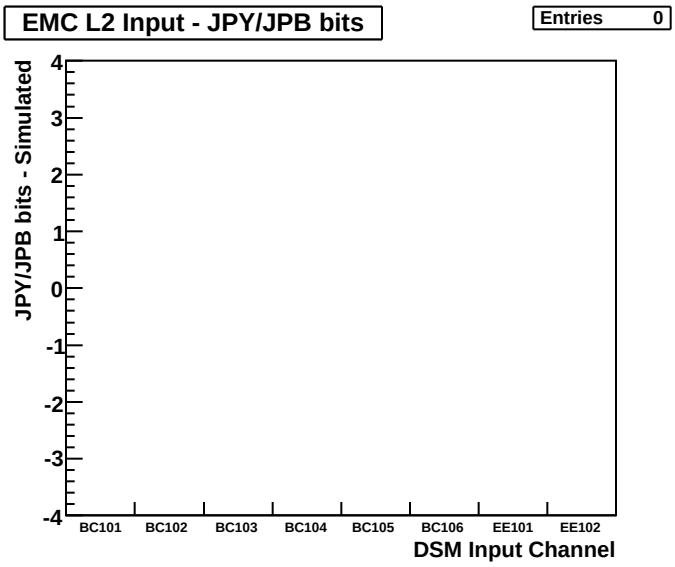

Endcap EMC L1 Input - High Tower Bits
Entries 2786580
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

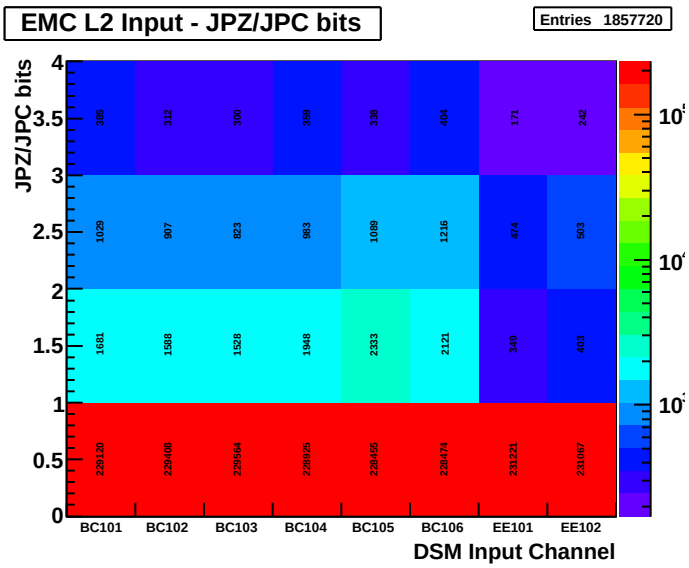

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

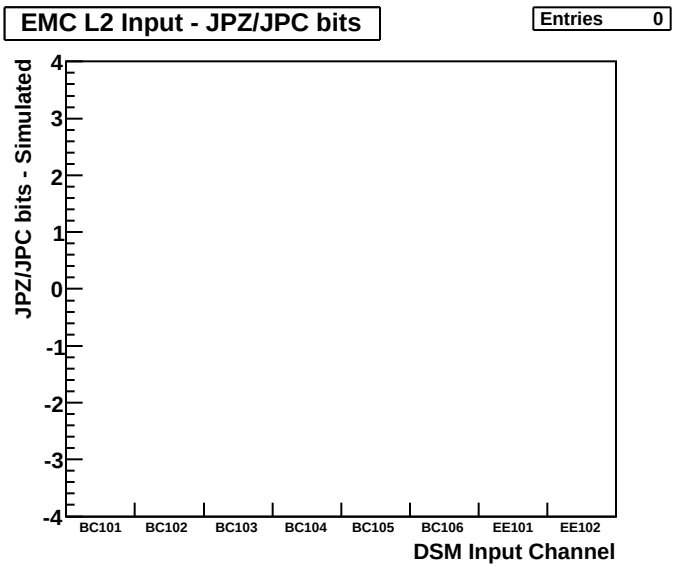

EMC L2 Input - JPX/JPA bits
Entries 1857720
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
779 556 489 327 294 430 203 353
1614 1420 1248 995 921 1061 470 845
2484 2477 2089 1847 1819 1667 381 356
227338 227762 228389 229046 229181 229057 231161 230661

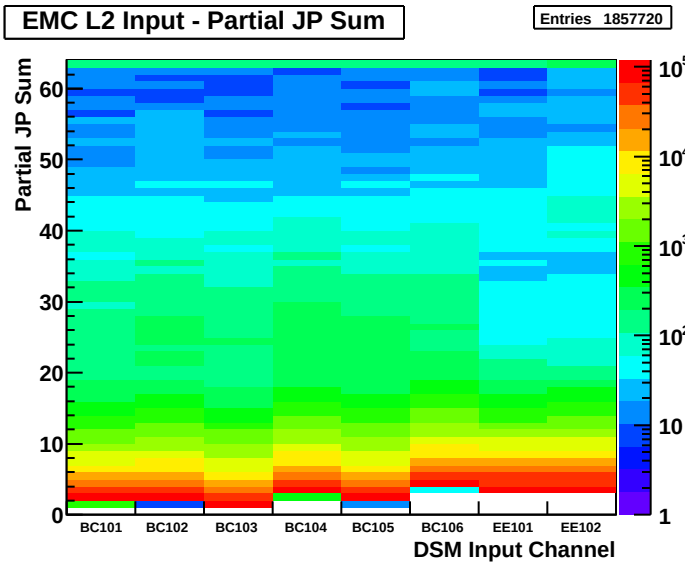

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

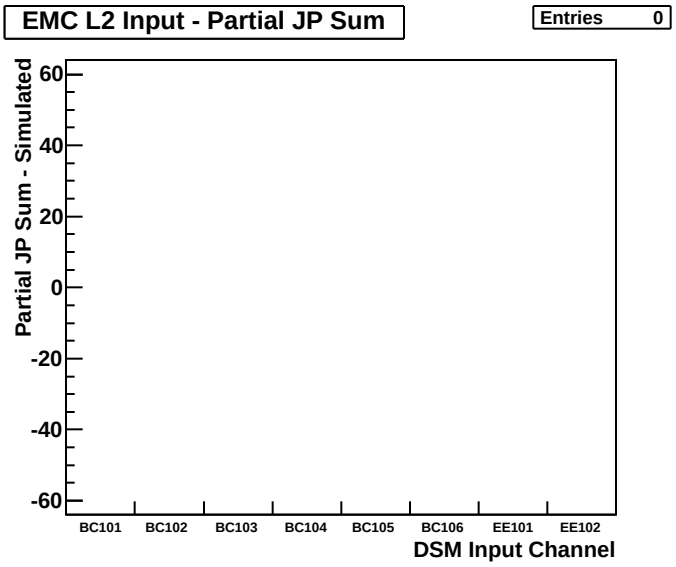

EMC L2 Input - JPY/JPB bits
Entries 1857720
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
525 330 388 284 279 424 183 260
1303 935 1047 894 1010 1173 453 564
2078 1671 1733 1732 2326 2118 338 381
228309 228279 228047 228305 228600 228590 231241 231010

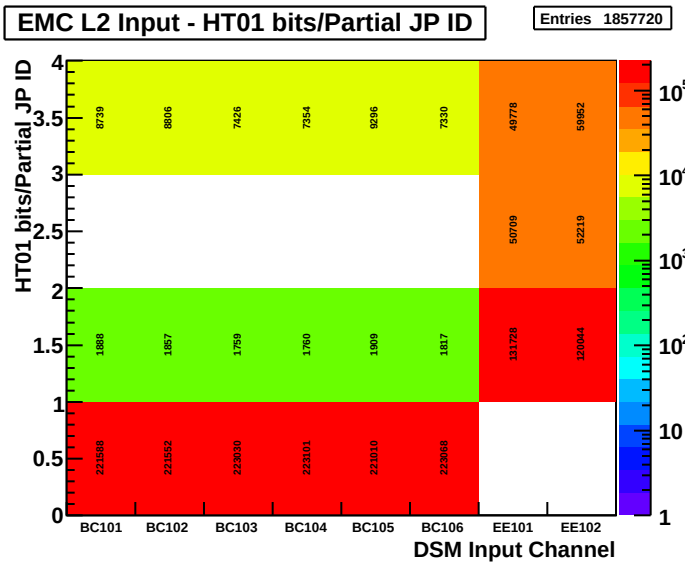

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

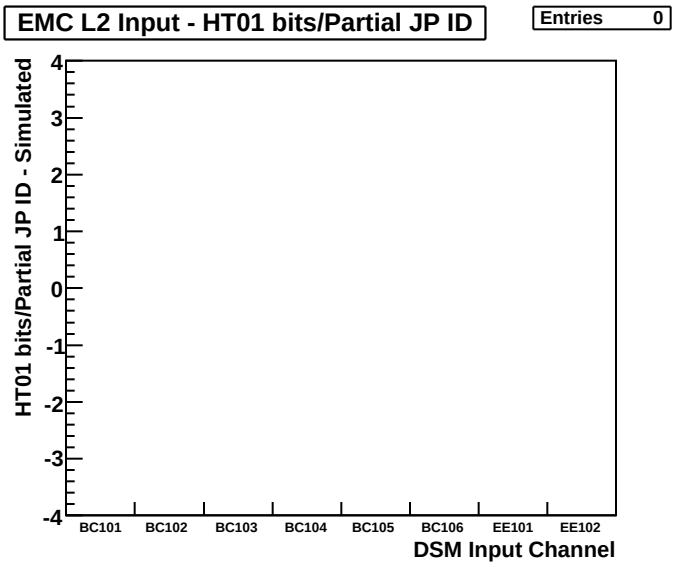

EMC L2 Input - JPZ/JPC bits
Entries 1857720
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
385 312 300 359 338 404 171 242
1029 907 823 983 1089 1216 474 503
1681 1588 1528 1948 2333 2121 349 403
229120 229408 229564 228925 228455 228474 231221 231067

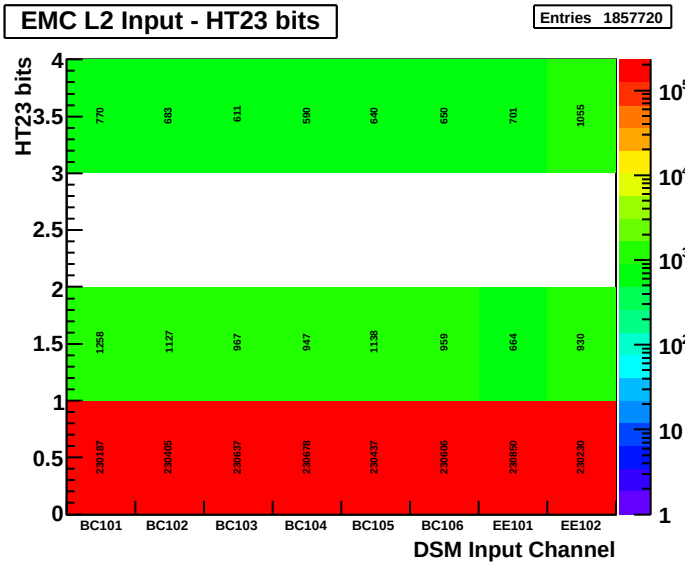

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

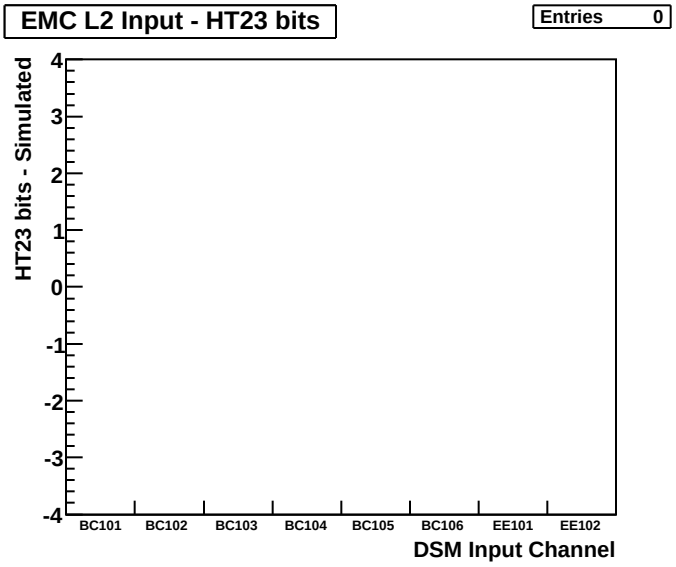

EMC L2 Input - Partial JP Sum
Entries 1857720
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

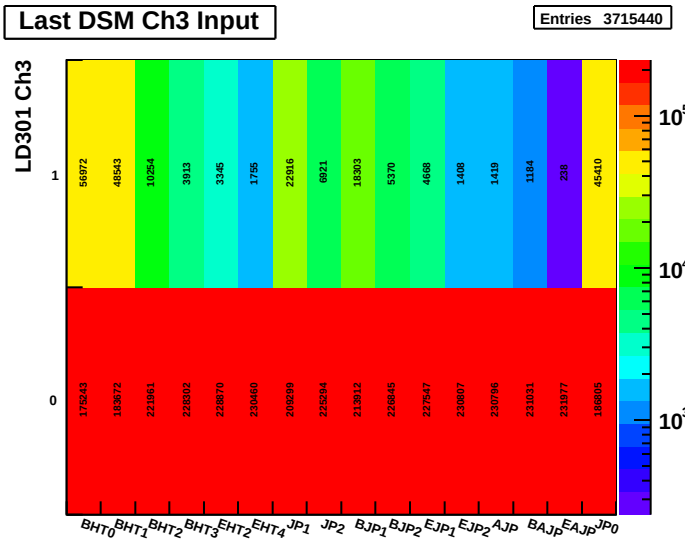

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

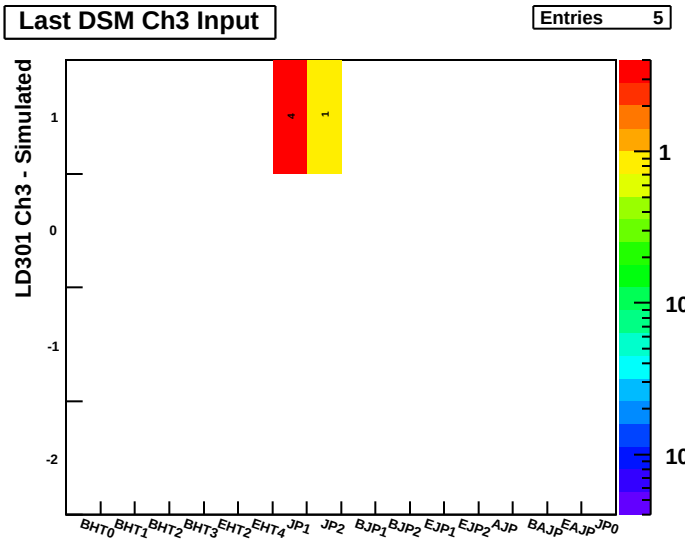

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1857720
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

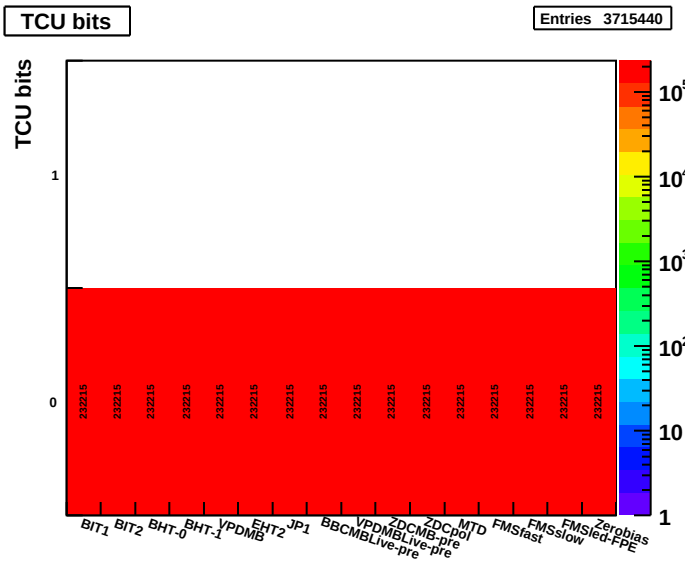

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

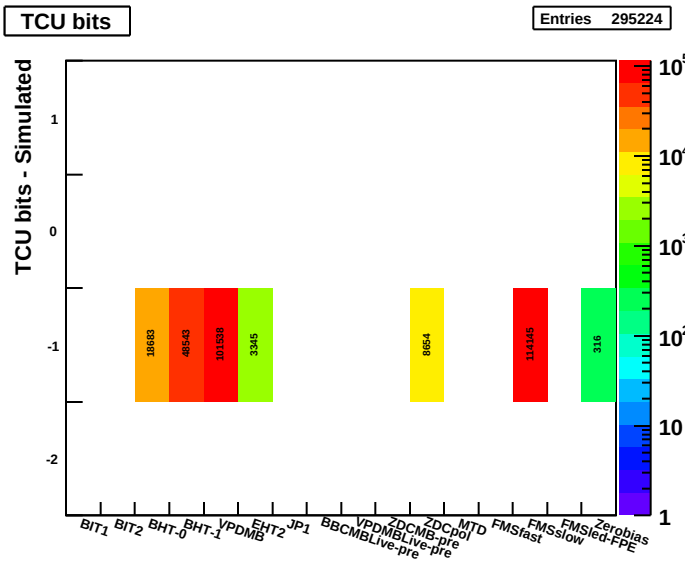

EMC L2 Input - HT23 bits
Entries 1857720
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

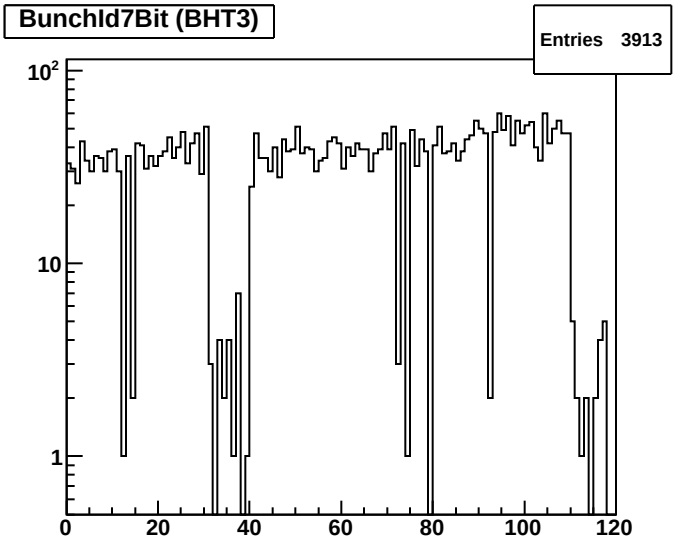

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

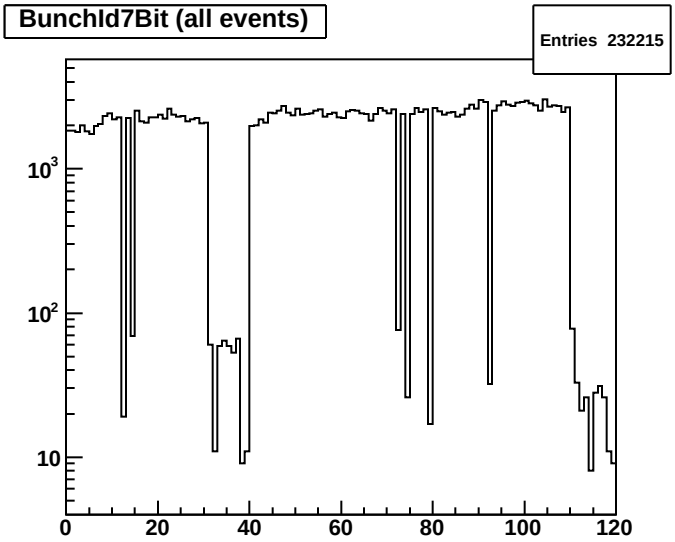

Last DSM Ch3 Input
Entries 3715440
LD301 Ch3
56972 48543 10254 3913 3345 1755 22916 6921 18303 5370 4668 1408 1419 1184 238 45410
175243 183672 221961 228302 228870 230460 209299 225934 213912 226845 227547 230807 230796 231031 231977 186805
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

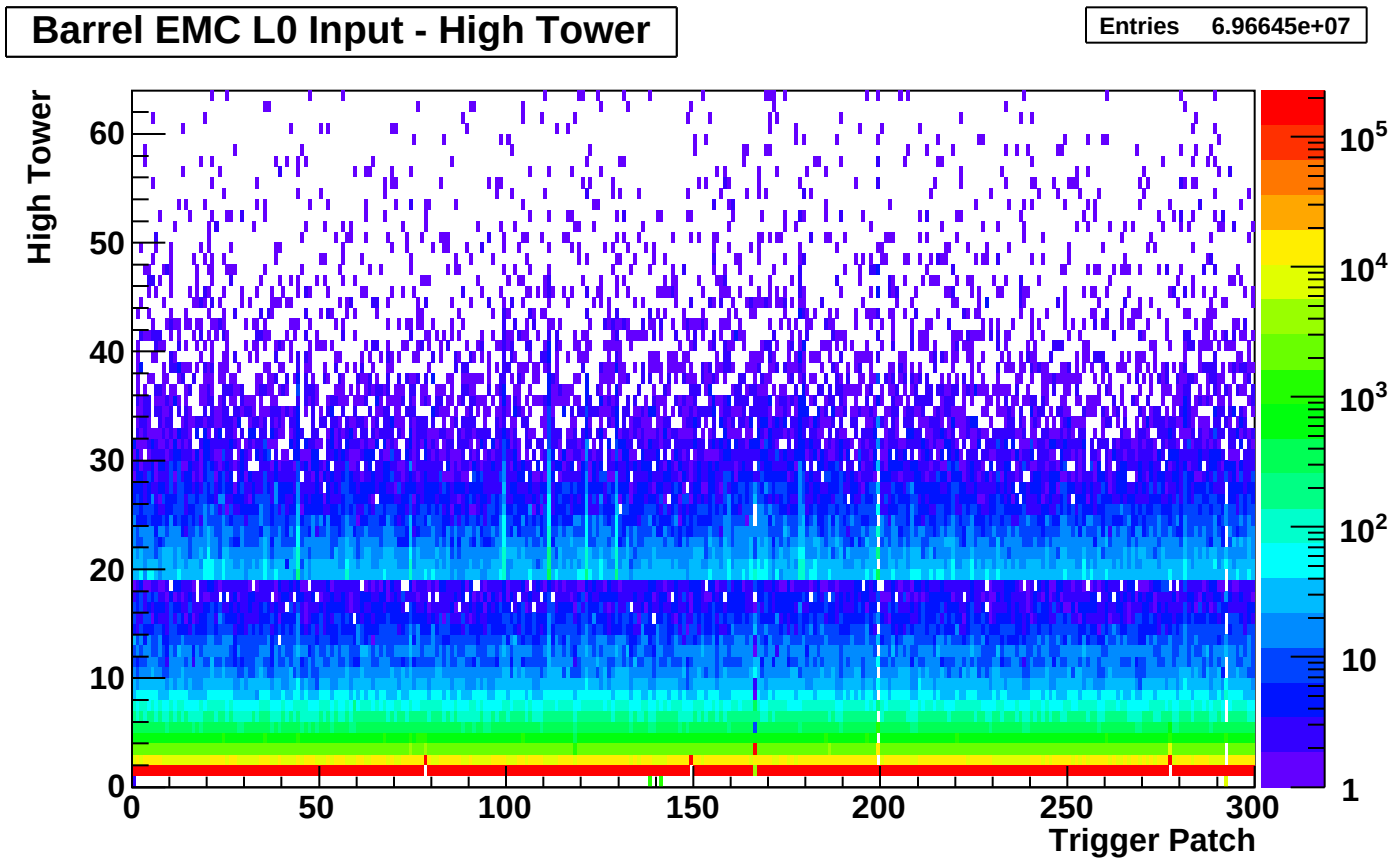

Last DSM Ch3 Input
Entries 5
LD301 Ch3 - Simulated
4 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

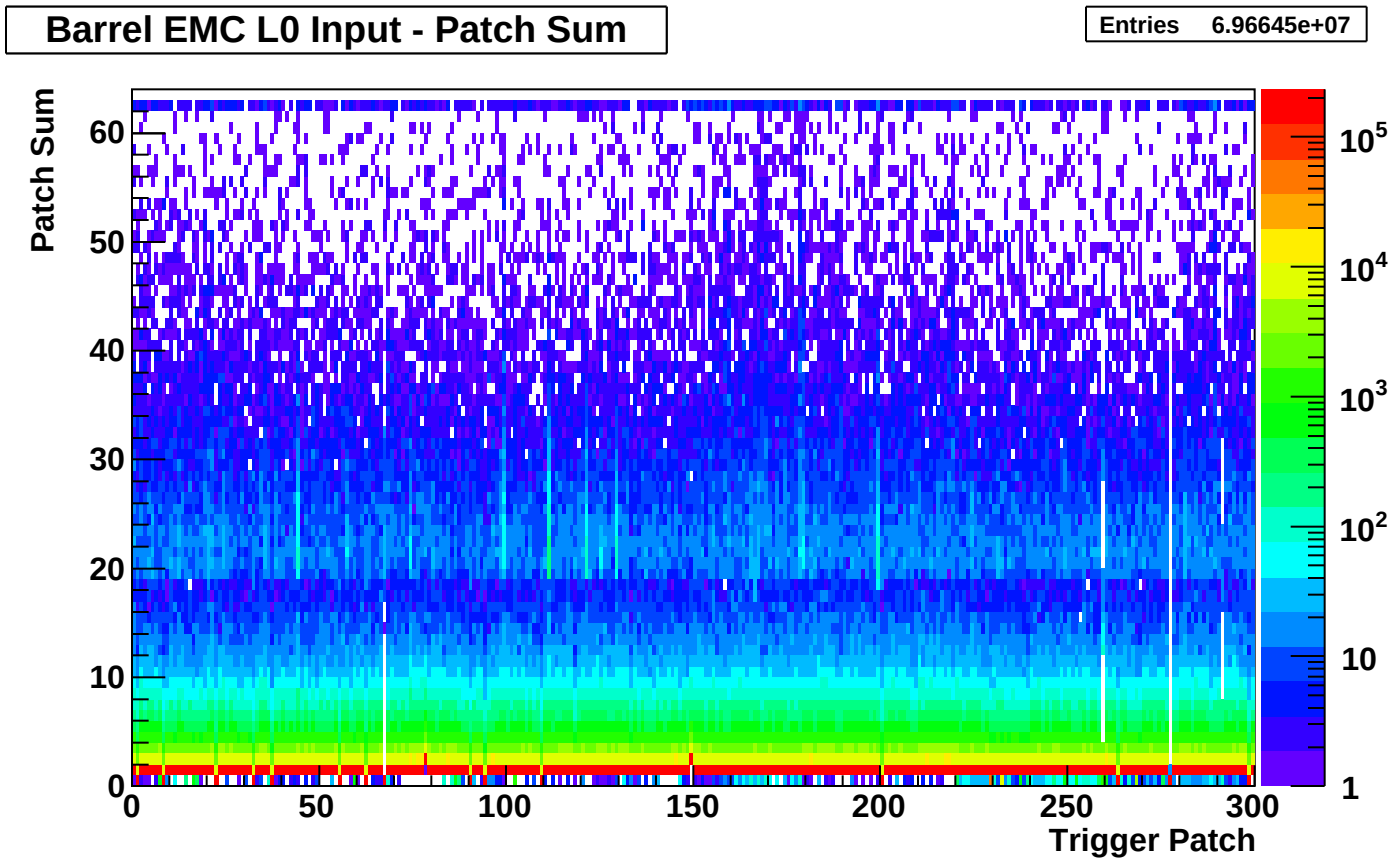

TCU bits
Entries 3715440
TCU bits
232215 232215 232215 232215 232215 232215 232215 232215 232215 232215 232215 232215 232215 232215 232215 232215
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

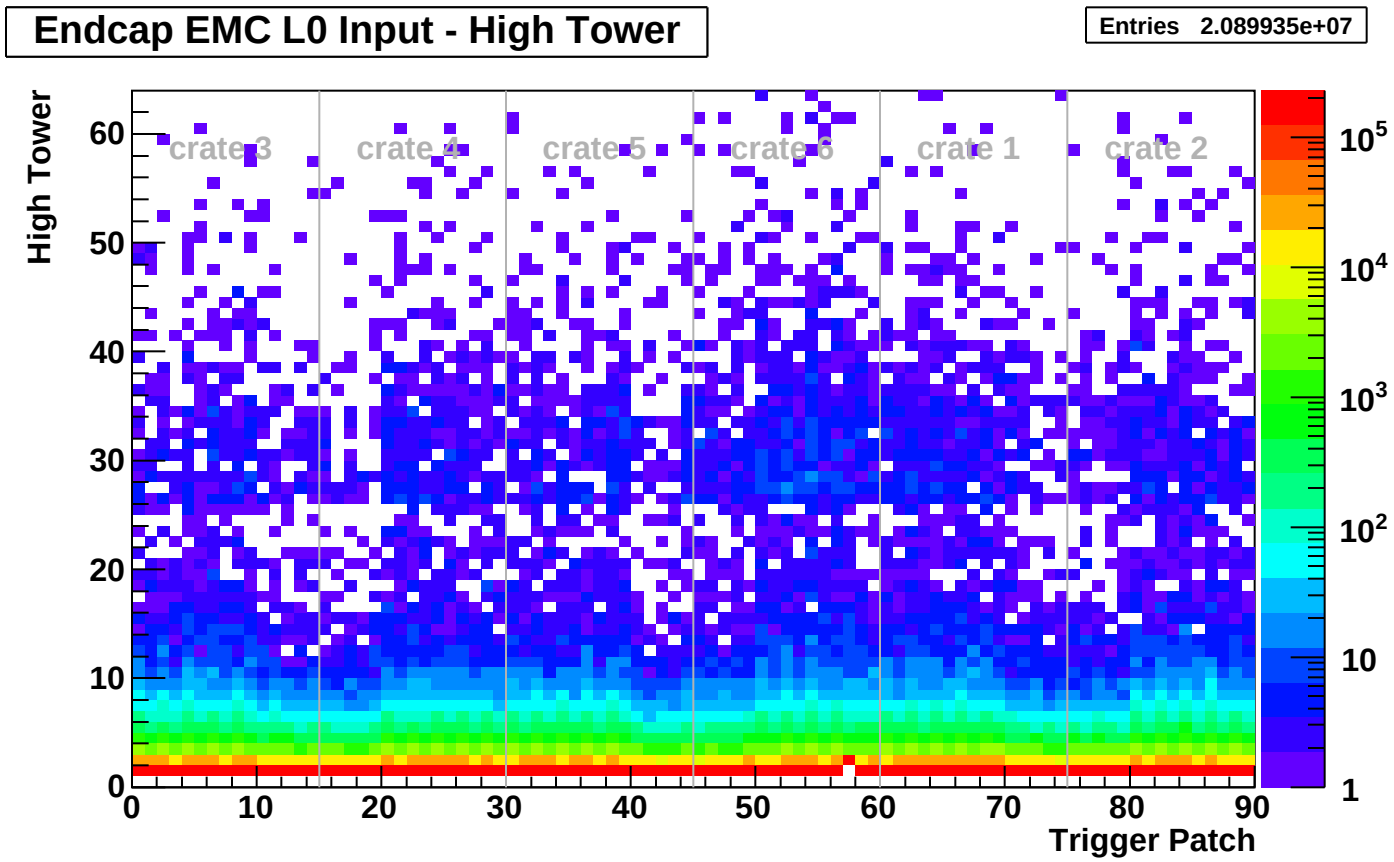

TCU bits
Entries 295224
TCU bits - Simulated
18683 48543 101538 3345 8664 114145 316
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

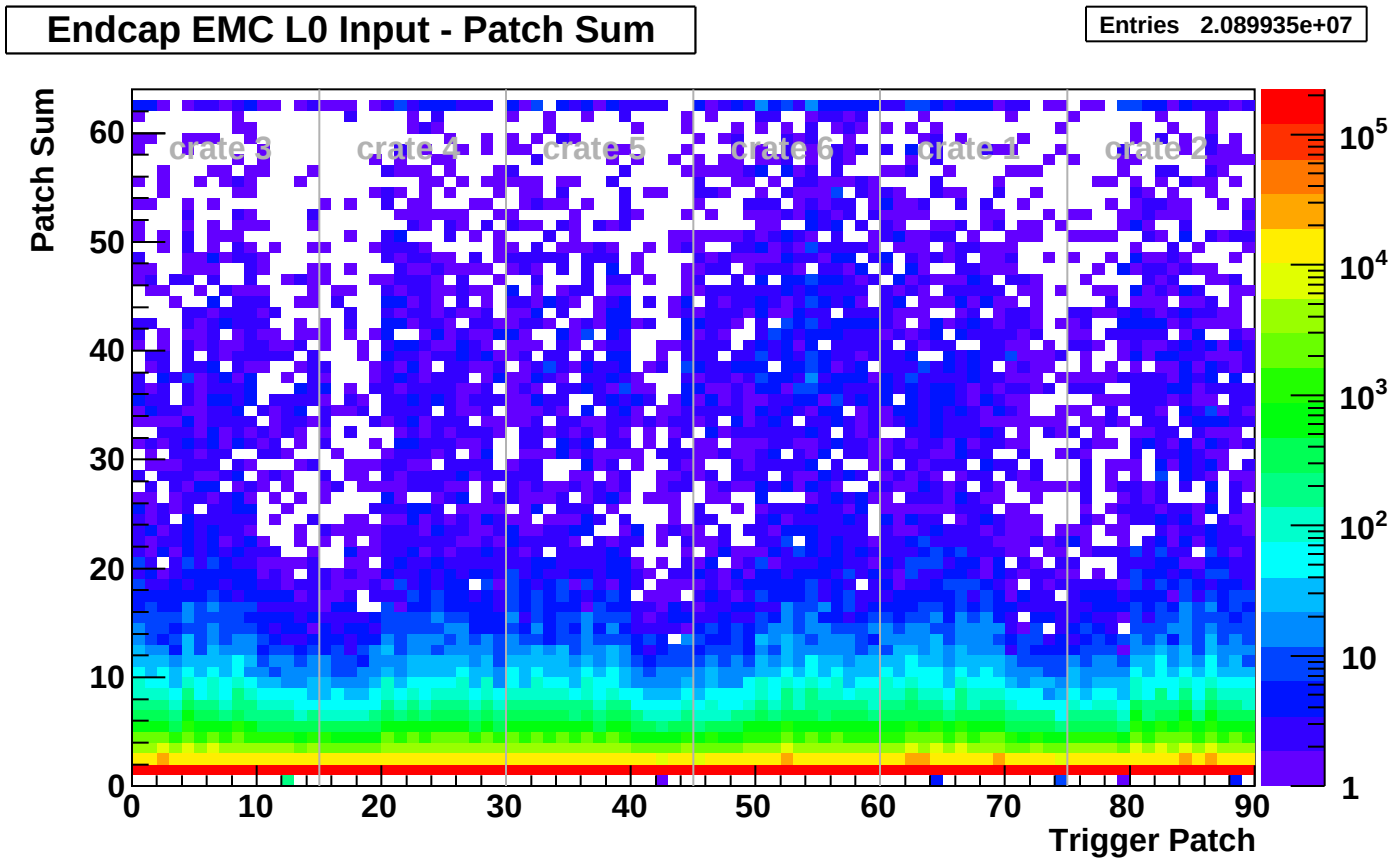

BunchId7Bit (BHT3)
Entries 3913

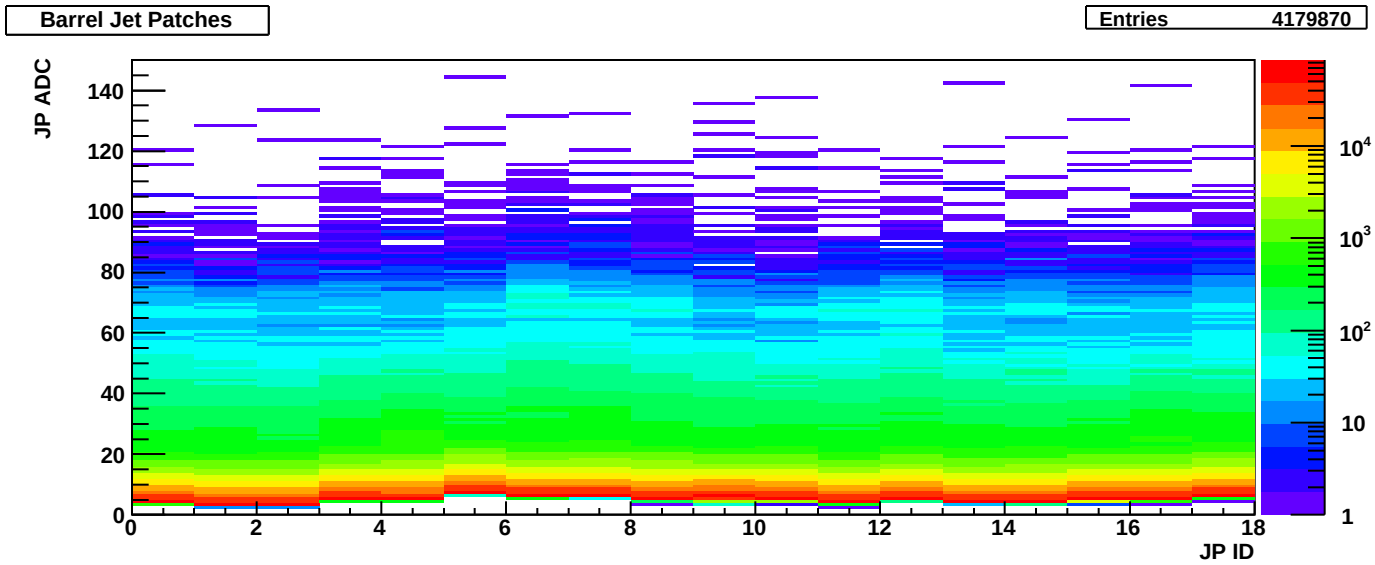

BunchId7Bit (all events)
Entries 232215

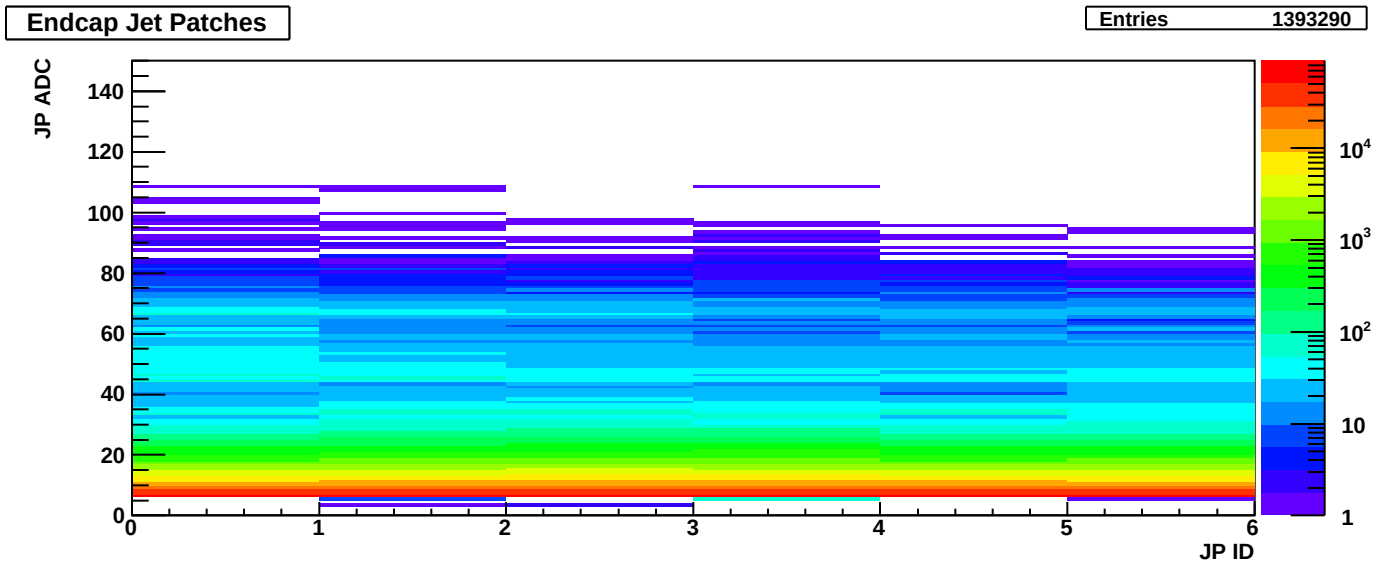

Barrel EMC L0 Input - High Tower
Entries 6.96645e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

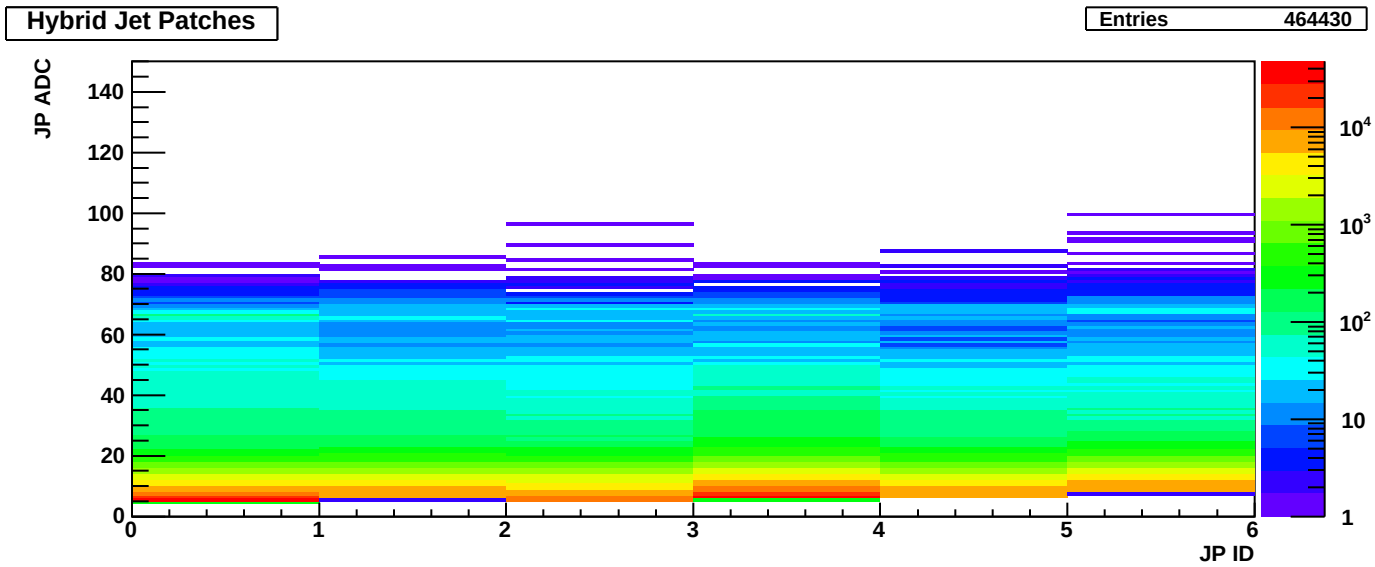

Barrel EMC L0 Input - Patch Sum
Entries 6.96645e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

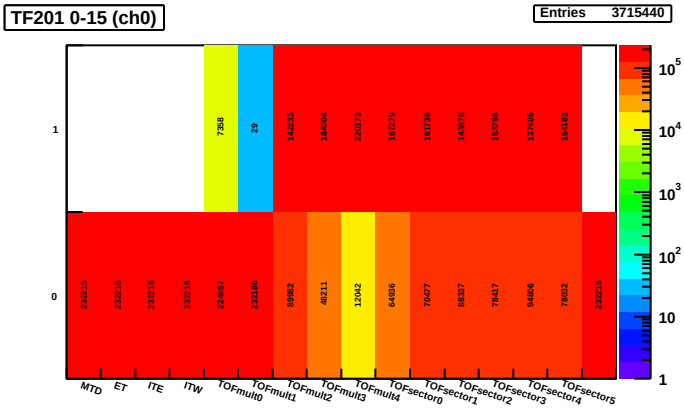

Endcap EMC L0 Input - High Tower
Entries 2.089935e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

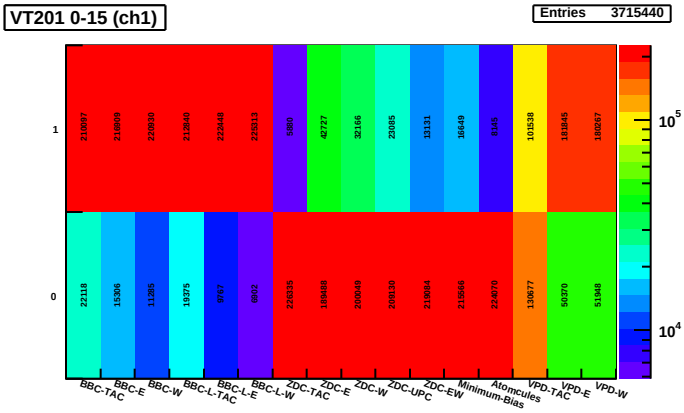

Endcap EMC L0 Input - Patch Sum
Entries 2.089935e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

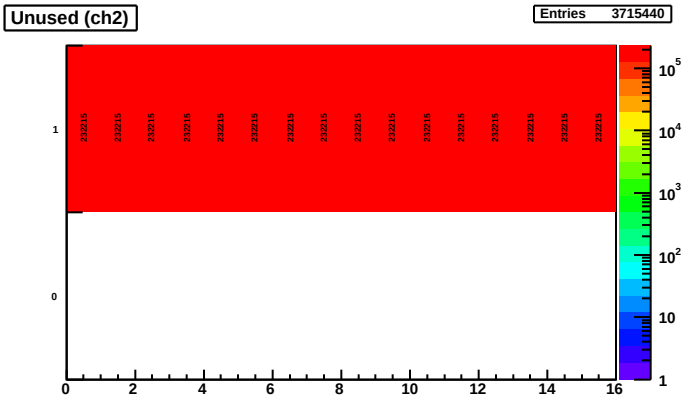

Barrel Jet Patches
Entries 4179870
JP ADC
JP ID

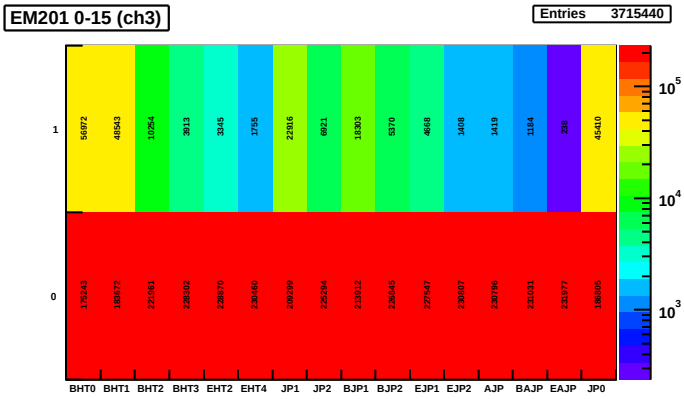

Endcap Jet Patches
Entries 1393290
JP ADC
JP ID

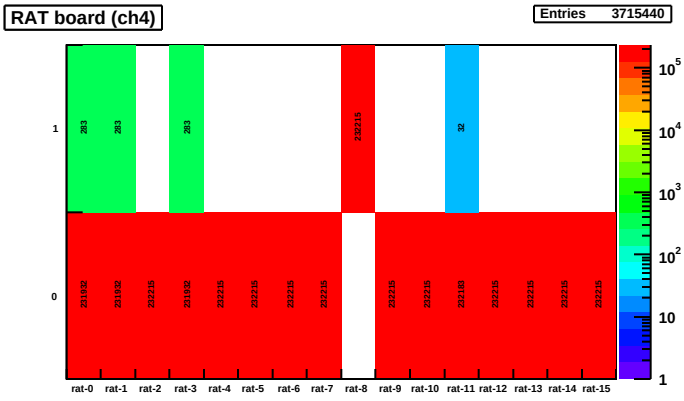

Hybrid Jet Patches
Entries 464430
JP ADC
JP ID

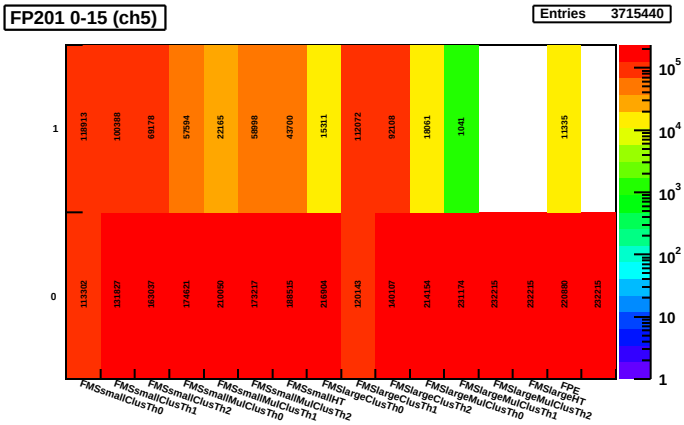

TF201 0-15 (ch0)
Entries 3715440
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

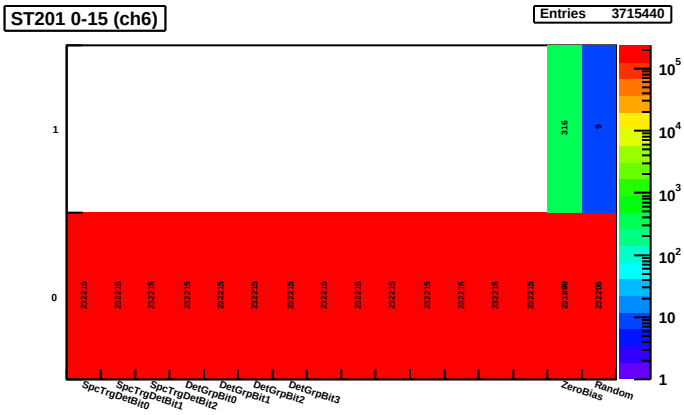

VT201 0-15 (ch1)
Entries 3715440
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

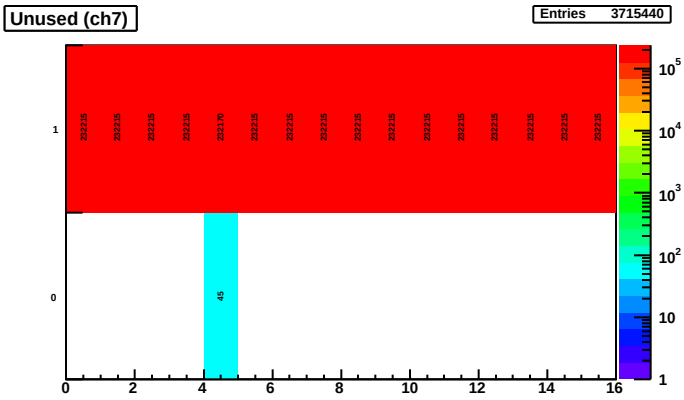

Unused (ch2)
Entries 3715440

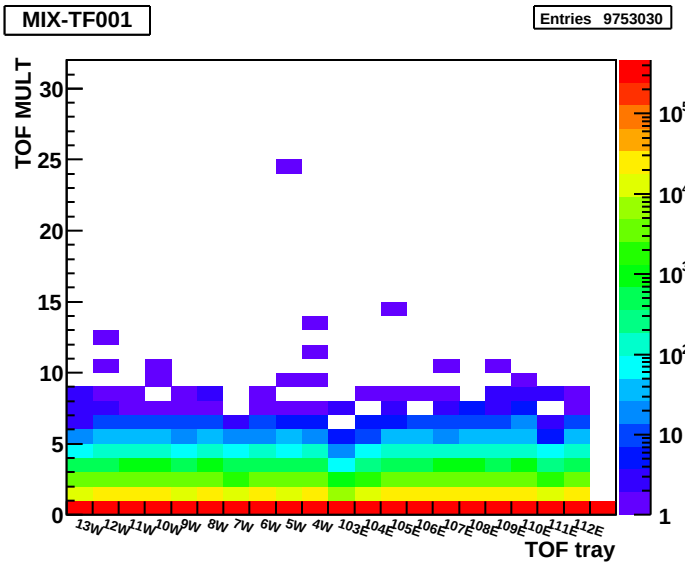

EM201 0-15 (ch3)
Entries 3715440
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

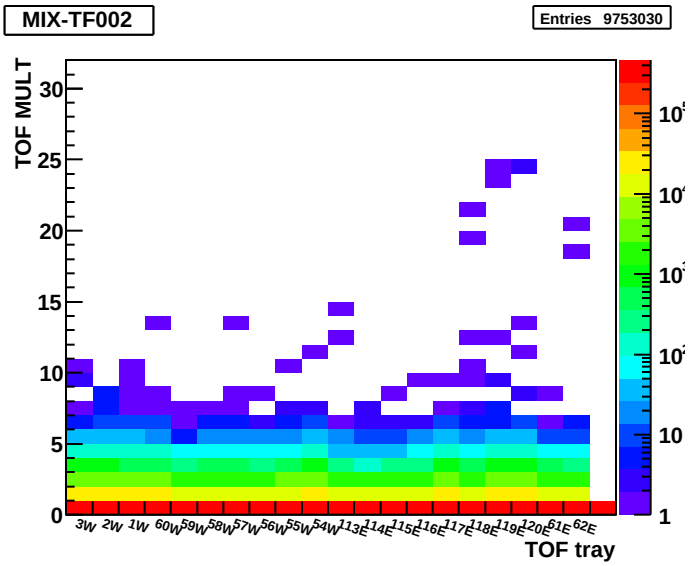

RAT board (ch4)
Entries 3715440
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

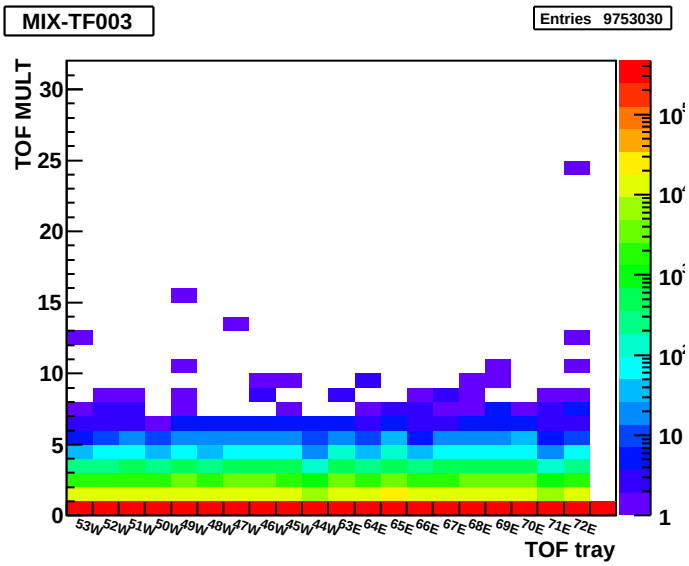

FP201 0-15 (ch5)
Entries 3715440
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

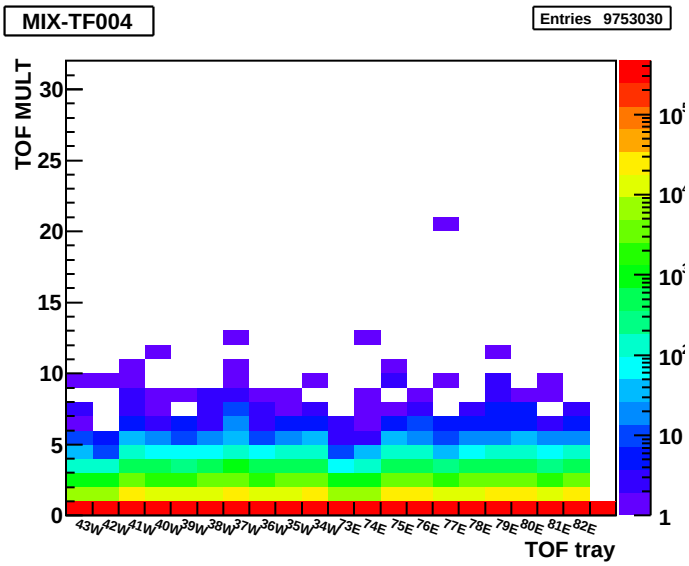

ST201 0-15 (ch6)
Entries 3715440
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

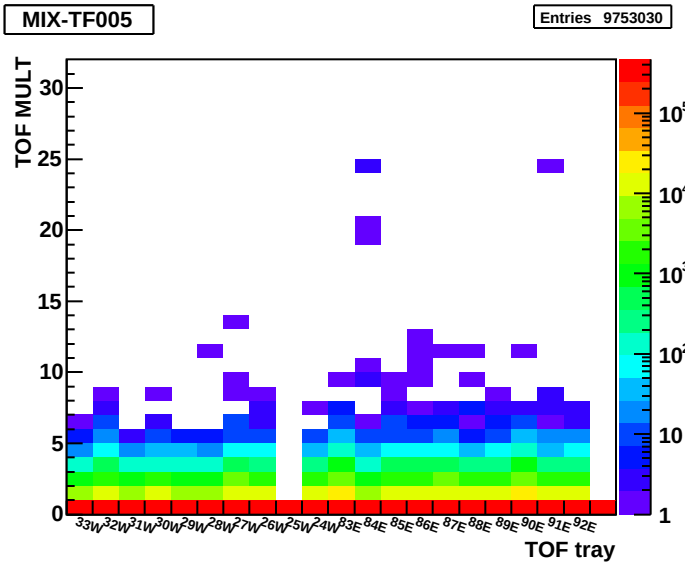

Unused (ch7)
Entries 3715440

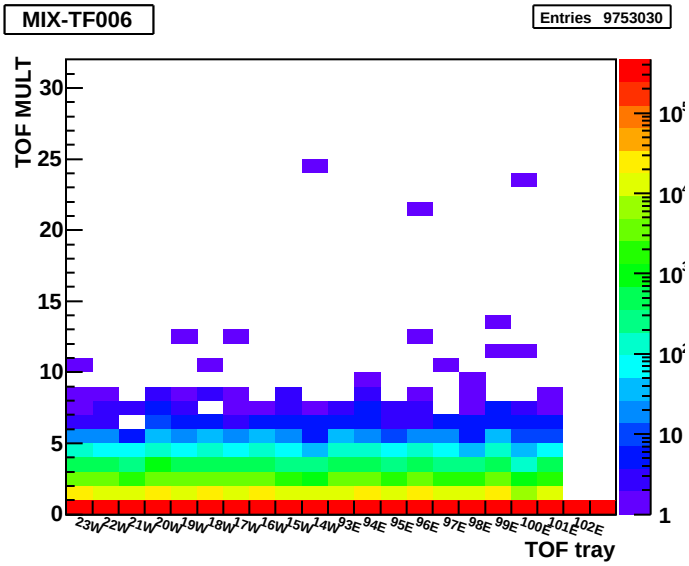

MIX-TF001
Entries 9753030
TOF MULT
TOF tray

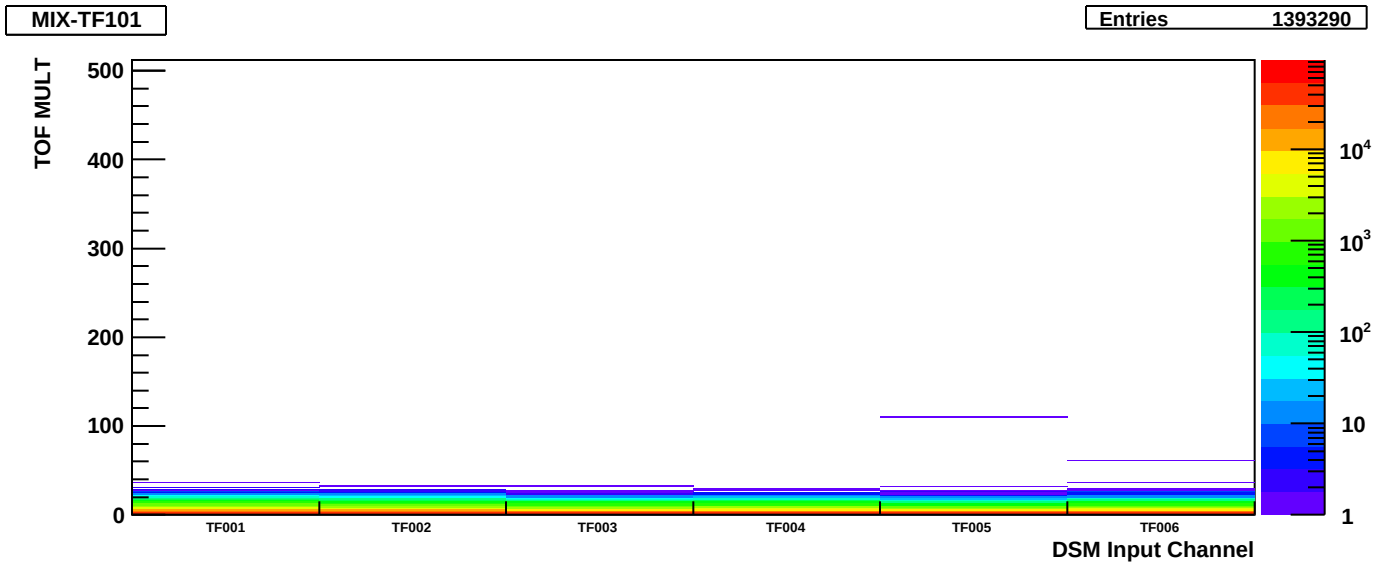

MIX-TF002
Entries 9753030
TOF MULT
TOF tray

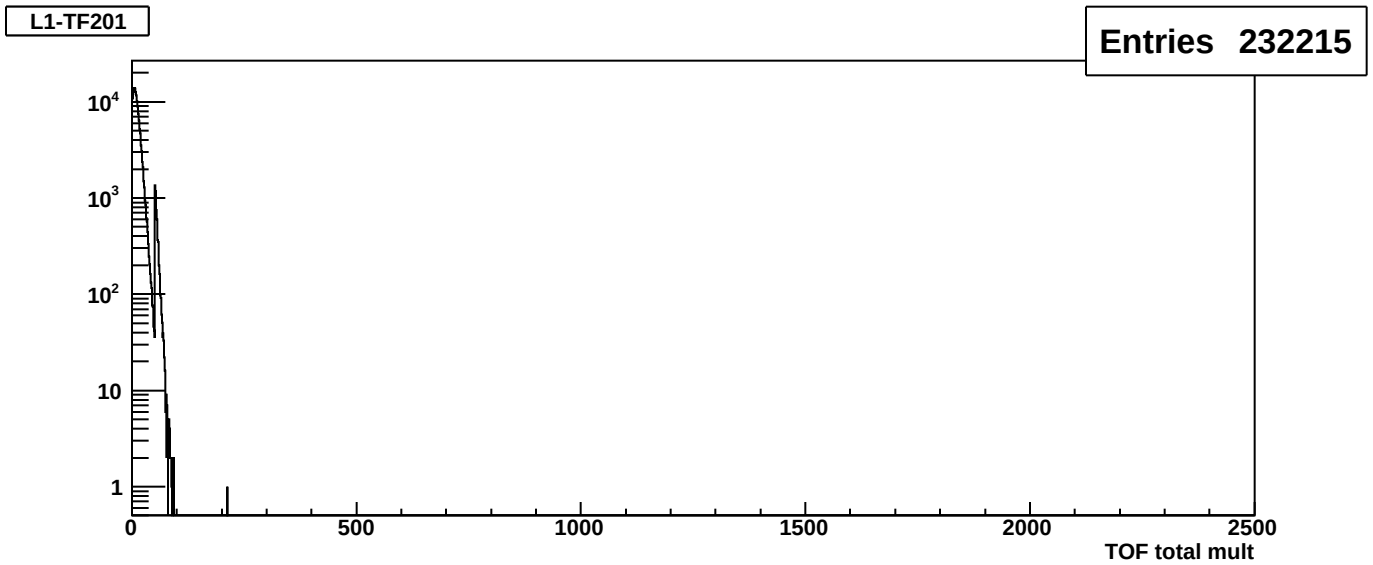

MIX-TF003
Entries 9753030
TOF MULT
TOF tray

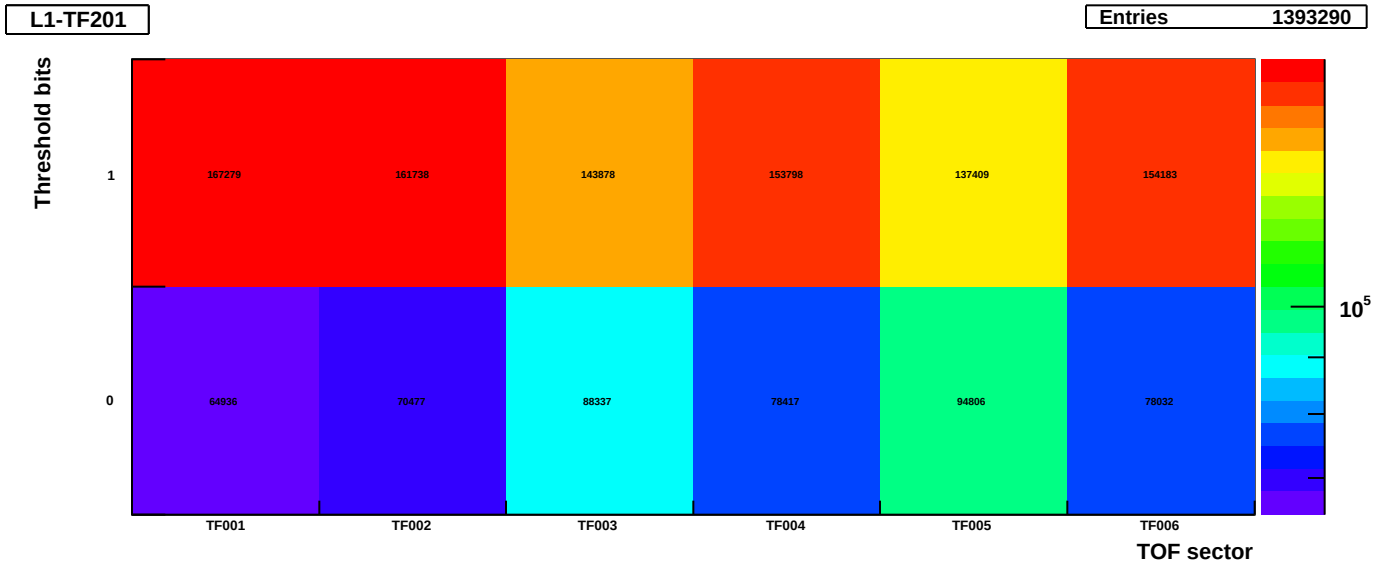

MIX-TF004
Entries 9753030
TOF MULT
TOF tray

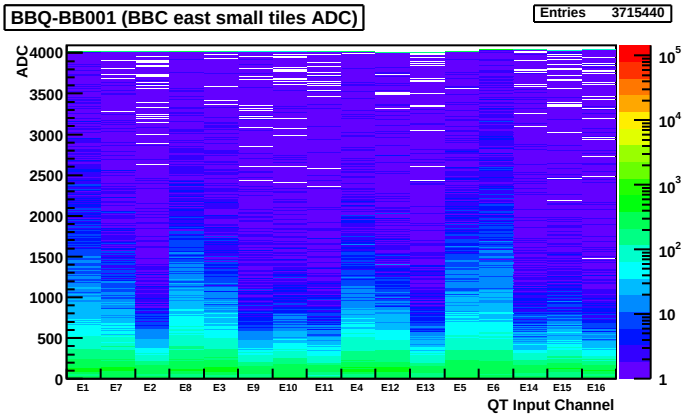

MIX-TF005
Entries 9753030
TOF MULT
TOF tray

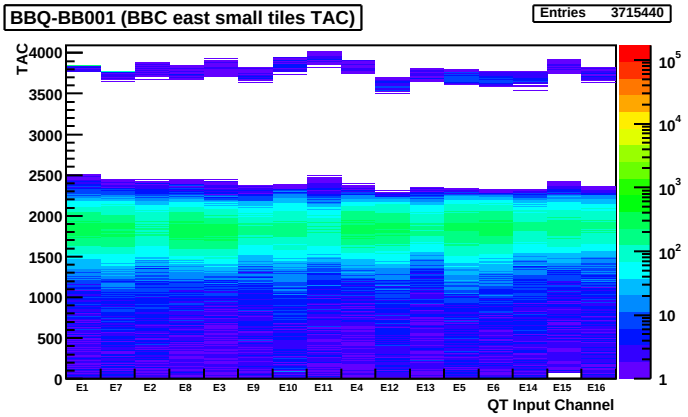

MIX-TF006
Entries 9753030
TOF MULT
TOF tray

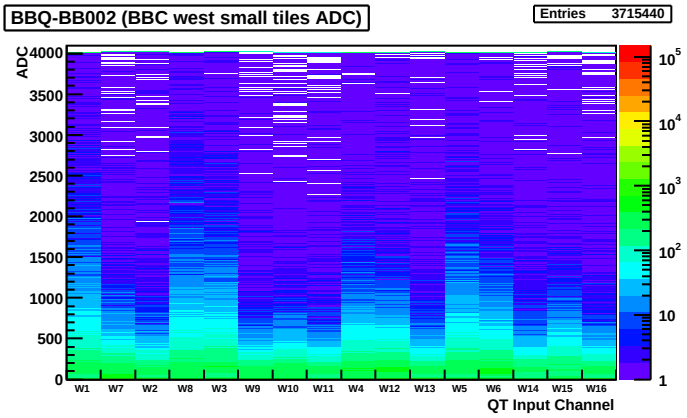

MIX-TF101
Entries 1393290
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

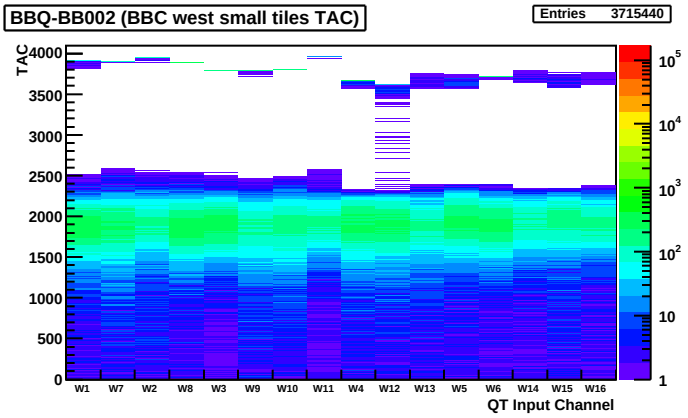

L1-TF201
Entries 232215
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

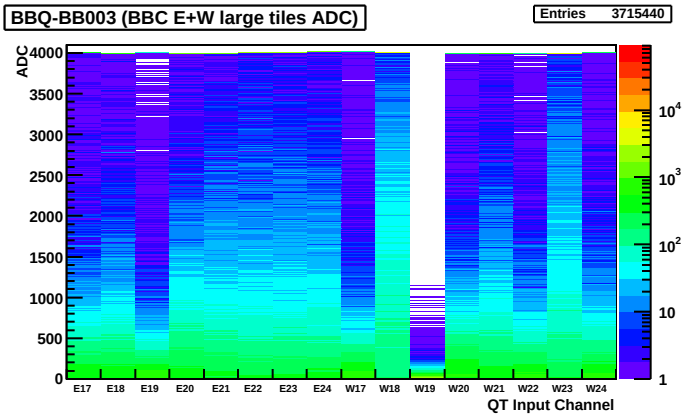

L1-TF201
Entries 1393290
Threshold bits
1
0
167279
161738
143878
153798
137409
154183
64936
70477
88337
78417
94806
78032
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

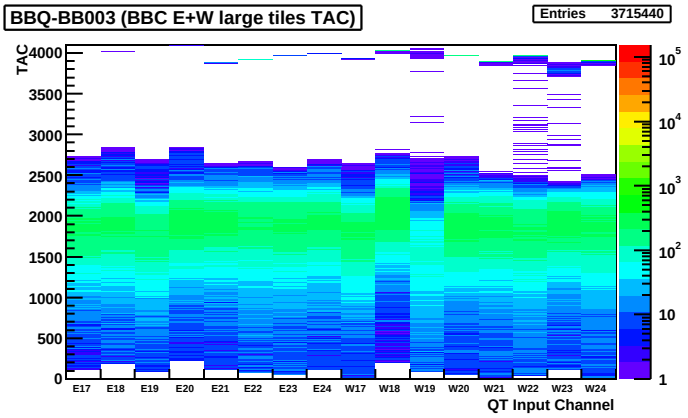

BBQ-BB001 (BBC east small tiles ADC)
Entries 3715440
ADC
QT Input Channel

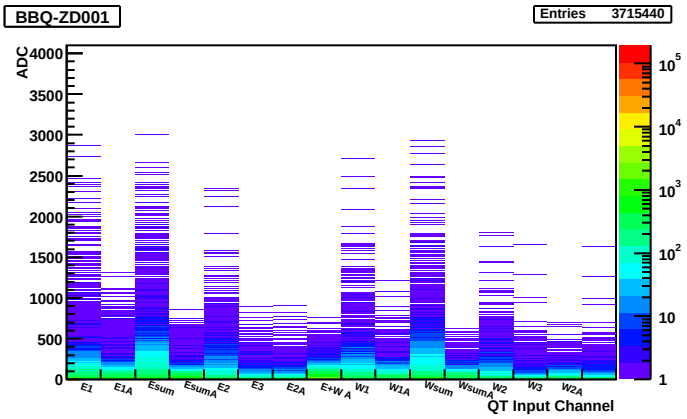

BBQ-BB001 (BBC east small tiles TAC)
Entries 3715440
TAC
QT Input Channel

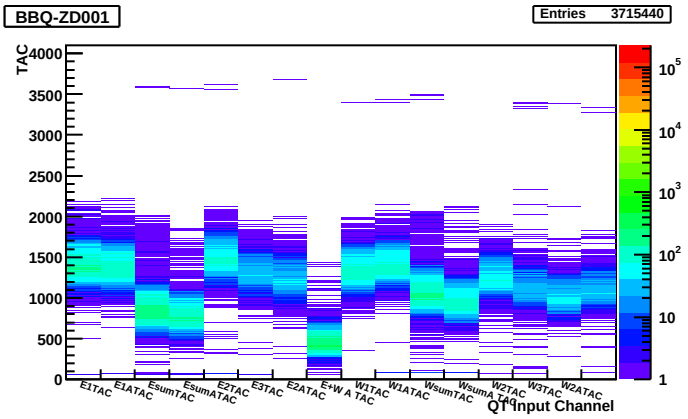

BBQ-BB002 (BBC west small tiles ADC)
Entries 3715440
ADC
QT Input Channel

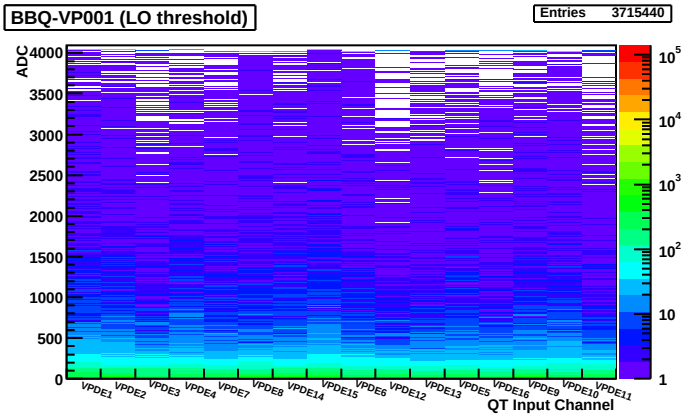

BBQ-BB002 (BBC west small tiles TAC)
Entries 3715440
TAC
QT Input Channel

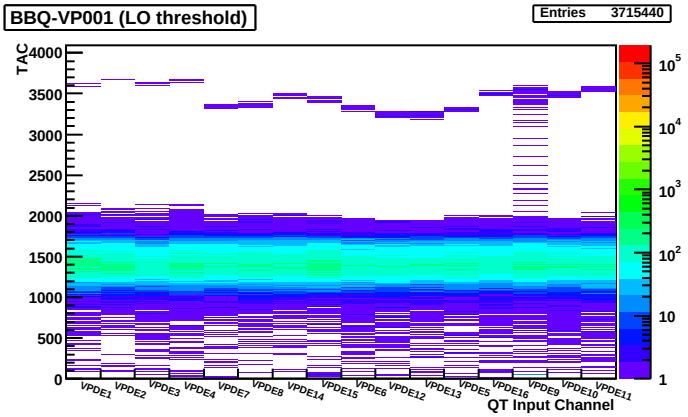

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3715440
ADC
QT Input Channel

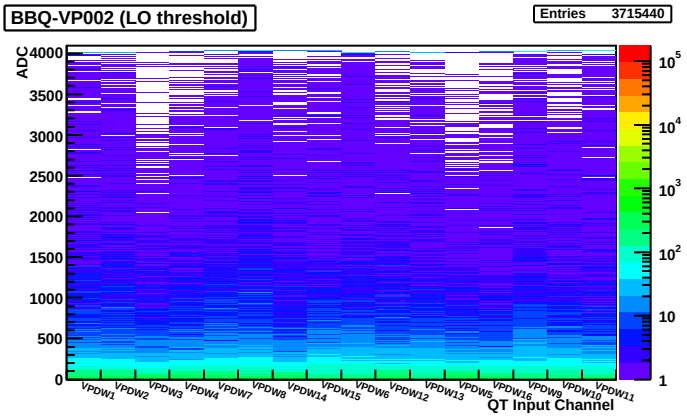

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3715440
TAC
QT Input Channel

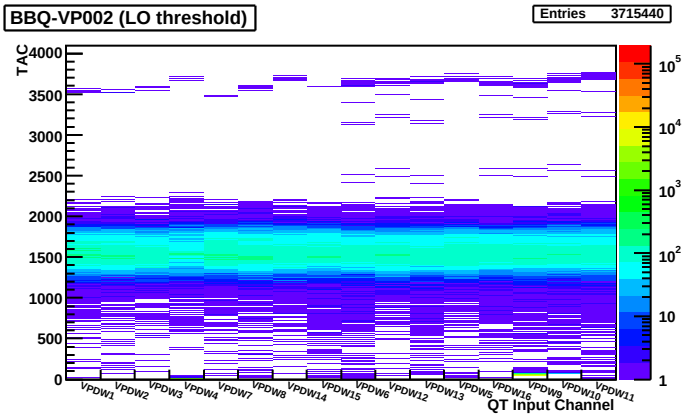

BBQ-ZD001
Entries 3715440
ADC
QT Input Channel

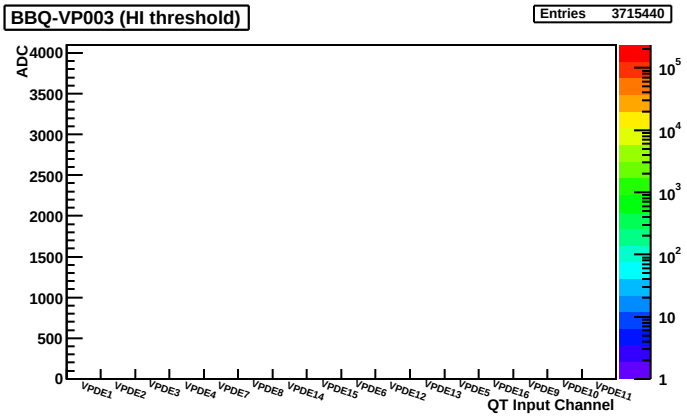

BBQ-ZD001
Entries 3715440
TAC
QT Input Channel

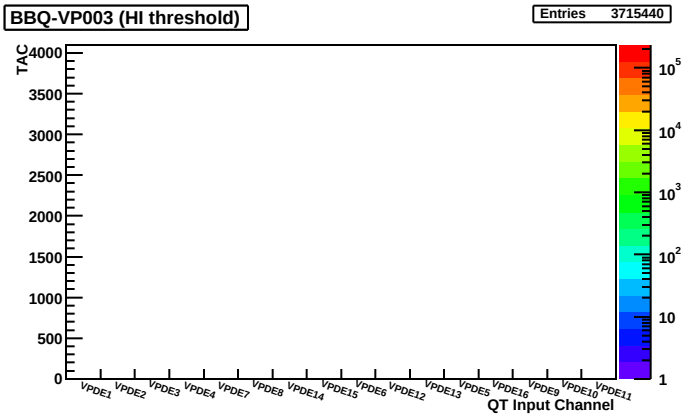

BBQ-VP001 (LO threshold)
Entries 3715440
ADC
QT Input Channel

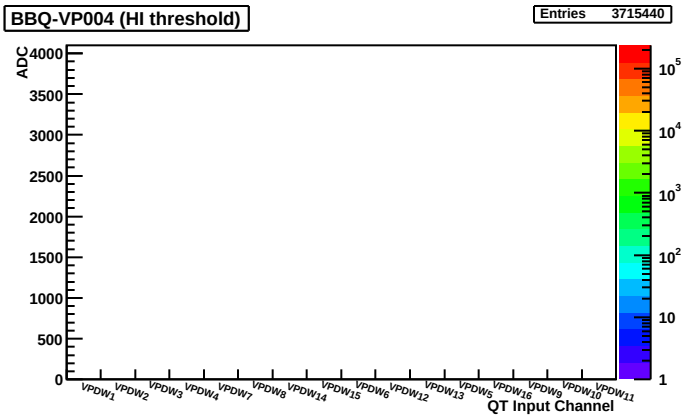

BBQ-VP001 (LO threshold)
Entries 3715440
TAC
QT Input Channel

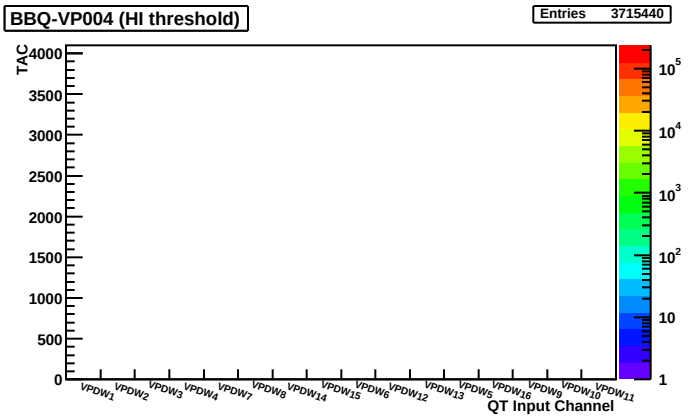

BBQ-VP002 (LO threshold)
Entries 3715440
ADC
QT Input Channel

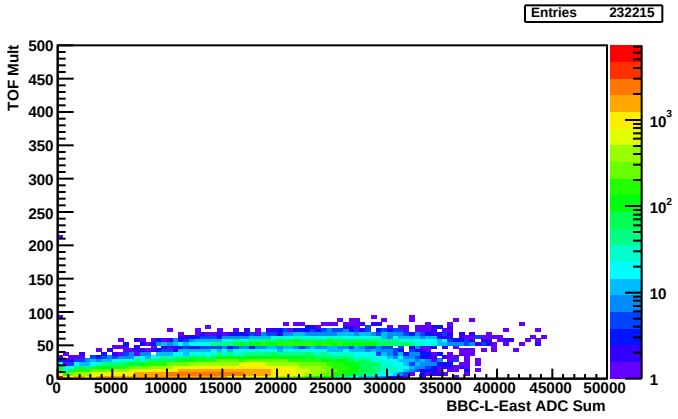

BBQ-VP002 (LO threshold)
Entries 3715440
TAC
QT Input Channel

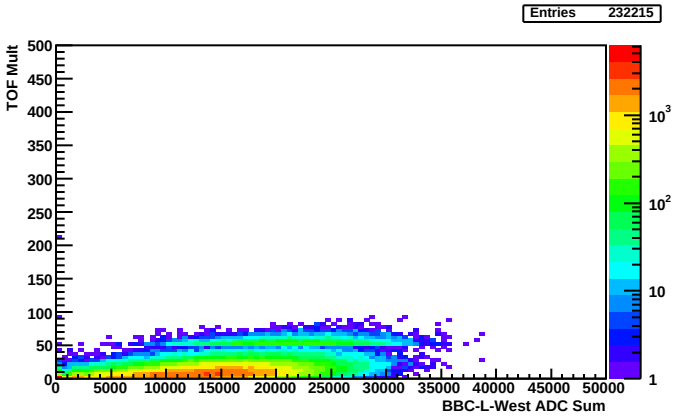

BBQ-VP003 (HI threshold)
Entries 3715440
ADC
QT Input Channel

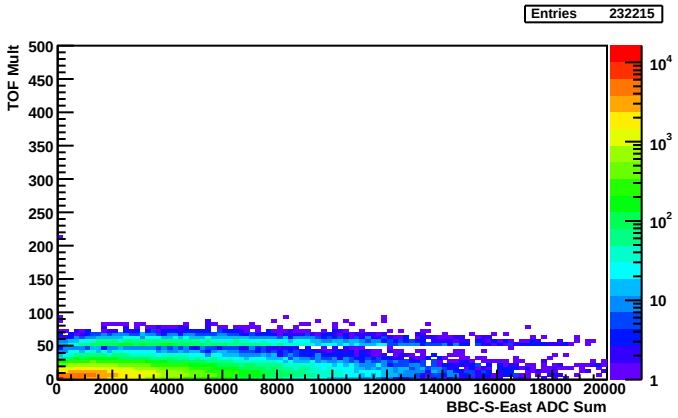

BBQ-VP003 (HI threshold)
Entries 3715440
TAC
QT Input Channel

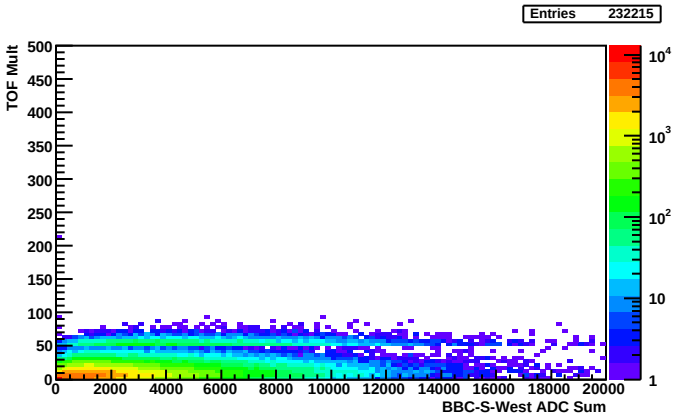

BBQ-VP004 (HI threshold)
Entries 3715440
ADC
QT Input Channel

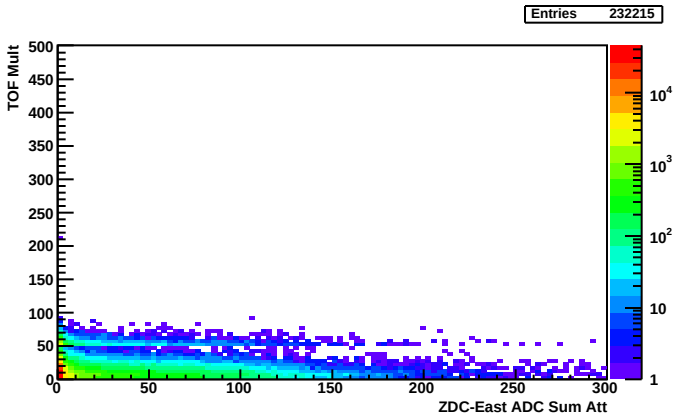

BBQ-VP004 (HI threshold)
Entries 3715440
TAC
QT Input Channel

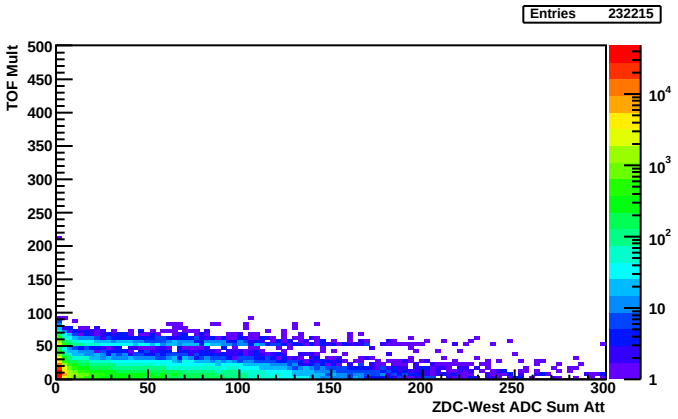

Entries 232215
TOF Mult
BBC-L-East ADC Sum

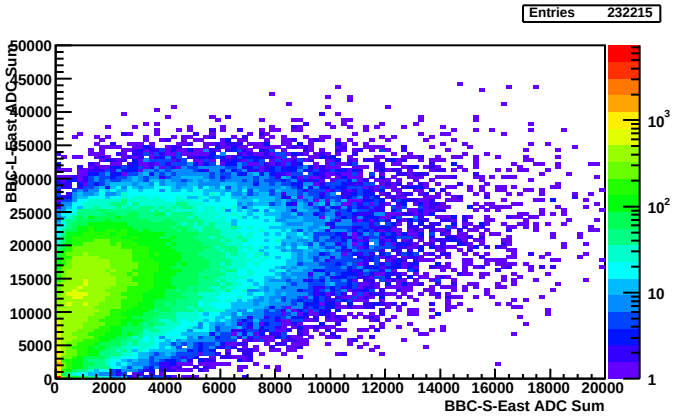

Entries 232215
TOF Mult
BBC-L-West ADC Sum

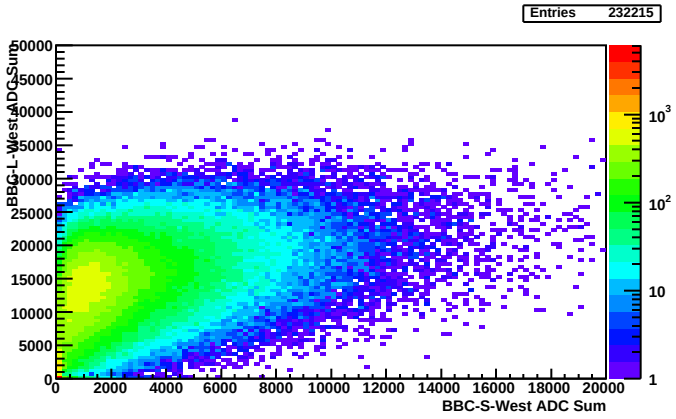

Entries 232215
TOF Mult
BBC-S-East ADC Sum

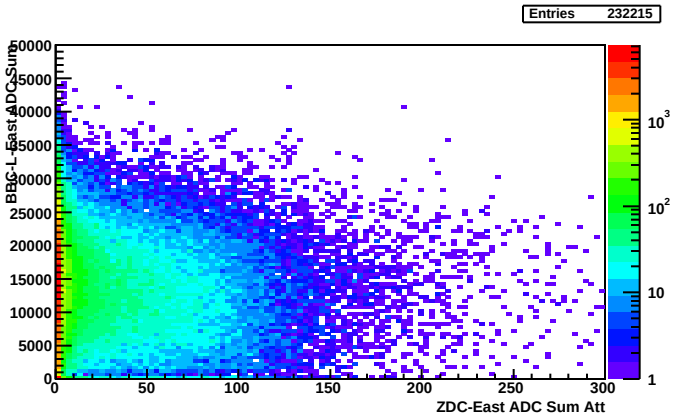

Entries 232215
TOF Mult
BBC-S-West ADC Sum

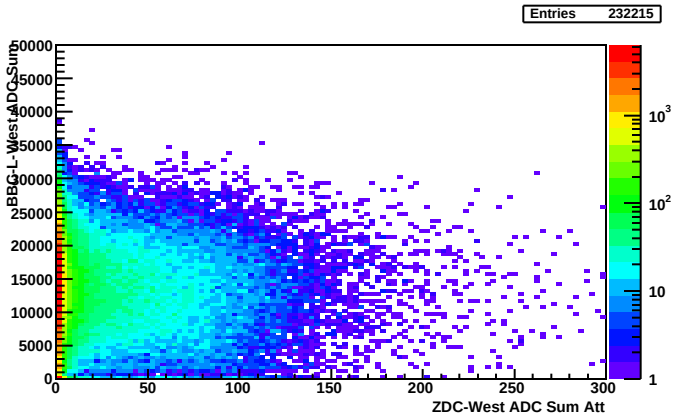

Entries 232215
TOF Mult
ZDC-East ADC Sum Att

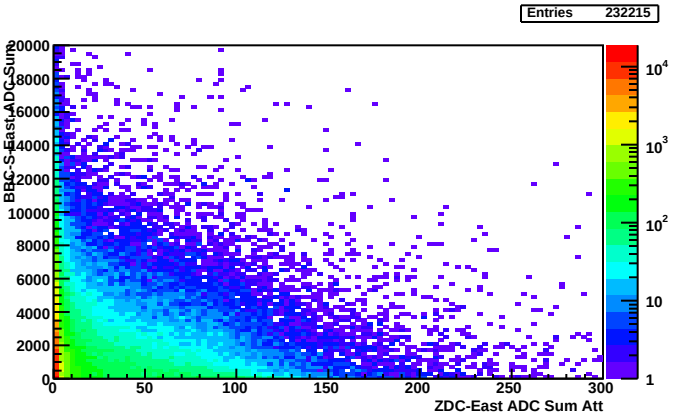

Entries 232215
TOF Mult
ZDC-West ADC Sum Att

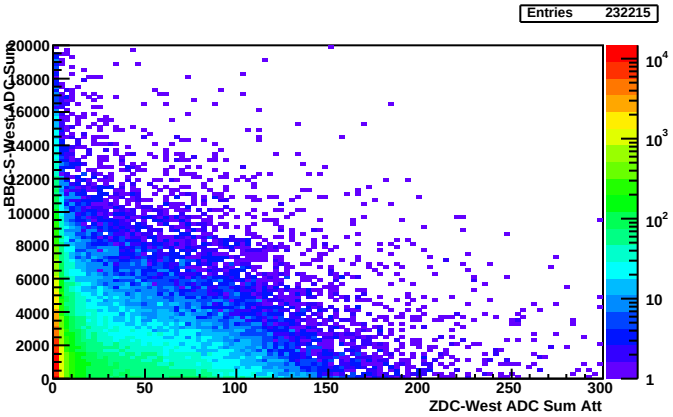

Entries 232215
BBC-L-East ADC Sum
BBC-S-East ADC Sum

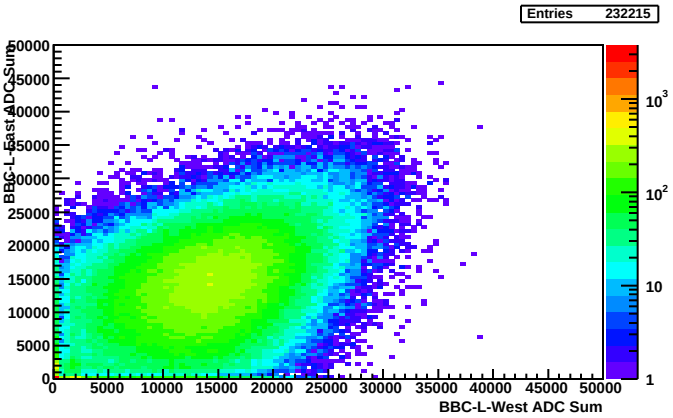

Entries 232215
BBC-L-West ADC Sum
BBC-S-West ADC Sum

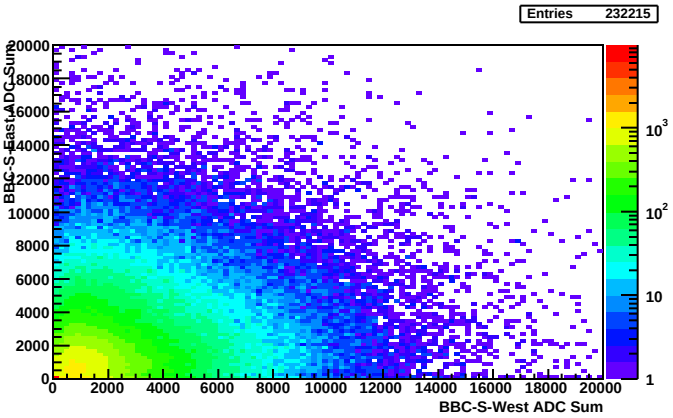

Entries 232215
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

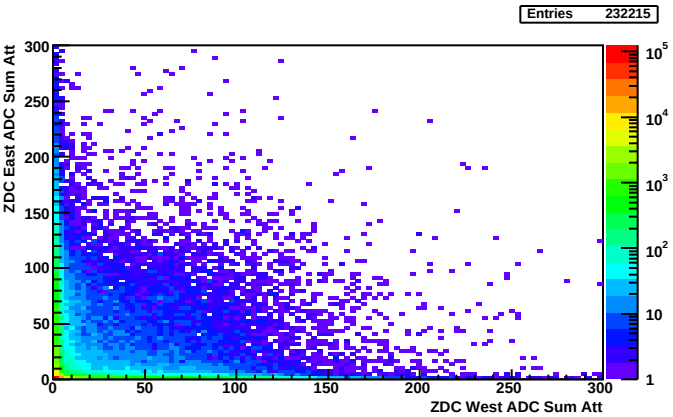

Entries 232215
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

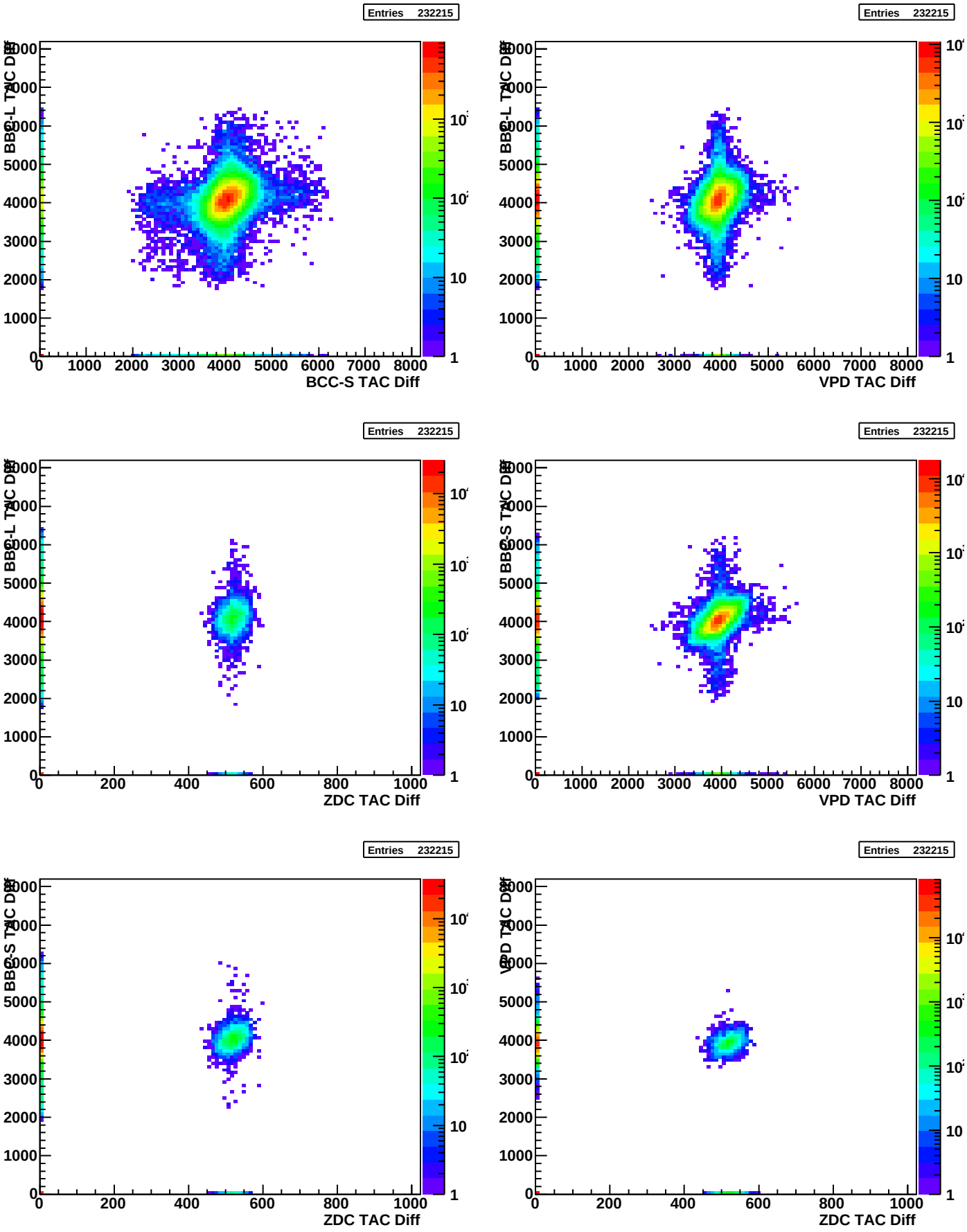

Entries 232215
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

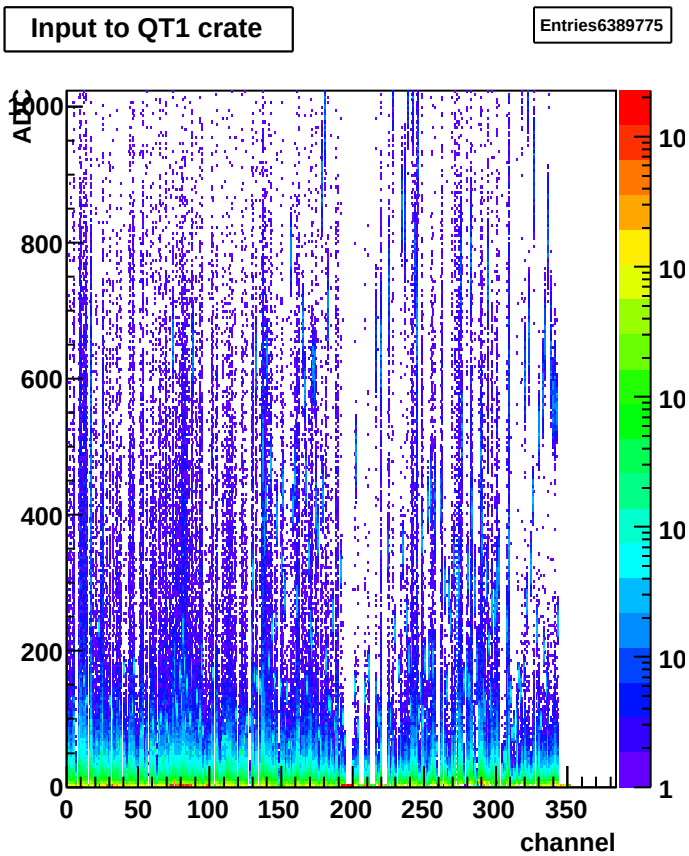

Entries 232215
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

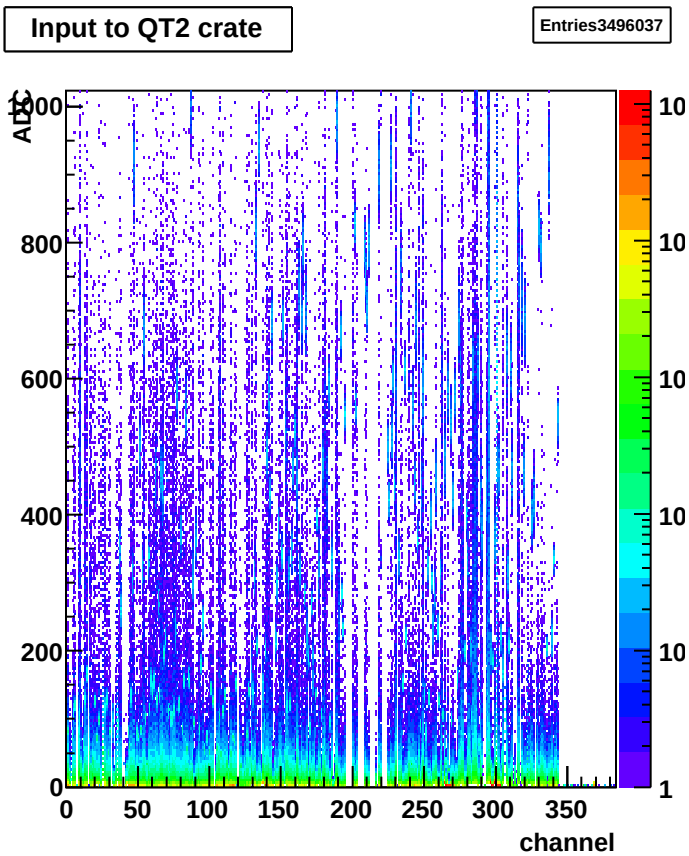

Entries 232215
BBC-L-East ADC Sum
BBC-L-West ADC Sum

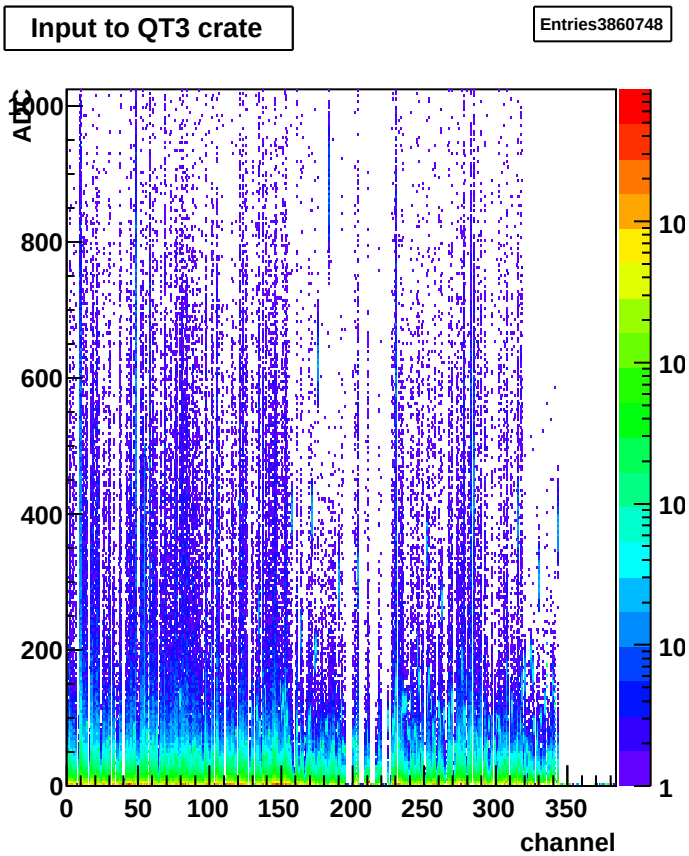

Entries 232215
BBC-S-East ADC Sum
BBC-S-West ADC Sum

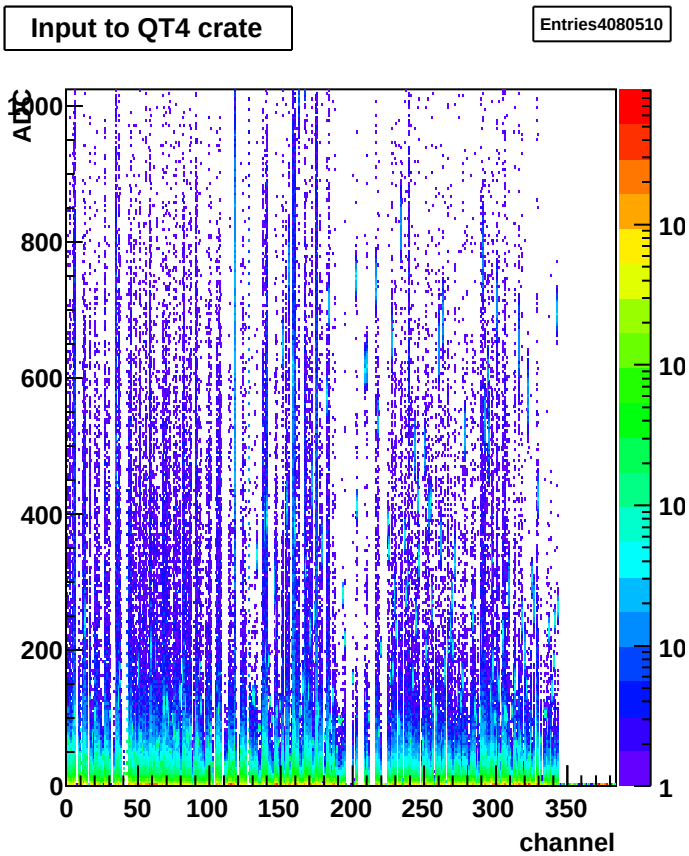

Entries 232215
ZDC East ADC Sum Att
ZDC West ADC Sum Att

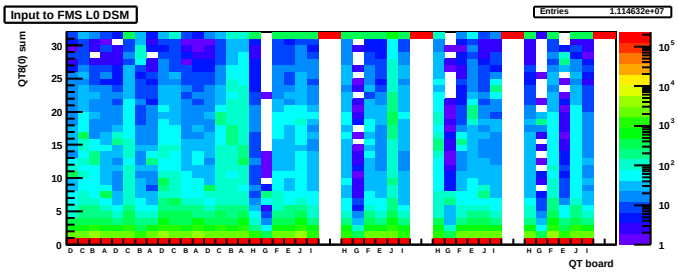

Entries 232215
BBC-L TAC Diff
BCC-S TAC Diff
Entries 232215
BBC-L TAC Diff
VPD TAC Diff
Entries 232215
BBC-L TAC Diff
ZDC TAC Diff
Entries 232215
BBC-S TAC Diff
VPD TAC Diff
Entries 232215
BBC-S TAC Diff
ZDC TAC Diff
Entries 232215
VPD TAC Diff
ZDC TAC Diff

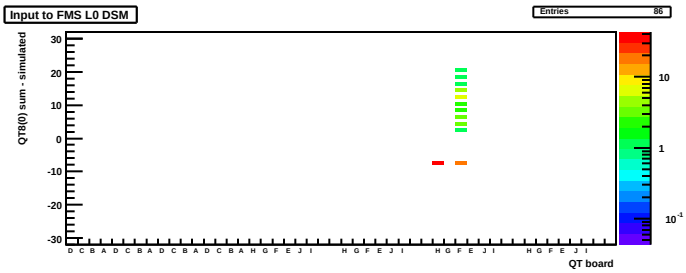

Input to QT1 crate
Entries6389775
ADC
channel

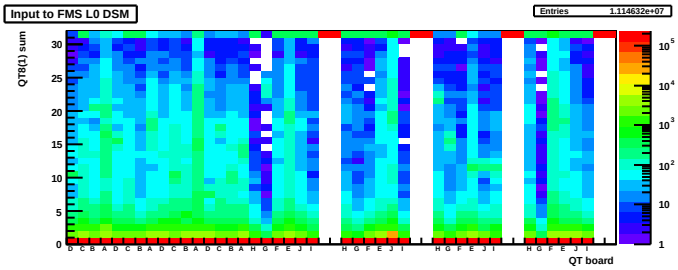

Input to QT2 crate
Entries3496037
ADC
channel

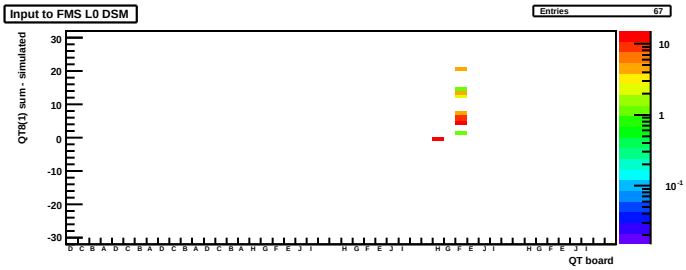

Input to QT3 crate
Entries3860748
ADC
channel

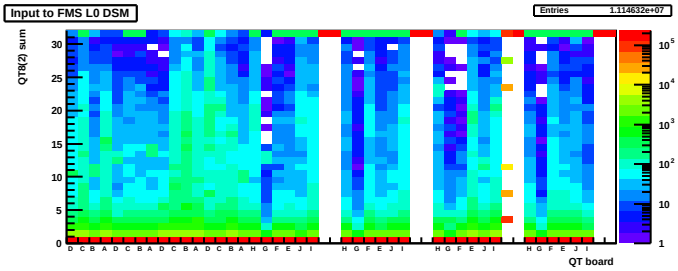

Input to QT4 crate
Entries4080510
ADC
channel

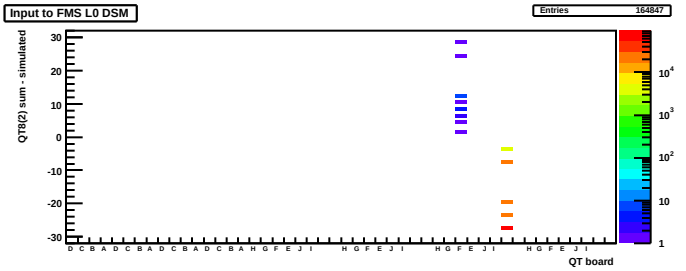

Input to FMS L0 DSM
Entries 1.114632e+07
QT8(0) sum
QT board

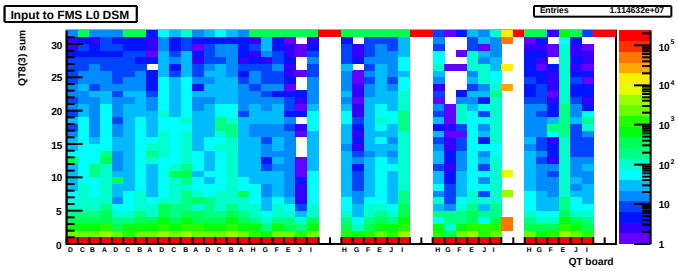

Input to FMS L0 DSM
Entries 86
QT8(0) sum - simulated
QT board

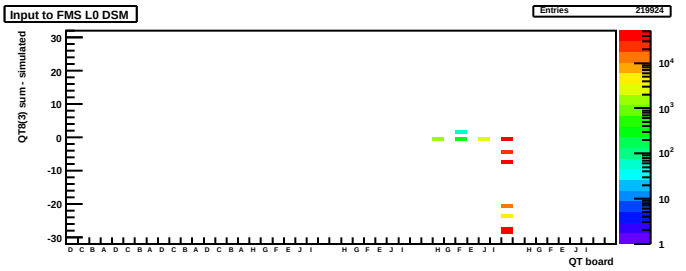

Input to FMS L0 DSM
Entries 1.114632e+07
QT8(1) sum
QT board

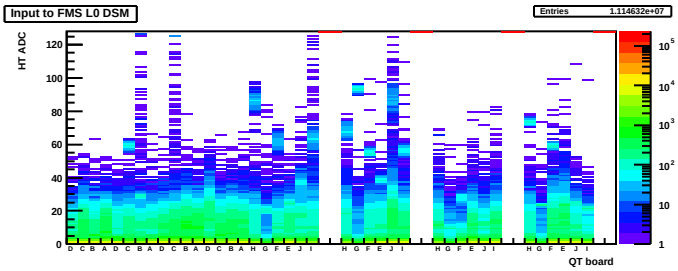

Input to FMS L0 DSM
Entries 67
QT8(1) sum - simulated
QT board

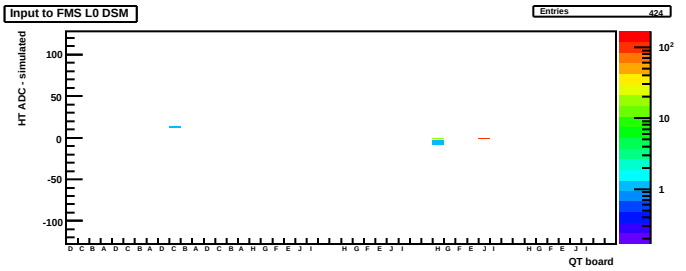

Input to FMS L0 DSM
Entries 1.114632e+07
QT8(2) sum
QT board

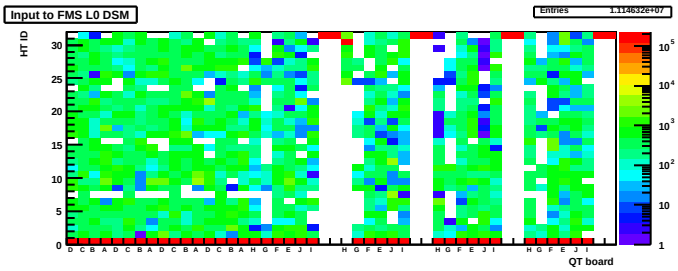

Input to FMS L0 DSM
Entries 164847
QT8(2) sum - simulated
QT board

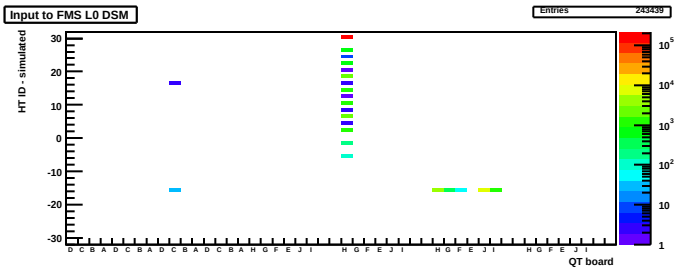

Input to FMS L0 DSM
Entries 1.114632e+07
QT8(3) sum
QT board

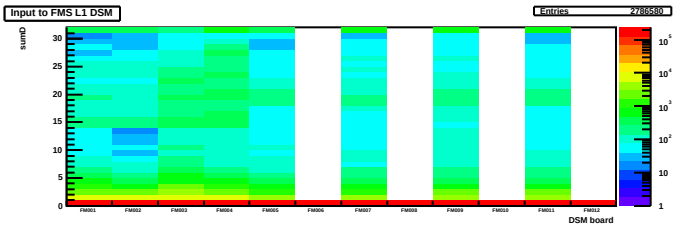

Input to FMS L0 DSM
Entries 219924
QT8(3) sum - simulated
QT board

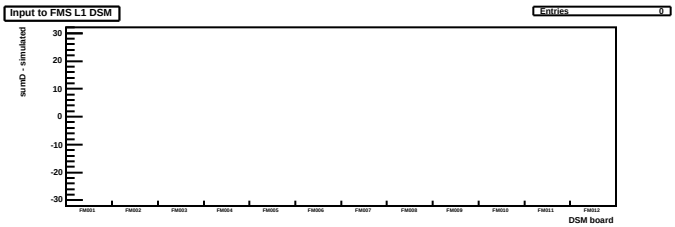

Input to FMS L0 DSM
Entries 1.114632e+07
HT ADC
QT board

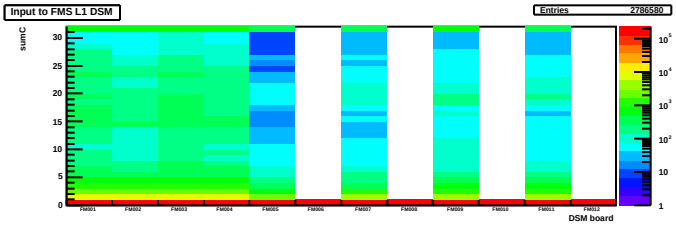

Input to FMS L0 DSM
Entries 424
HT ADC - simulated
QT board

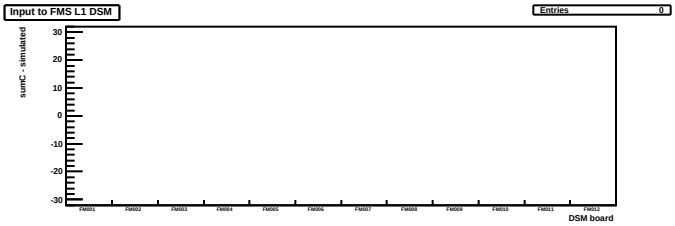

Input to FMS L0 DSM
Entries 1.114632e+07
HT ID
QT board

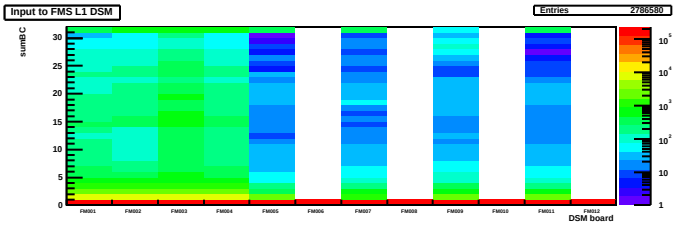

Input to FMS L0 DSM
Entries 243439
HT ID - simulated
QT board

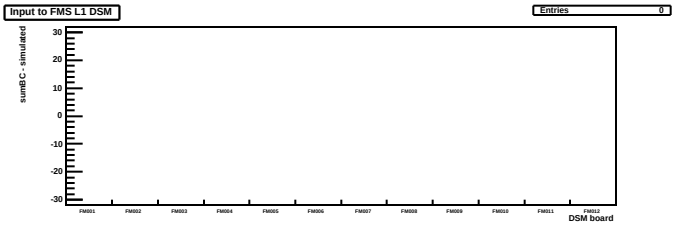

Input to FMS L1 DSM
Entries 2786580
sumD
DSM board

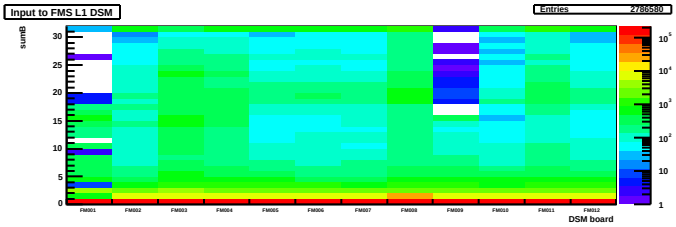

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

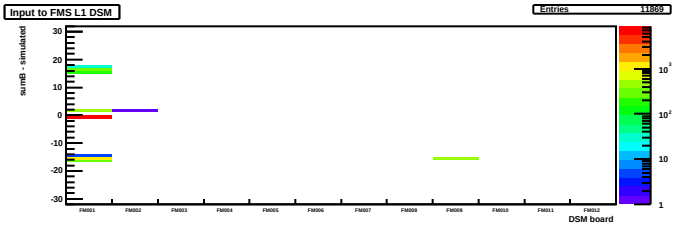

Input to FMS L1 DSM
Entries 2786580
sumC
DSM board

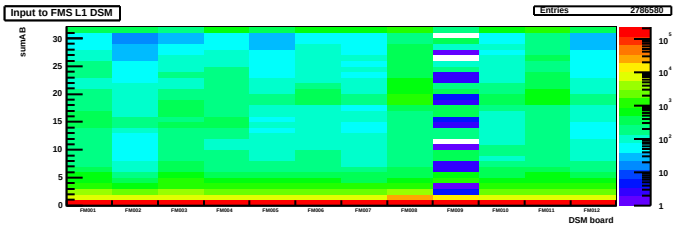

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

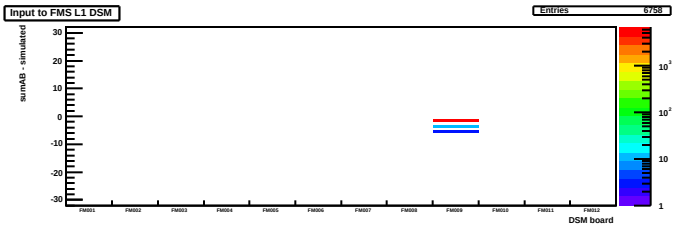

Input to FMS L1 DSM
Entries 2786580
sumBC
DSM board

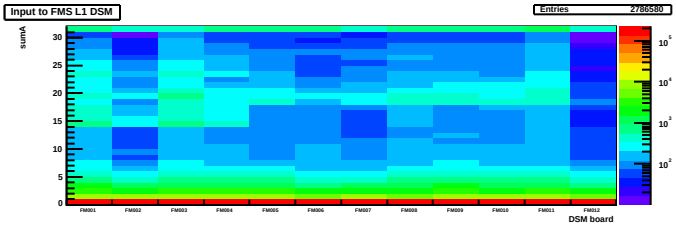

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

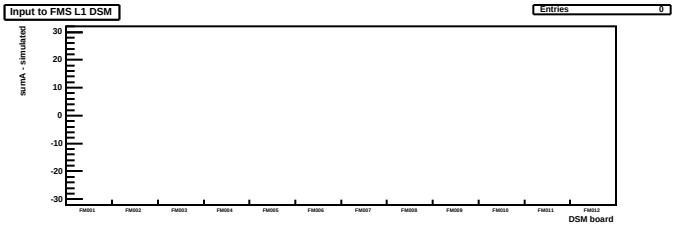

Input to FMS L1 DSM
Entries 2786580
sumB
DSM board

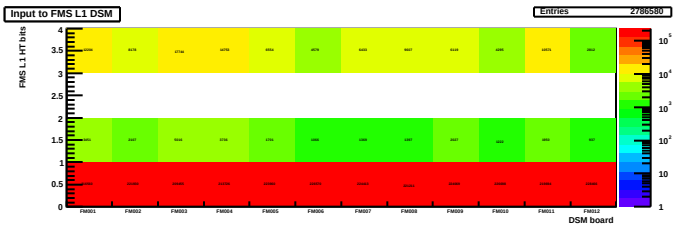

Input to FMS L1 DSM
Entries 11869
sumB - simulated
DSM board

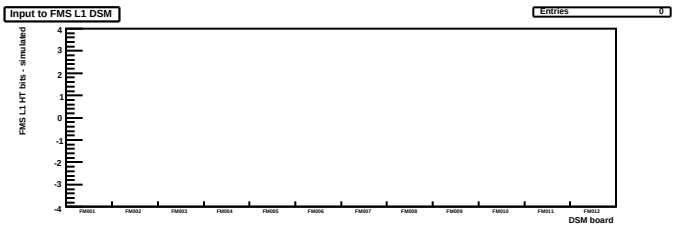

Input to FMS L1 DSM
Entries 2786580
sumAB
DSM board

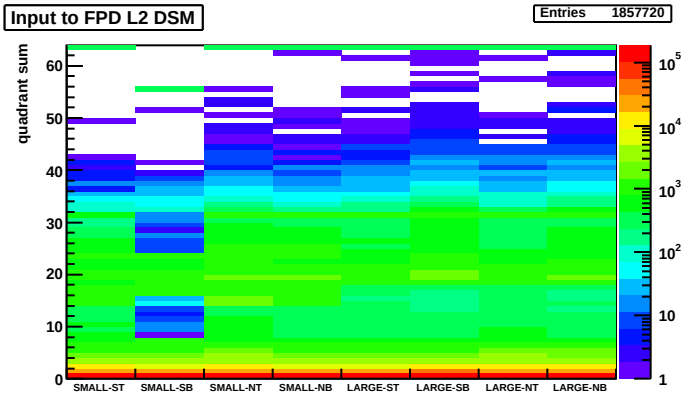

Input to FMS L1 DSM
Entries 6758
sumAB - simulated
DSM board

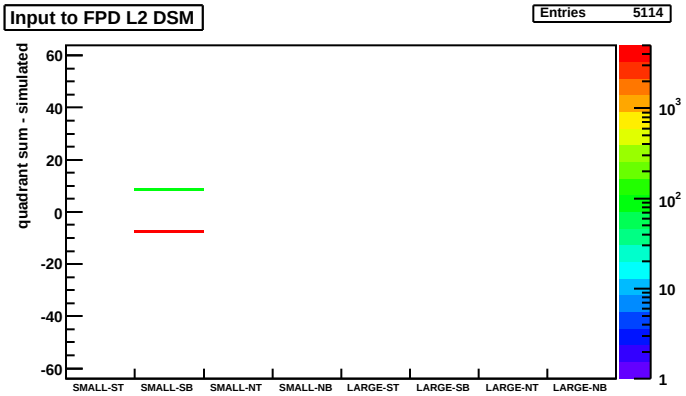

Input to FMS L1 DSM
Entries 2786580
sumA
DSM board

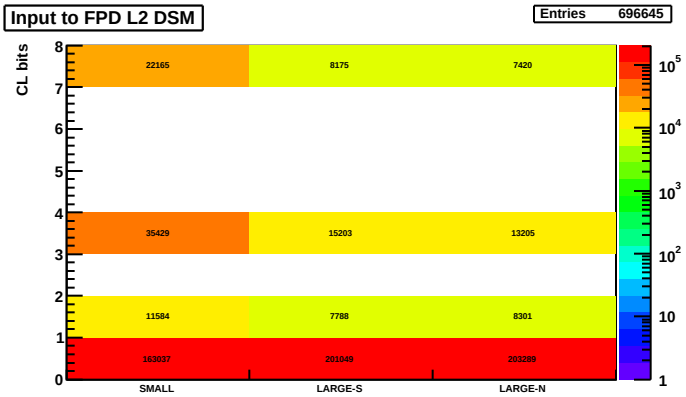

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

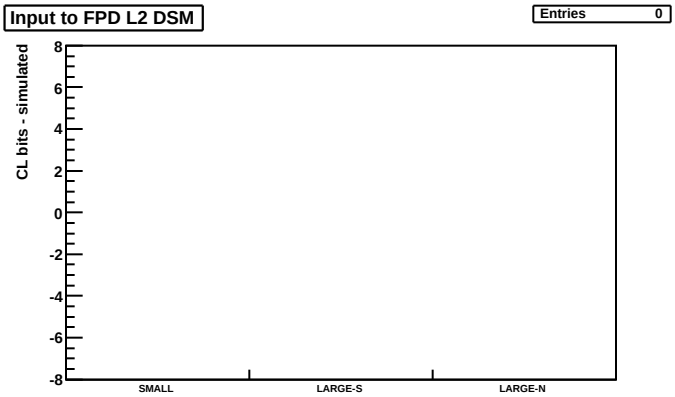

Input to FMS L1 DSM
Entries 2786580
FMS L1 HT bits
DSM board

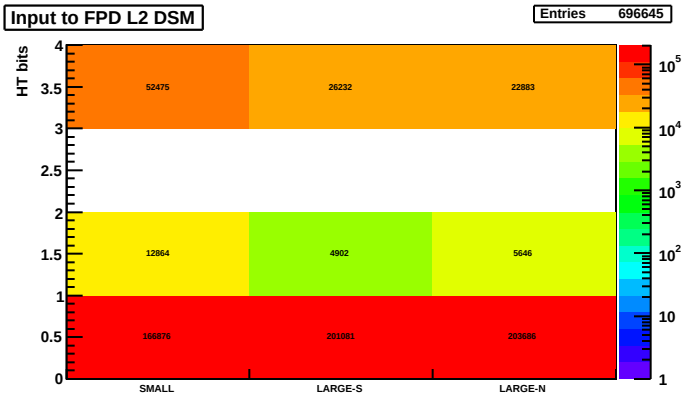

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

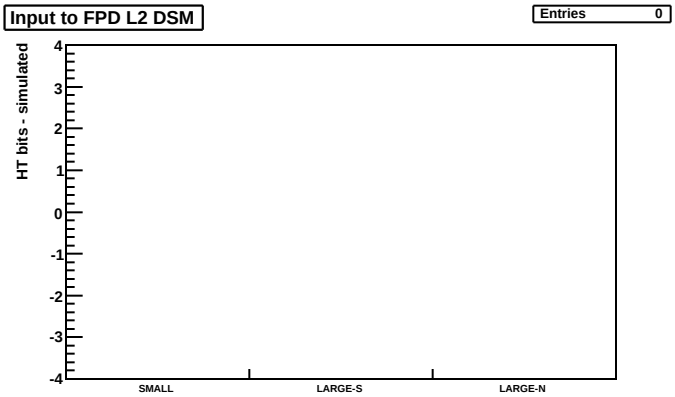

Input to FPD L2 DSM
Entries 1857720
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

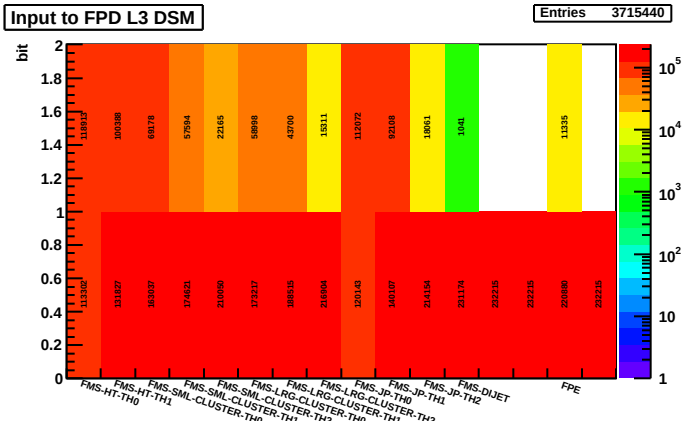

Input to FPD L2 DSM
Entries 5114
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

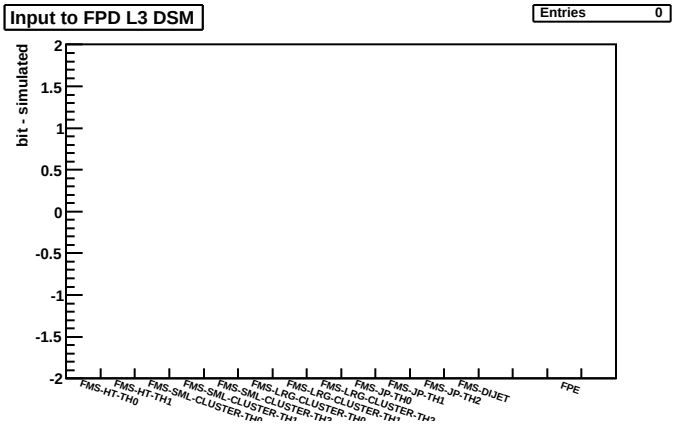

Input to FPD L2 DSM
Entries 696645
CL bits
22165
8175
7420
35429
15203
13205
11584
7788
8301
163037
201049
203289
SMALL
LARGE-S
LARGE-N

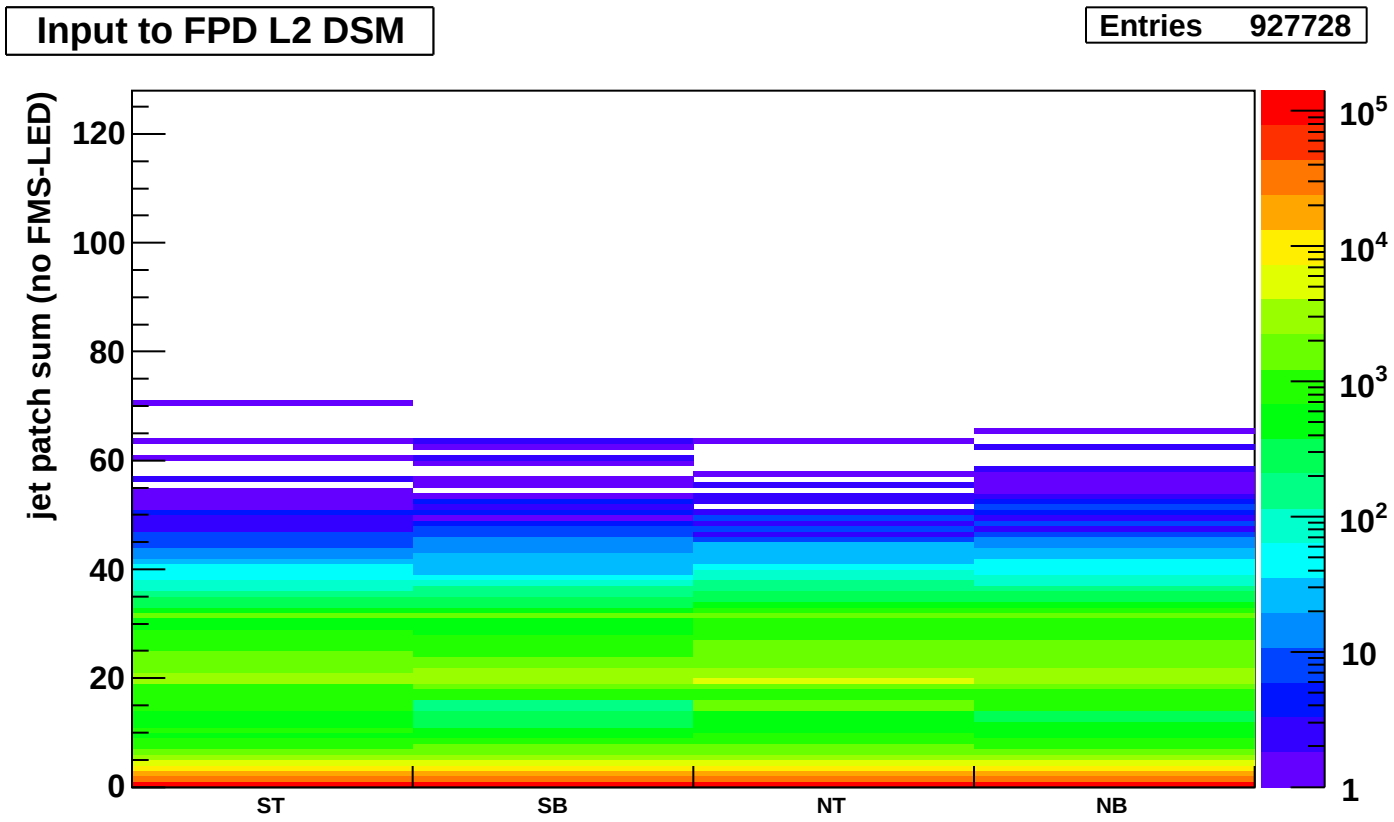

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

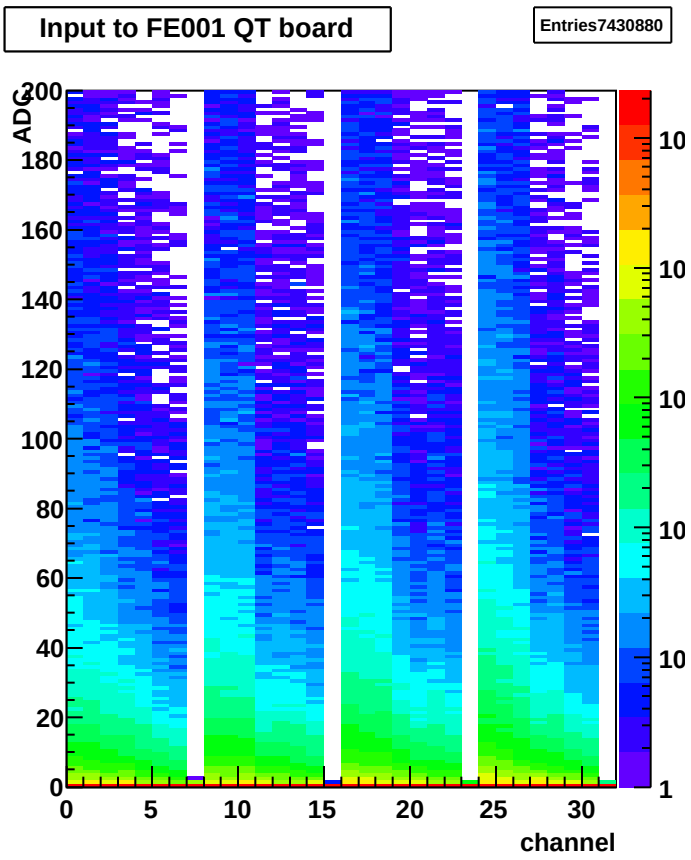

Input to FPD L2 DSM
Entries 696645
HT bits
52475
26232
22883
12864
4902
5646
166876
201081
203686
SMALL
LARGE-S
LARGE-N

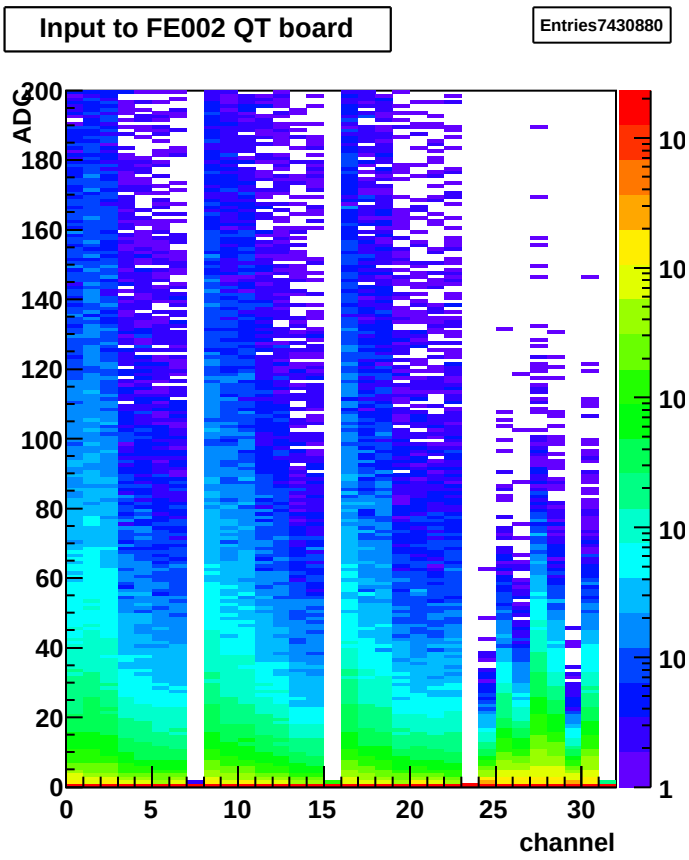

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

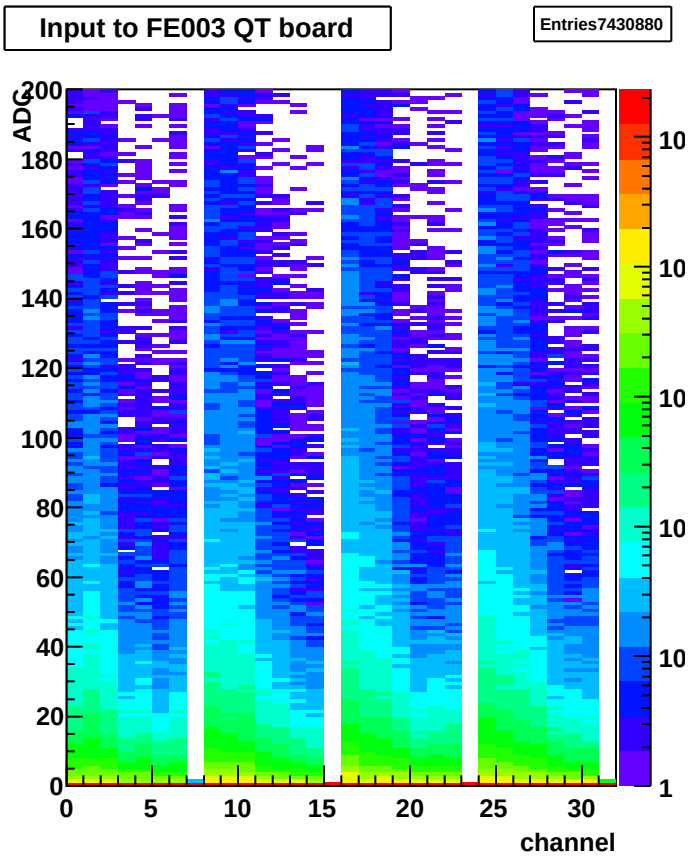

Input to FPD L3 DSM
Entries 3715440
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

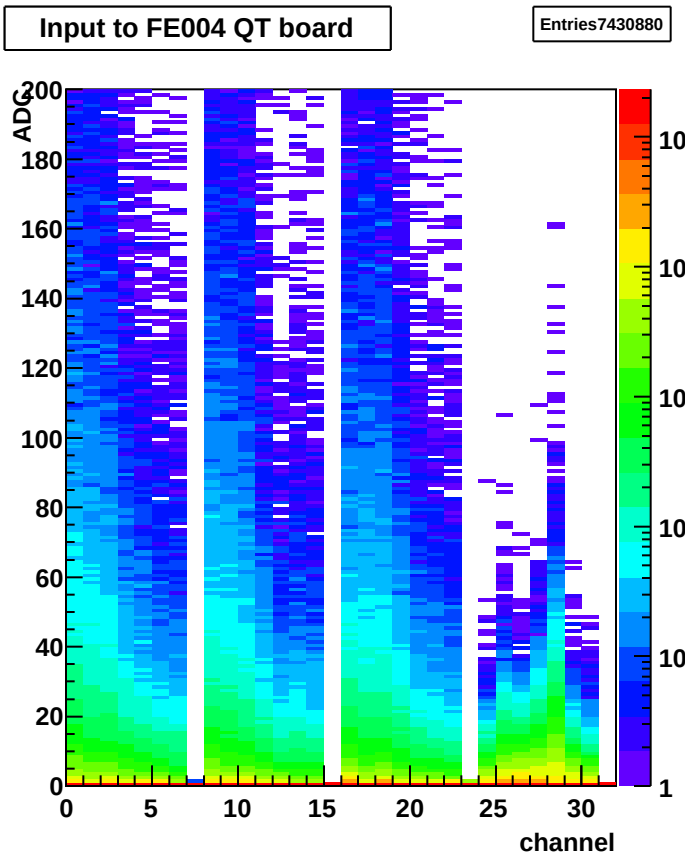

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

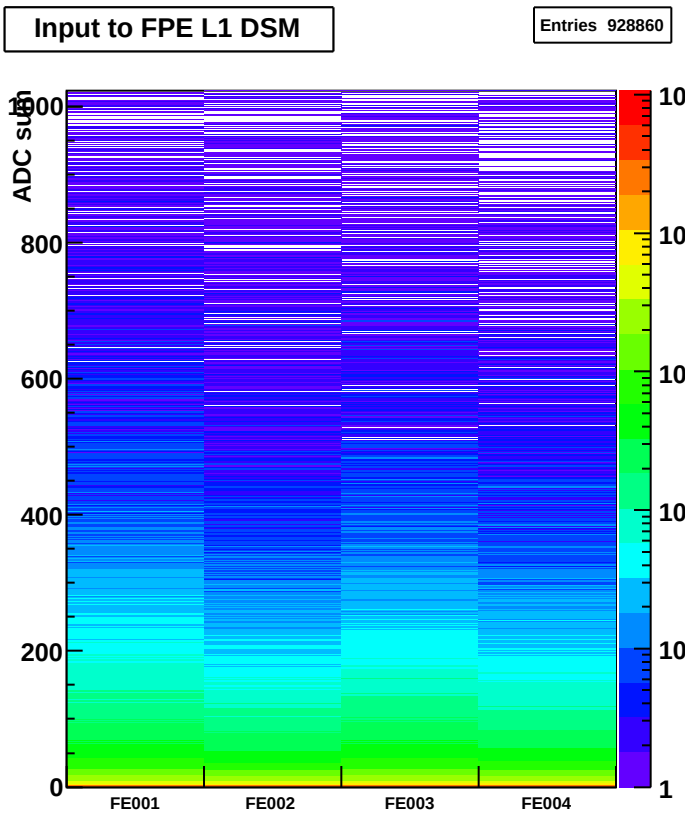

Input to FPD L2 DSM
Entries 927728
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

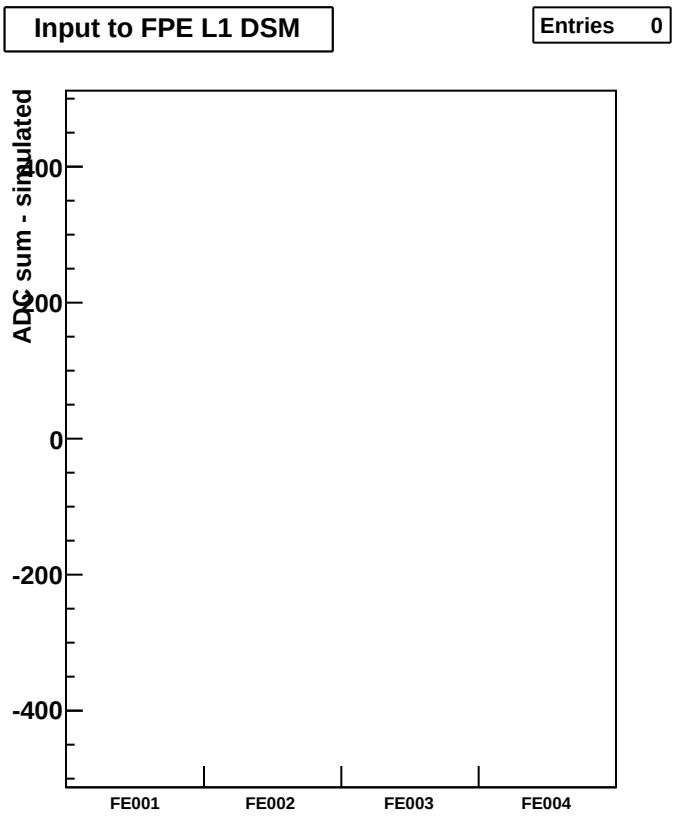

Input to FE001 QT board
Entries7430880
ADC
channel

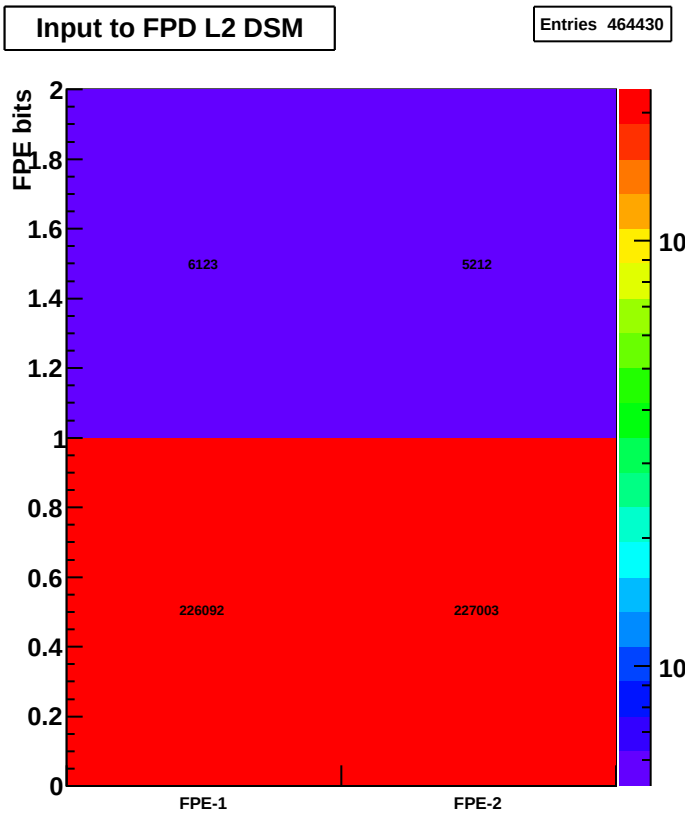

Input to FE002 QT board
Entries7430880
ADC
channel

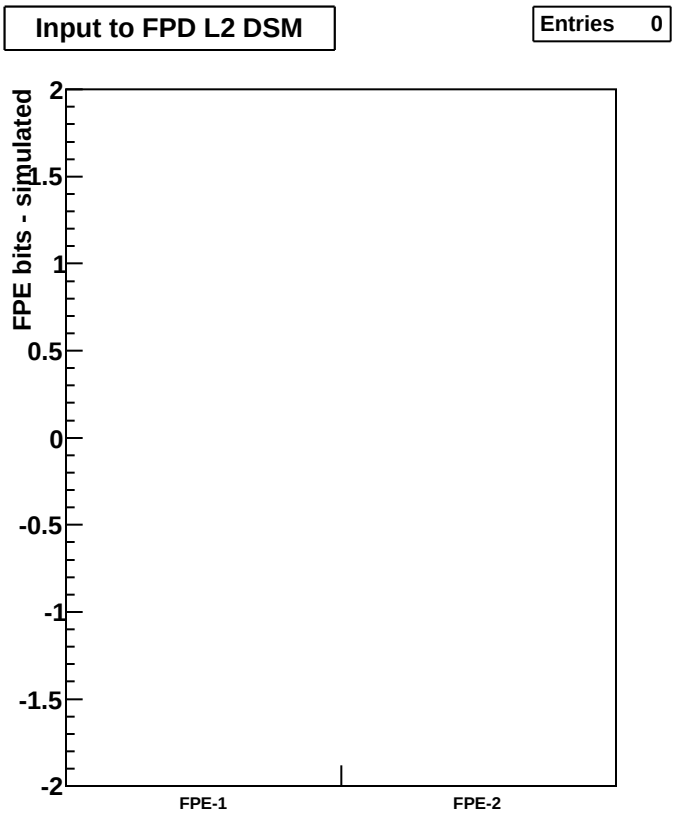

Input to FE003 QT board
Entries7430880
ADC
channel


Input to FE004 QT board
Entries7430880
ADC
channel


Input to FPE L1 DSM
Entries 928860
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 464430
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
6123
5212
226092
227003
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2