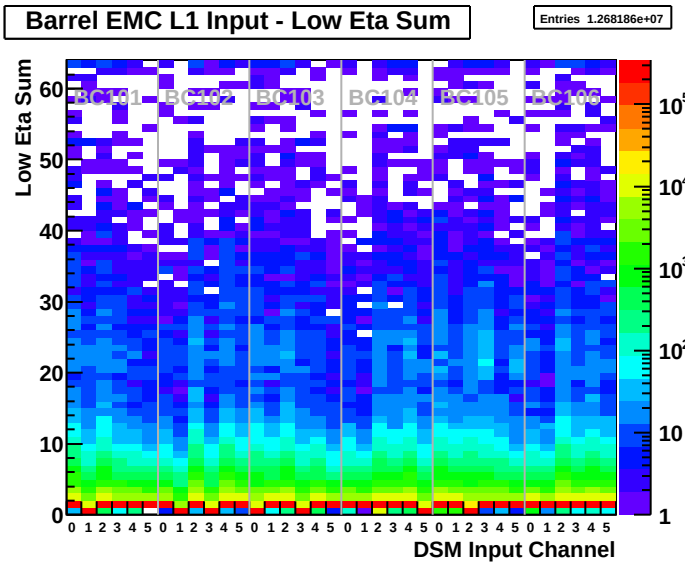
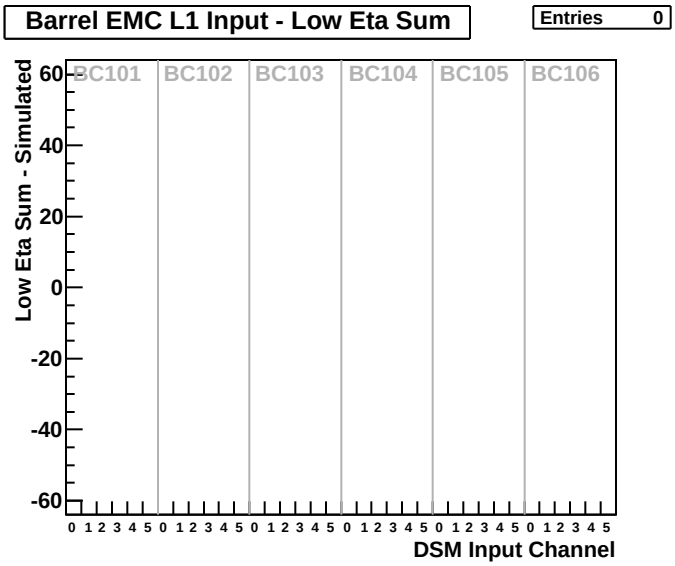
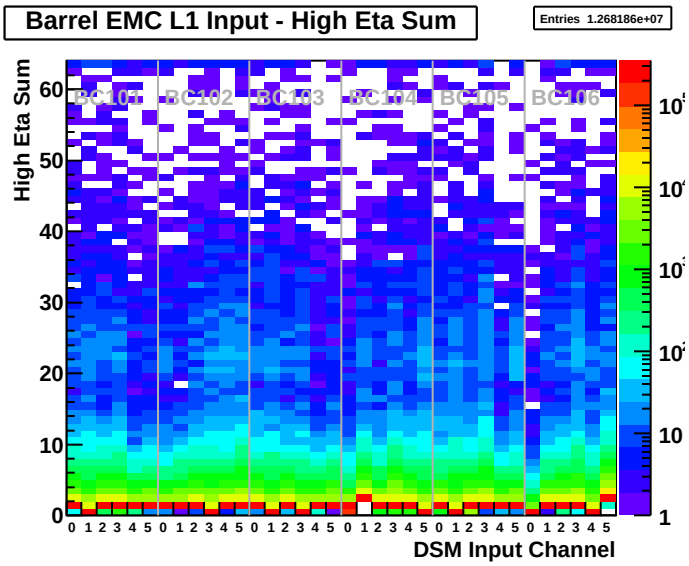
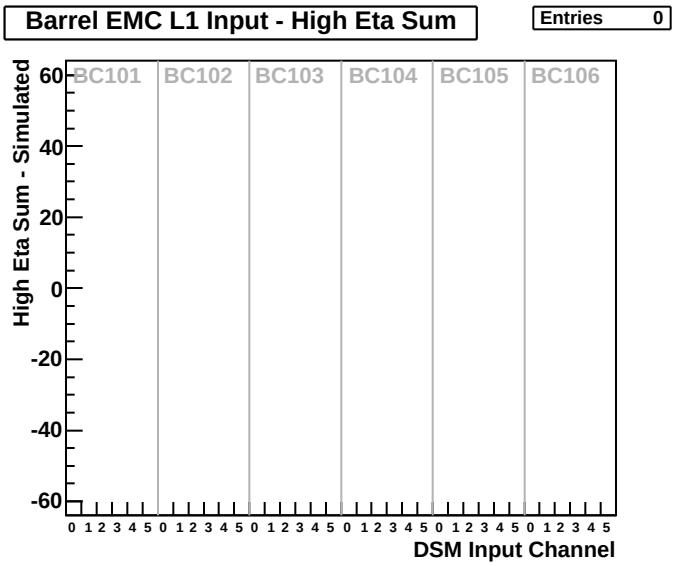
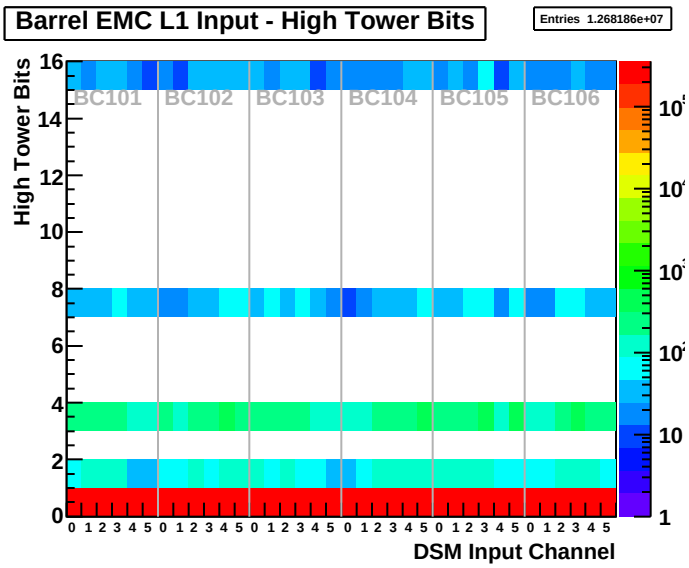
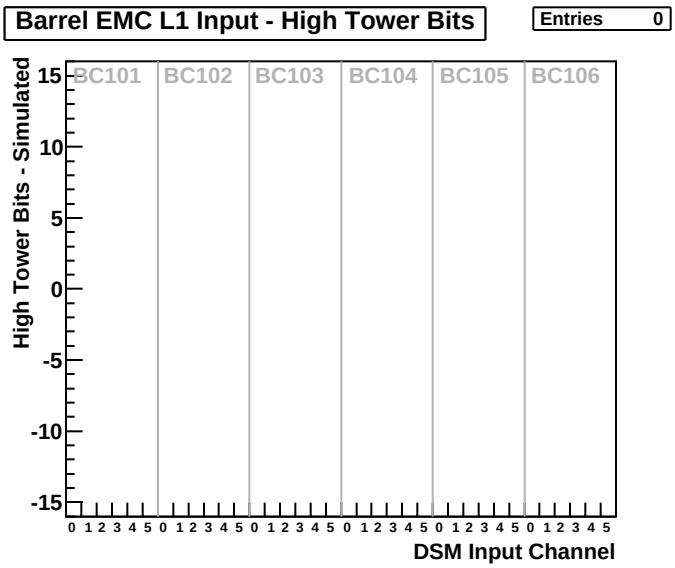

Barrel EMC L1 Input - Low Eta Sum
Entries 1.268186e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

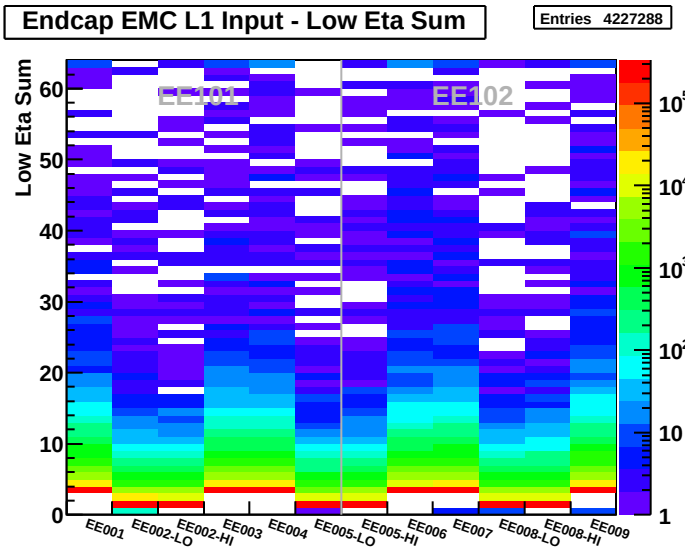

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

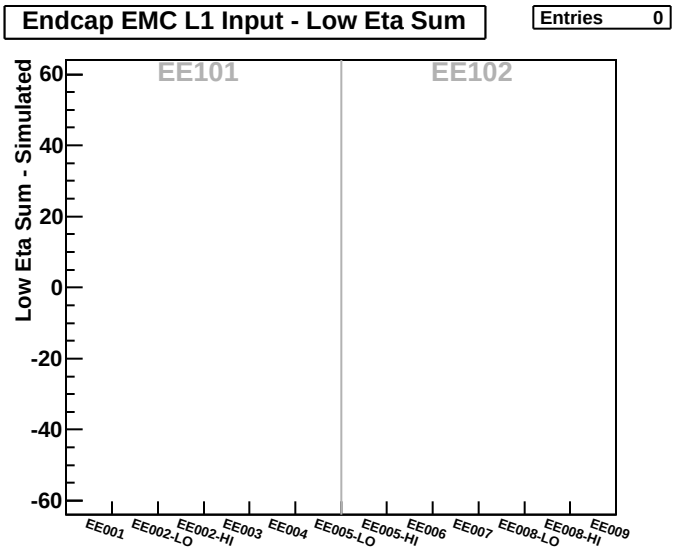

Barrel EMC L1 Input - High Eta Sum
Entries 1.268186e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

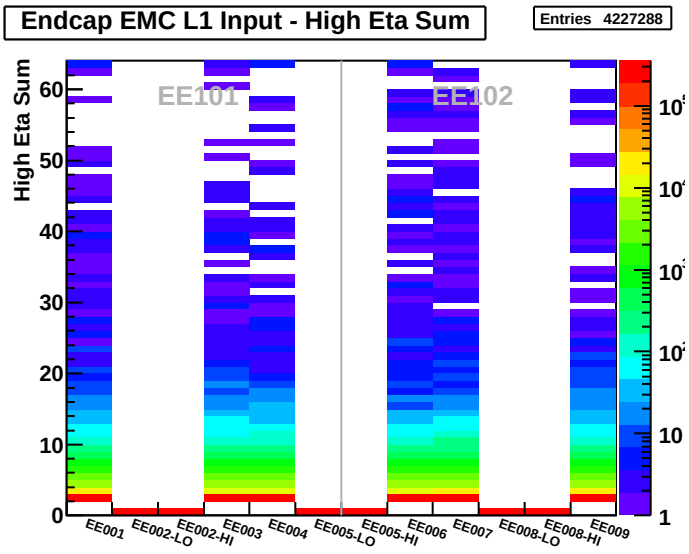

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

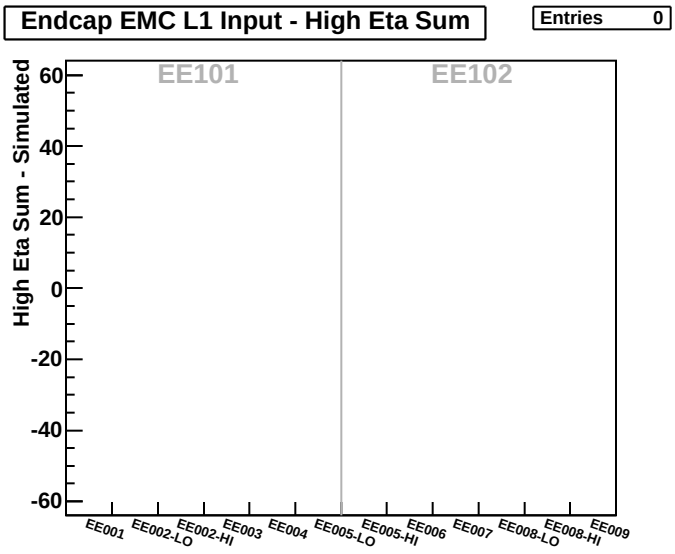

Barrel EMC L1 Input - High Tower Bits
Entries 1.268186e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

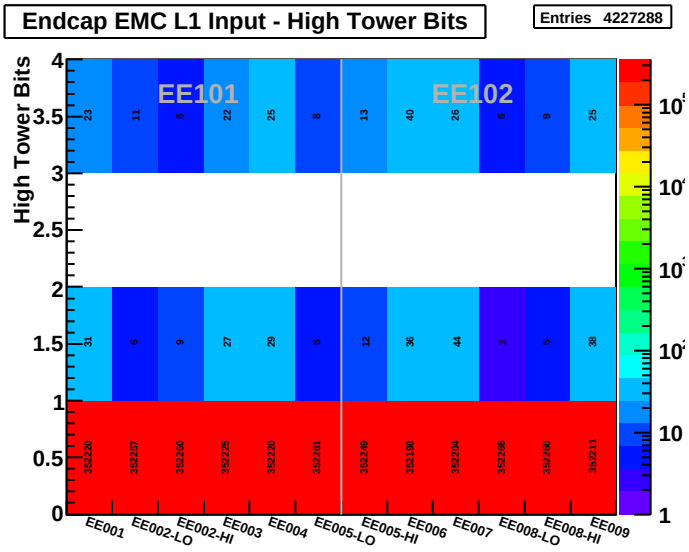

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

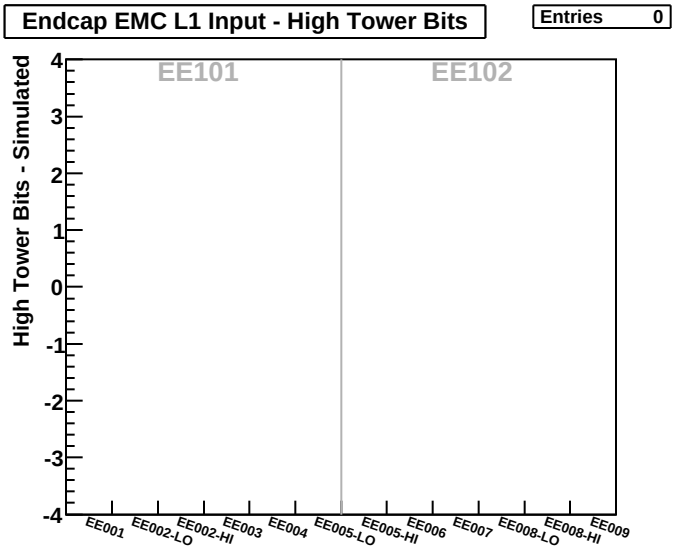

Endcap EMC L1 Input - Low Eta Sum
Entries 4227288
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

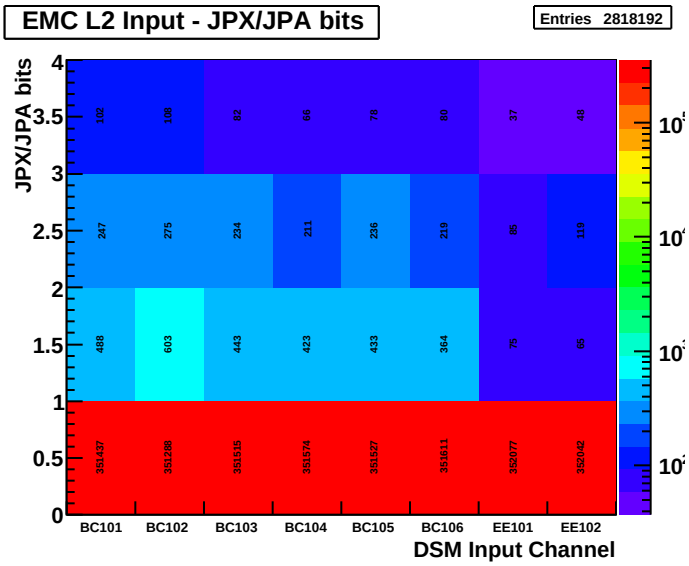

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

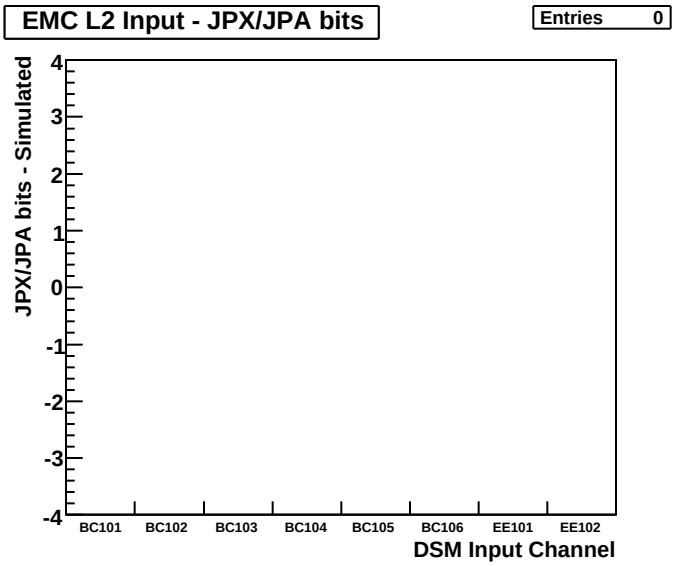

Endcap EMC L1 Input - High Eta Sum
Entries 4227288
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

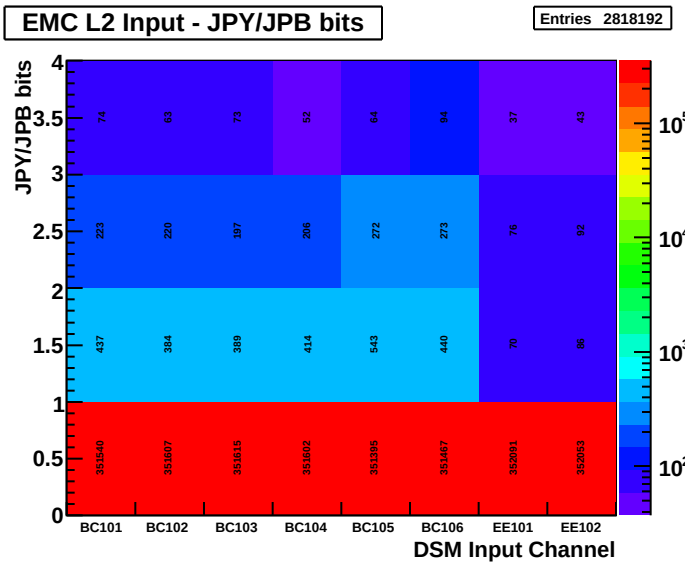

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

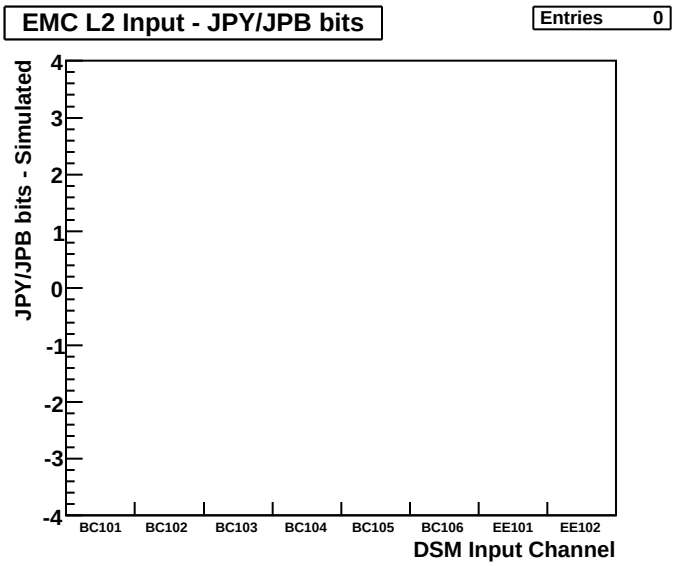

Endcap EMC L1 Input - High Tower Bits
Entries 4227288
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

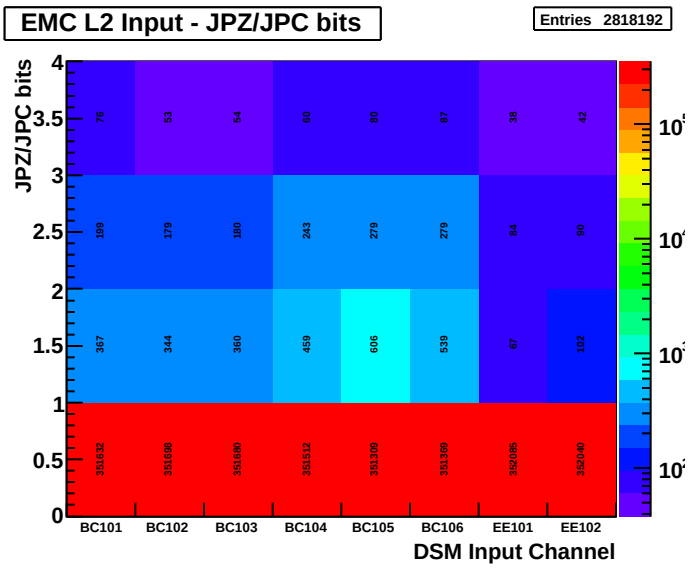

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

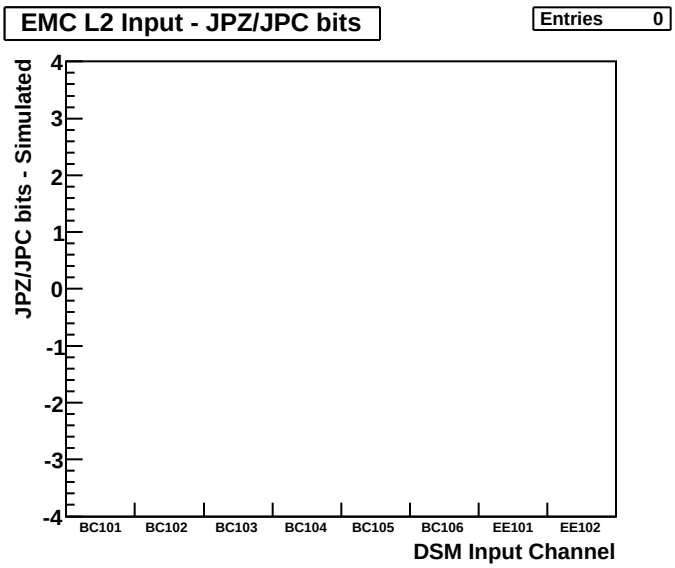

EMC L2 Input - JPX/JPA bits
Entries 2818192
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
102 108 82 66 78 80 37 48
247 275 224 211 236 219 85 119
488 603 443 423 433 364 75 65
351437 351288 351515 351574 351527 351611 352077 352042

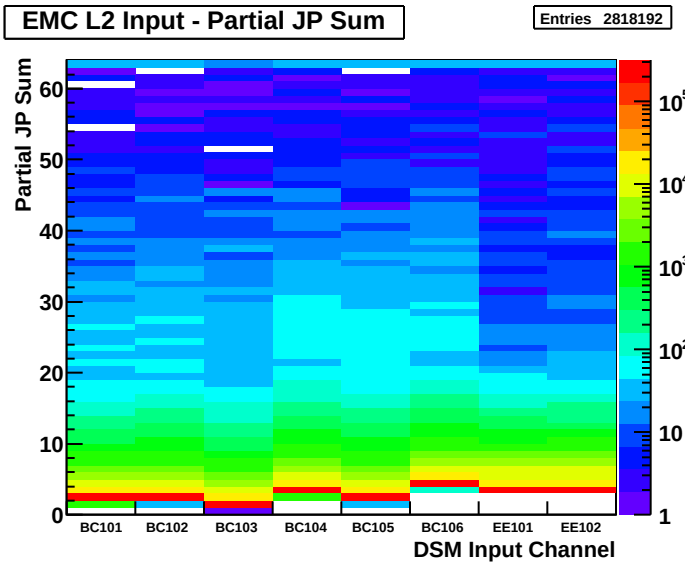

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

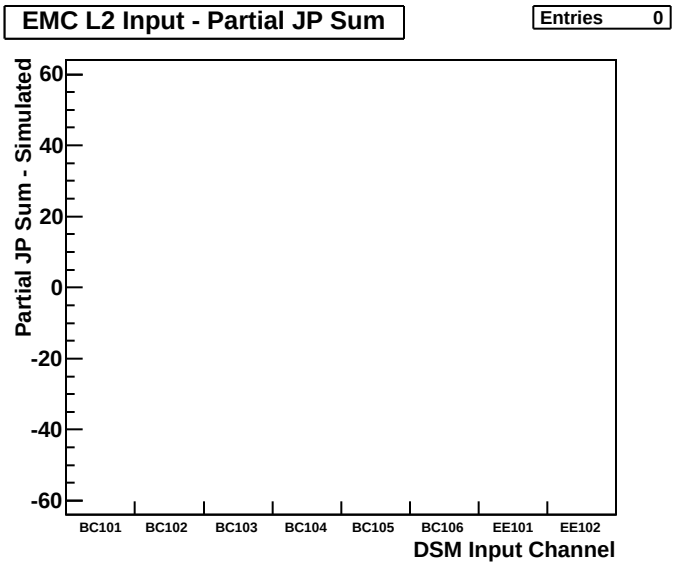

EMC L2 Input - JPY/JPB bits
Entries 2818192
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
74 63 73 52 64 94 37 43
223 220 197 206 272 273 76 92
437 384 389 414 543 440 70 86
351540 351607 351615 351602 351395 351467 352091 352053

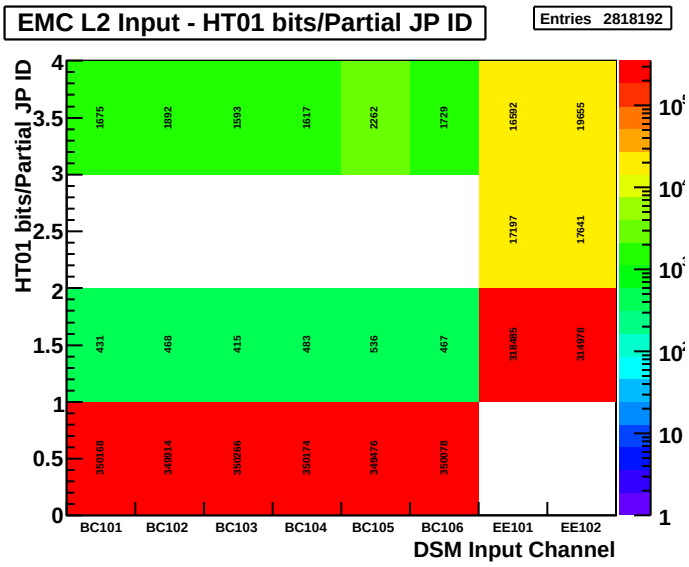

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

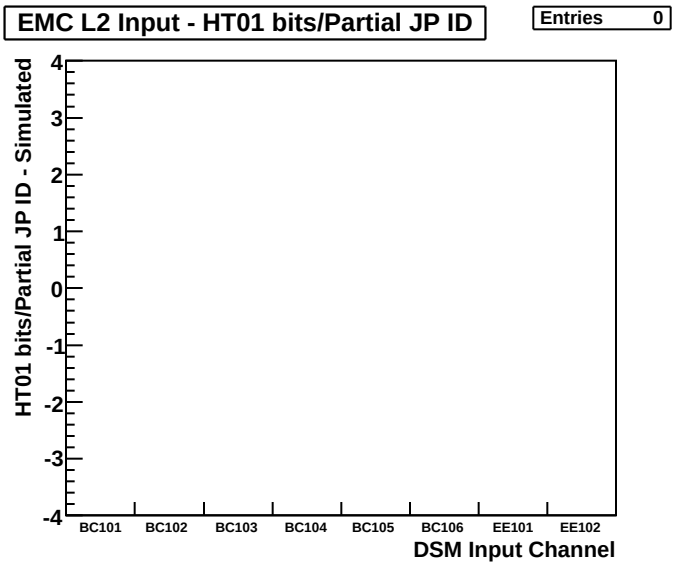

EMC L2 Input - JPZ/JPC bits
Entries 2818192
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
76 53 54 60 80 87 38 42
199 179 180 243 279 279 84 90
367 344 360 459 606 539 67 102
351532 351598 351580 351512 351309 351369 352085 352040

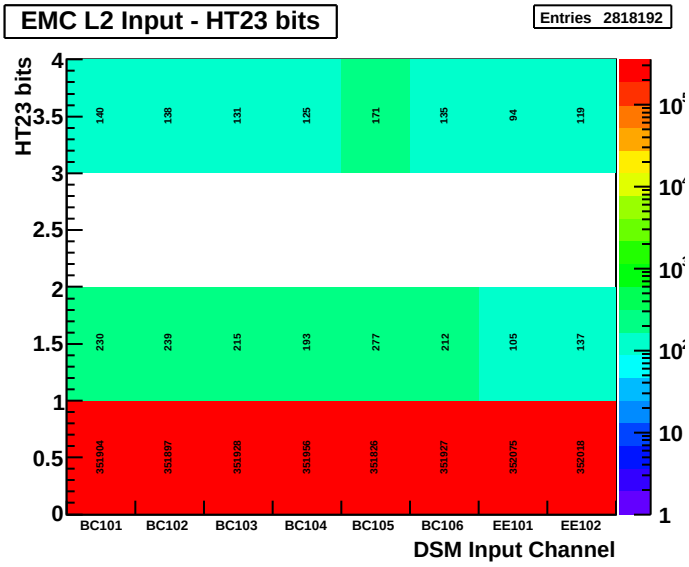

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

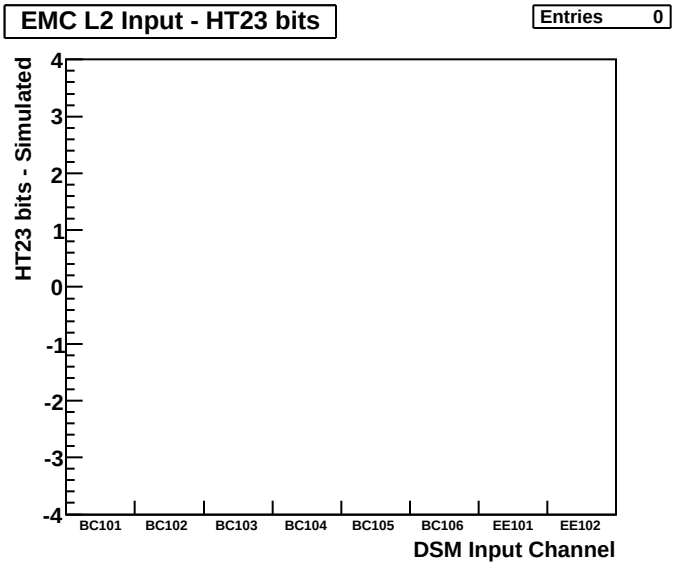

EMC L2 Input - Partial JP Sum
Entries 2818192
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

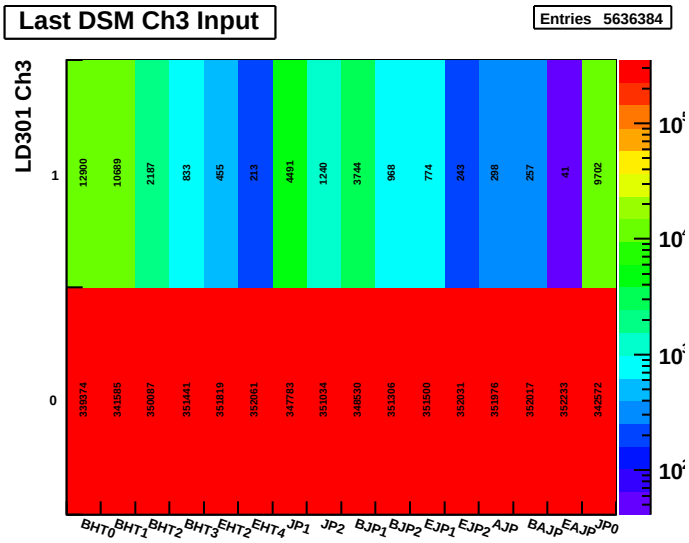

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

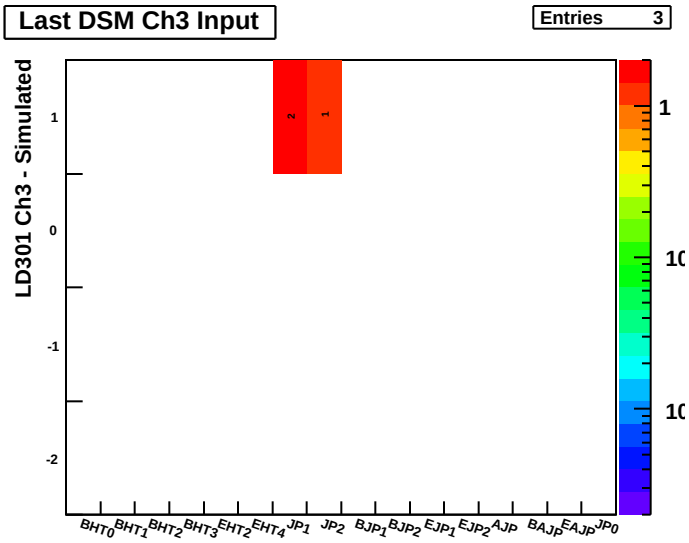

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2818192
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

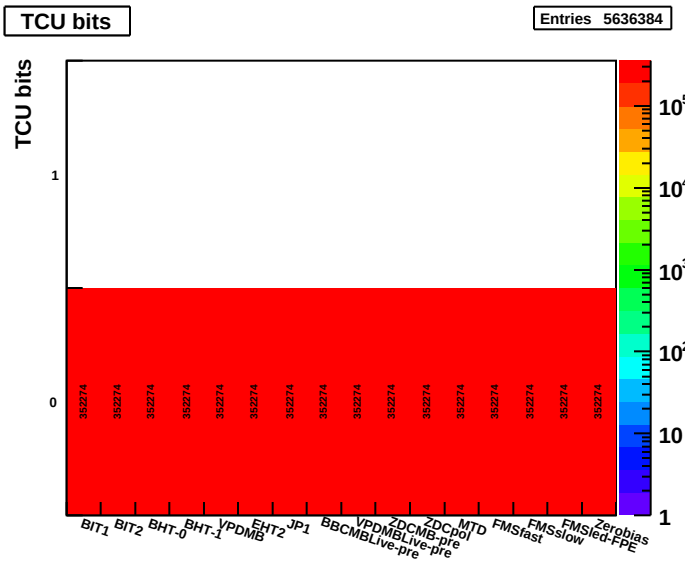

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

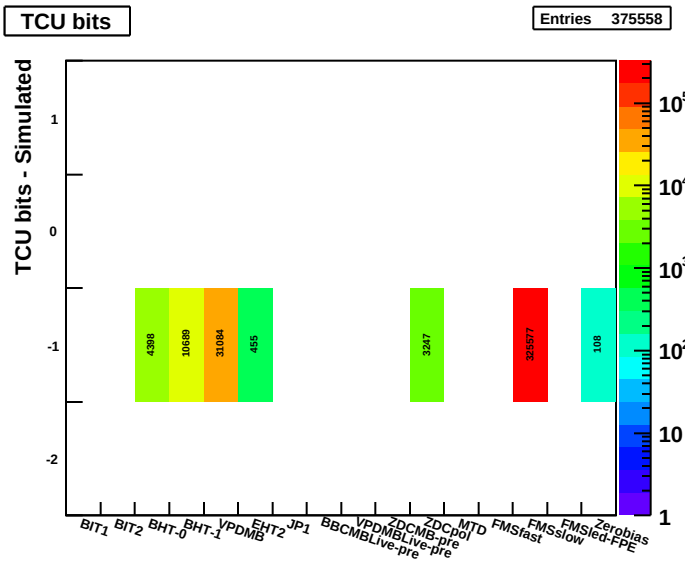

EMC L2 Input - HT23 bits
Entries 2818192
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

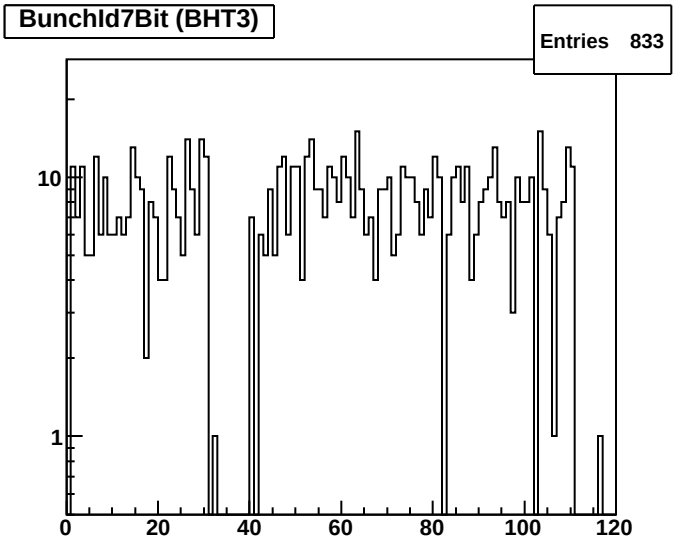

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

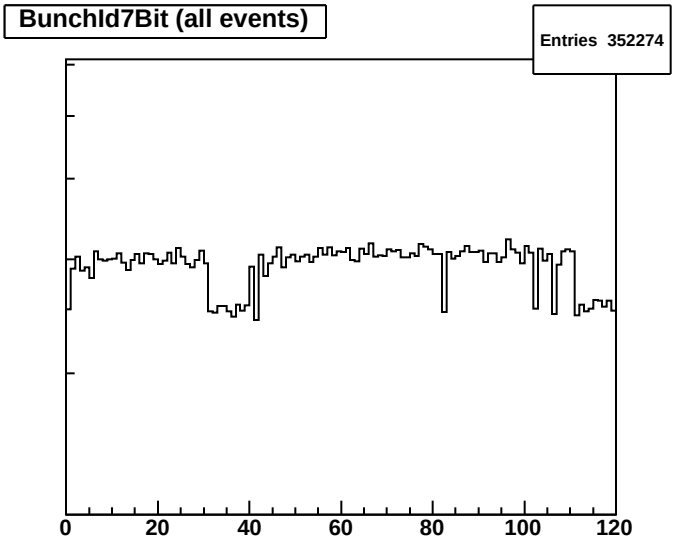

Last DSM Ch3 Input
Entries 5636384
LD301 Ch3
12900 10689 2187 833 455 213 4491 1240 3744 968 774 243 298 257 41 9702
339374 341585 350187 351441 351819 352061 347783 351034 348530 351306 351500 352031 351976 352017 352233 342572
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

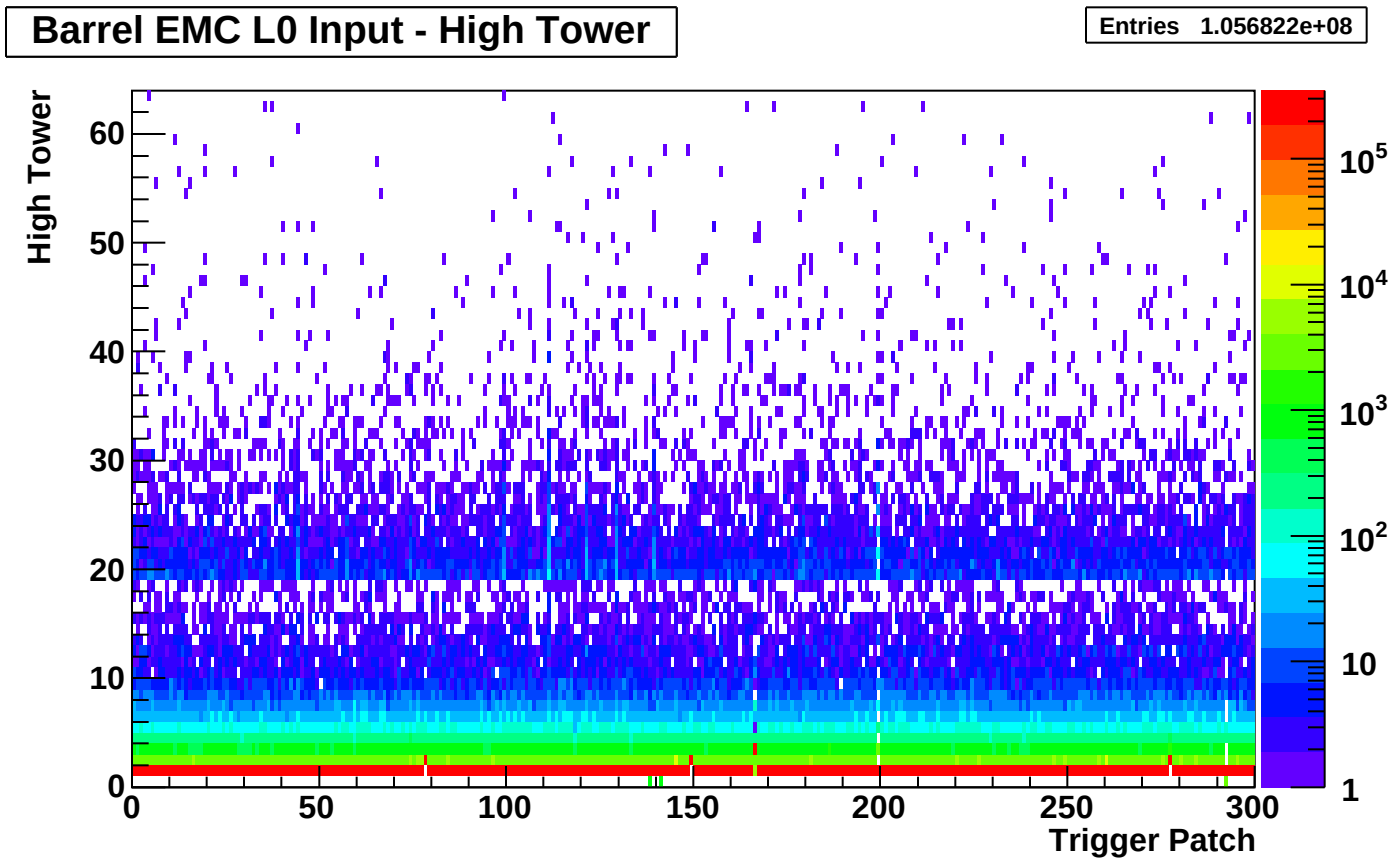

Last DSM Ch3 Input
Entries 3
LD301 Ch3 - Simulated
2 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

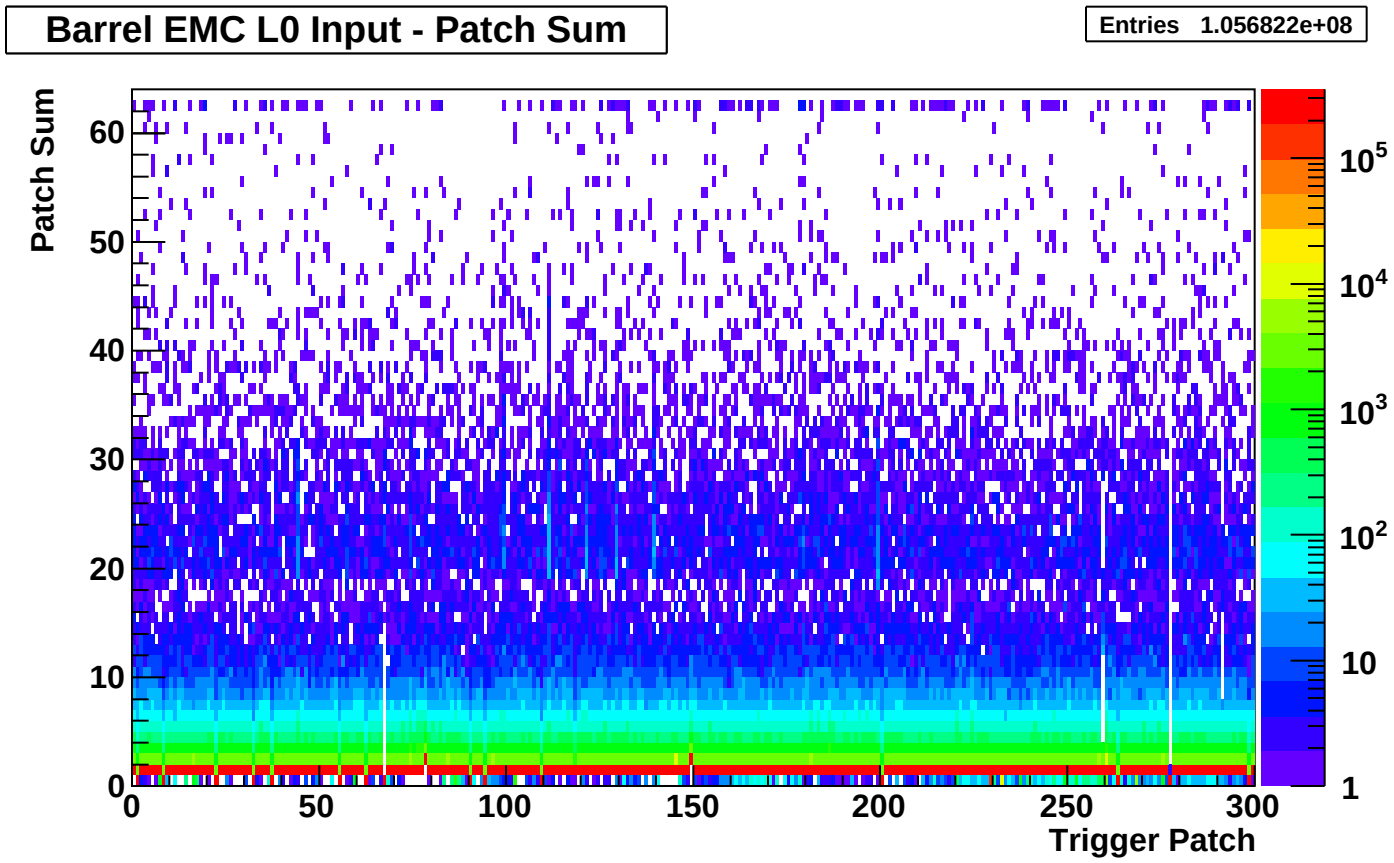

TCU bits
Entries 5636384
TCU bits
352274
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

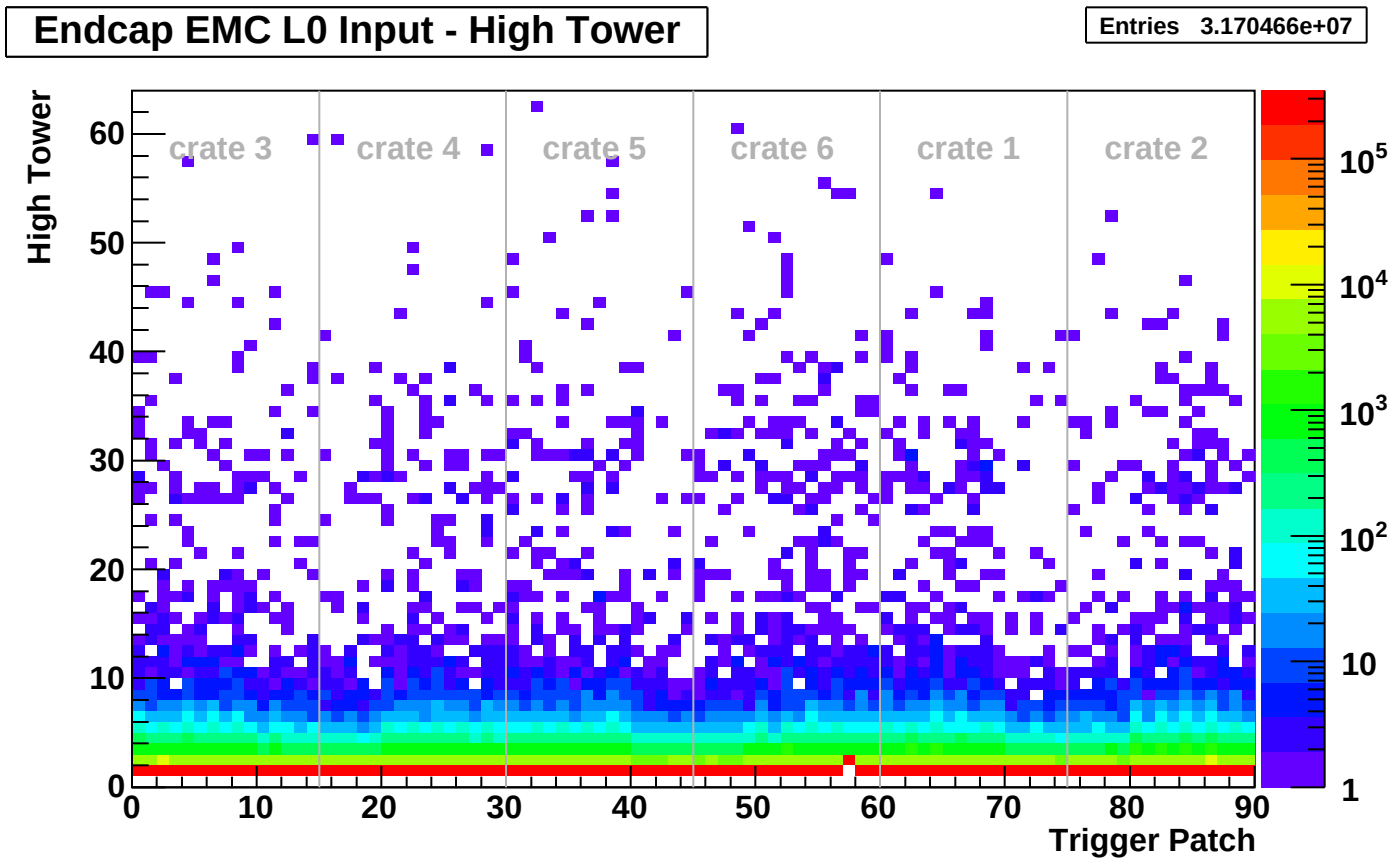

TCU bits
Entries 375558
TCU bits - Simulated
4398 10689 31084 455 3247 325577 108
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

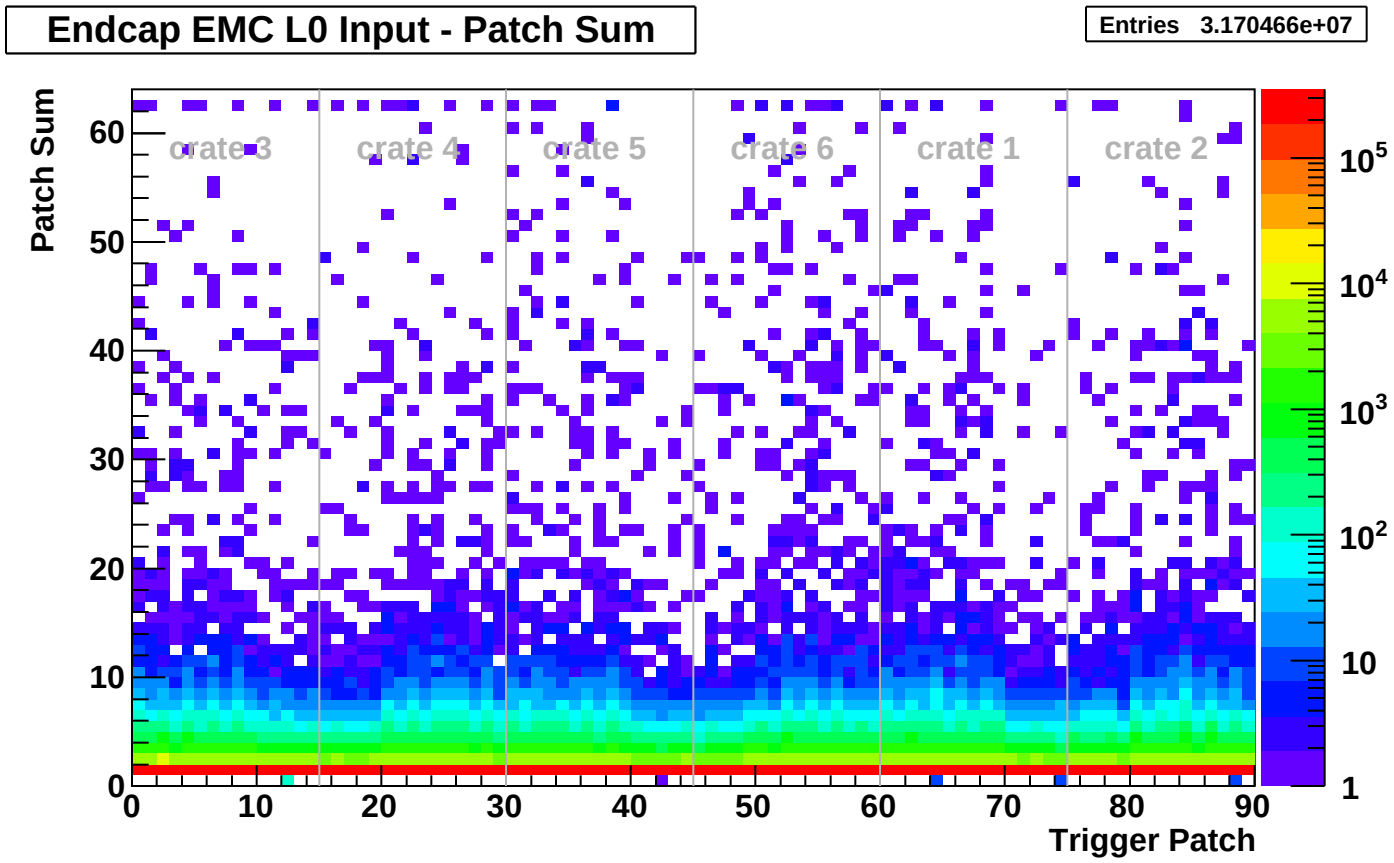

BunchId7Bit (BHT3)
Entries 833

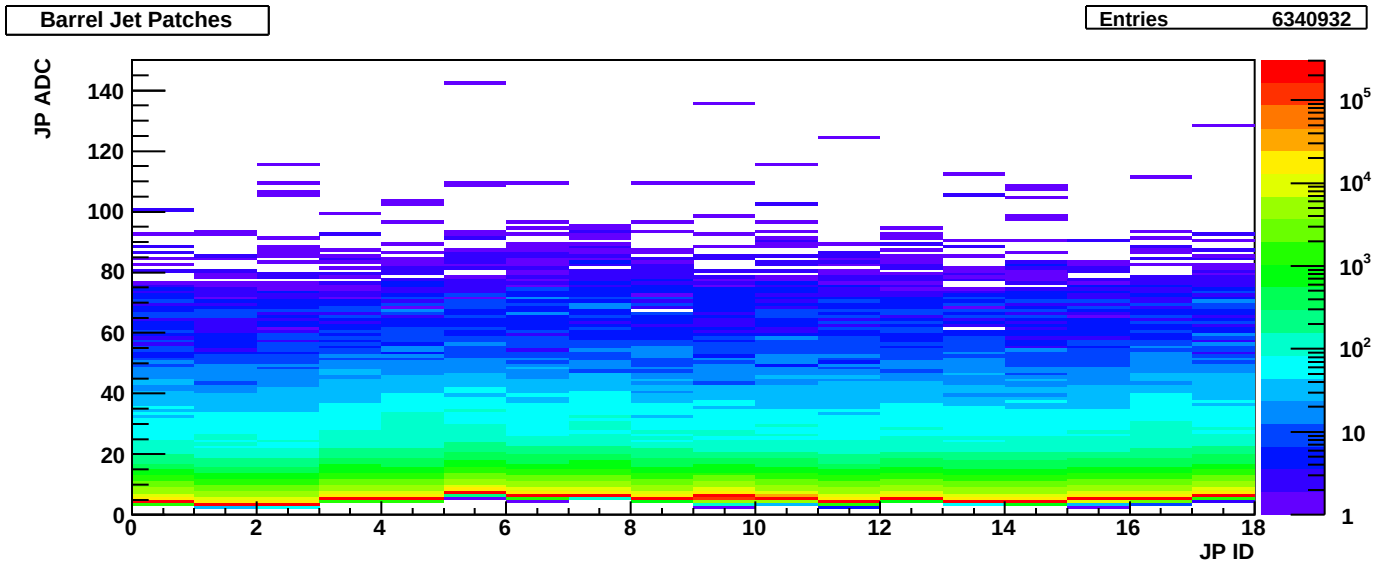

BunchId7Bit (all events)
Entries 352274

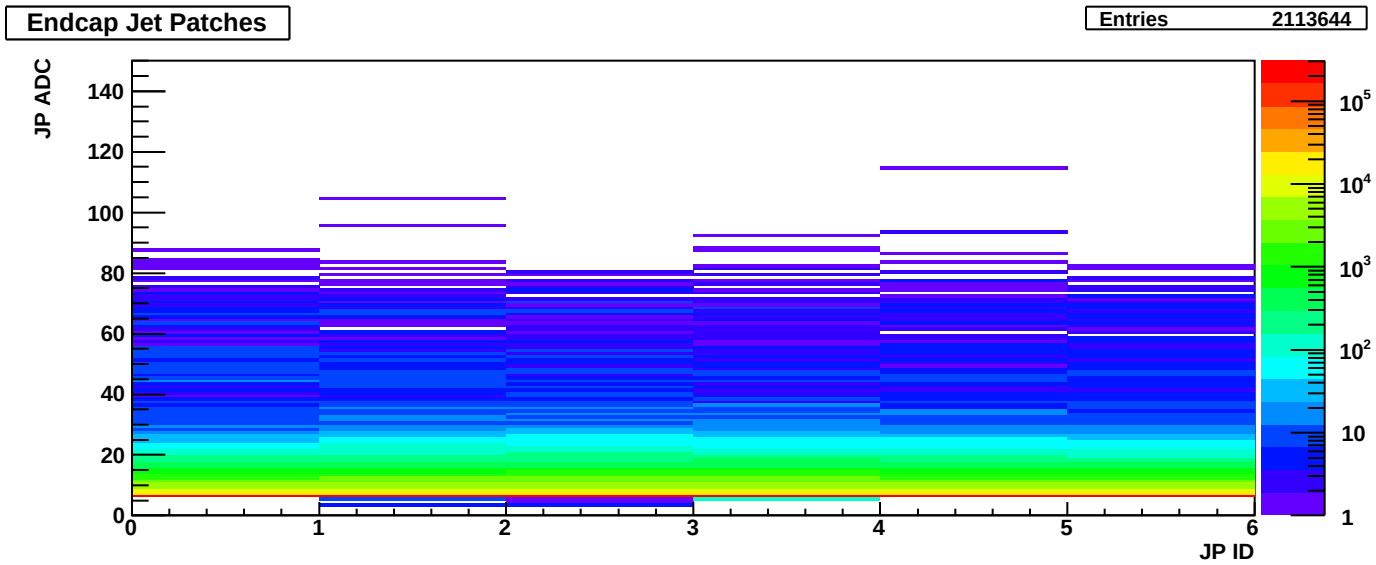

Barrel EMC L0 Input - High Tower
Entries 1.056822e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

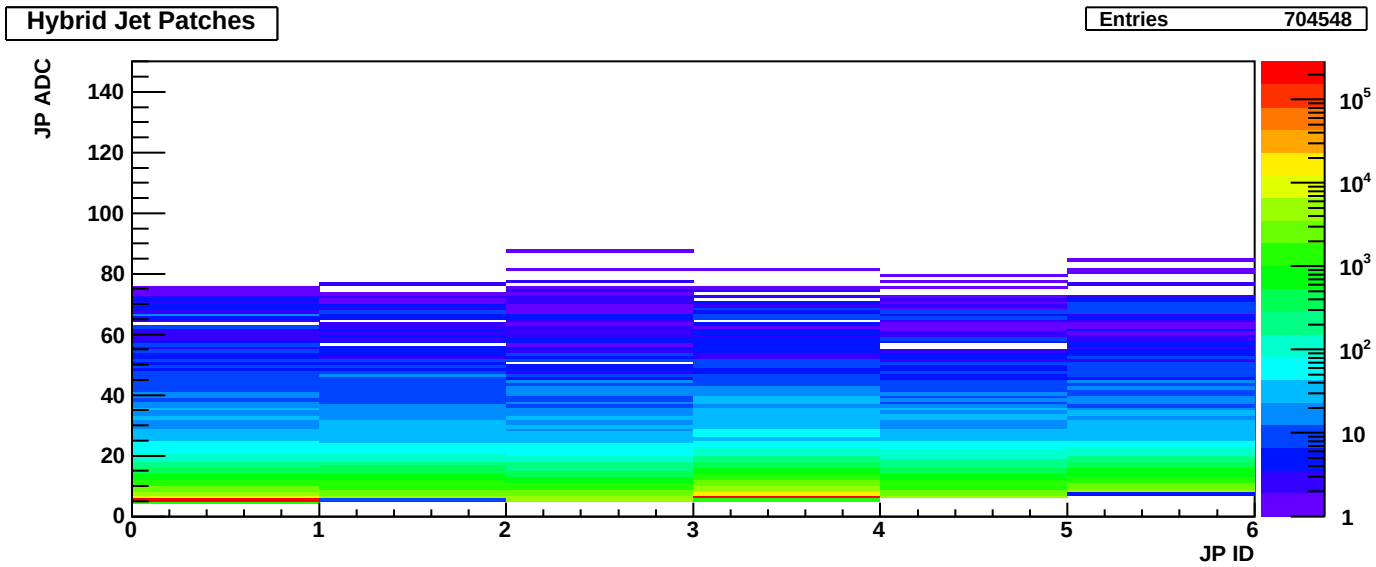

Barrel EMC L0 Input - Patch Sum
Entries 1.056822e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

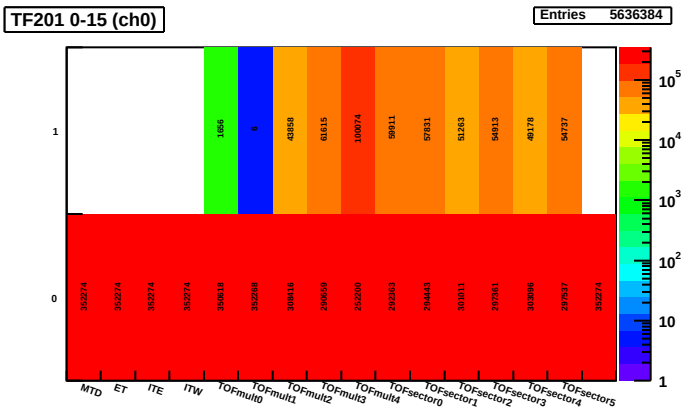

Endcap EMC L0 Input - High Tower
Entries 3.170466e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

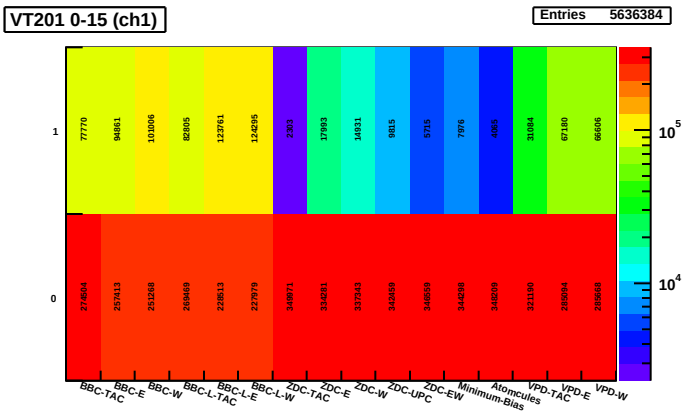

Endcap EMC L0 Input - Patch Sum
Entries 3.170466e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

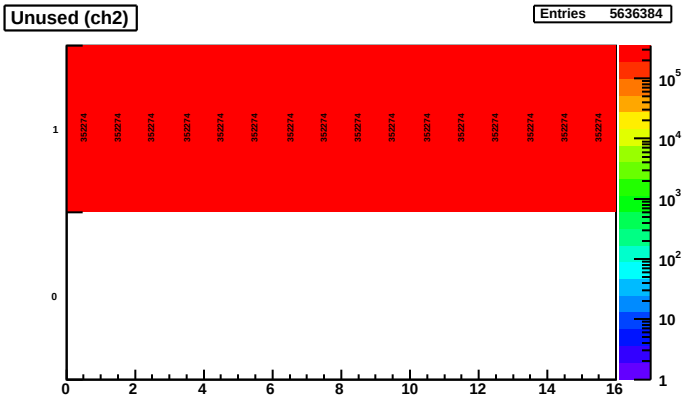

Barrel Jet Patches
Entries 6340932
JP ADC
JP ID

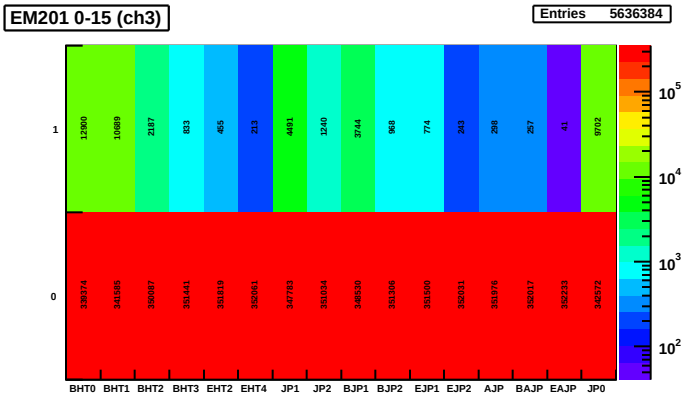

Endcap Jet Patches
Entries 2113644
JP ADC
JP ID

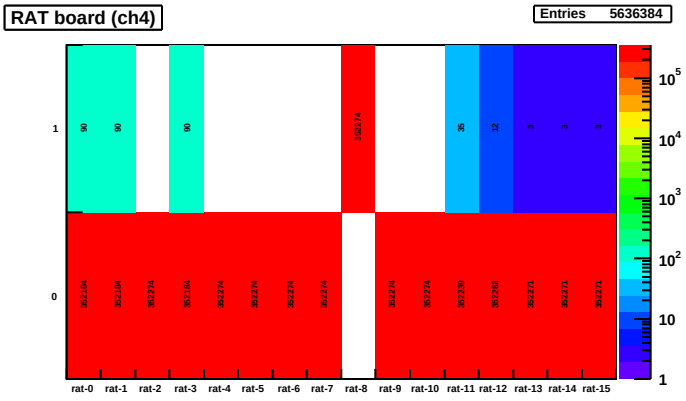

Hybrid Jet Patches
Entries 704548
JP ADC
JP ID

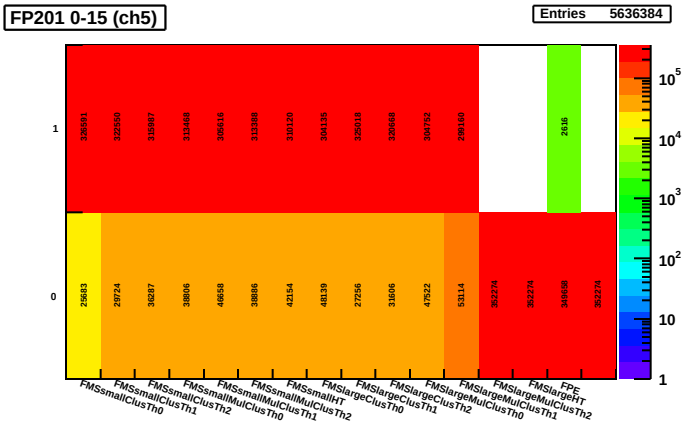

TF201 0-15 (ch0)
Entries 5636384
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

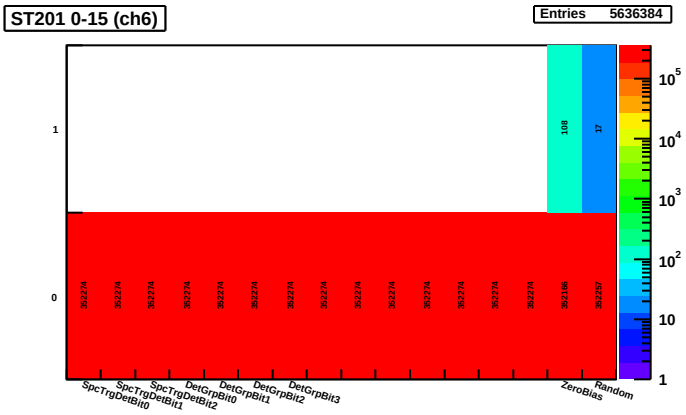

VT201 0-15 (ch1)
Entries 5636384
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

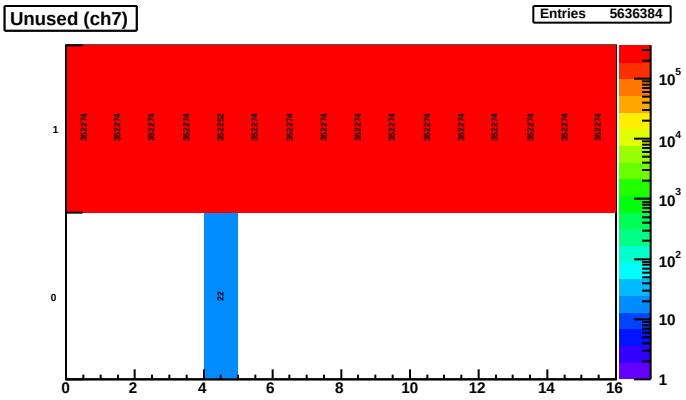

Unused (ch2)
Entries 5636384

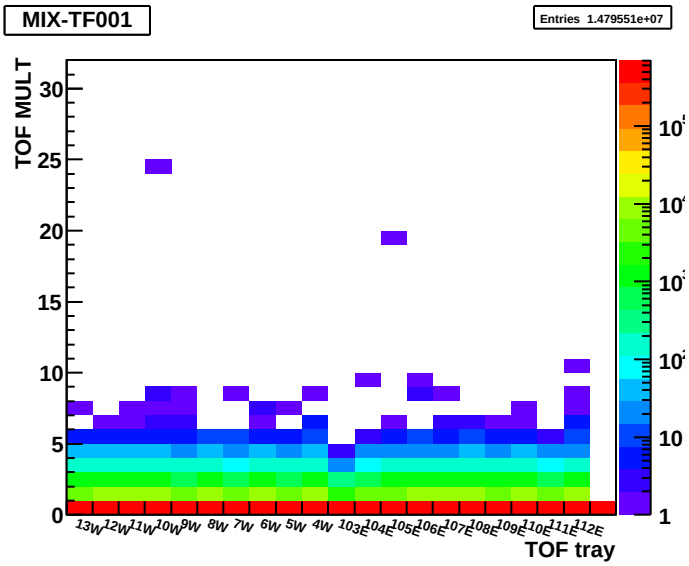

EM201 0-15 (ch3)
Entries 5636384
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

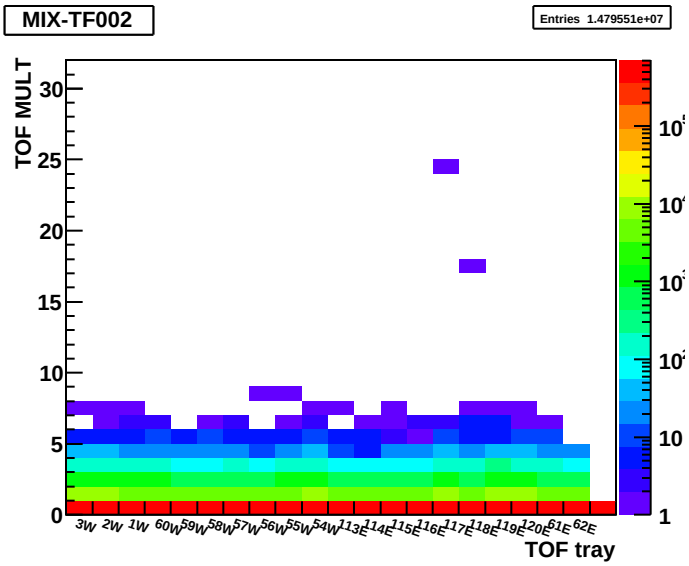

RAT board (ch4)
Entries 5636384
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

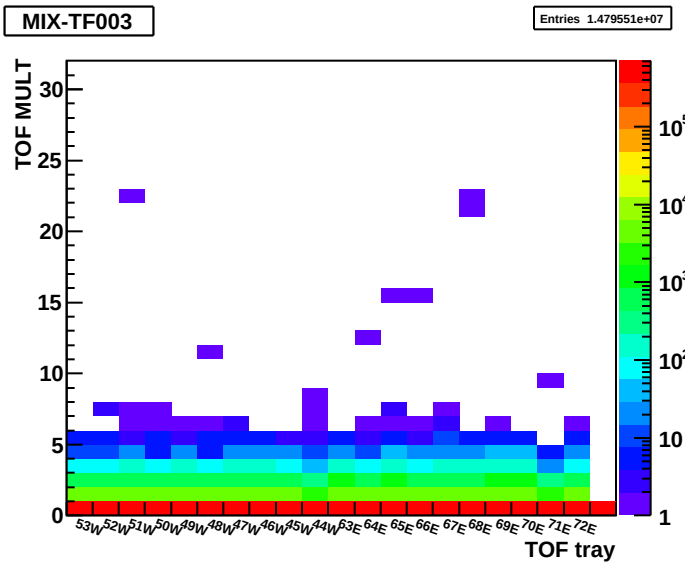

FP201 0-15 (ch5)
Entries 5636384
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

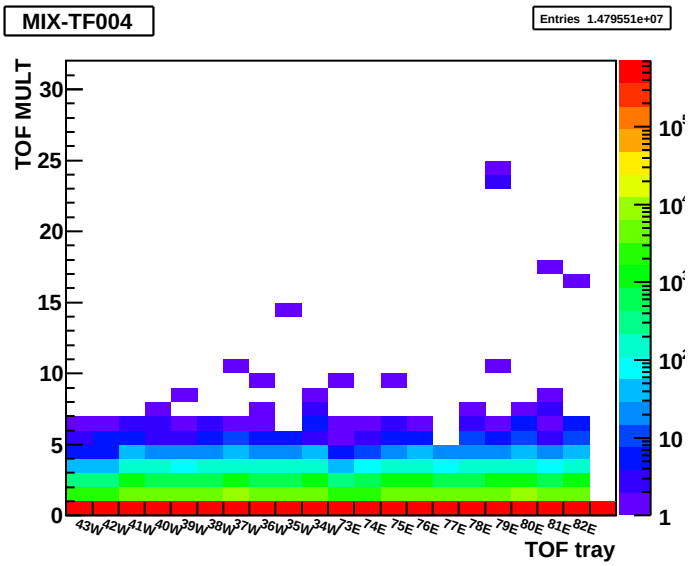

ST201 0-15 (ch6)
Entries 5636384
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

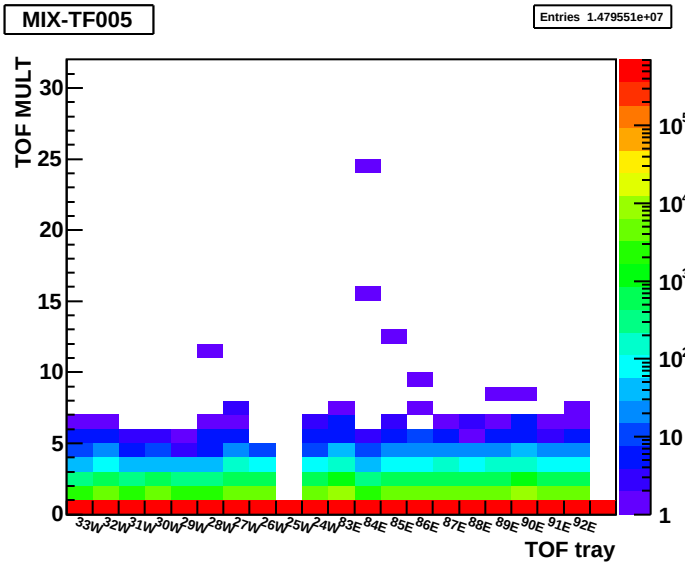

Unused (ch7)
Entries 5636384

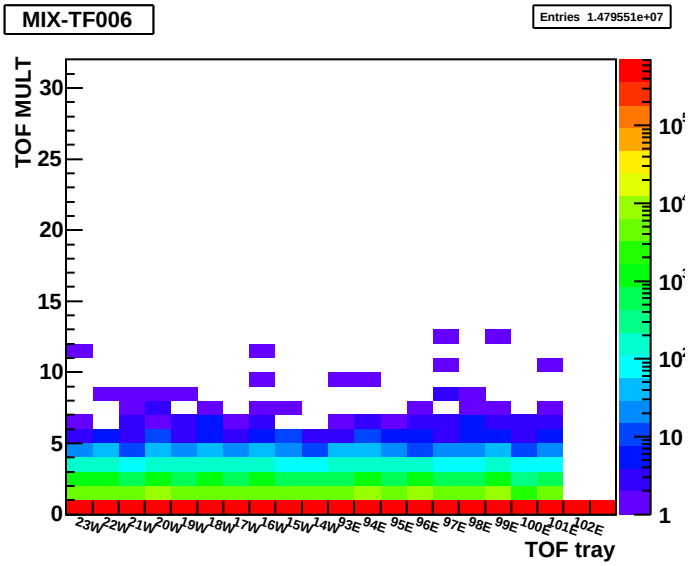

MIX-TF001
Entries 1.479551e+07
TOF MULT
TOF tray

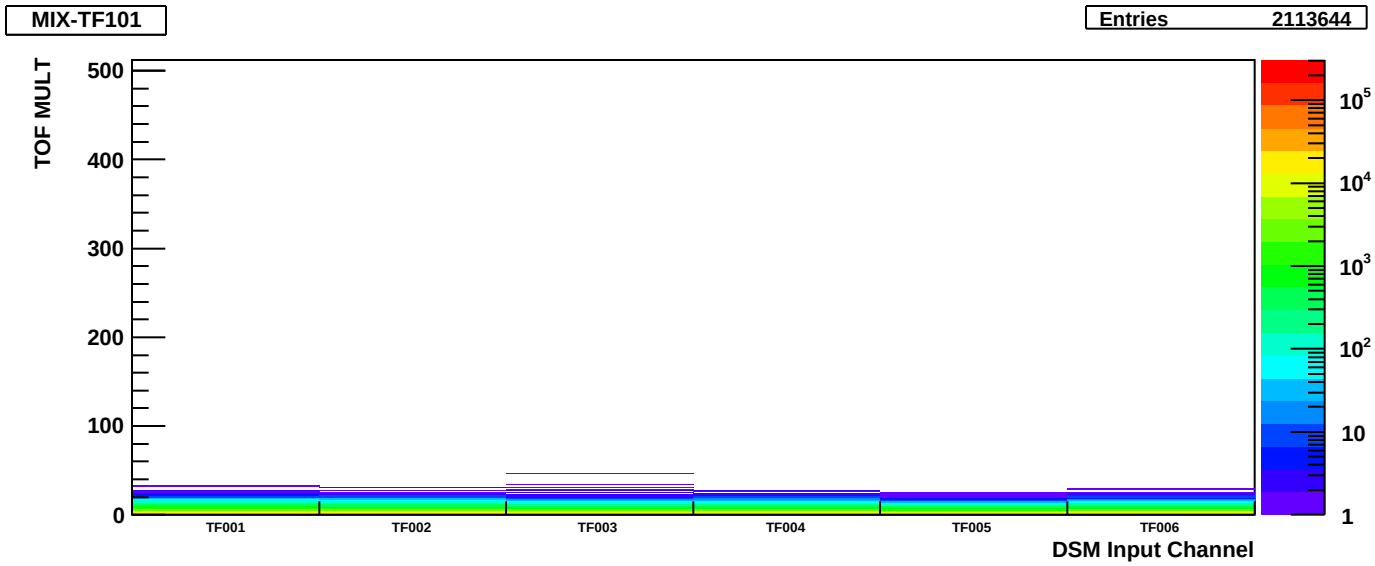

MIX-TF002
Entries 1.479551e+07
TOF MULT
TOF tray

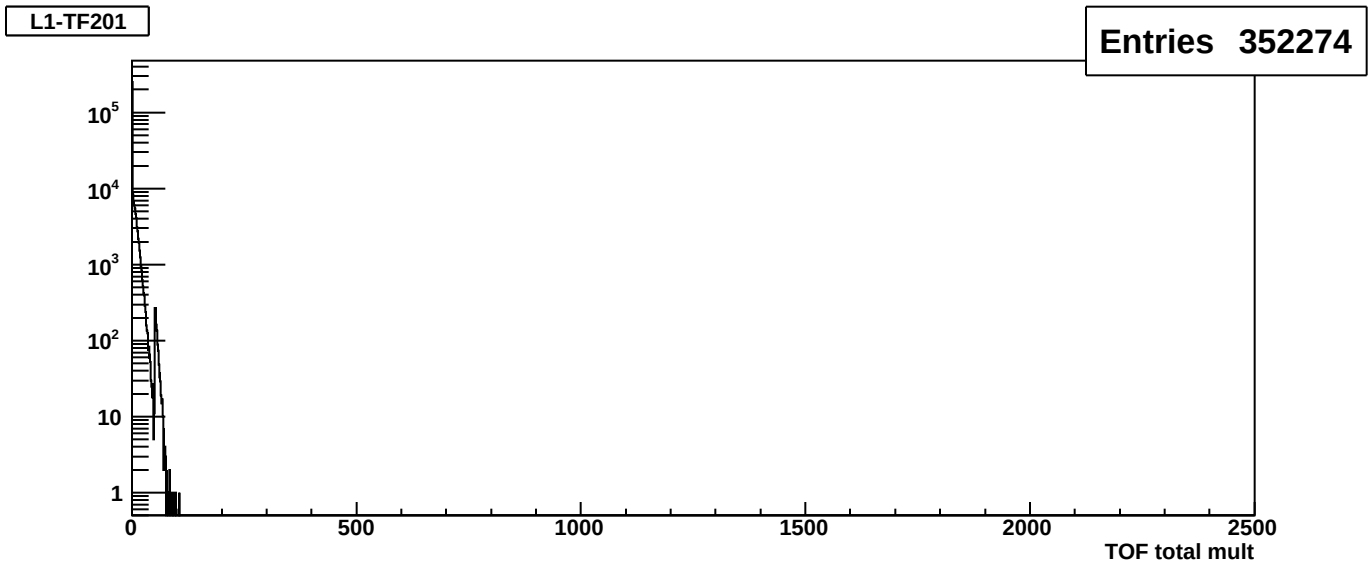

MIX-TF003
Entries 1.479551e+07
TOF MULT
TOF tray

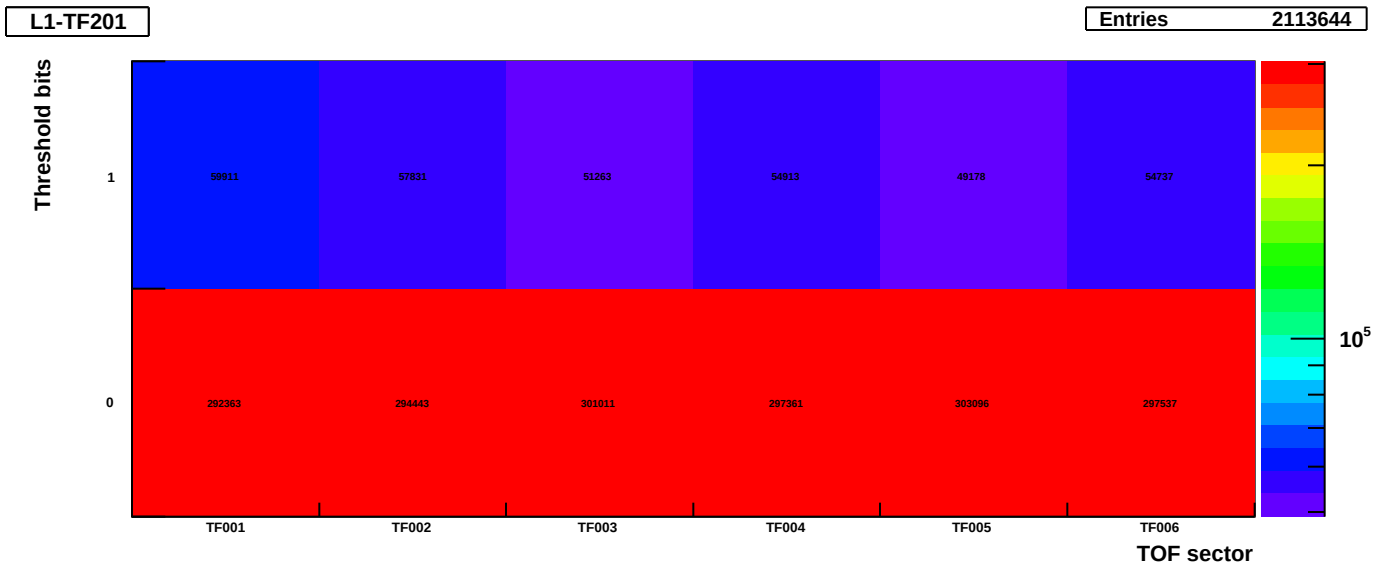

MIX-TF004
Entries 1.479551e+07
TOF MULT
TOF tray

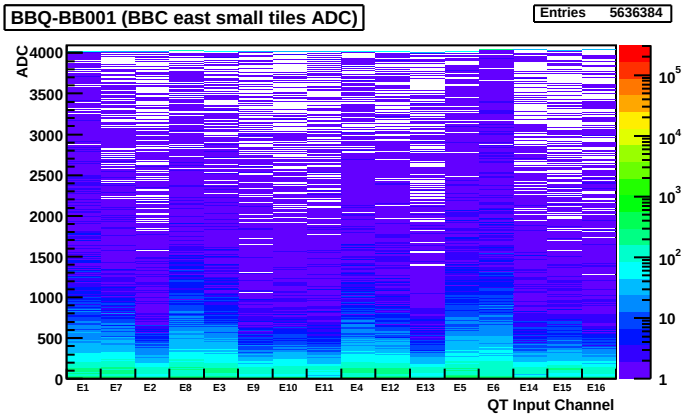

MIX-TF005
Entries 1.479551e+07
TOF MULT
TOF tray

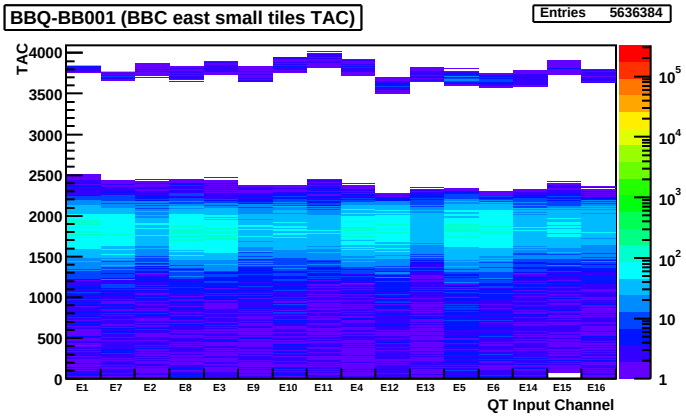

MIX-TF006
Entries 1.479551e+07
TOF MULT
TOF tray

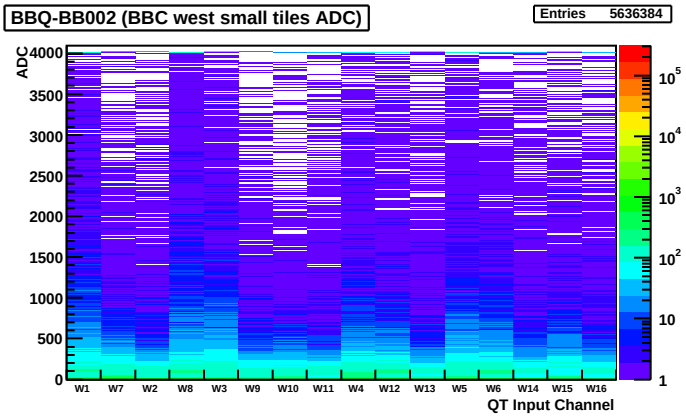

MIX-TF101
Entries 2113644
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

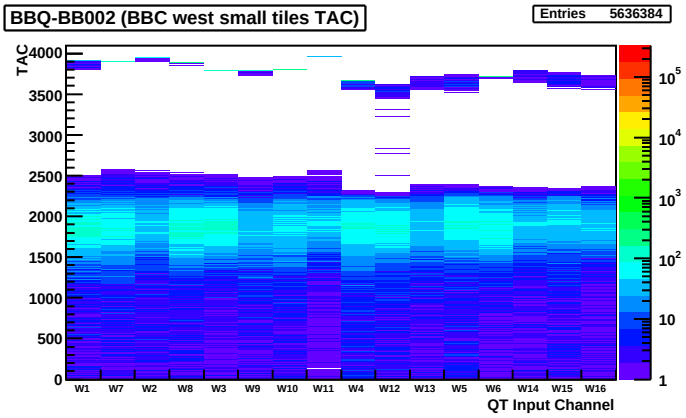

L1-TF201
Entries 352274
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

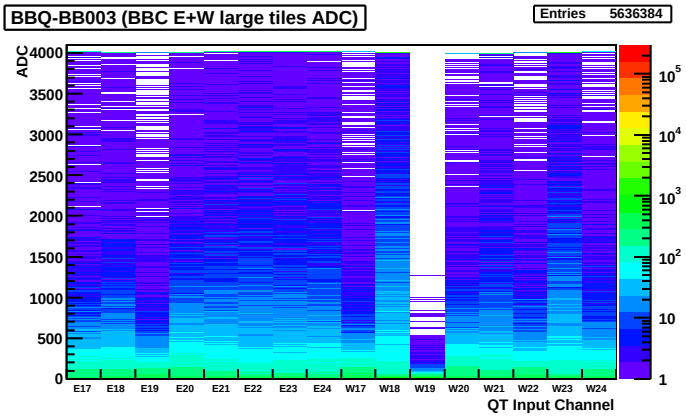

L1-TF201
Entries 2113644
Threshold bits
1
0
59911
57831
51263
54913
49178
54737
292363
294443
301011
297361
303096
297537
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

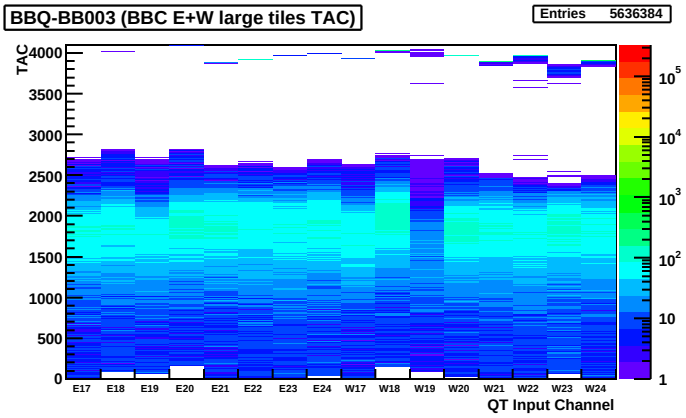

BBQ-BB001 (BBC east small tiles ADC)
Entries 5636384
ADC
QT Input Channel

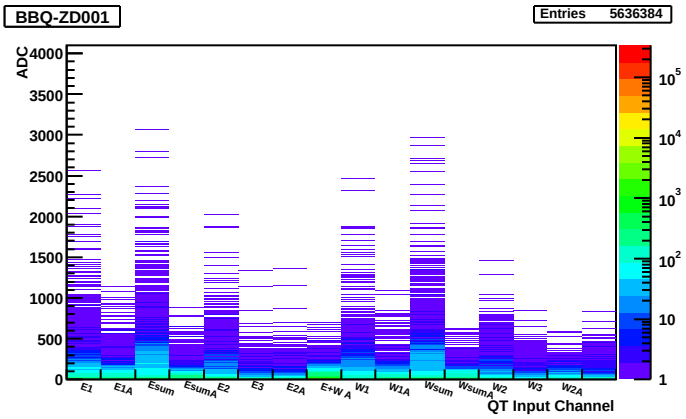

BBQ-BB001 (BBC east small tiles TAC)
Entries 5636384
TAC
QT Input Channel

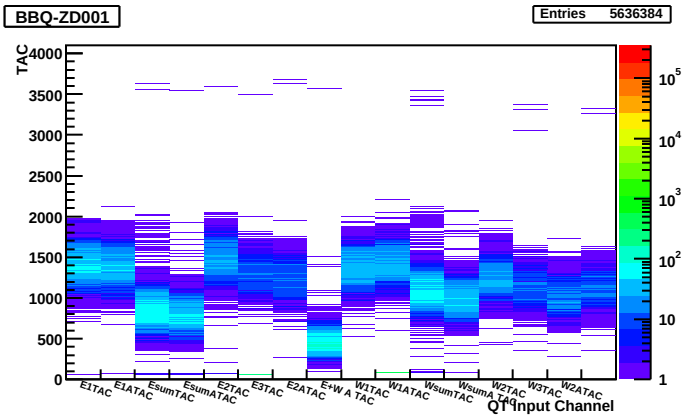

BBQ-BB002 (BBC west small tiles ADC)
Entries 5636384
ADC
QT Input Channel

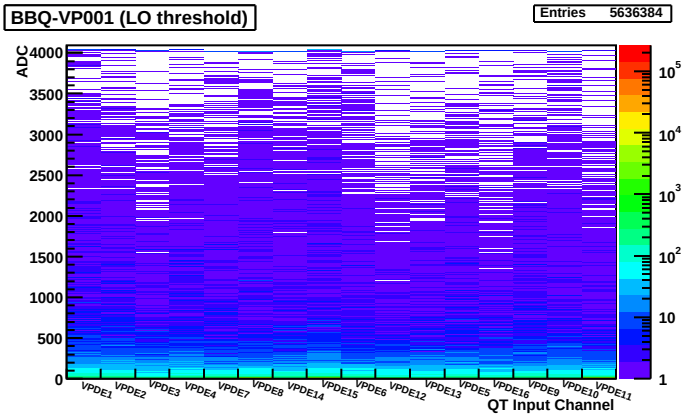

BBQ-BB002 (BBC west small tiles TAC)
Entries 5636384
TAC
QT Input Channel

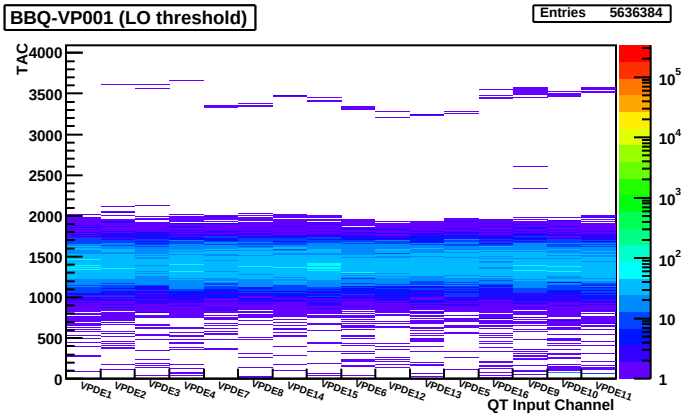

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5636384
ADC
QT Input Channel

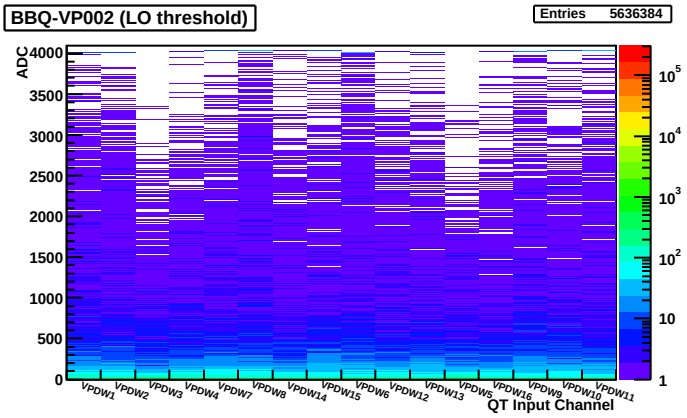

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5636384
TAC
QT Input Channel

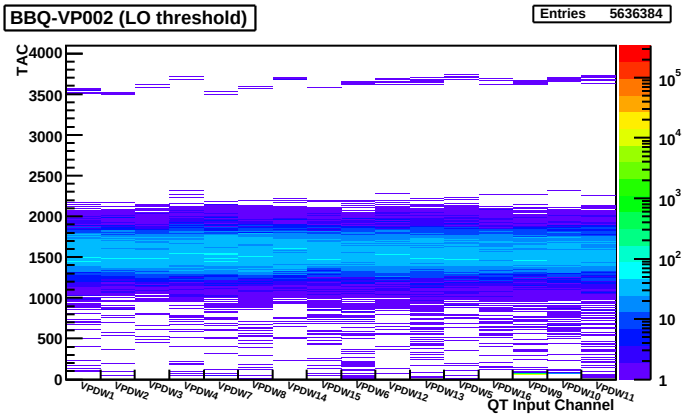

BBQ-ZD001
Entries 5636384
ADC
QT Input Channel

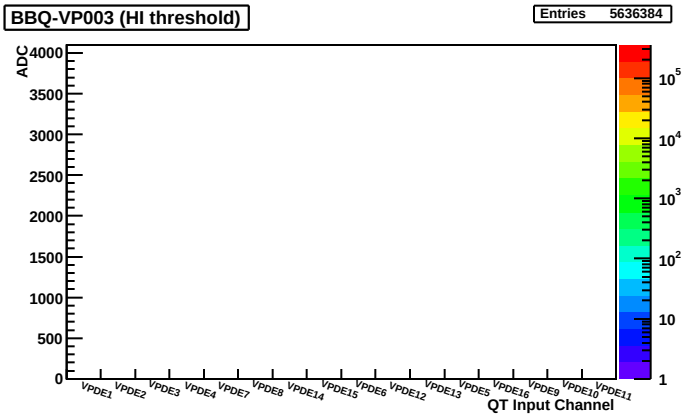

BBQ-ZD001
Entries 5636384
TAC
QT Input Channel

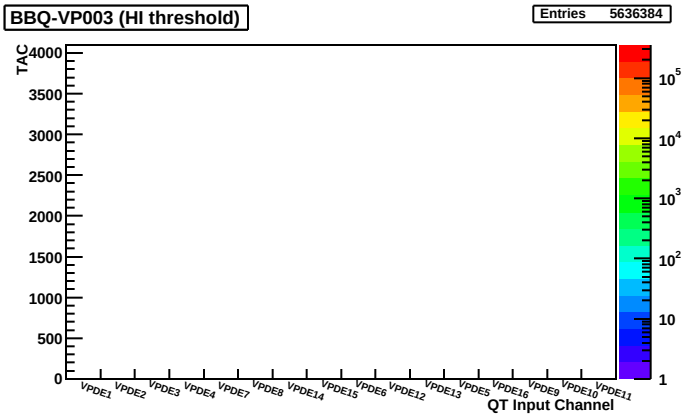

BBQ-VP001 (LO threshold)
Entries 5636384
ADC
QT Input Channel

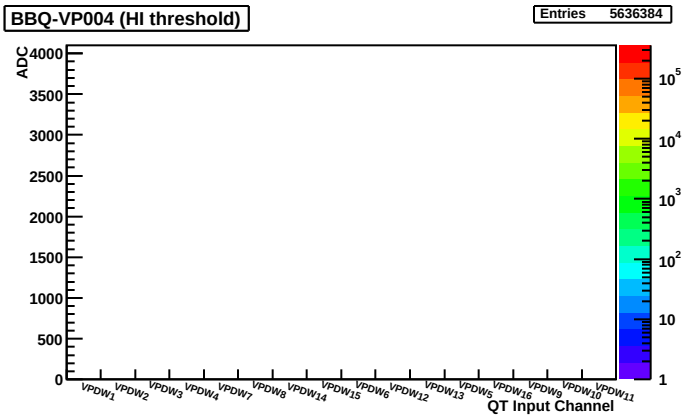

BBQ-VP001 (LO threshold)
Entries 5636384
TAC
QT Input Channel

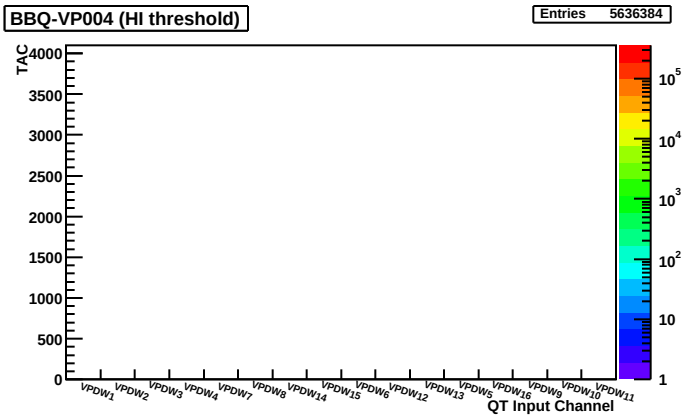

BBQ-VP002 (LO threshold)
Entries 5636384
ADC
QT Input Channel

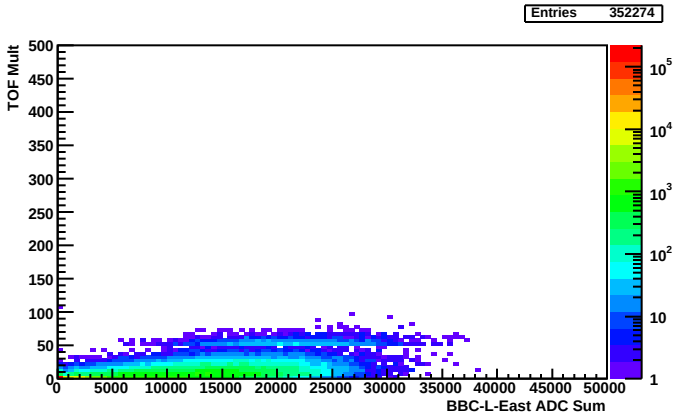

BBQ-VP002 (LO threshold)
Entries 5636384
TAC
QT Input Channel

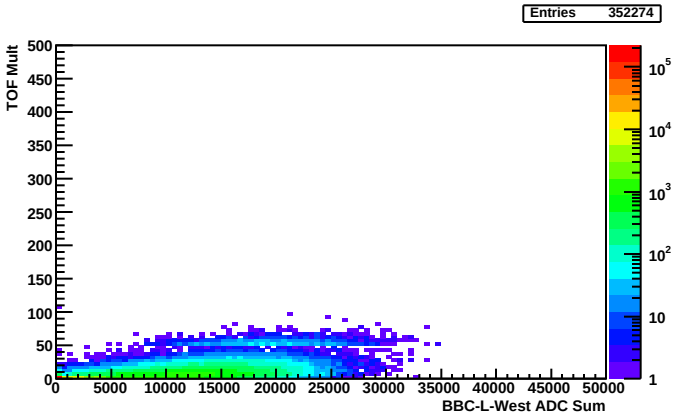

BBQ-VP003 (HI threshold)
Entries 5636384
ADC
QT Input Channel

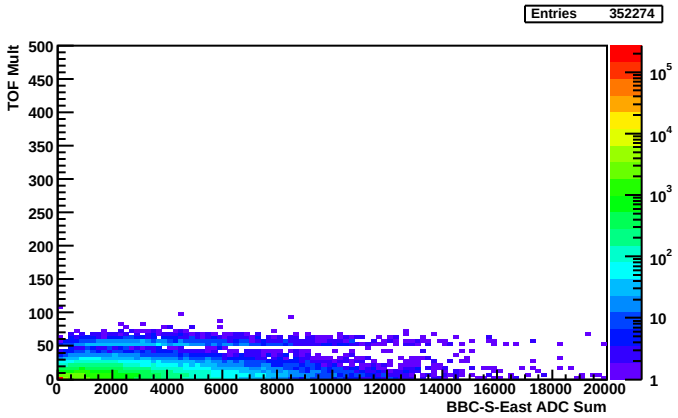

BBQ-VP003 (HI threshold)
Entries 5636384
TAC
QT Input Channel

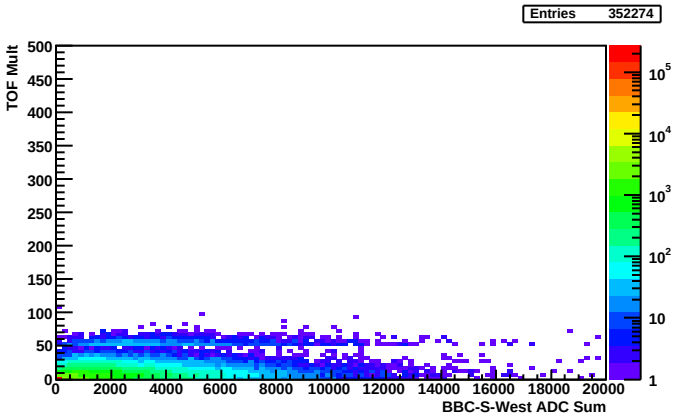

BBQ-VP004 (HI threshold)
Entries 5636384
ADC
QT Input Channel

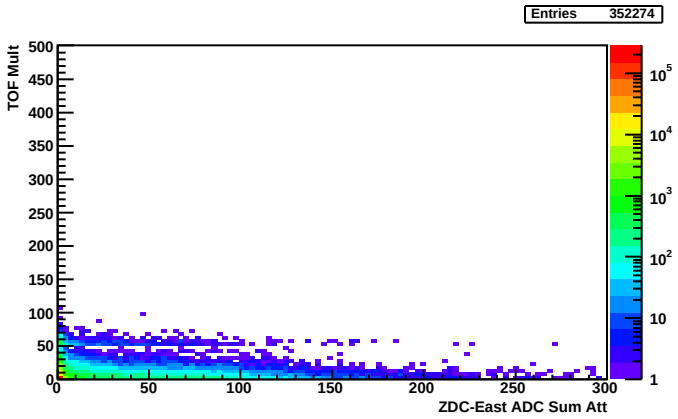

BBQ-VP004 (HI threshold)
Entries 5636384
TAC
QT Input Channel

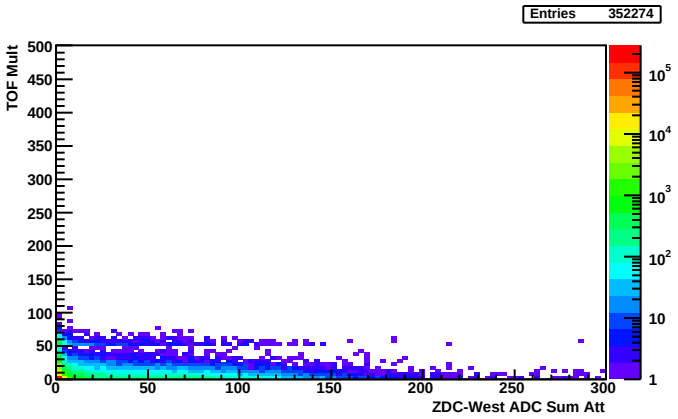

Entries 352274
TOF Mult
BBC-L-East ADC Sum

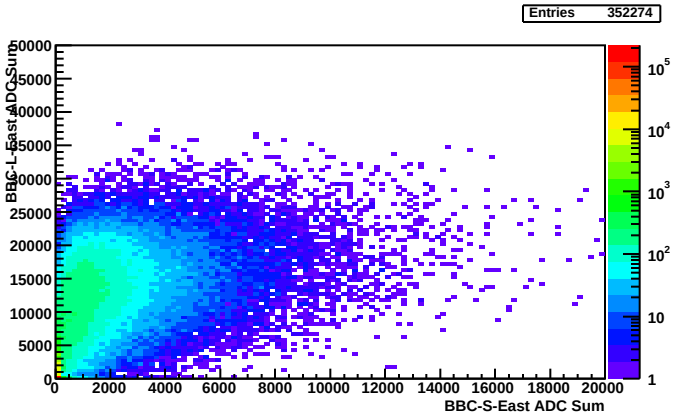

Entries 352274
TOF Mult
BBC-L-West ADC Sum

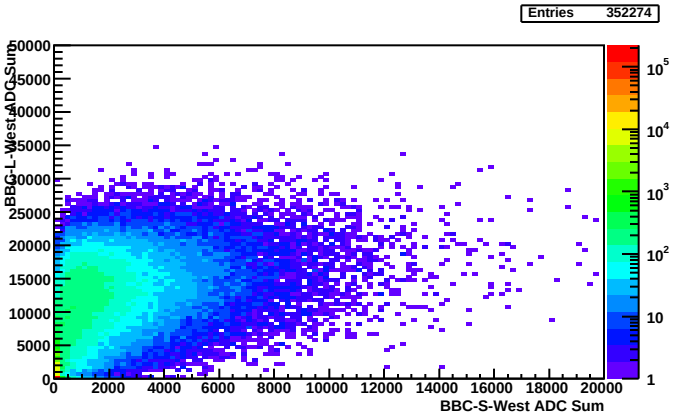

Entries 352274
TOF Mult
BBC-S-East ADC Sum

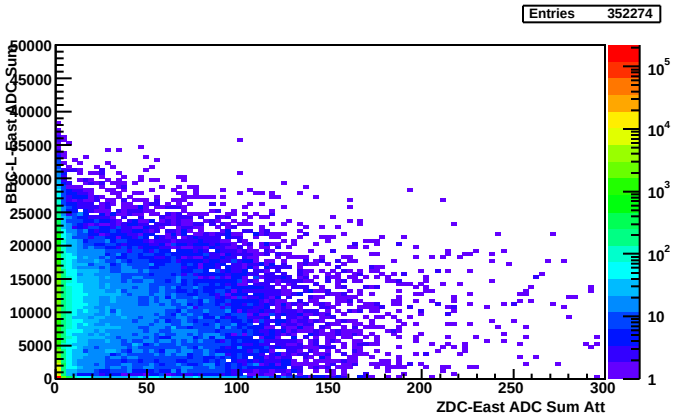

Entries 352274
TOF Mult
BBC-S-West ADC Sum

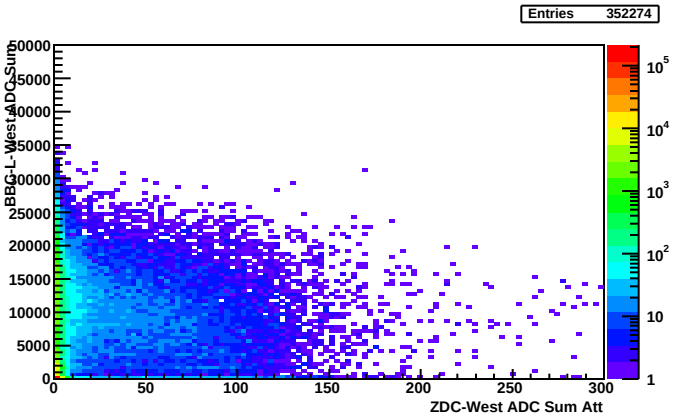

Entries 352274
TOF Mult
ZDC-East ADC Sum Att

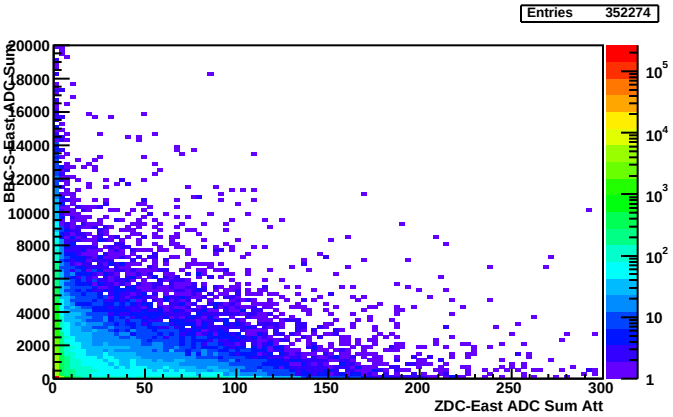

Entries 352274
TOF Mult
ZDC-West ADC Sum Att

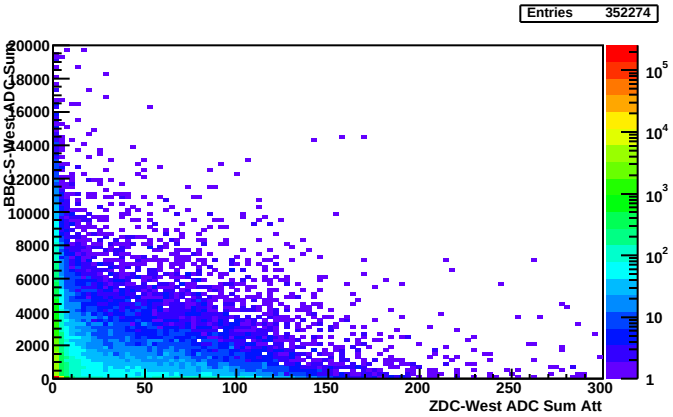

Entries 352274
BBC-L-East ADC Sum
BBC-S-East ADC Sum

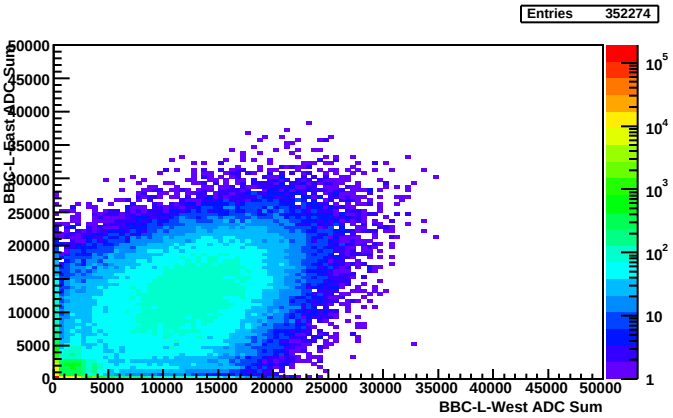

Entries 352274
BBC-L-West ADC Sum
BBC-S-West ADC Sum

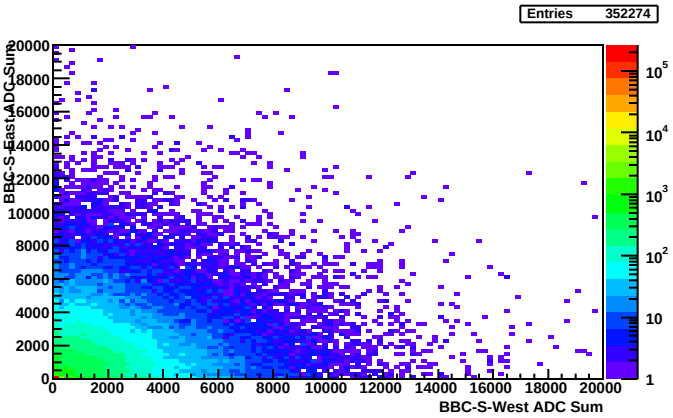

Entries 352274
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

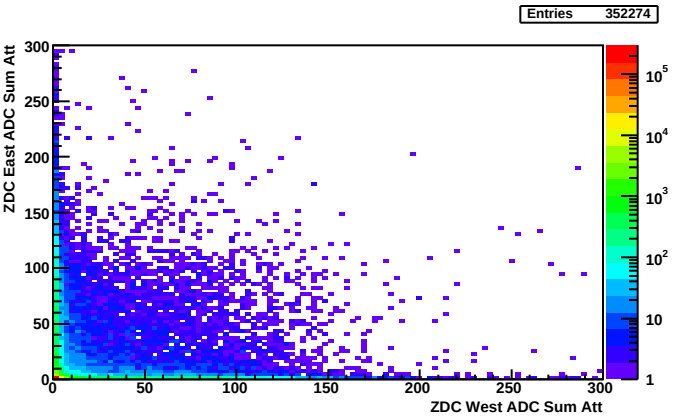

Entries 352274
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

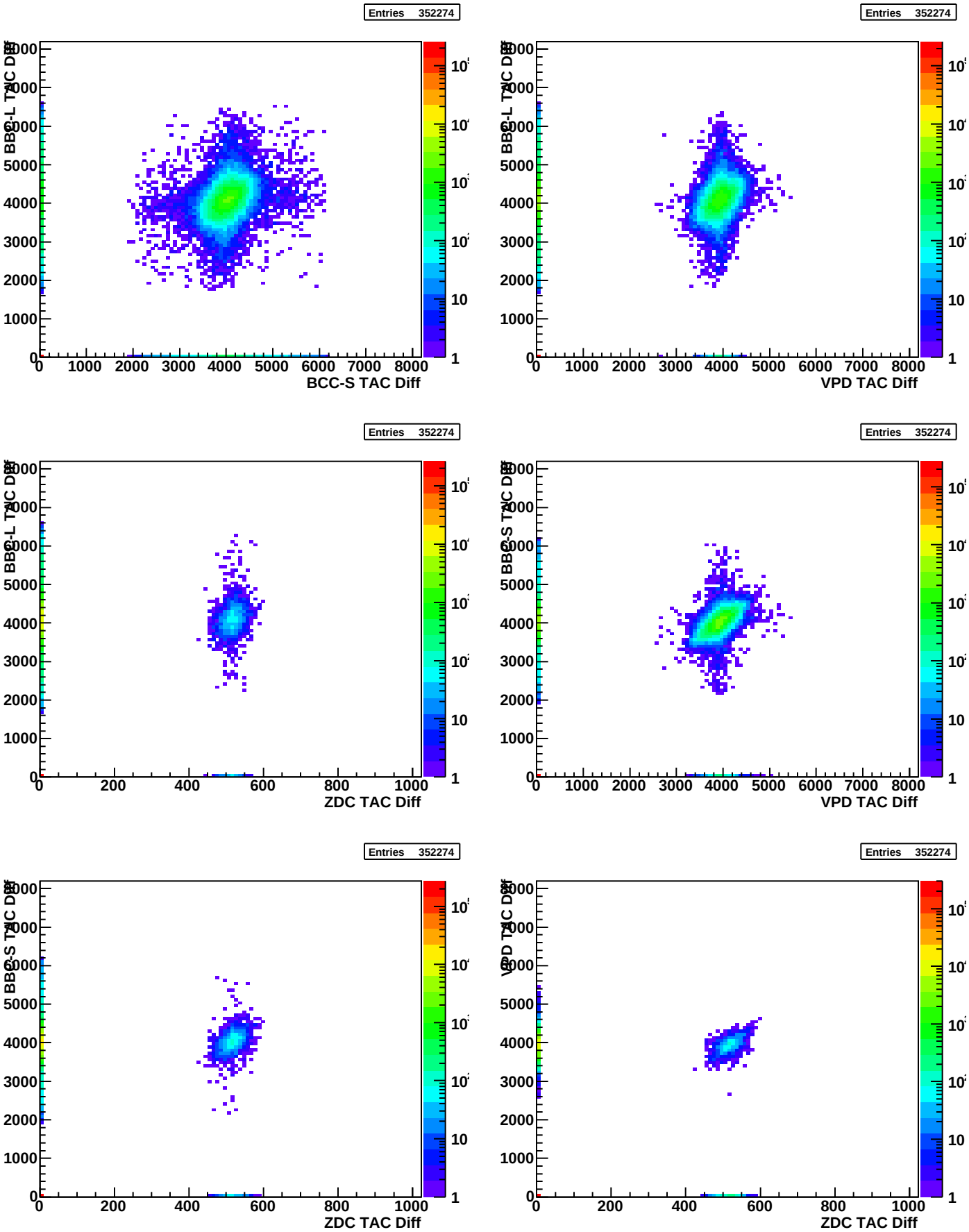

Entries 352274
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

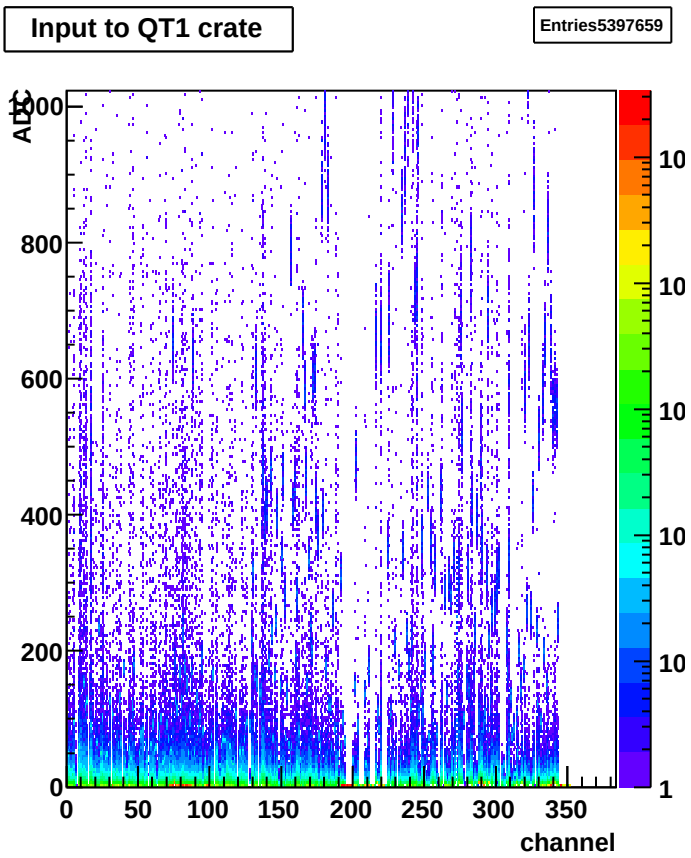

Entries 352274
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

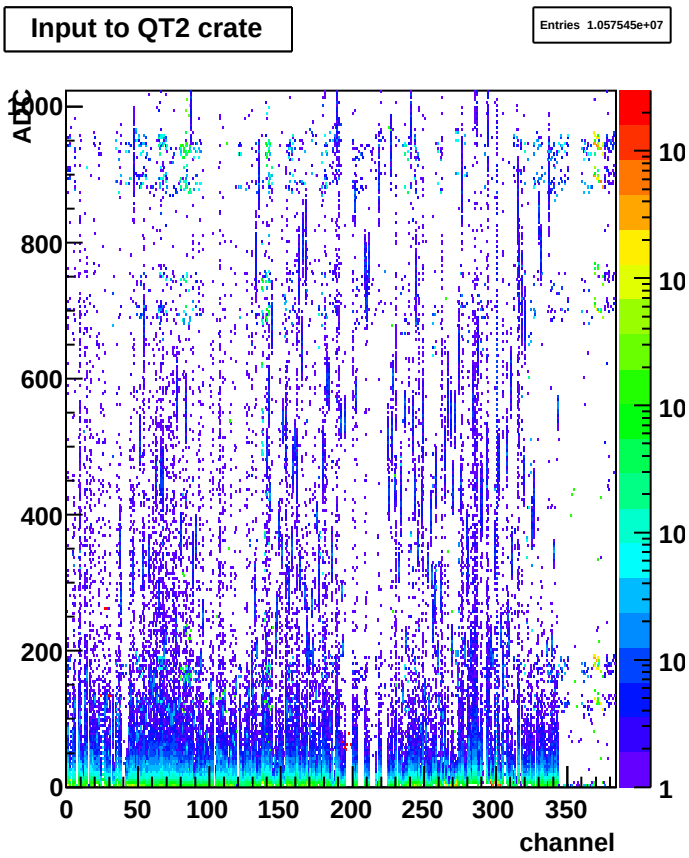

Entries 352274
BBC-L-East ADC Sum
BBC-L-West ADC Sum

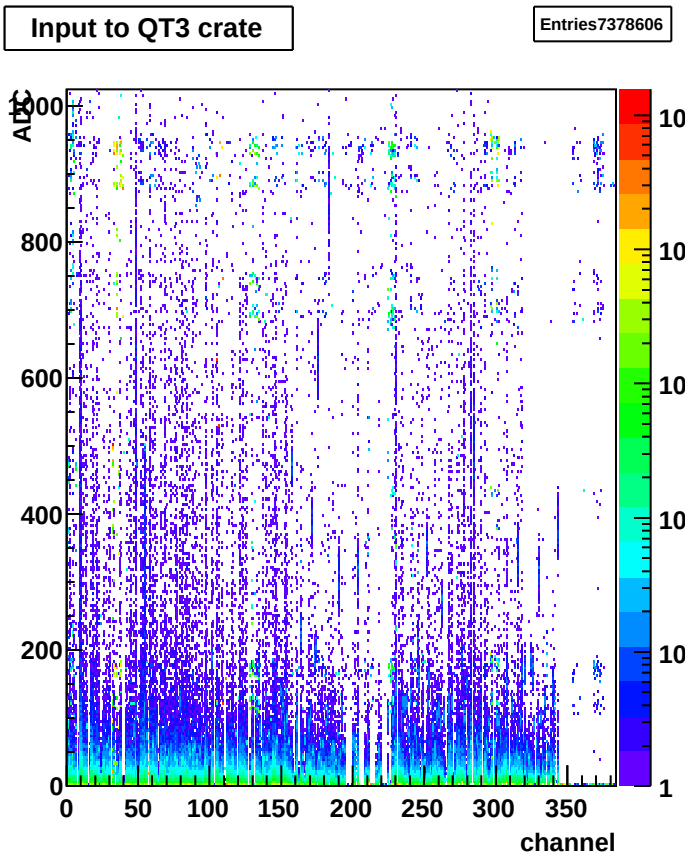

Entries 352274
BBC-S-East ADC Sum
BBC-S-West ADC Sum

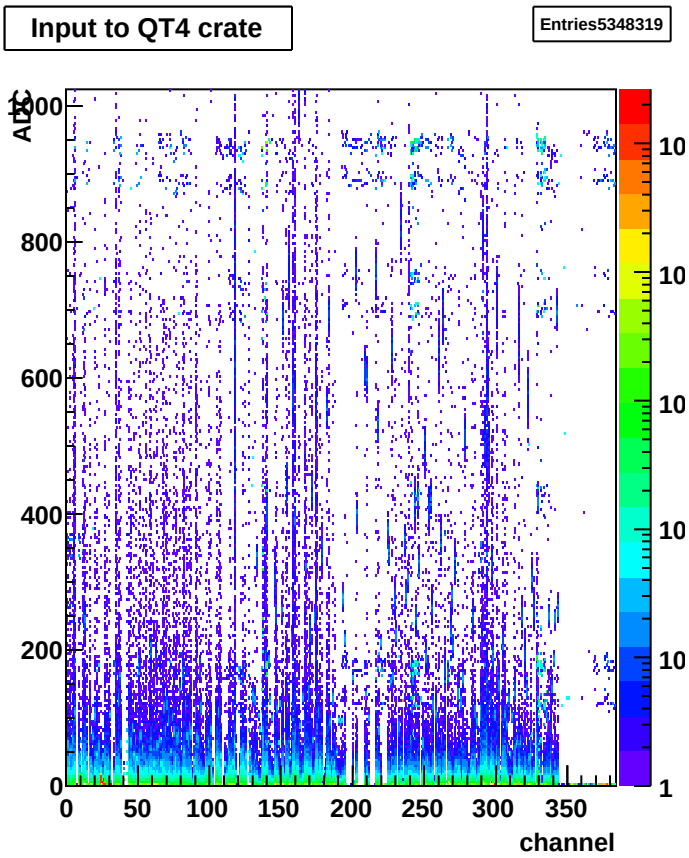

Entries 352274
ZDC East ADC Sum Att
ZDC West ADC Sum Att

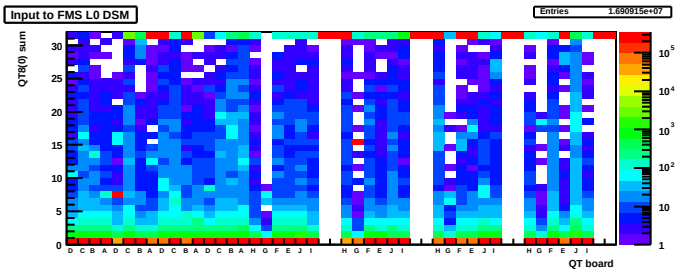

Entries 352274
BBC-L TAC Diff
BCC-S TAC Diff
Entries 352274
BBC-L TAC Diff
VPD TAC Diff
Entries 352274
BBC-L TAC Diff
ZDC TAC Diff
Entries 352274
BBC-S TAC Diff
VPD TAC Diff
Entries 352274
BBC-S TAC Diff
ZDC TAC Diff
Entries 352274
VPD TAC Diff
ZDC TAC Diff

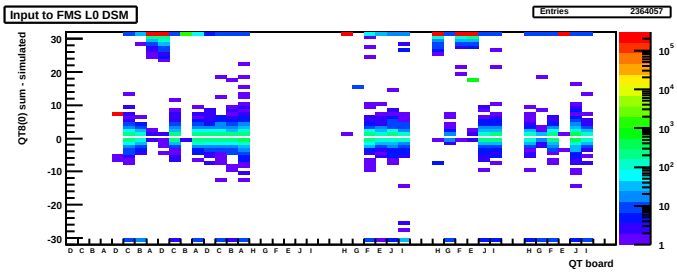

Input to QT1 crate
Entries5397659
ADC
channel

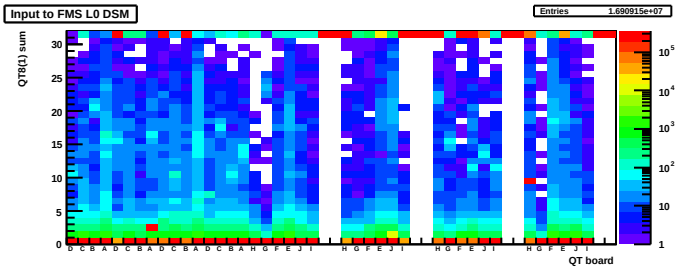

Input to QT2 crate
Entries 1.057545e+07
ADC
channel

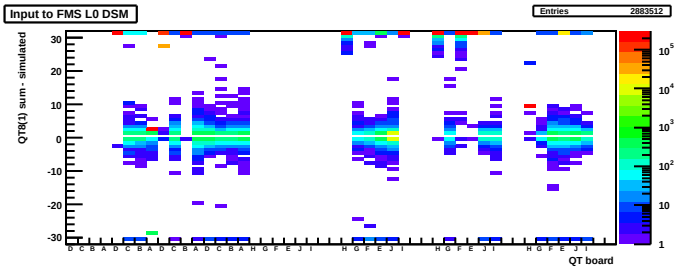

Input to QT3 crate
Entries7378606
ADC
channel

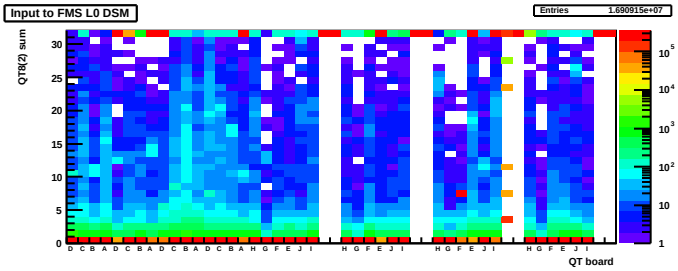

Input to QT4 crate
Entries5348319
ADC
channel

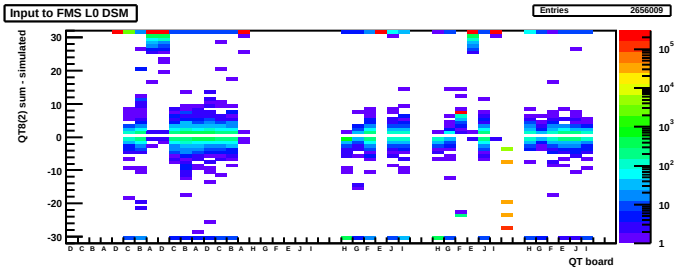

Input to FMS L0 DSM
Entries 1.690915e+07
QT8(0) sum
QT board

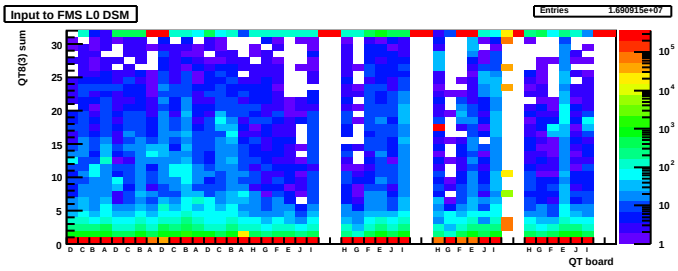

Input to FMS L0 DSM
Entries 2364057
QT8(0) sum - simulated
QT board

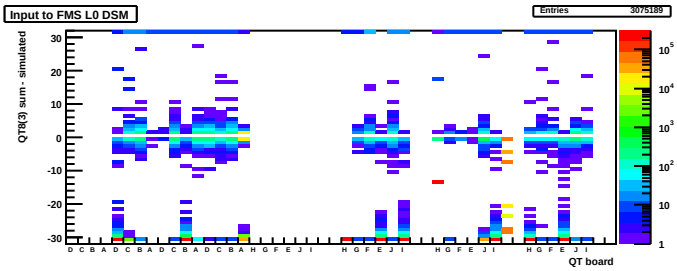

Input to FMS L0 DSM
Entries 1.690915e+07
QT8(1) sum
QT board

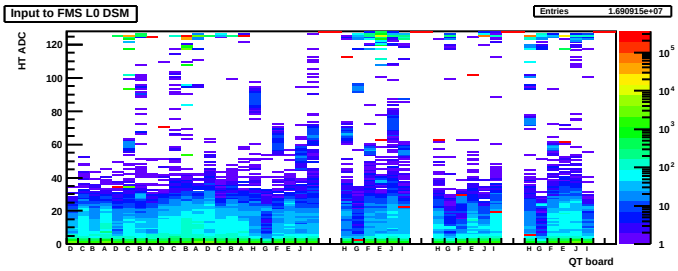

Input to FMS L0 DSM
Entries 2883512
QT8(1) sum - simulated
QT board

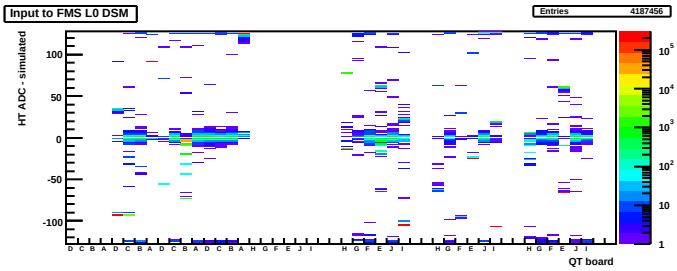

Input to FMS L0 DSM
Entries 1.690915e+07
QT8(2) sum
QT board

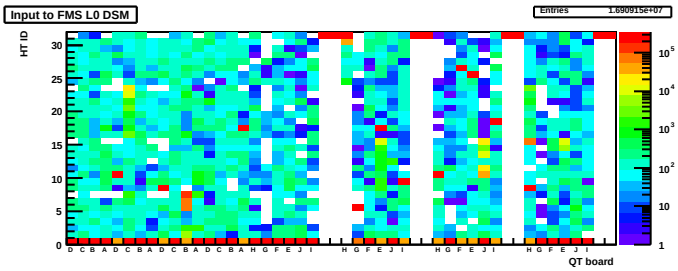

Input to FMS L0 DSM
Entries 2656009
QT8(2) sum - simulated
QT board

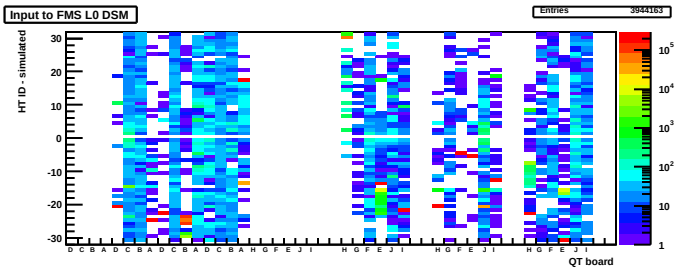

Input to FMS L0 DSM
Entries 1.690915e+07
QT8(3) sum
QT board

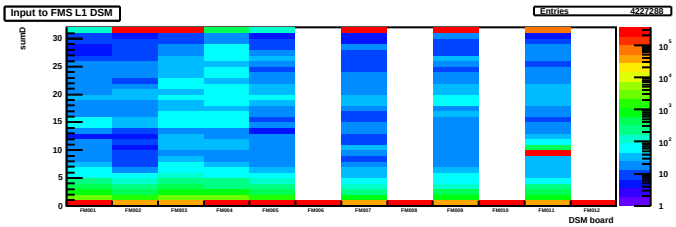

Input to FMS L0 DSM
Entries 3075189
QT8(3) sum - simulated
QT board

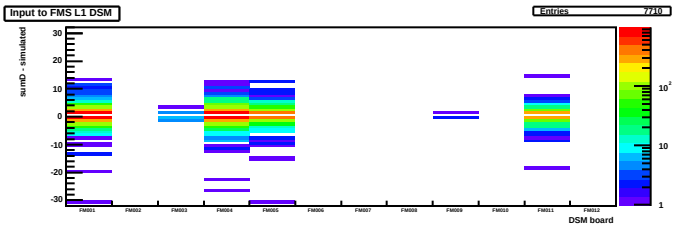

Input to FMS L0 DSM
Entries 1.690915e+07
HT ADC
QT board

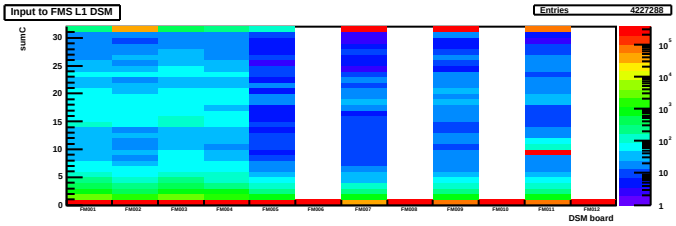

Input to FMS L0 DSM
Entries 4187456
HT ADC - simulated
QT board

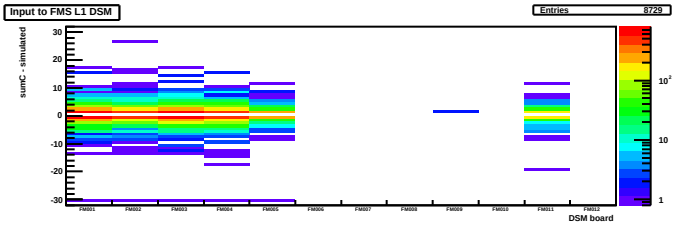

Input to FMS L0 DSM
Entries 1.690915e+07
HT ID
QT board

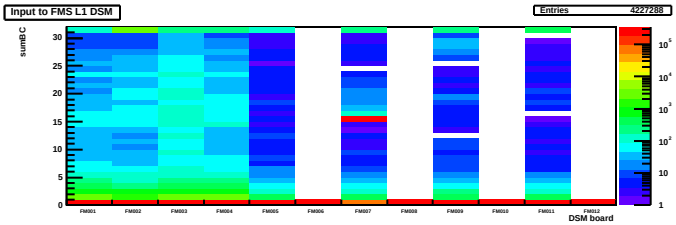

Input to FMS L0 DSM
Entries 3944163
HT ID - simulated
QT board

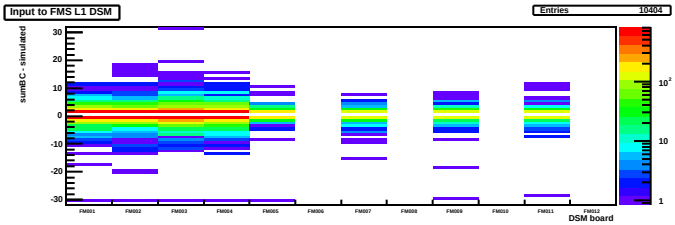

Input to FMS L1 DSM
Entries 4227288
sumD
DSM board

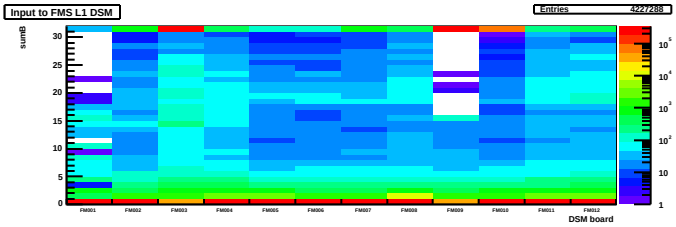

Input to FMS L1 DSM
Entries 7710
sumD - simulated
DSM board

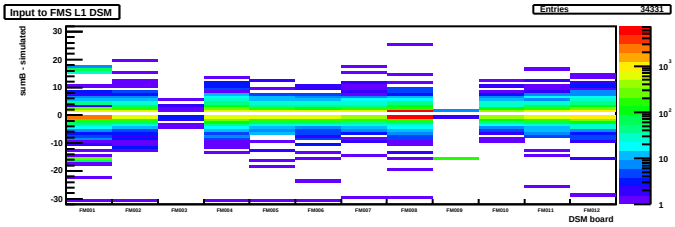

Input to FMS L1 DSM
Entries 4227288
sumC
DSM board

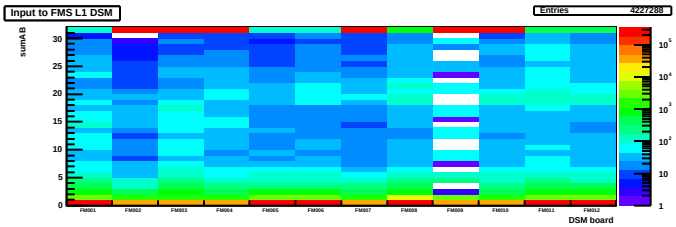

Input to FMS L1 DSM
Entries 8729
sumC - simulated
DSM board

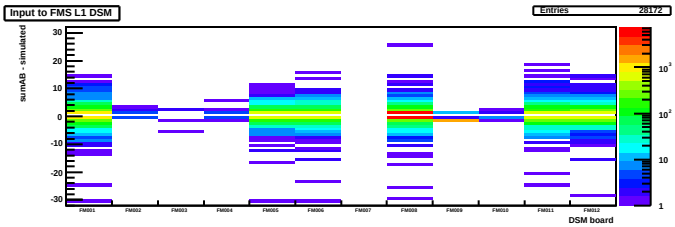

Input to FMS L1 DSM
Entries 4227288
sumBC
DSM board

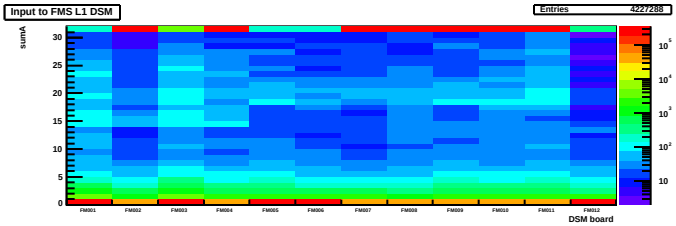

Input to FMS L1 DSM
Entries 10404
sumBC - simulated
DSM board

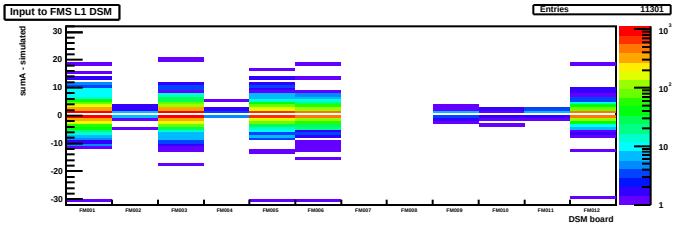

Input to FMS L1 DSM
Entries 4227288
sumB
DSM board

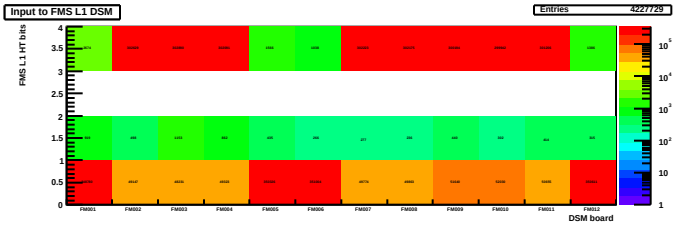

Input to FMS L1 DSM
Entries 34331
sumB - simulated
DSM board

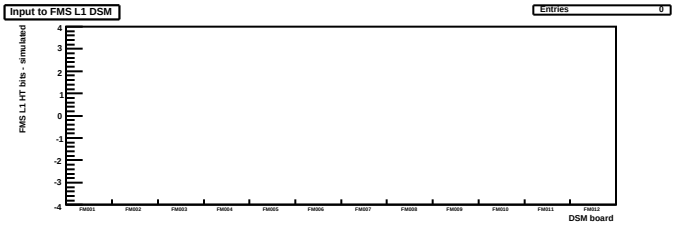

Input to FMS L1 DSM
Entries 4227288
sumAB
DSM board

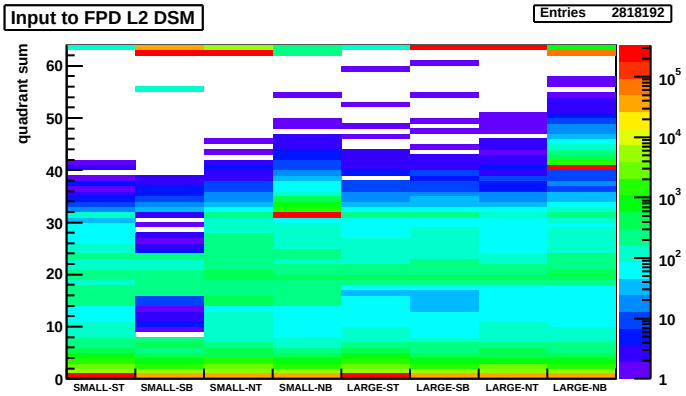

Input to FMS L1 DSM
Entries 28172
sumAB - simulated
DSM board

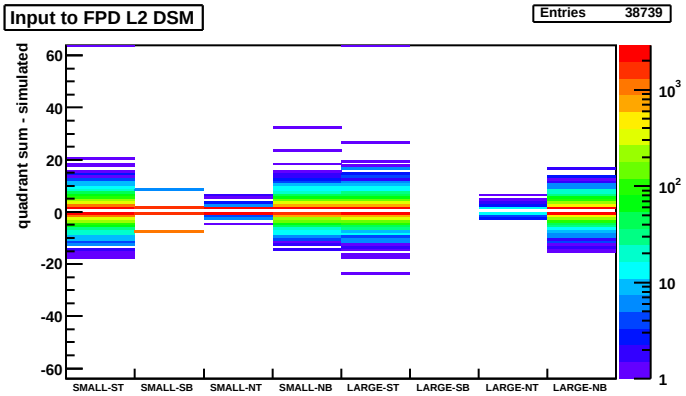

Input to FMS L1 DSM
Entries 4227288
sumA
DSM board

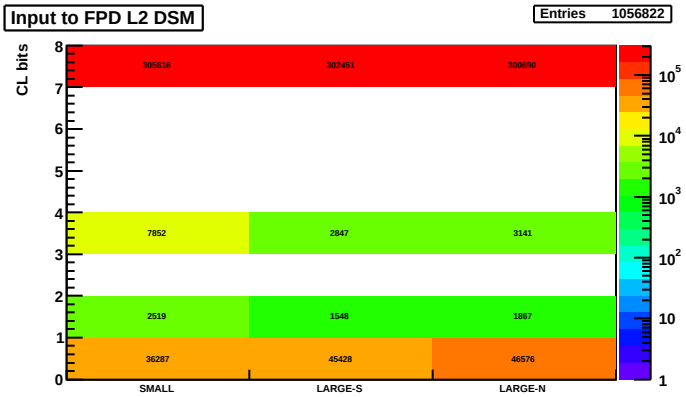

Input to FMS L1 DSM
Entries 11301
sumA - simulated
DSM board

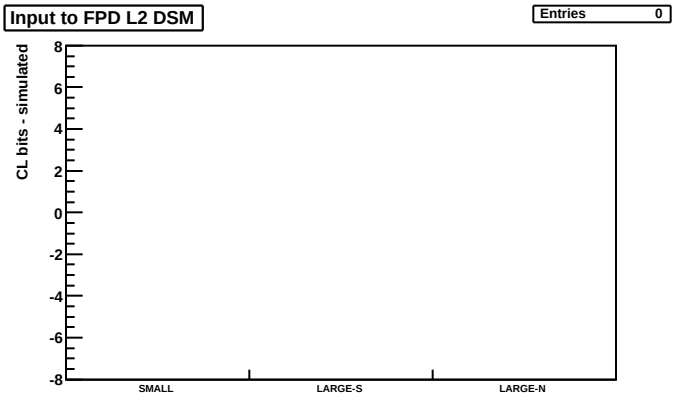

Input to FMS L1 DSM
Entries 4227729
FMS L1 HT bits
DSM board

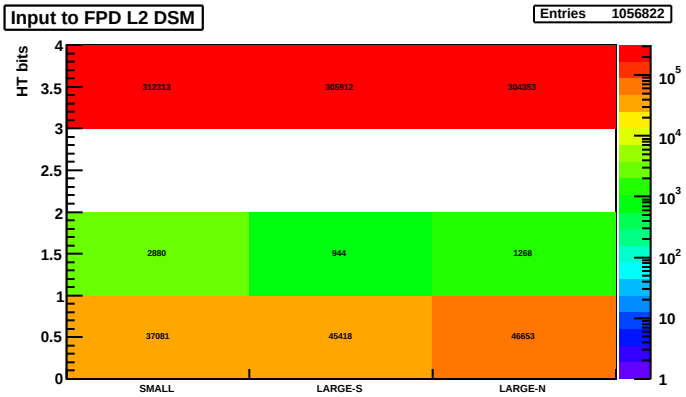

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

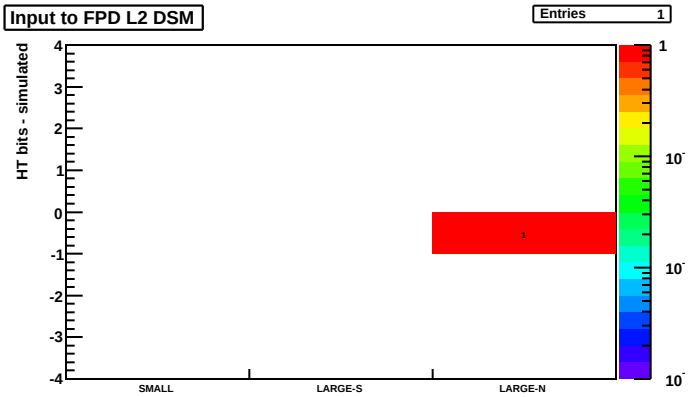

Input to FPD L2 DSM
Entries 2818192
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

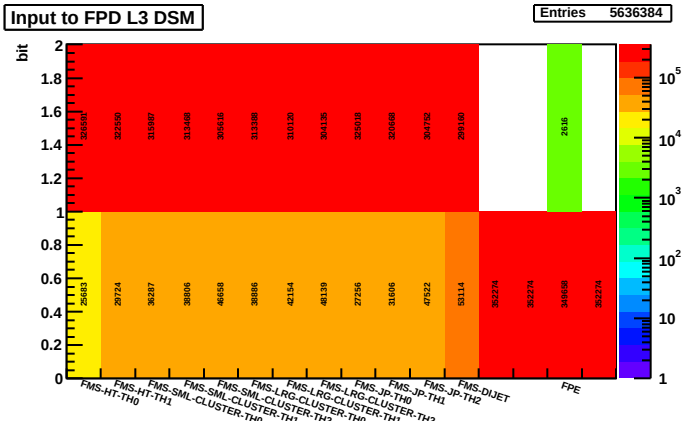

Input to FPD L2 DSM
Entries 38739
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

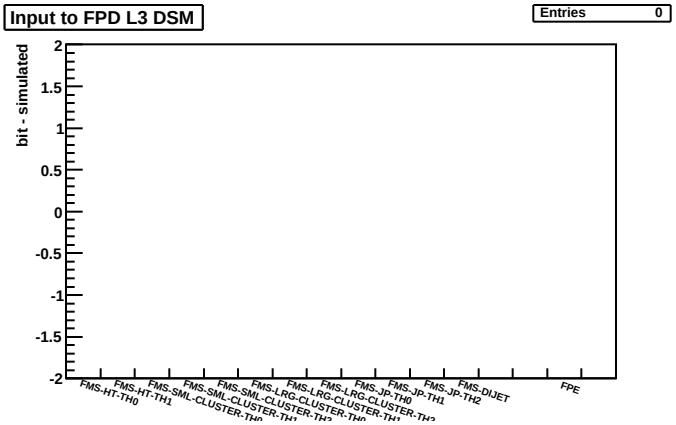

Input to FPD L2 DSM
Entries 1056822
CL bits
305616
302451
300690
7852
2847
3141
2519
1548
1867
36287
45428
46576
SMALL
LARGE-S
LARGE-N

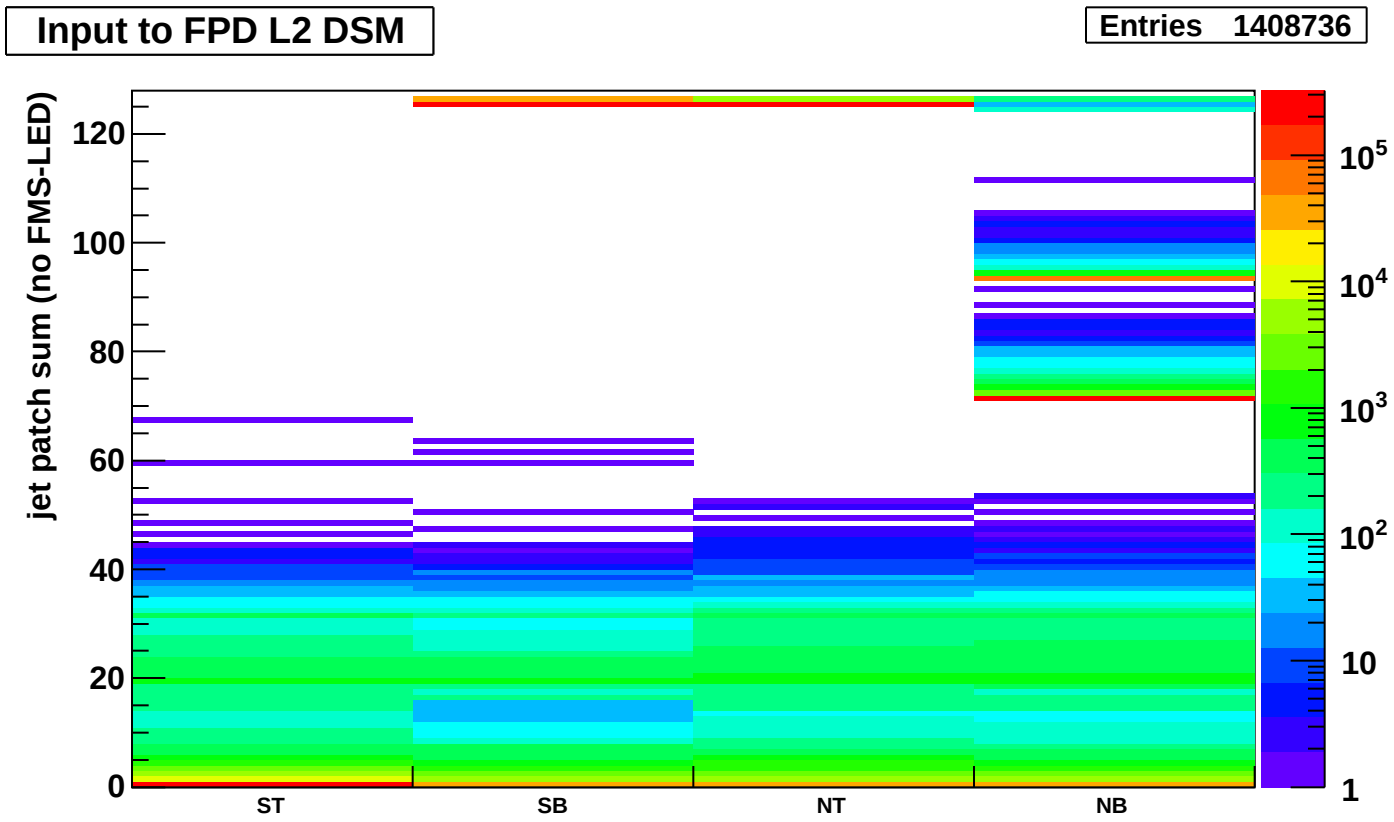

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

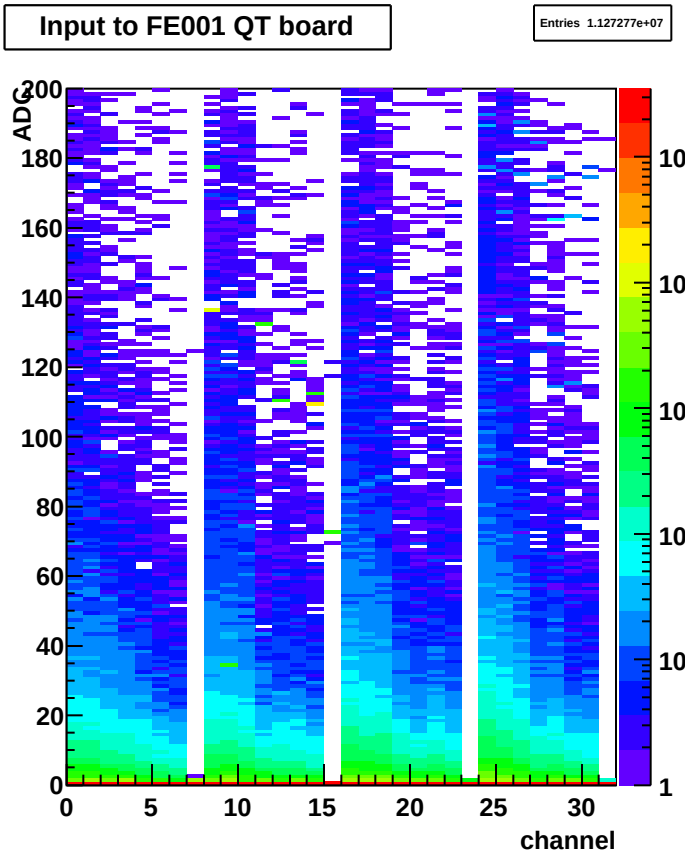

Input to FPD L2 DSM
Entries 1056822
HT bits
312313
305912
304353
2880
944
1268
37081
45418
46653
SMALL
LARGE-S
LARGE-N

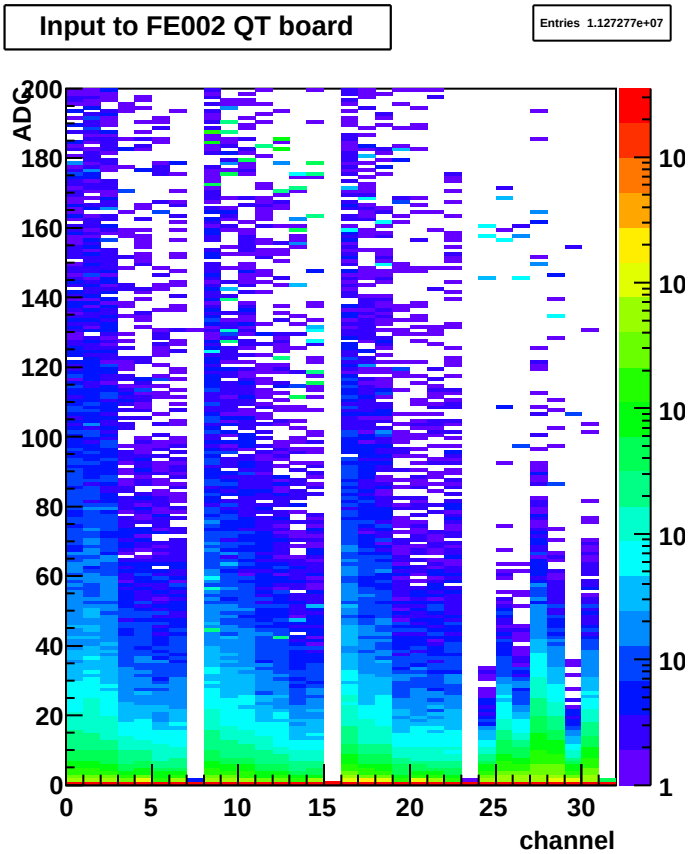

Input to FPD L2 DSM
Entries 1
HT bits - simulated
1
SMALL
LARGE-S
LARGE-N

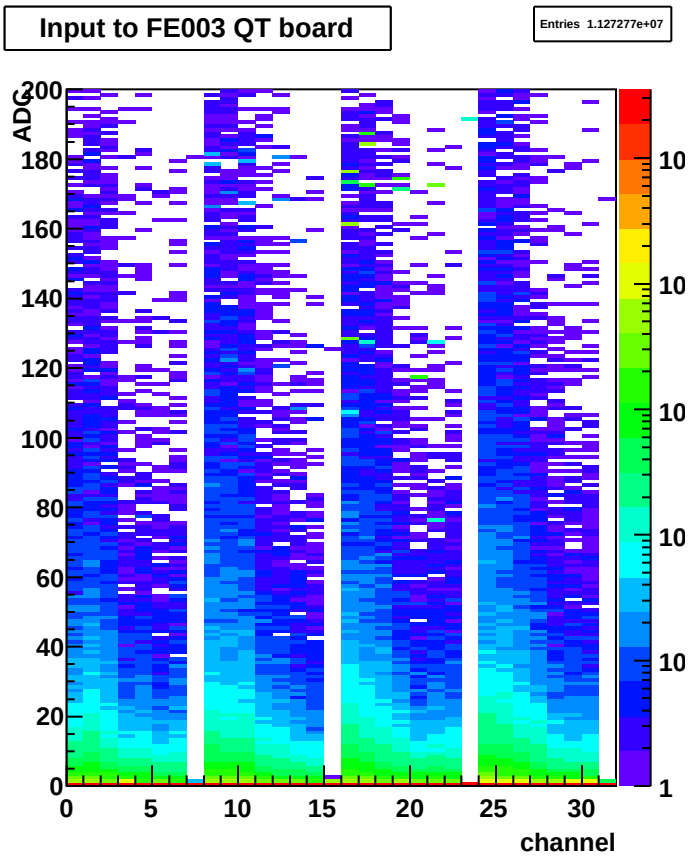

Input to FPD L3 DSM
Entries 5636384
bit
325682
322550
315987
313468
305616
313388
310120
304135
326918
320668
304752
289180
2616
25683
29724
36287
38806
46658
38886
42154
48139
27256
31606
47522
53114
392274
392274
349658
392274
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

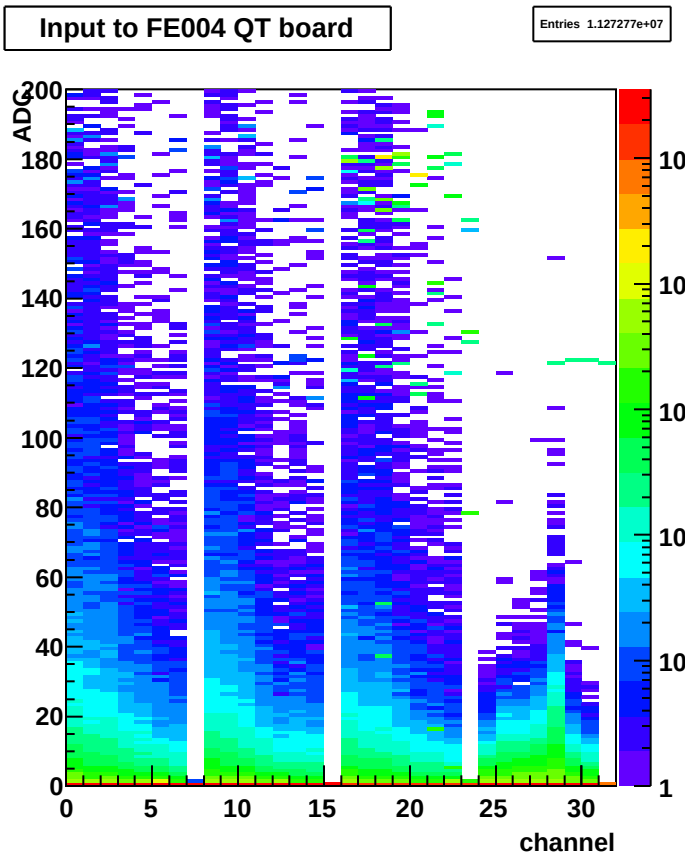

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

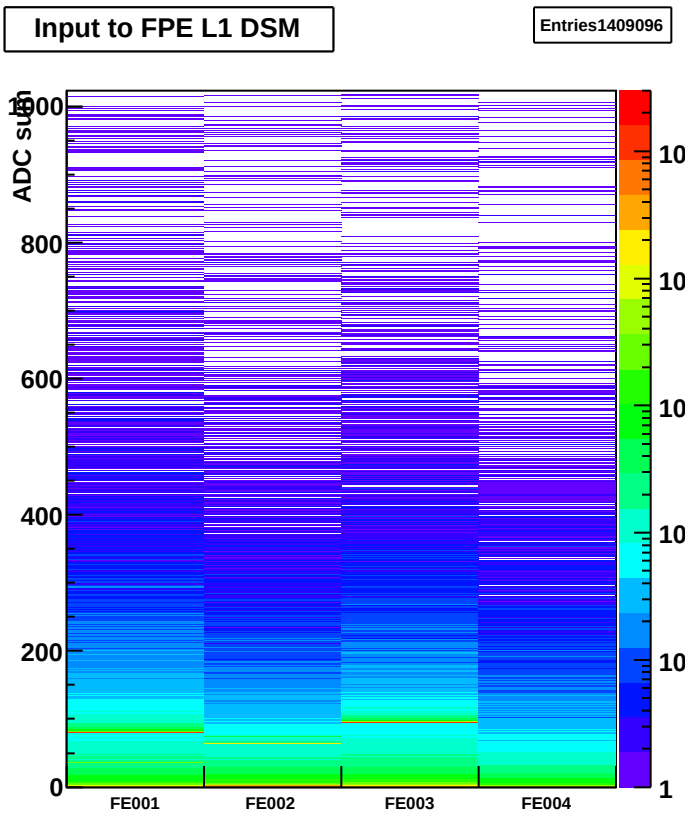

Input to FPD L2 DSM
Entries 1408736
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

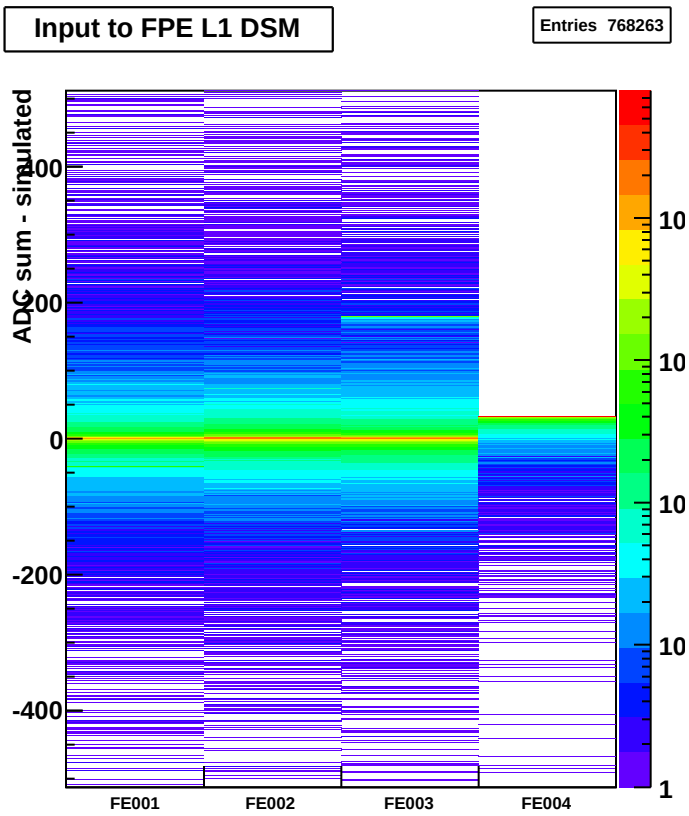

Input to FE001 QT board
Entries 1.127277e+07
ADC
channel

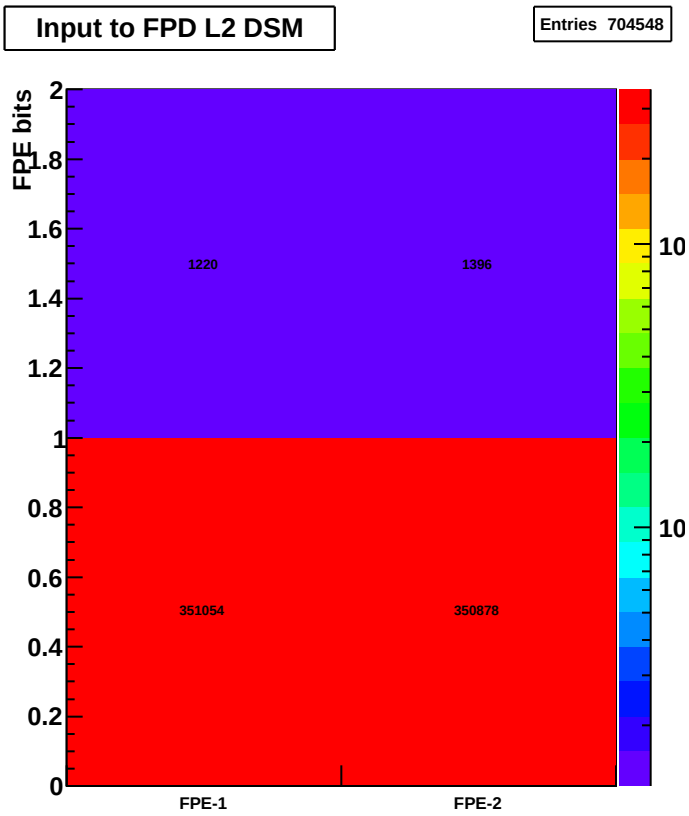

Input to FE002 QT board
Entries 1.127277e+07
ADC
channel

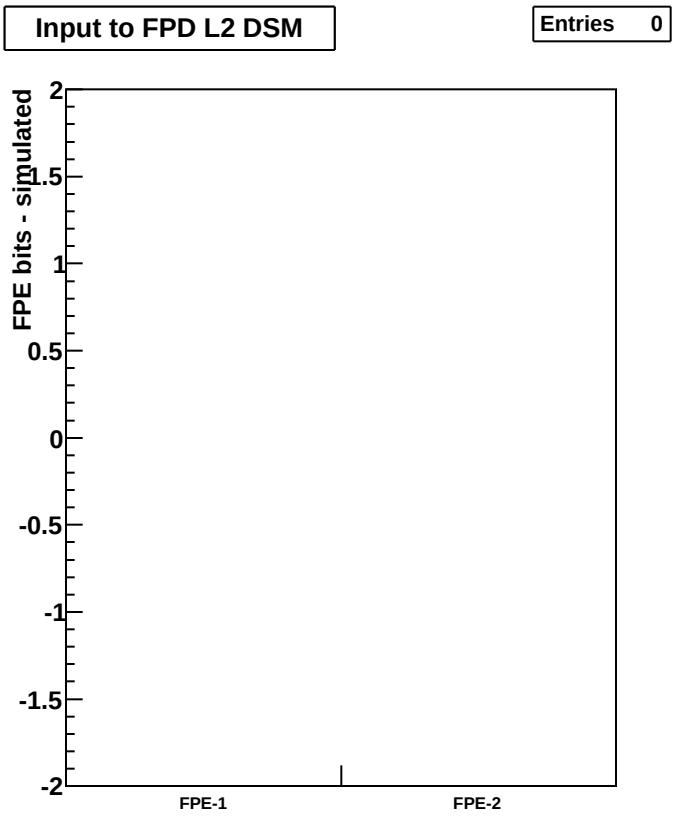

Input to FE003 QT board
Entries 1.127277e+07
ADC
channel


Input to FE004 QT board
Entries 1.127277e+07
ADC
channel


Input to FPE L1 DSM
Entries1409096
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 768263
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPD L2 DSM
Entries 704548
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
1220
1396
351054
350878
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2