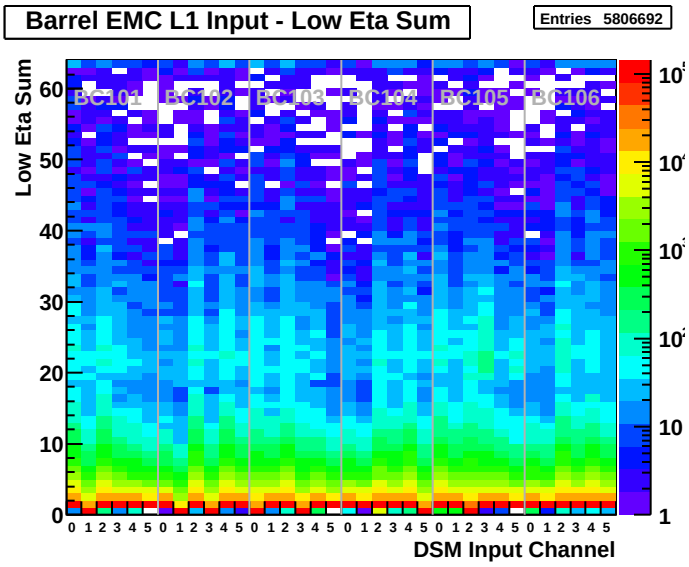
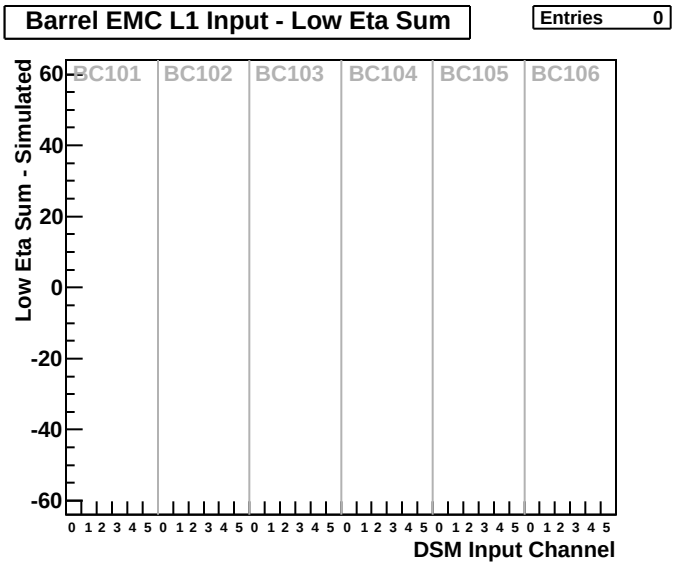
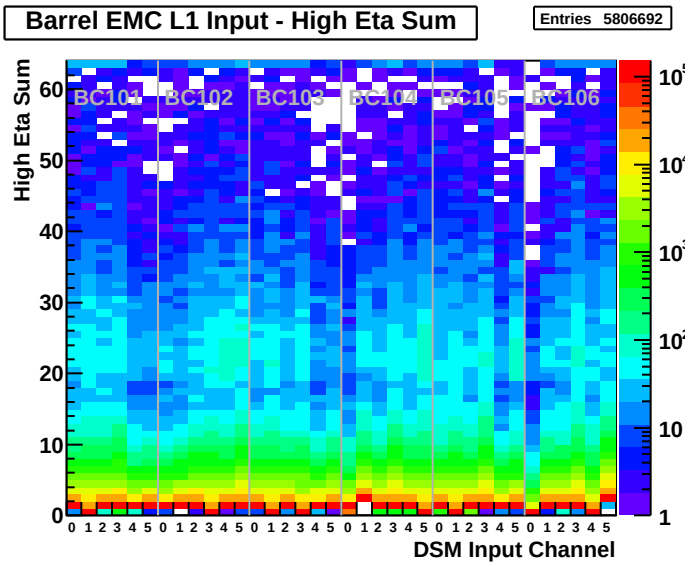
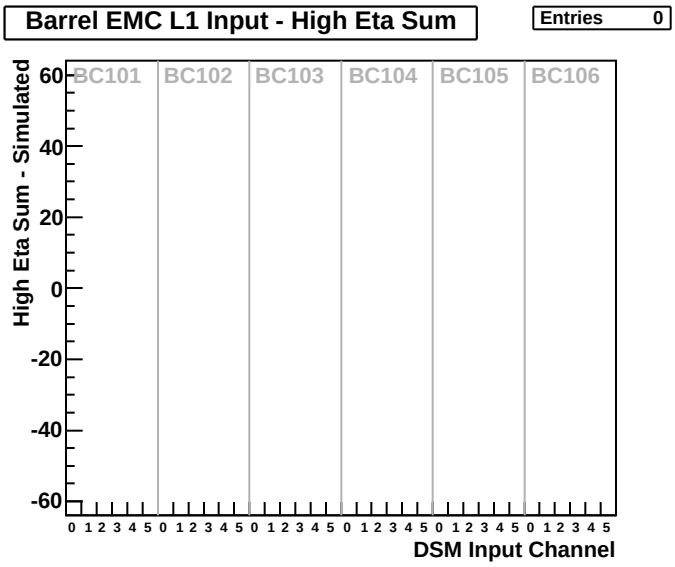
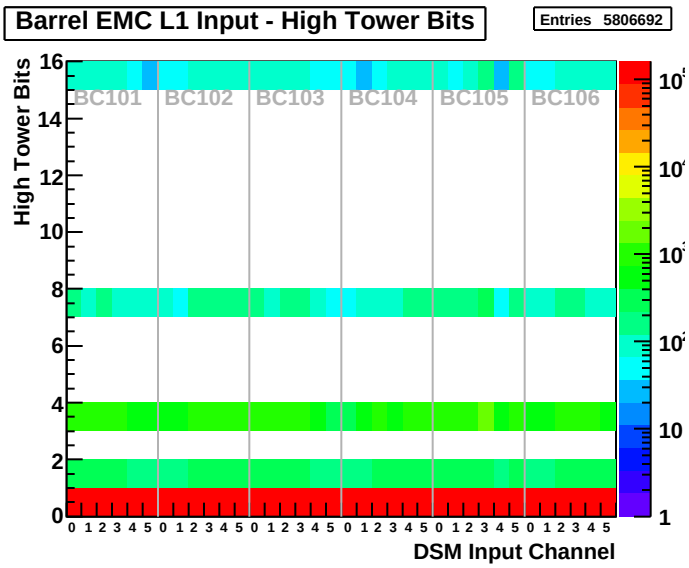
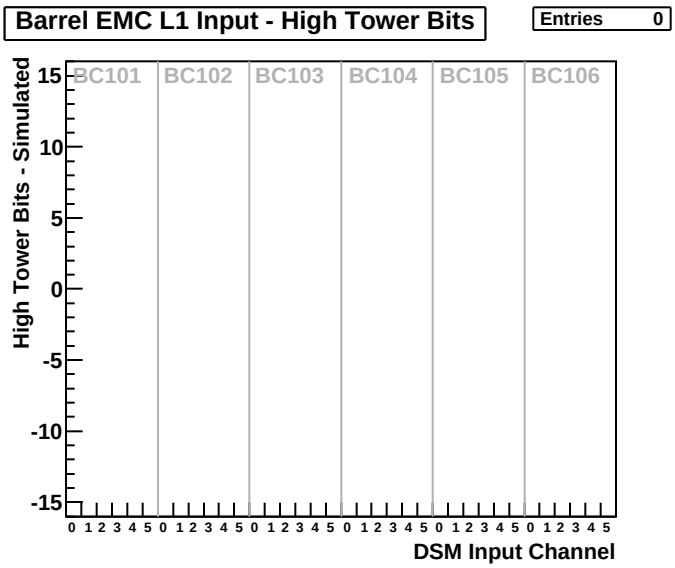

Barrel EMC L1 Input - Low Eta Sum
Entries 5806692
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

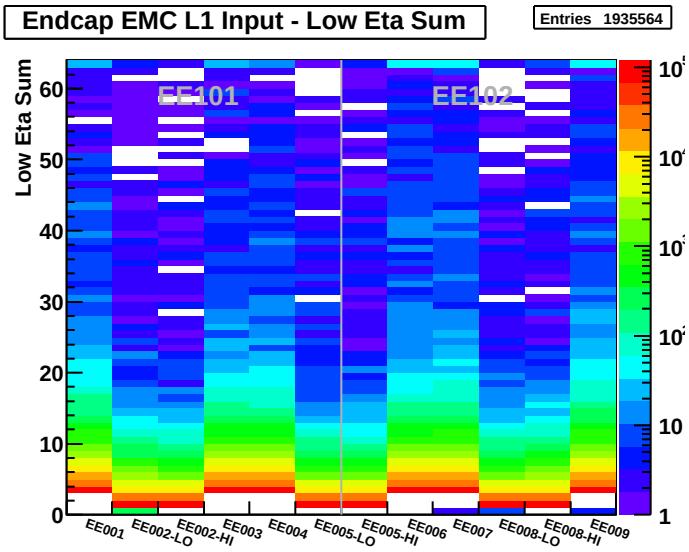

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

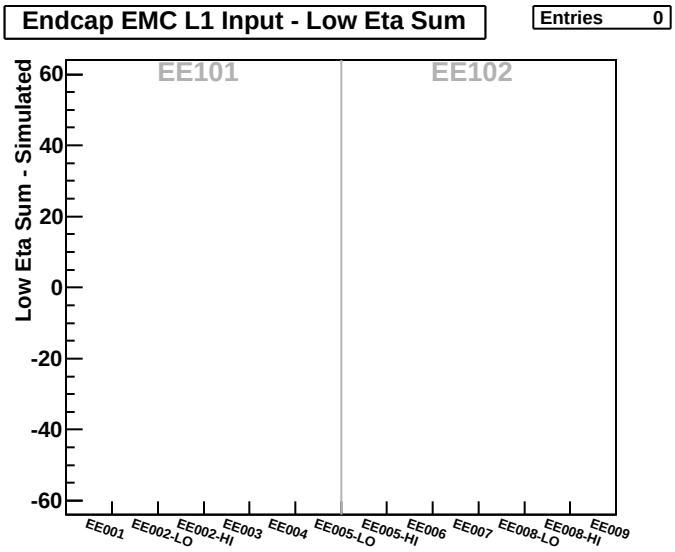

Barrel EMC L1 Input - High Eta Sum
Entries 5806692
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

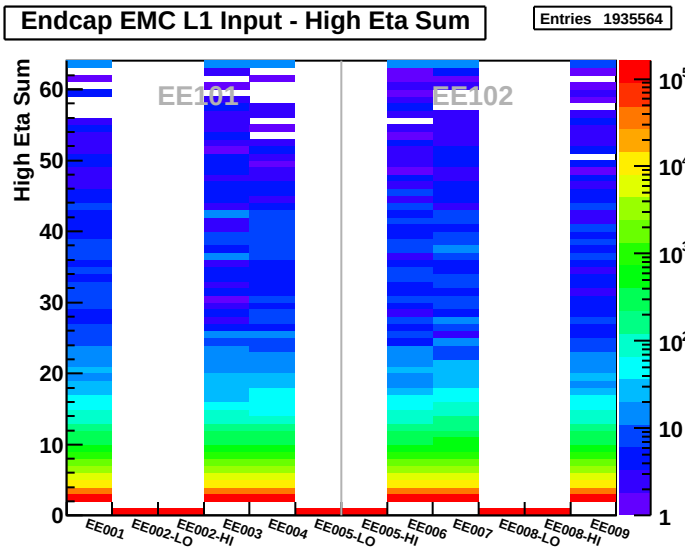

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

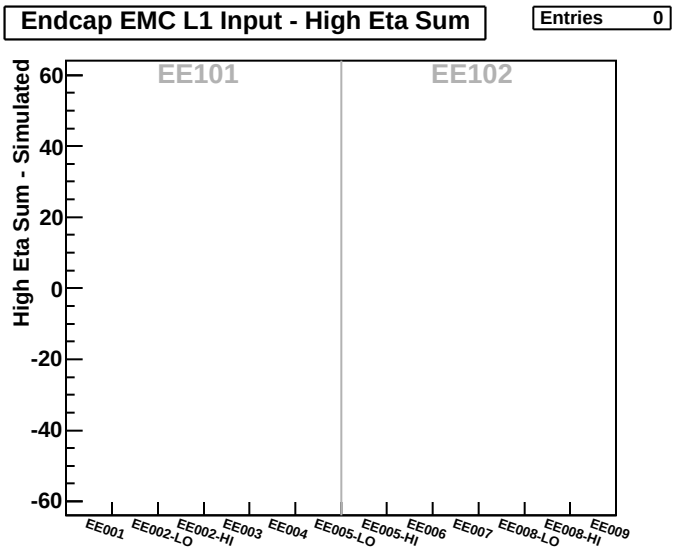

Barrel EMC L1 Input - High Tower Bits
Entries 5806692
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

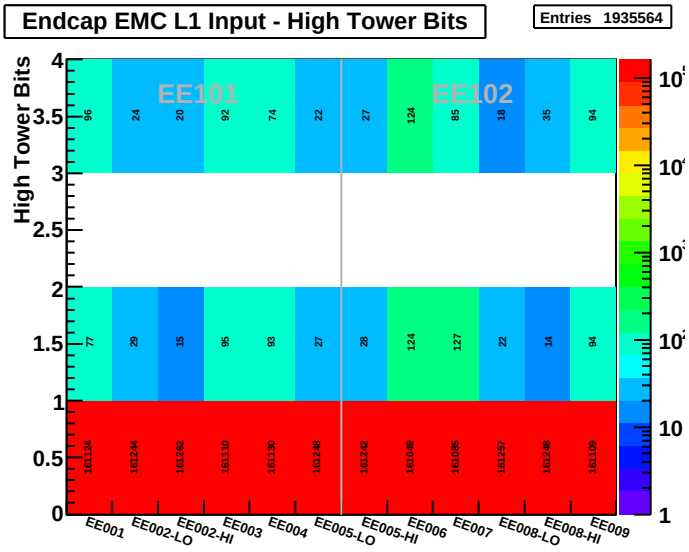

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

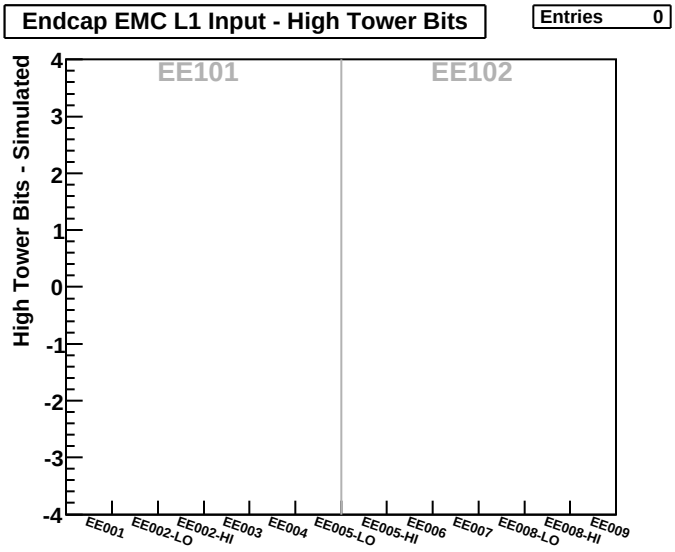

Endcap EMC L1 Input - Low Eta Sum
Entries 1935564
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

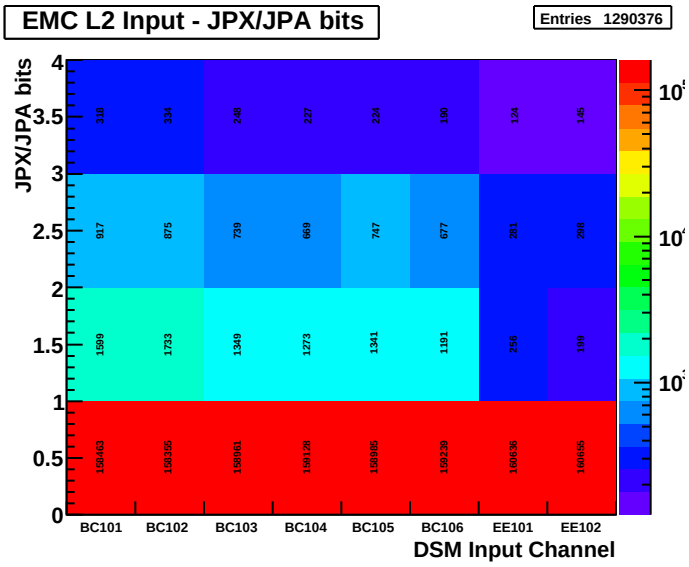

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

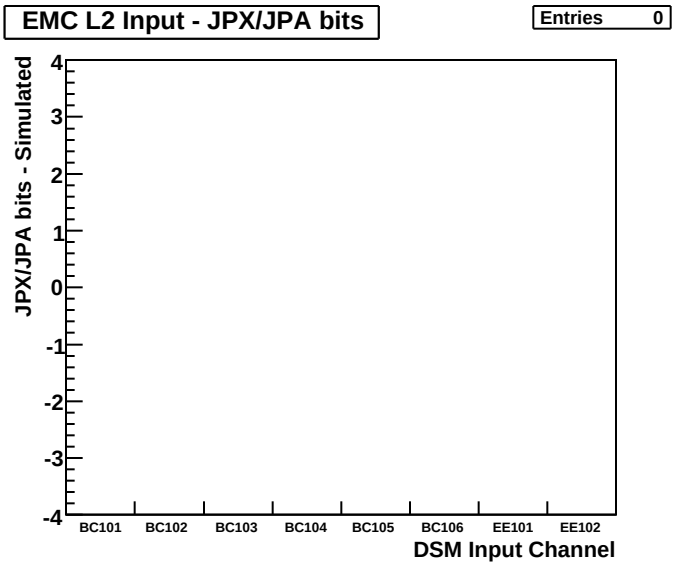

Endcap EMC L1 Input - High Eta Sum
Entries 1935564
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

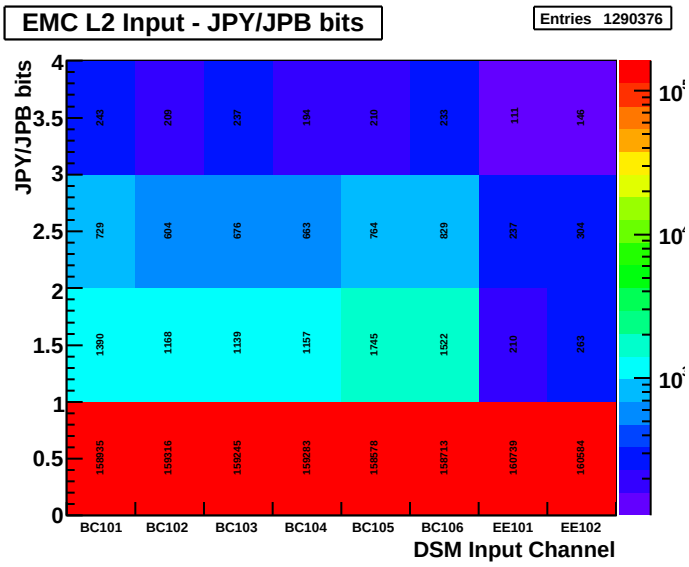

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

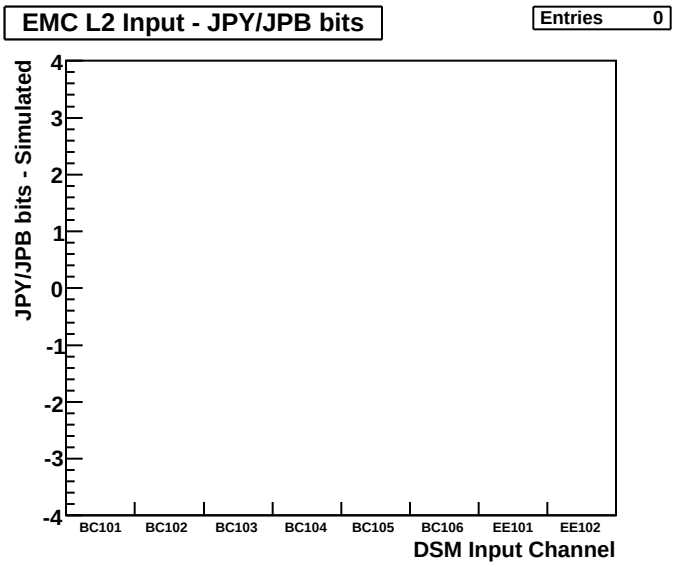

Endcap EMC L1 Input - High Tower Bits
Entries 1935564
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

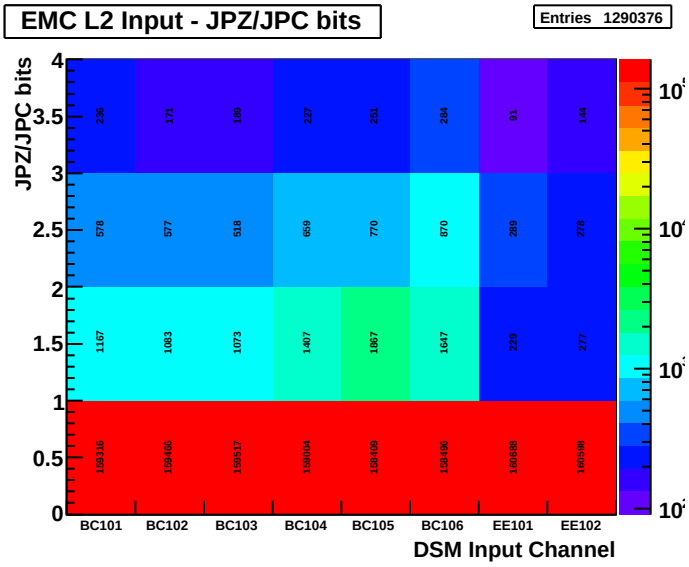

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

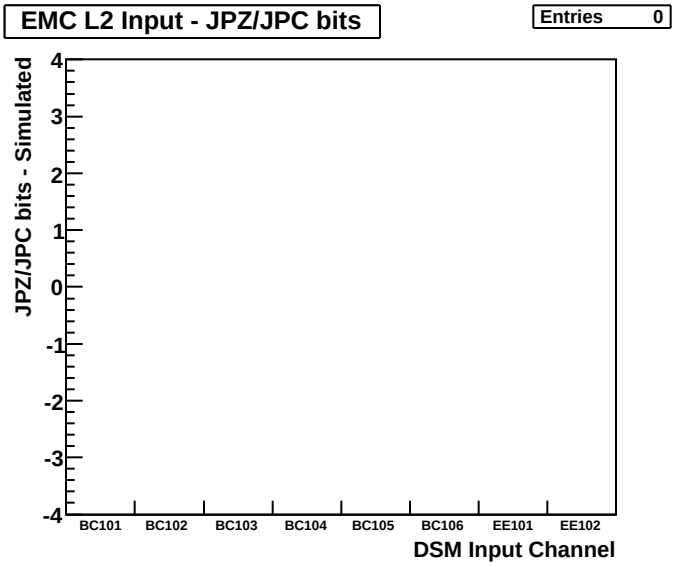

EMC L2 Input - JPX/JPA bits
Entries 1290376
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
318 334 248 227 224 190 124 145
917 875 739 669 747 677 281 288
1599 1733 1349 1273 1341 1191 256 199
158463 158355 158961 159128 158985 159239 160636 160655

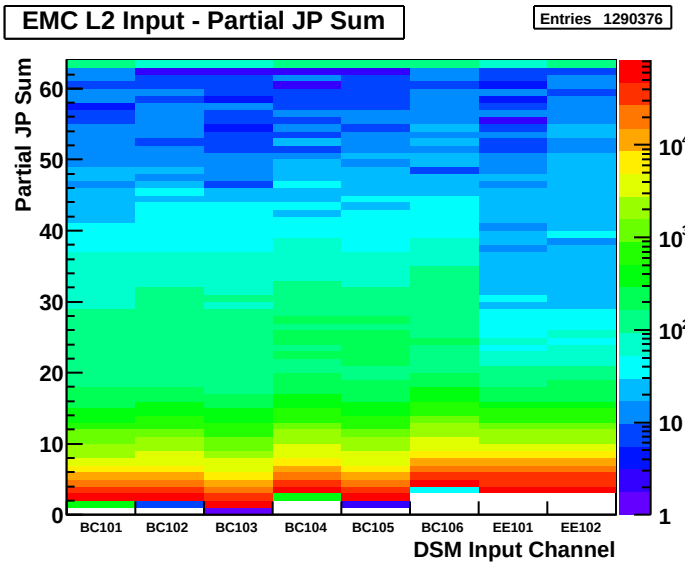

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

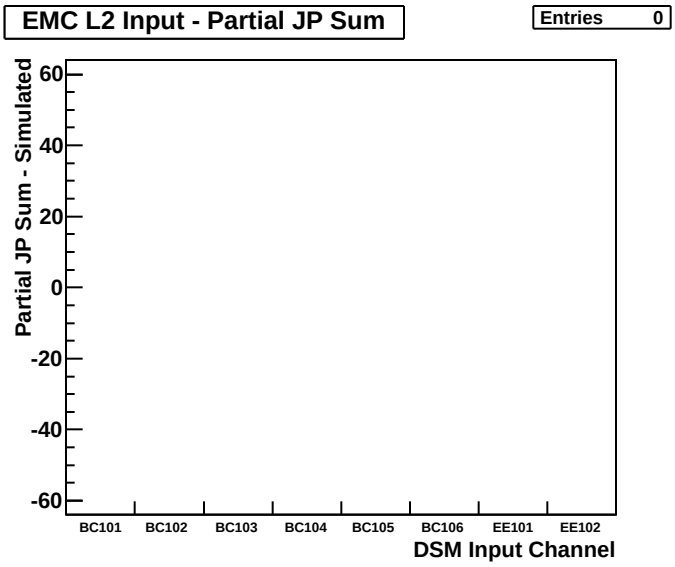

EMC L2 Input - JPY/JPB bits
Entries 1290376
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
248 209 237 194 210 233 111 146
729 604 676 663 764 829 237 304
1390 1168 1139 1157 1745 1522 210 263
158935 159316 159245 159283 158578 158713 160739 160584

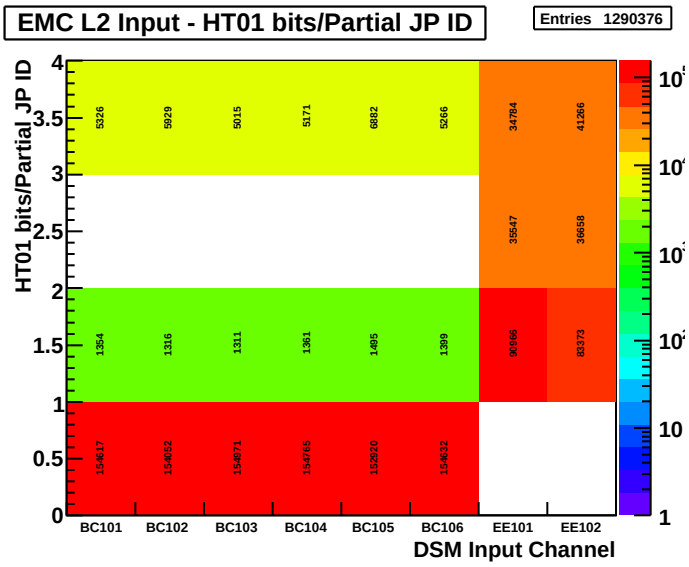

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

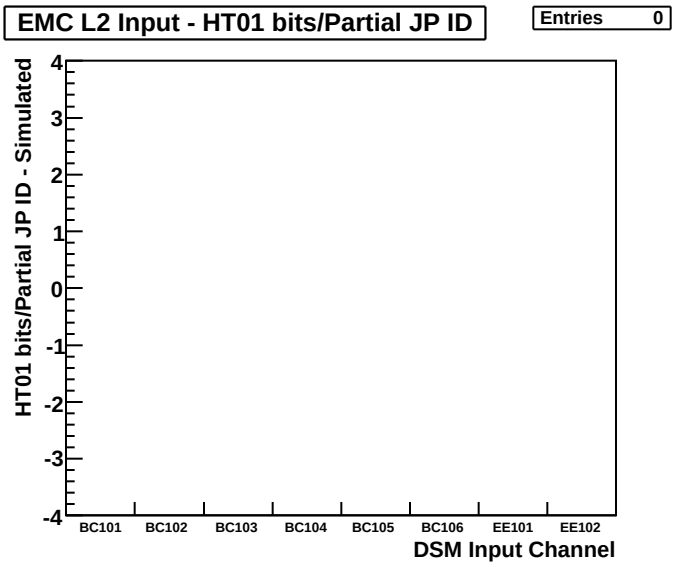

EMC L2 Input - JPZ/JPC bits
Entries 1290376
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
236 171 189 227 251 284 91 144
578 577 518 659 770 870 289 278
1167 1083 1073 1407 1867 1647 229 277
159316 159466 159517 159004 158409 158496 160588 160598

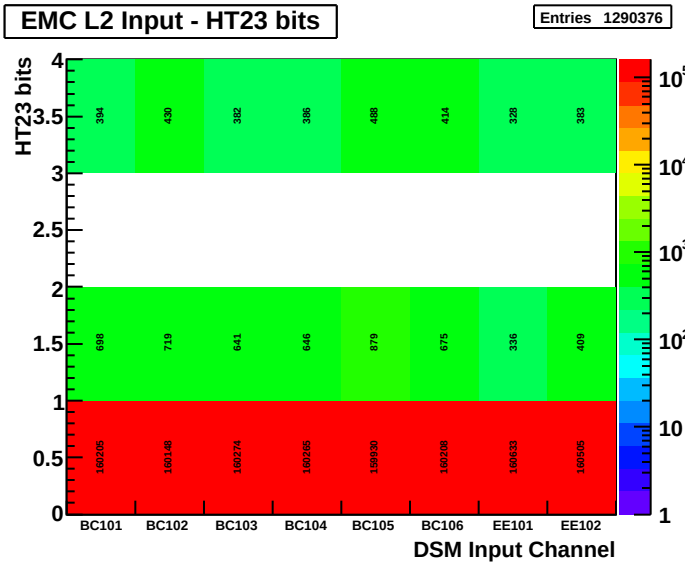

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

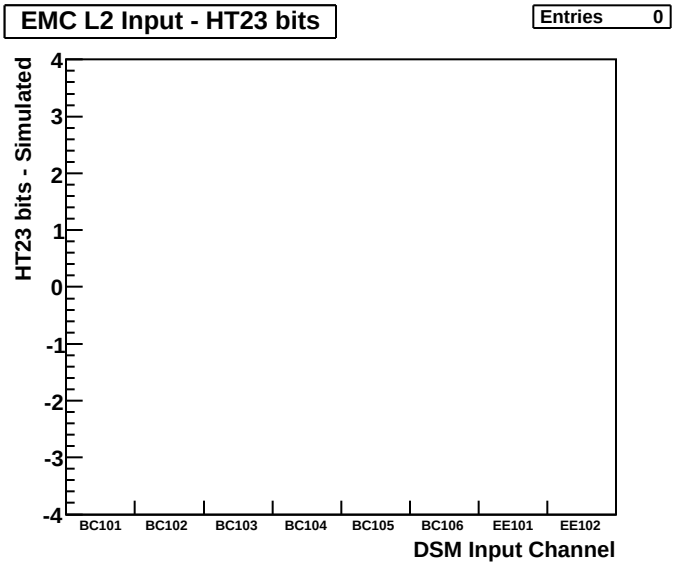

EMC L2 Input - Partial JP Sum
Entries 1290376
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

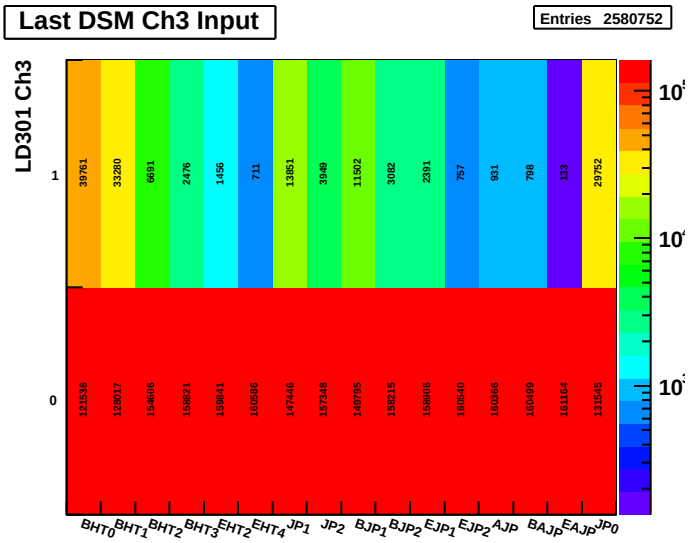

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

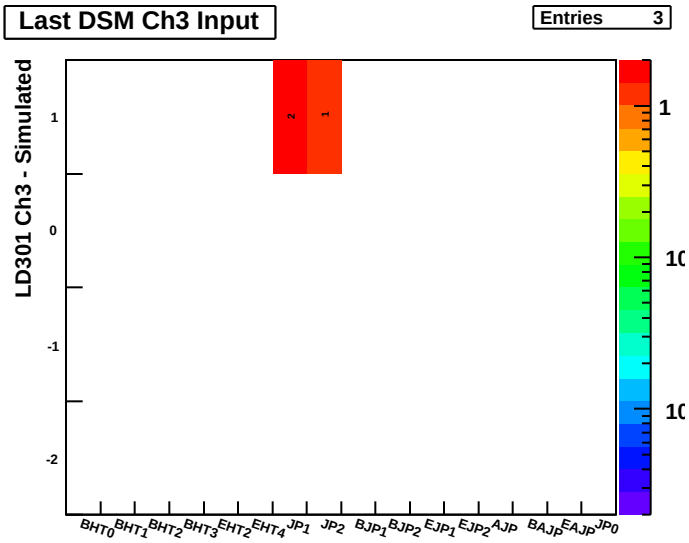

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1290376
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

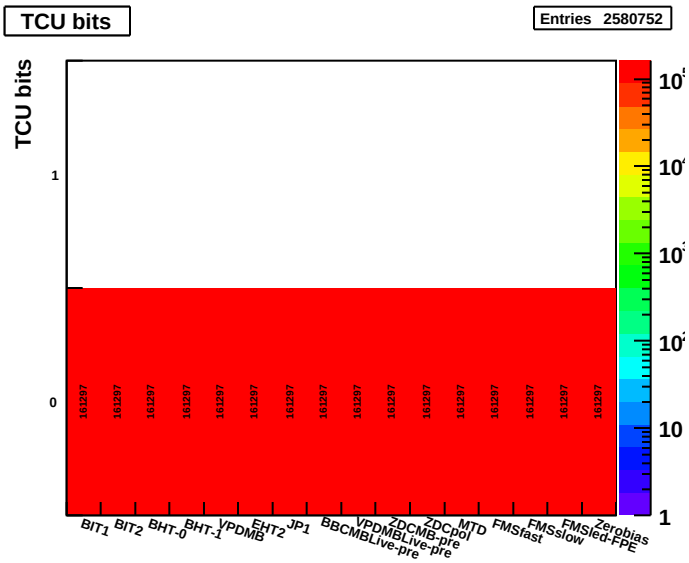

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

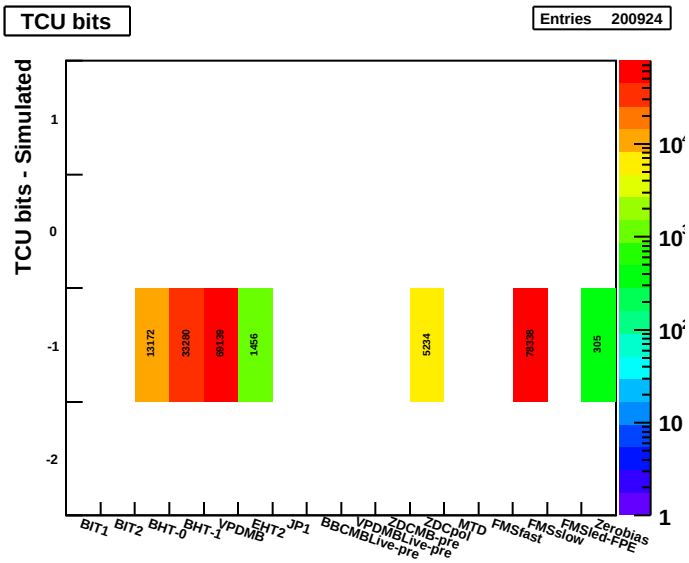

EMC L2 Input - HT23 bits
Entries 1290376
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

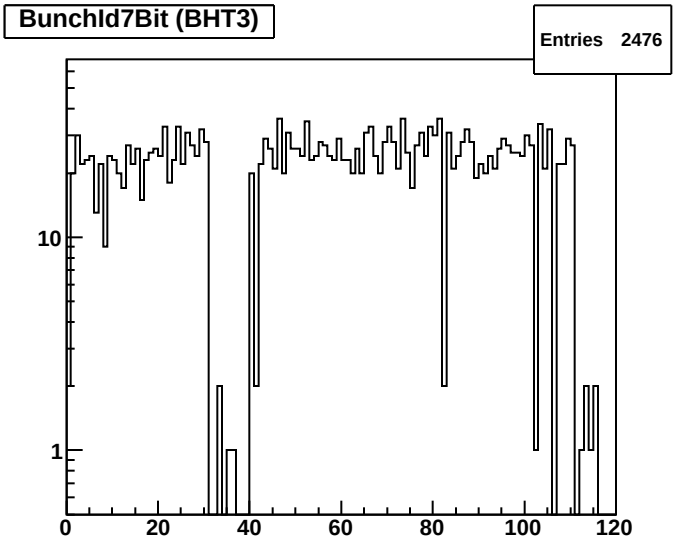

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

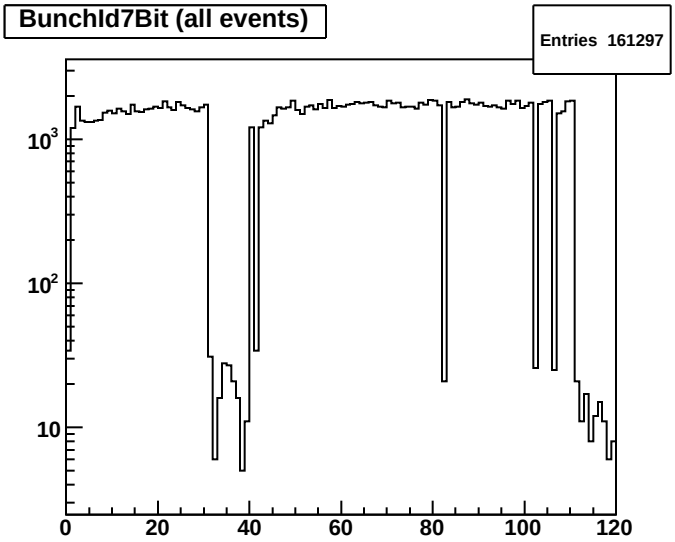

Last DSM Ch3 Input
Entries 2580752
LD301 Ch3
39761 33280 6691 2476 1456 711 13851 3949 11502 3082 2391 757 931 798 133 29752
121536 128017 154606 158821 159841 160586 147446 157348 149795 158215 158906 160540 160366 160499 161164 133545
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

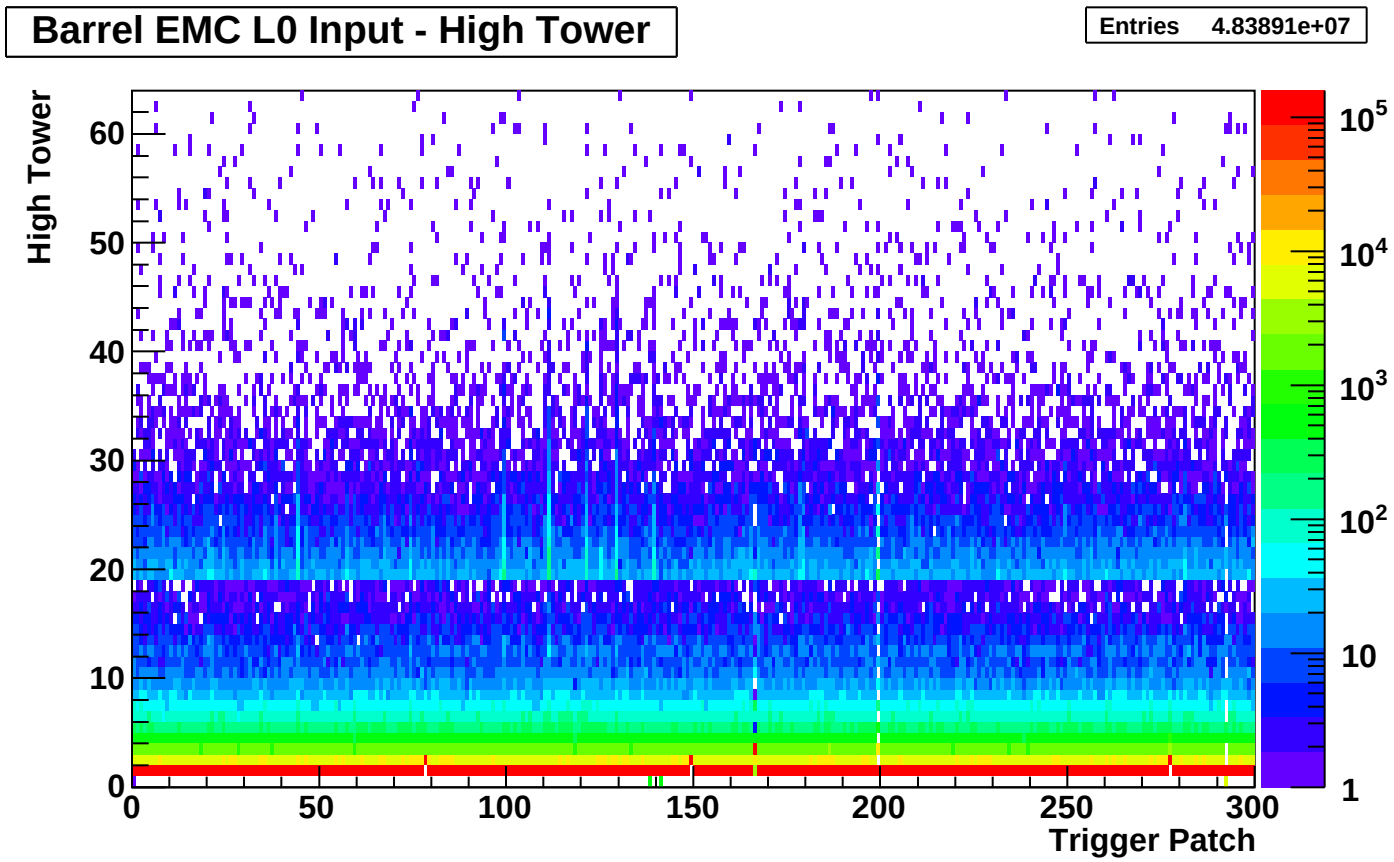

Last DSM Ch3 Input
Entries 3
LD301 Ch3 - Simulated
2 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

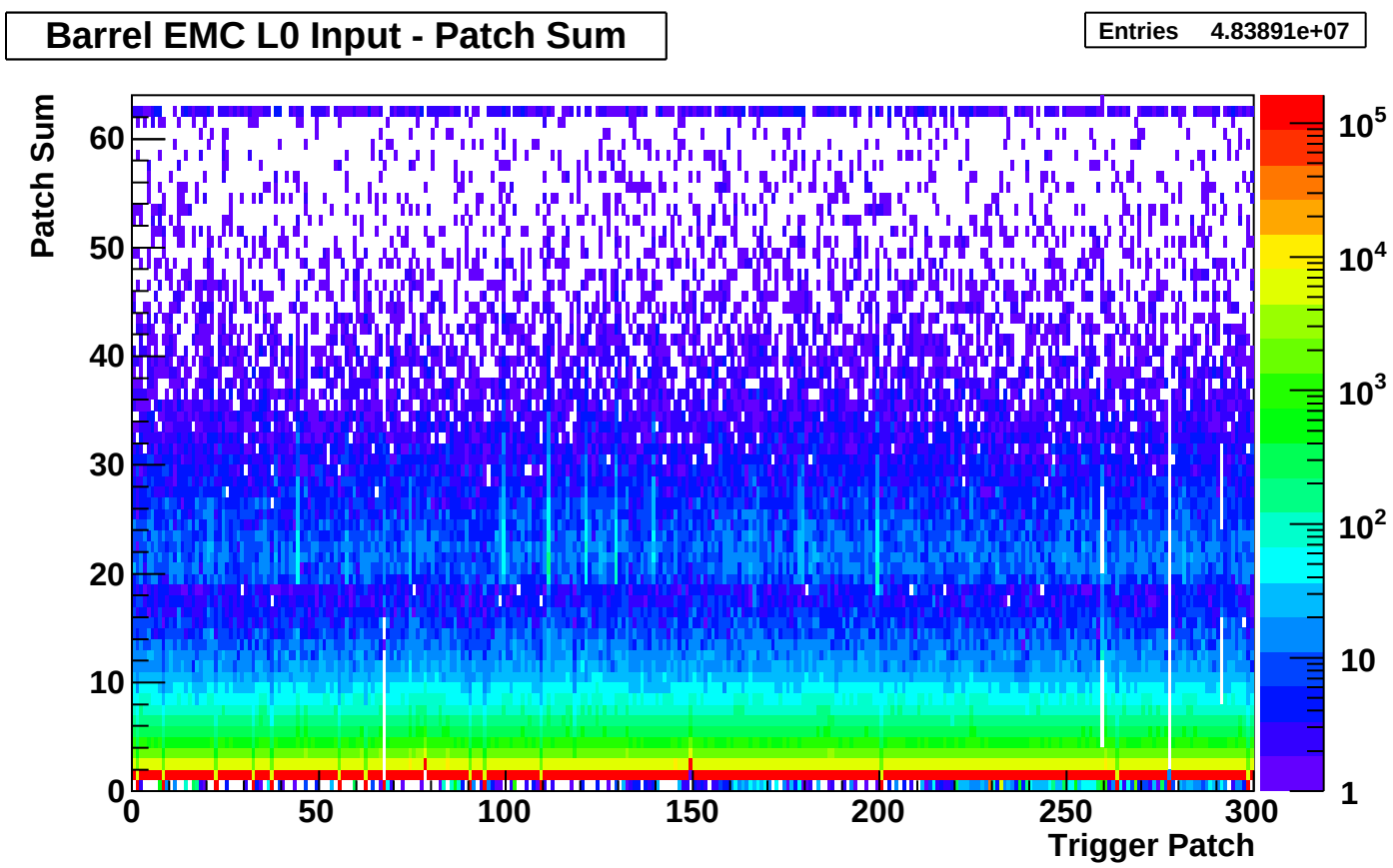

TCU bits
Entries 2580752
TCU bits
161297
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

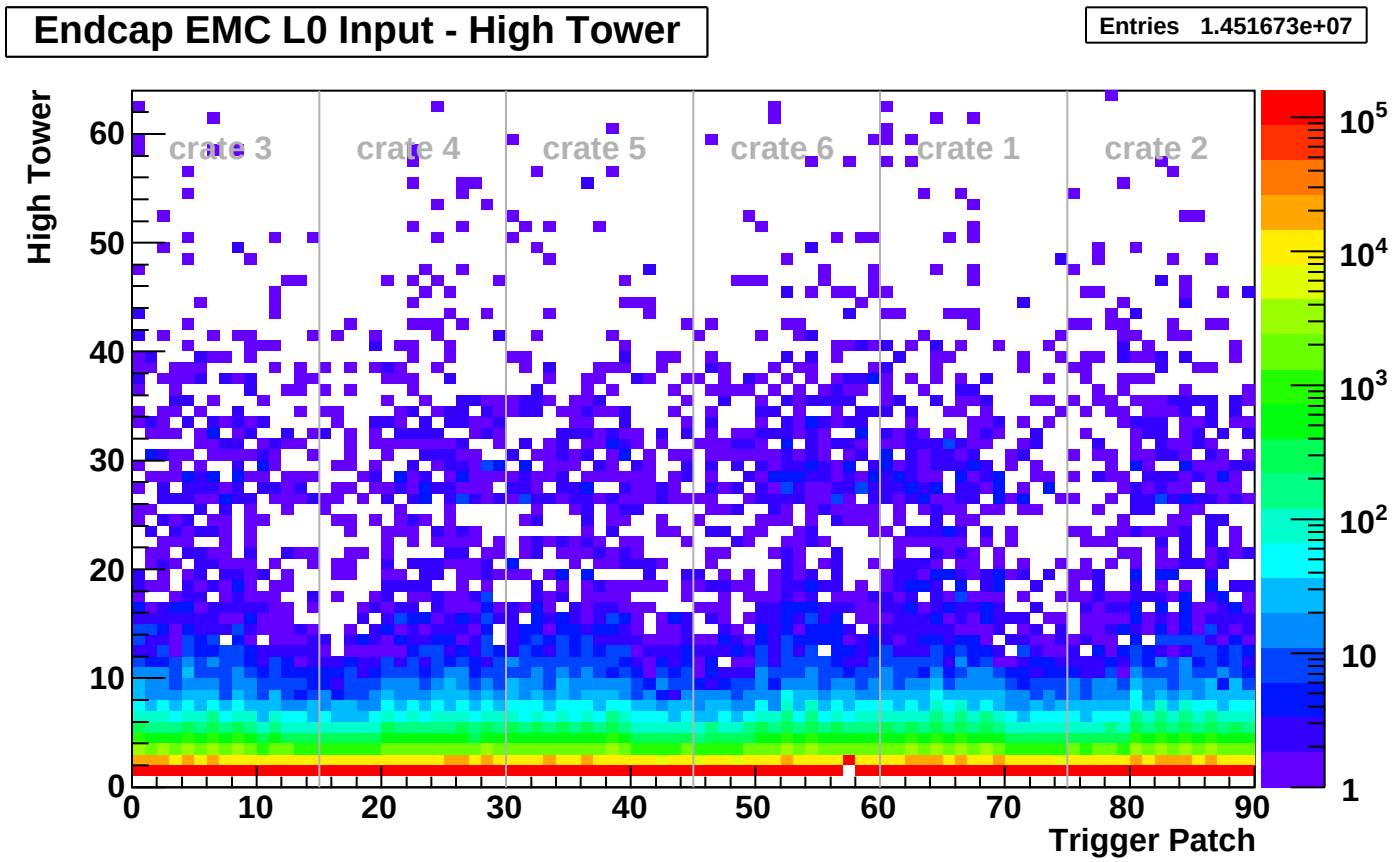

TCU bits
Entries 200924
TCU bits - Simulated
13172 33280 69139 1456 5234 78338 305
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

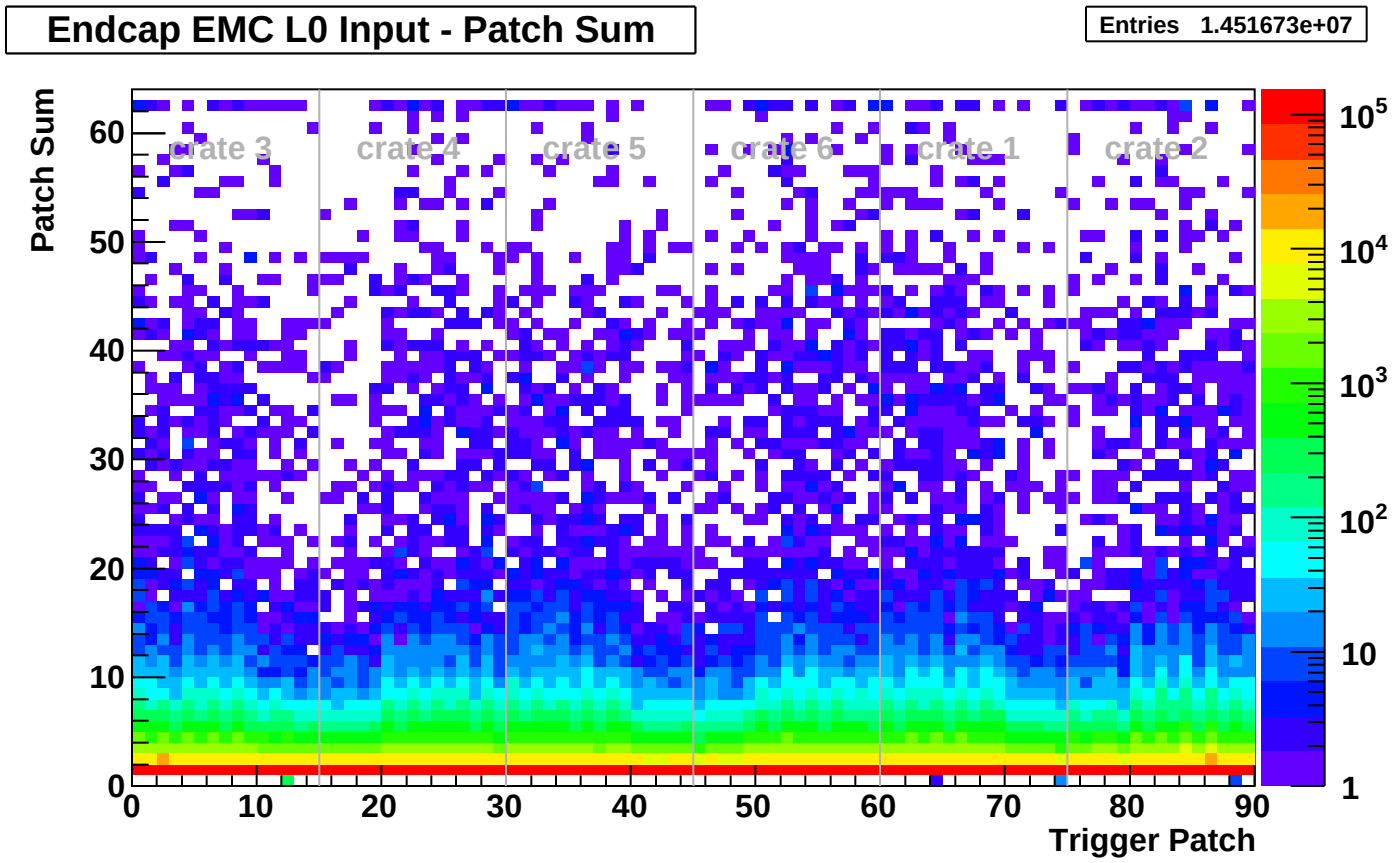

BunchId7Bit (BHT3)
Entries 2476

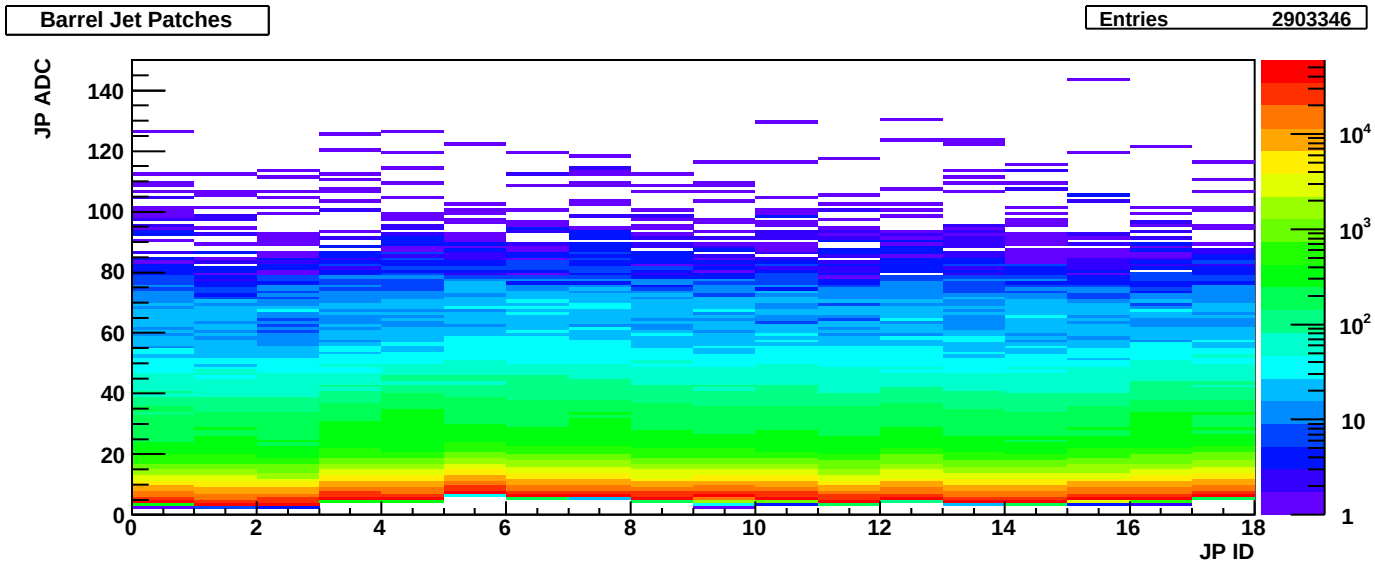

BunchId7Bit (all events)
Entries 161297

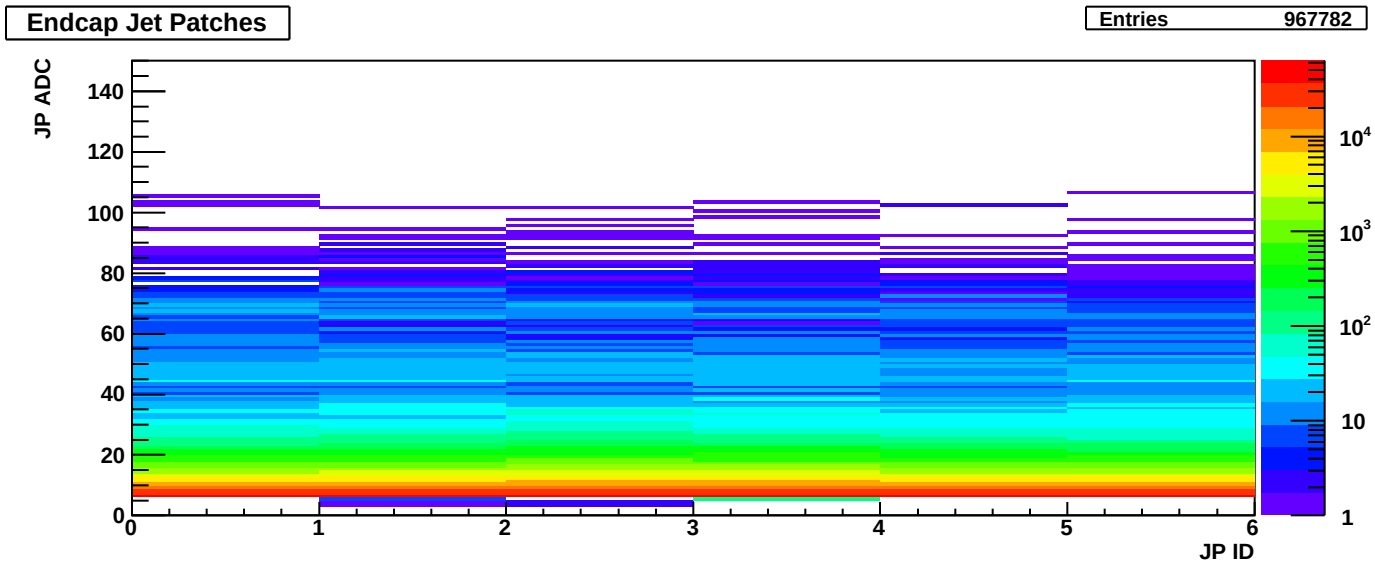

Barrel EMC L0 Input - High Tower
Entries 4.83891e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

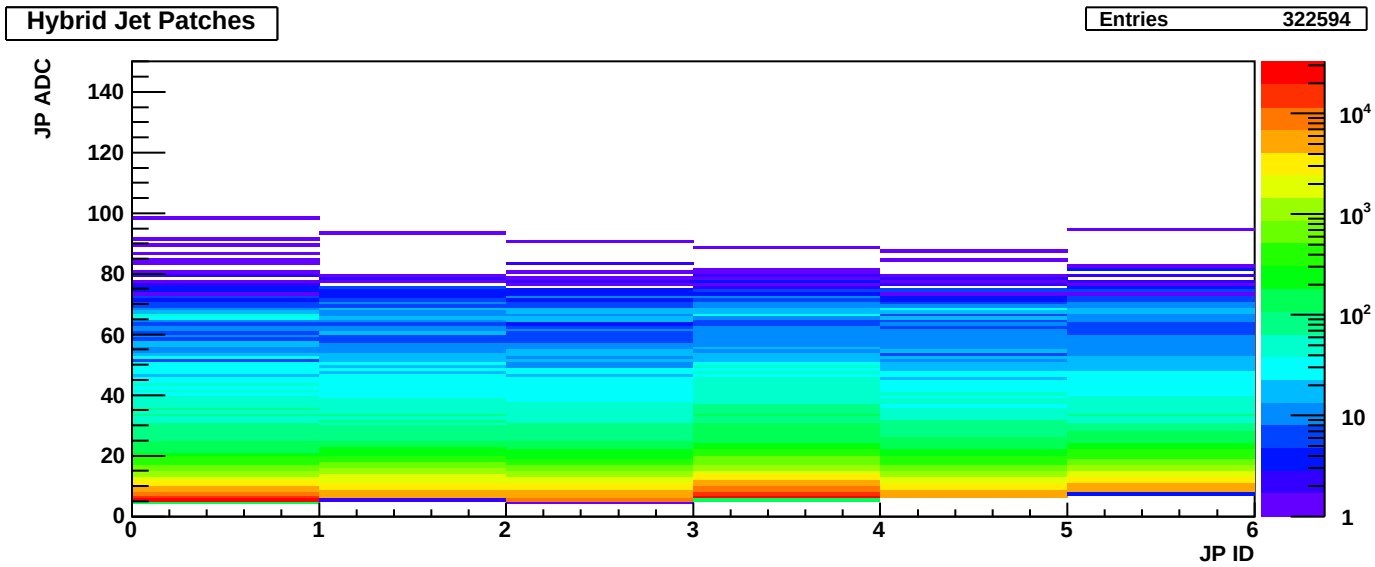

Barrel EMC L0 Input - Patch Sum
Entries 4.83891e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

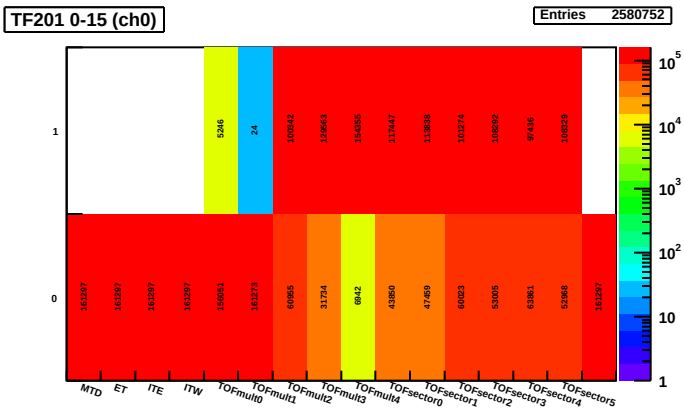

Endcap EMC L0 Input - High Tower
Entries 1.451673e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

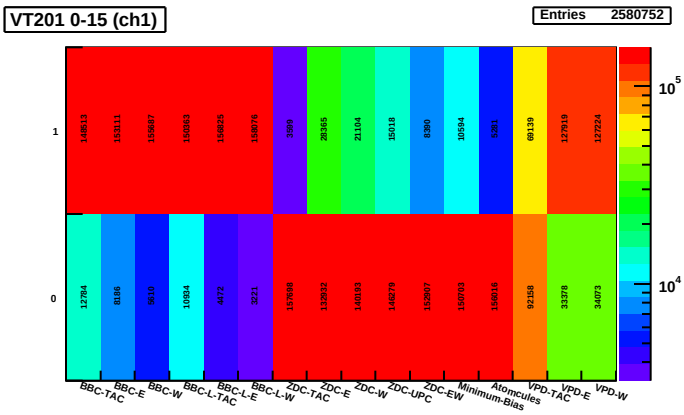

Endcap EMC L0 Input - Patch Sum
Entries 1.451673e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

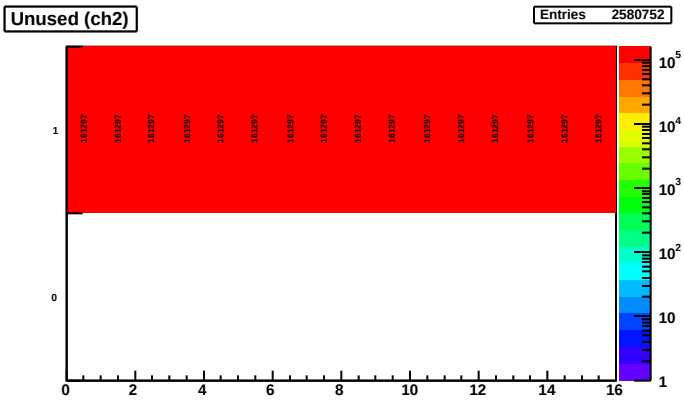

Barrel Jet Patches
Entries 2903346
JP ADC
JP ID

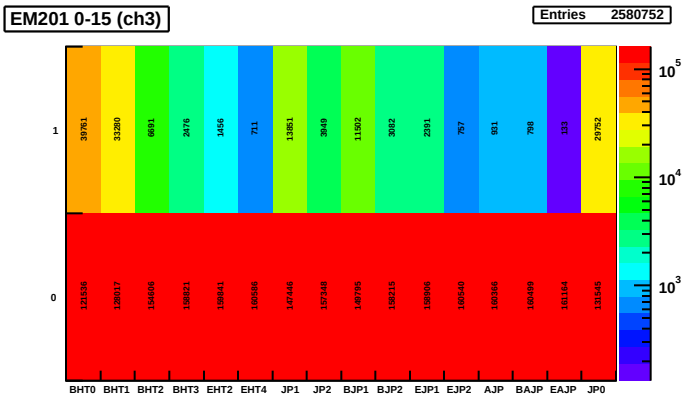

Endcap Jet Patches
Entries 967782
JP ADC
JP ID

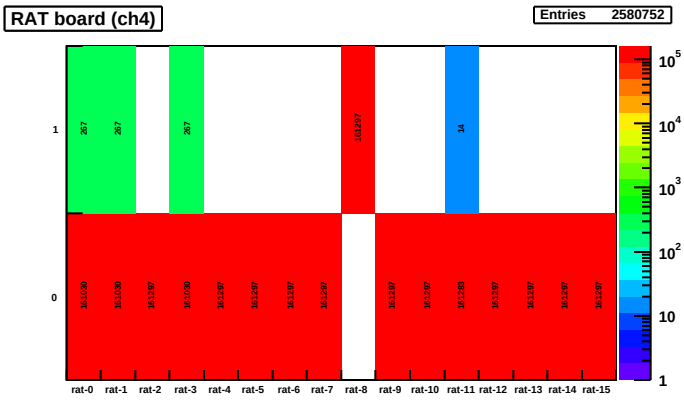

Hybrid Jet Patches
Entries 322594
JP ADC
JP ID

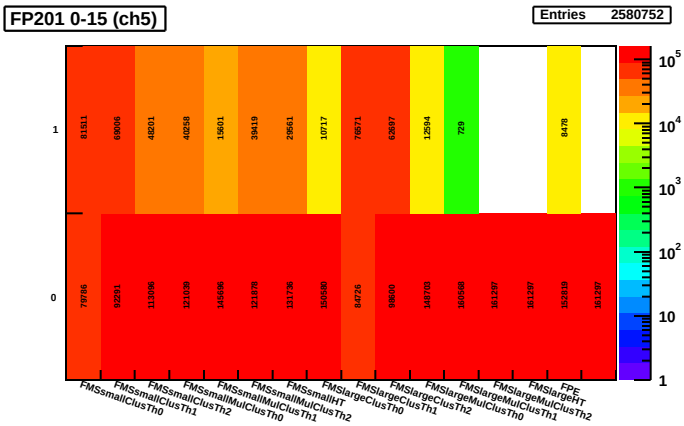

TF201 0-15 (ch0)
Entries 2580752
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

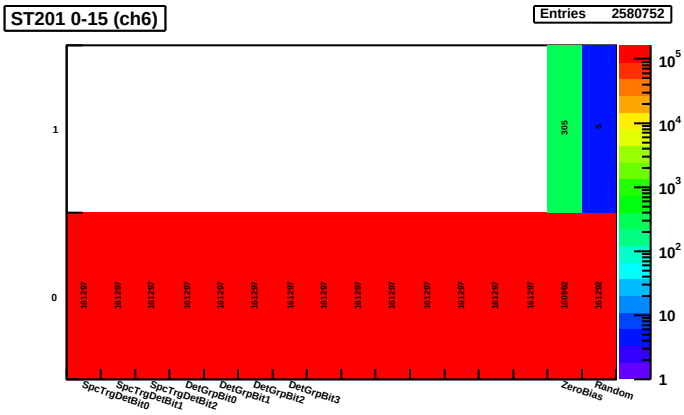

VT201 0-15 (ch1)
Entries 2580752
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

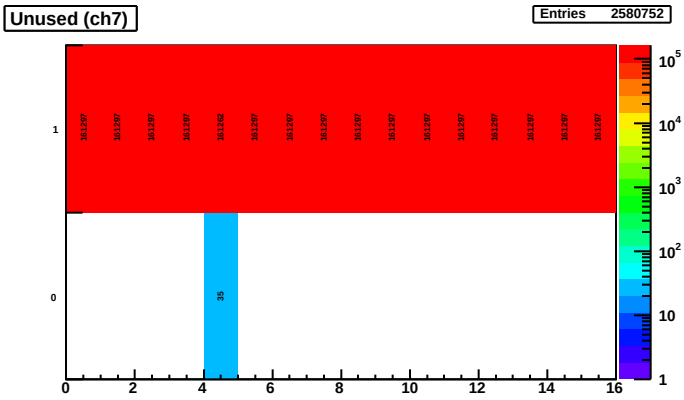

Unused (ch2)
Entries 2580752

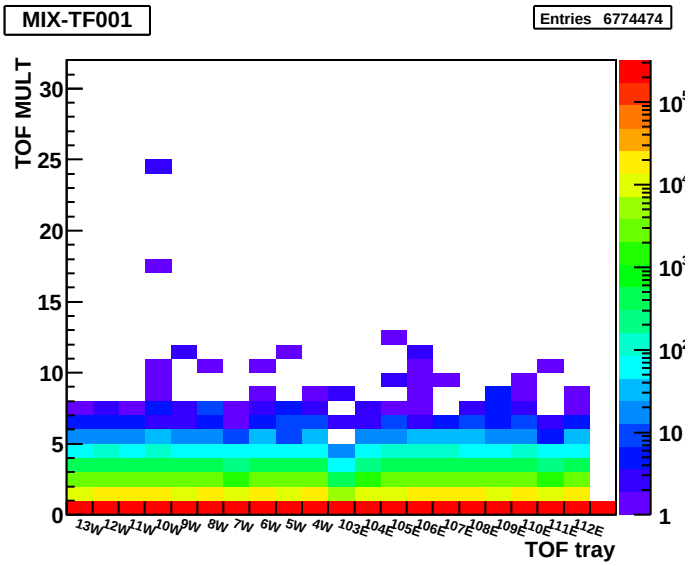

EM201 0-15 (ch3)
Entries 2580752
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

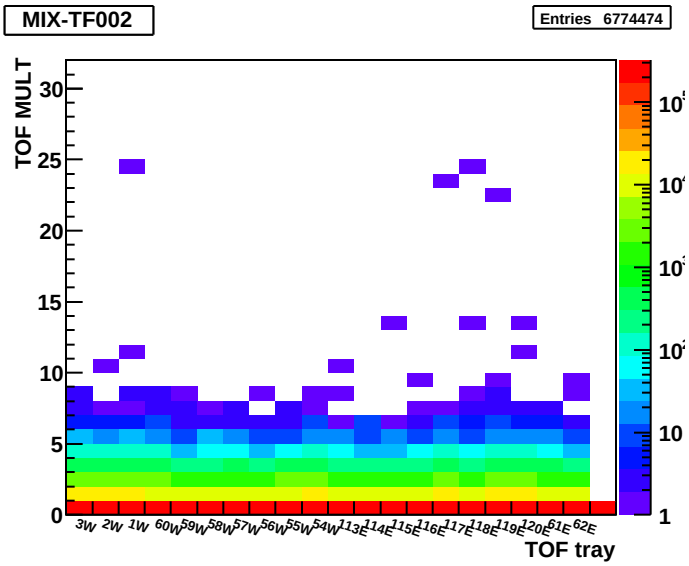

RAT board (ch4)
Entries 2580752
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

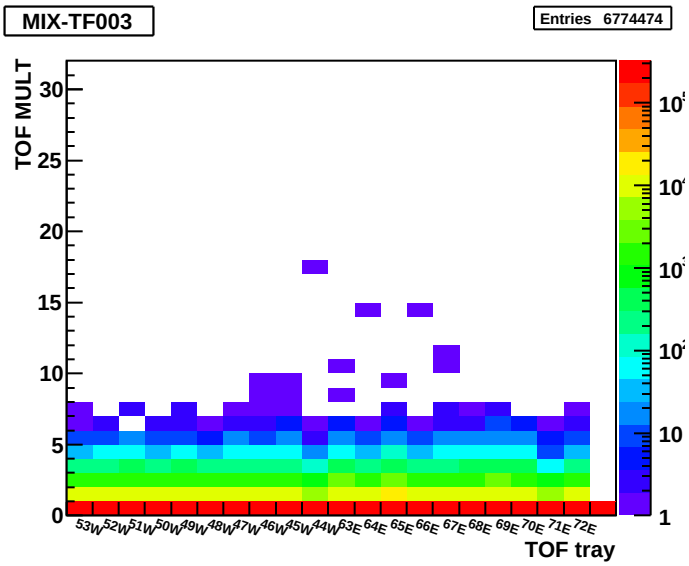

FP201 0-15 (ch5)
Entries 2580752
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

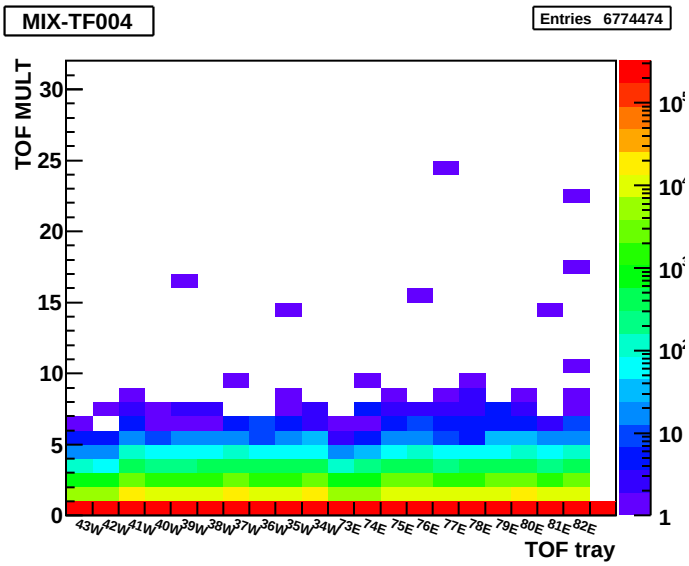

ST201 0-15 (ch6)
Entries 2580752
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

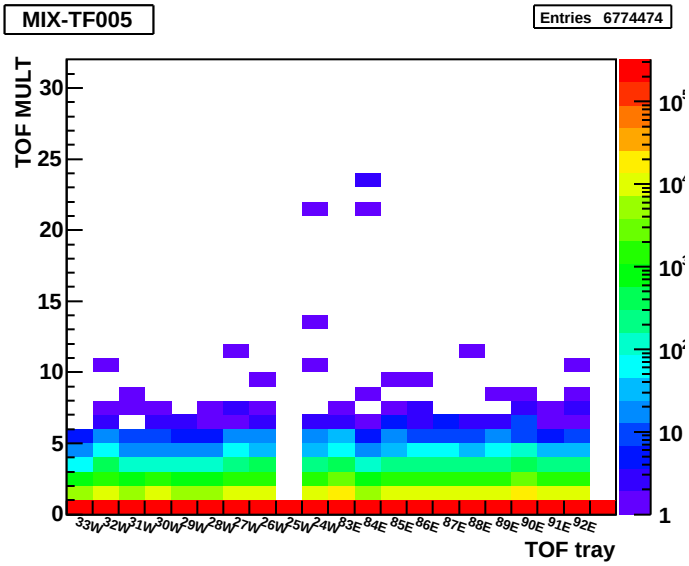

Unused (ch7)
Entries 2580752

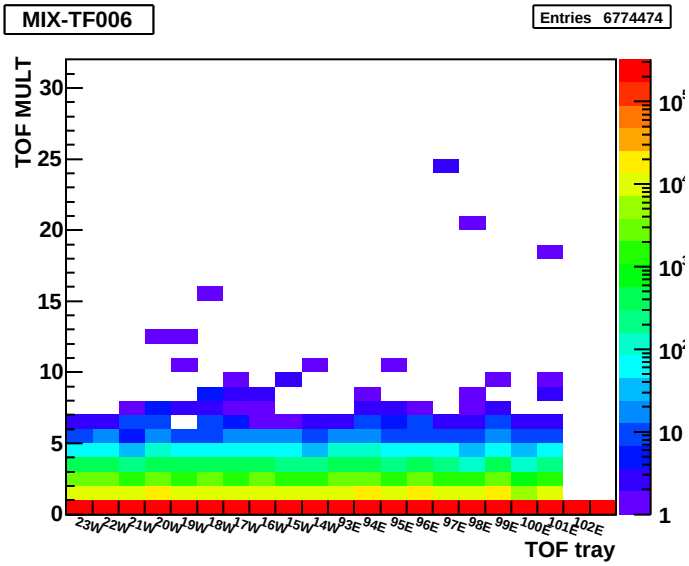

MIX-TF001
Entries 6774474
TOF MULT
TOF tray

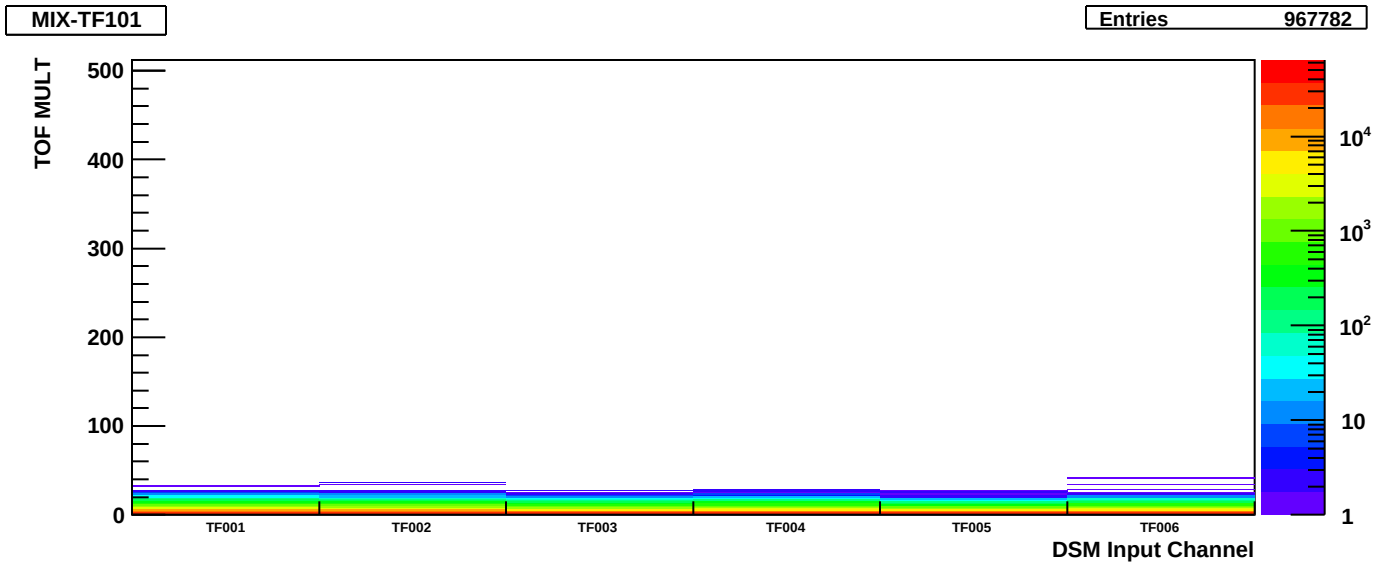

MIX-TF002
Entries 6774474
TOF MULT
TOF tray

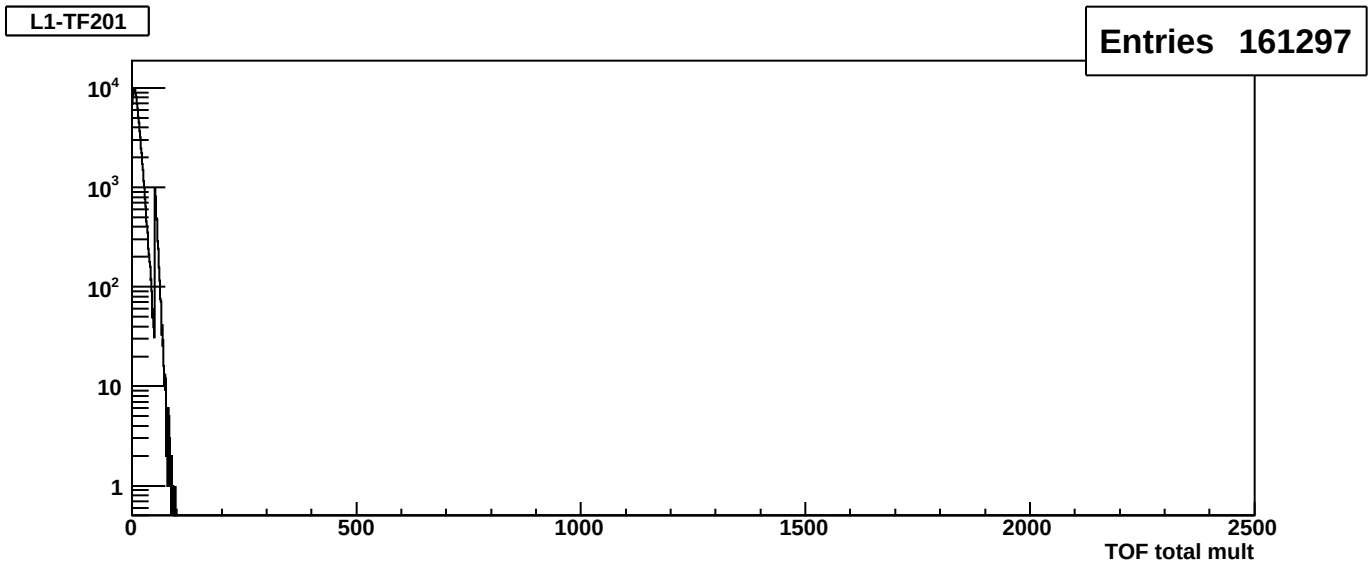

MIX-TF003
Entries 6774474
TOF MULT
TOF tray

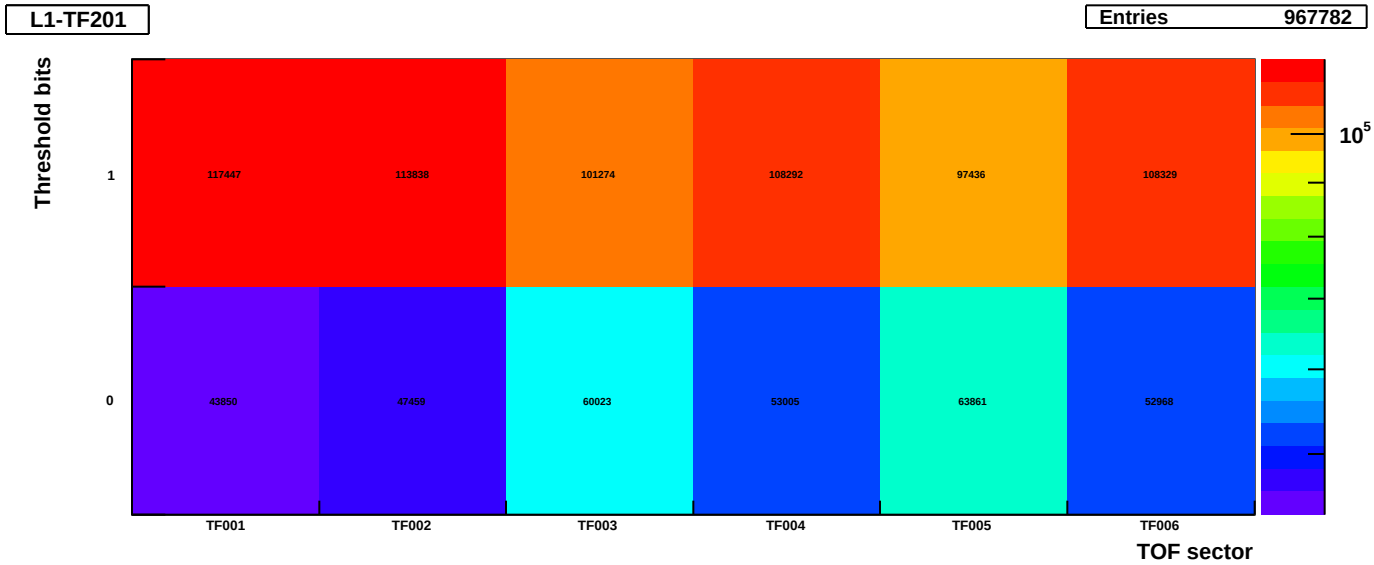

MIX-TF004
Entries 6774474
TOF MULT
TOF tray

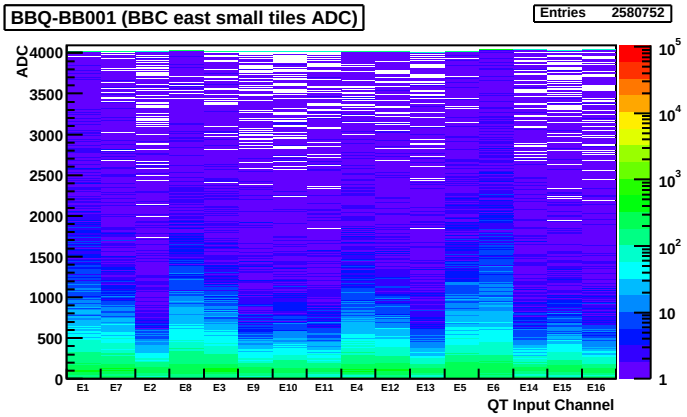

MIX-TF005
Entries 6774474
TOF MULT
TOF tray

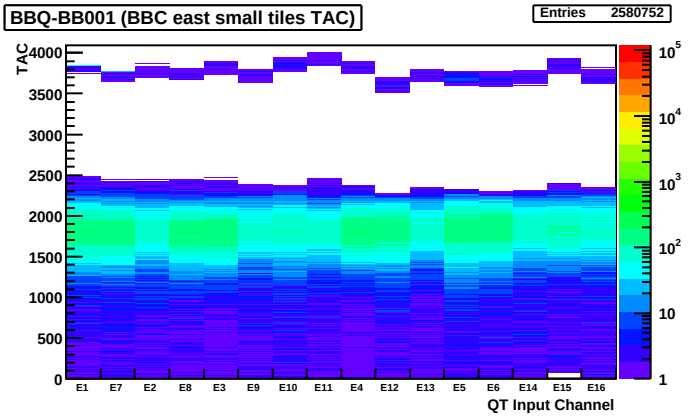

MIX-TF006
Entries 6774474
TOF MULT
TOF tray

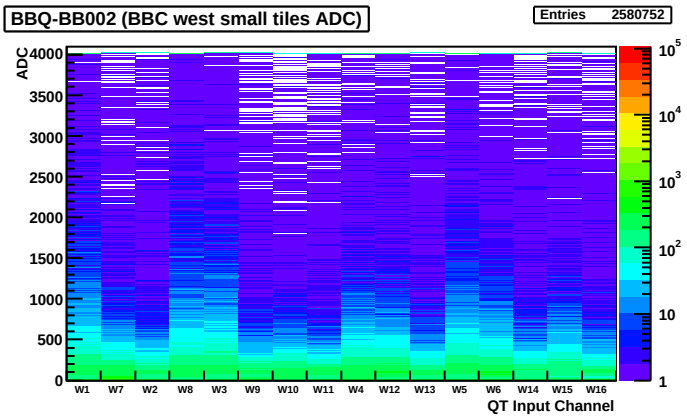

MIX-TF101
Entries 967782
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

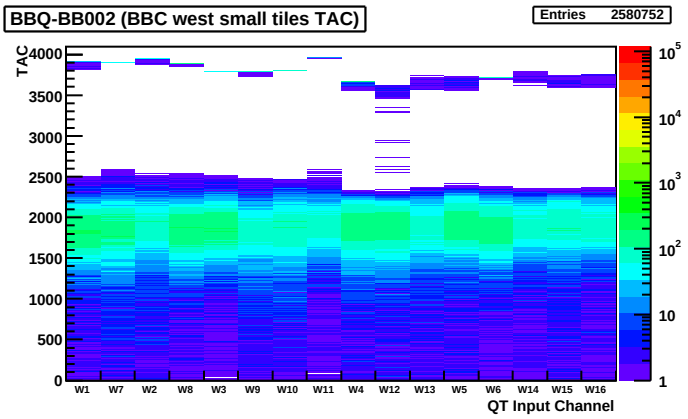

L1-TF201
Entries 161297
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

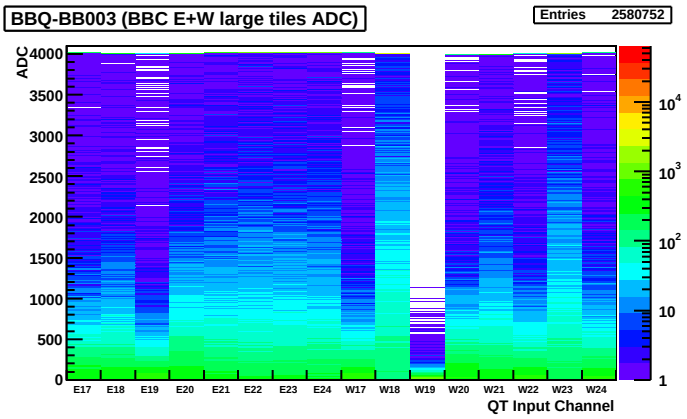

L1-TF201
Entries 967782
Threshold bits
1
0
117447
113838
101274
108292
97436
108329
43850
47459
60023
53005
63861
52968
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

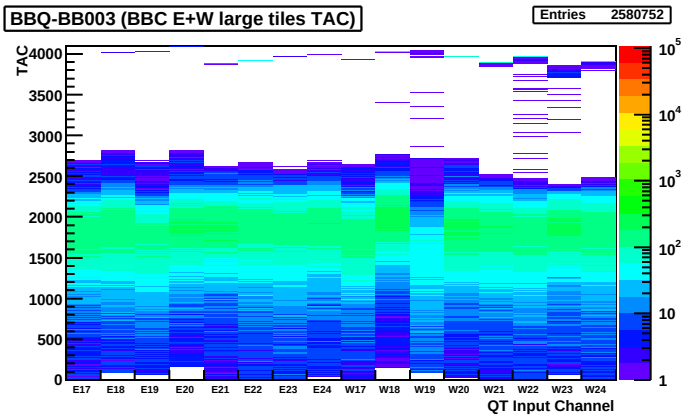

BBQ-BB001 (BBC east small tiles ADC)
Entries 2580752
ADC
QT Input Channel

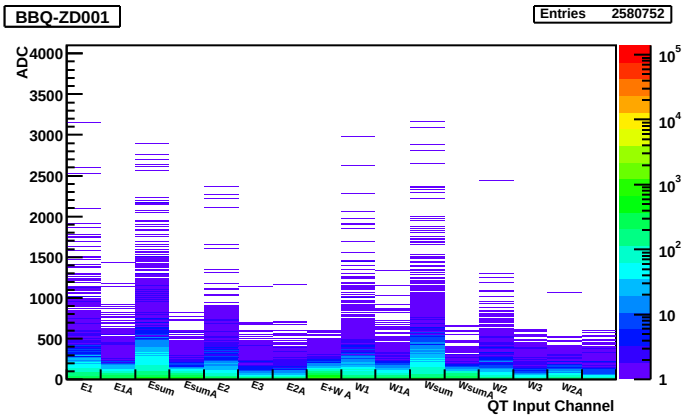

BBQ-BB001 (BBC east small tiles TAC)
Entries 2580752
TAC
QT Input Channel

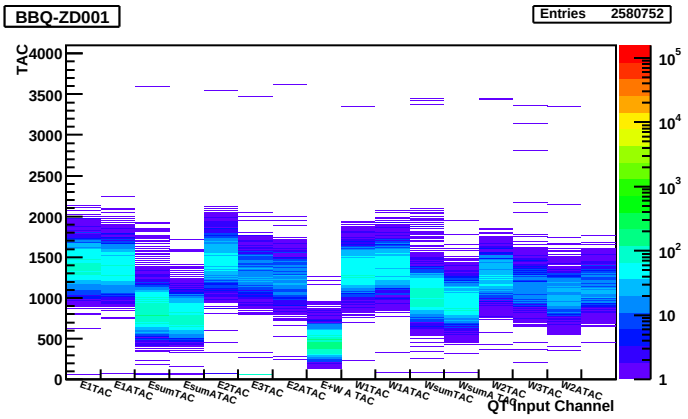

BBQ-BB002 (BBC west small tiles ADC)
Entries 2580752
ADC
QT Input Channel

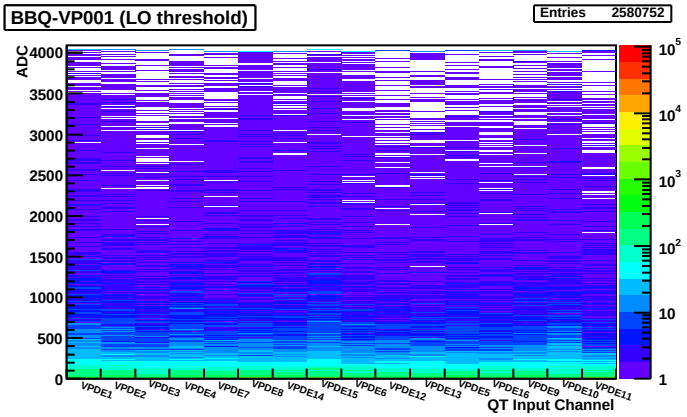

BBQ-BB002 (BBC west small tiles TAC)
Entries 2580752
TAC
QT Input Channel

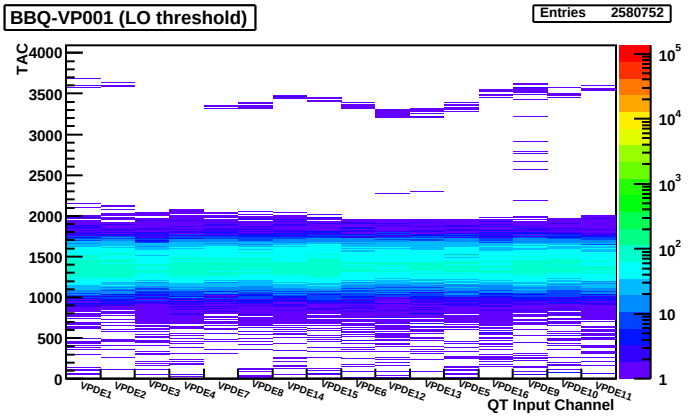

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2580752
ADC
QT Input Channel

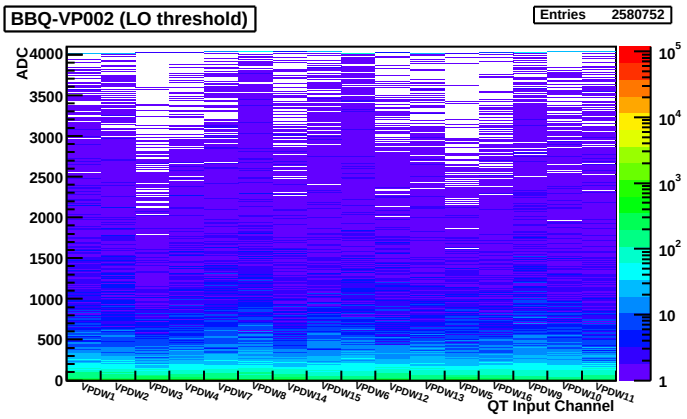

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2580752
TAC
QT Input Channel

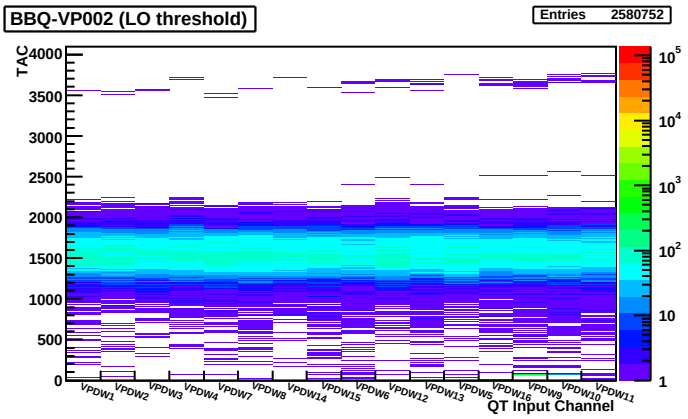

BBQ-ZD001
Entries 2580752
ADC
QT Input Channel

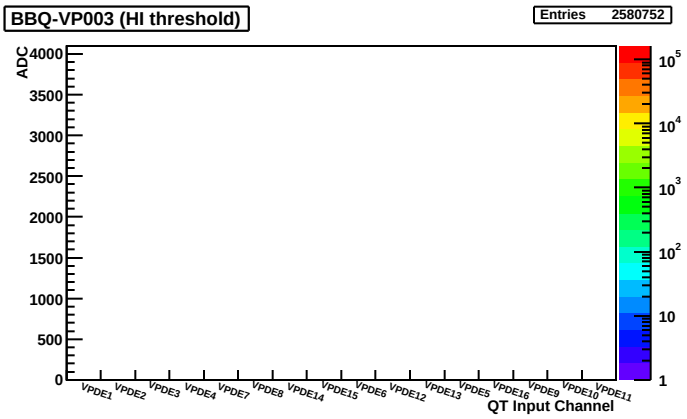

BBQ-ZD001
Entries 2580752
TAC
QT Input Channel

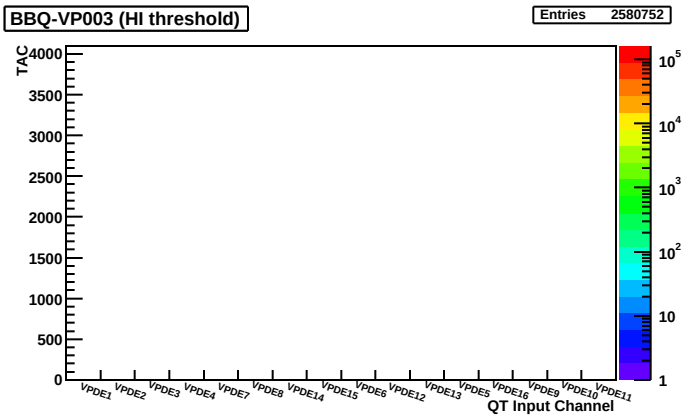

BBQ-VP001 (LO threshold)
Entries 2580752
ADC
QT Input Channel

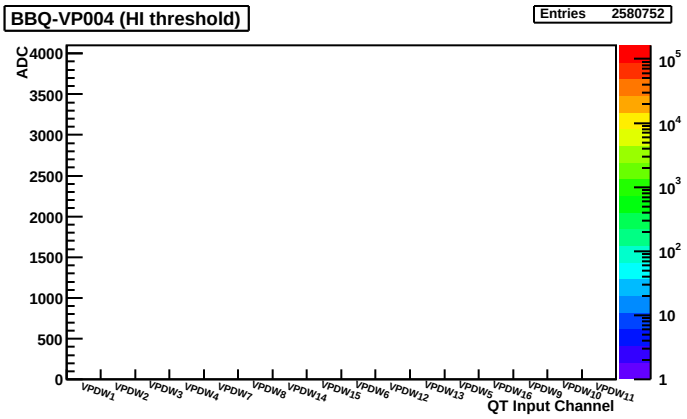

BBQ-VP001 (LO threshold)
Entries 2580752
TAC
QT Input Channel

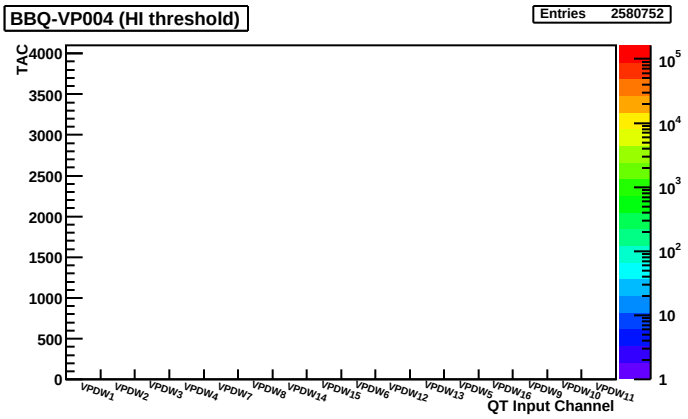

BBQ-VP002 (LO threshold)
Entries 2580752
ADC
QT Input Channel

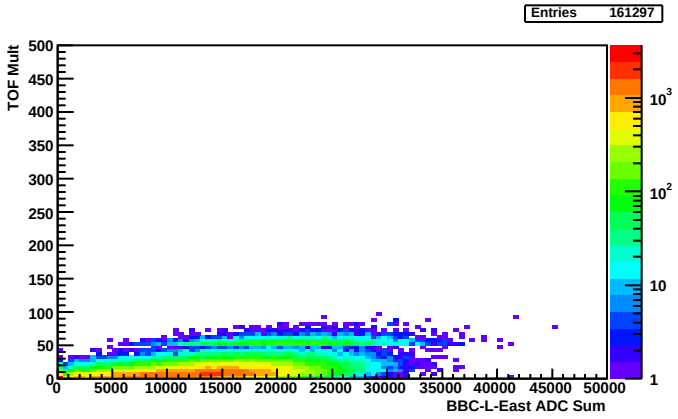

BBQ-VP002 (LO threshold)
Entries 2580752
TAC
QT Input Channel

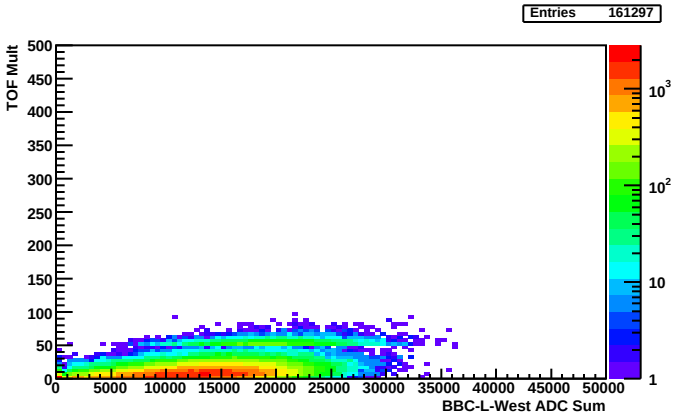

BBQ-VP003 (HI threshold)
Entries 2580752
ADC
QT Input Channel

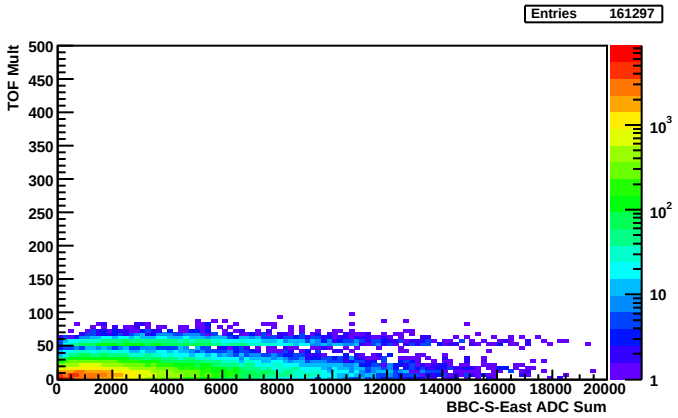

BBQ-VP003 (HI threshold)
Entries 2580752
TAC
QT Input Channel

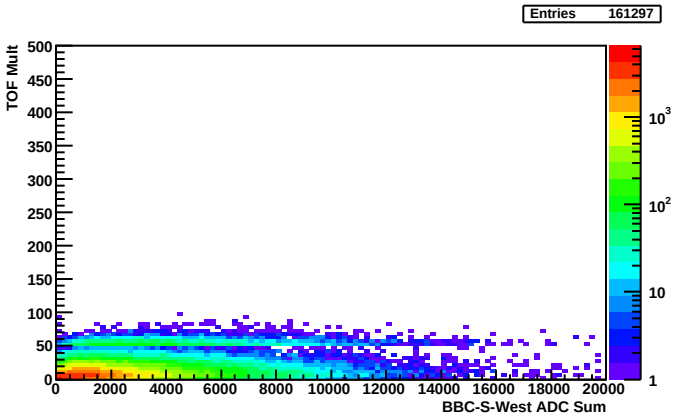

BBQ-VP004 (HI threshold)
Entries 2580752
ADC
QT Input Channel

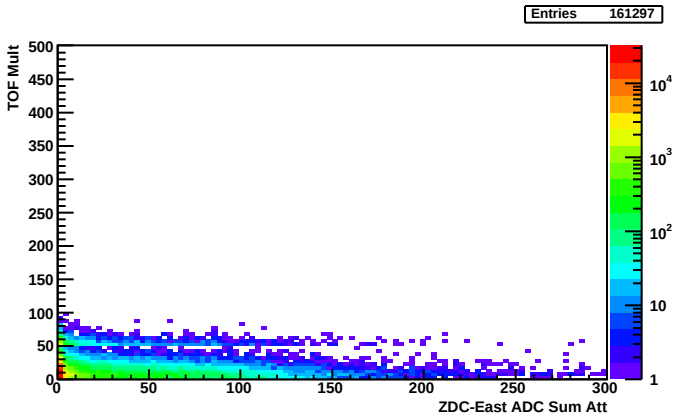

BBQ-VP004 (HI threshold)
Entries 2580752
TAC
QT Input Channel

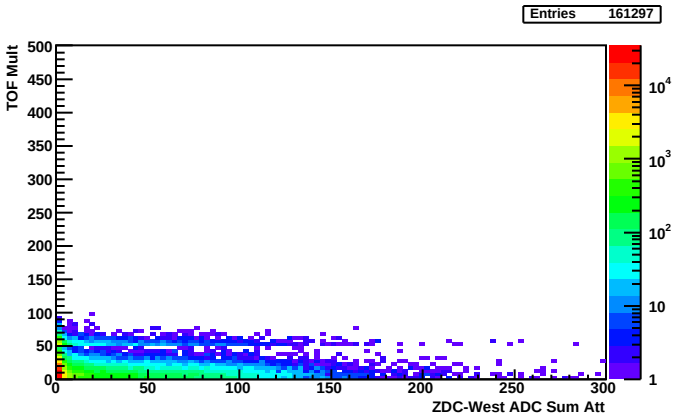

Entries 161297
TOF Mult
BBC-L-East ADC Sum

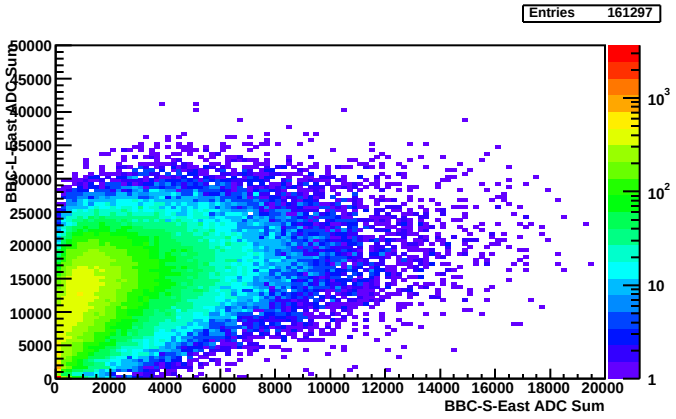

Entries 161297
TOF Mult
BBC-L-West ADC Sum

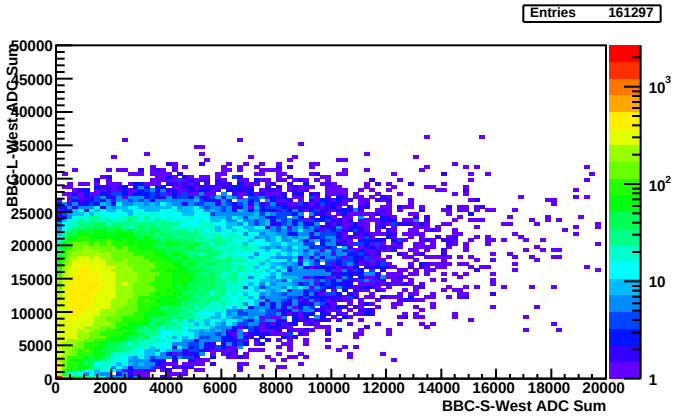

Entries 161297
TOF Mult
BBC-S-East ADC Sum

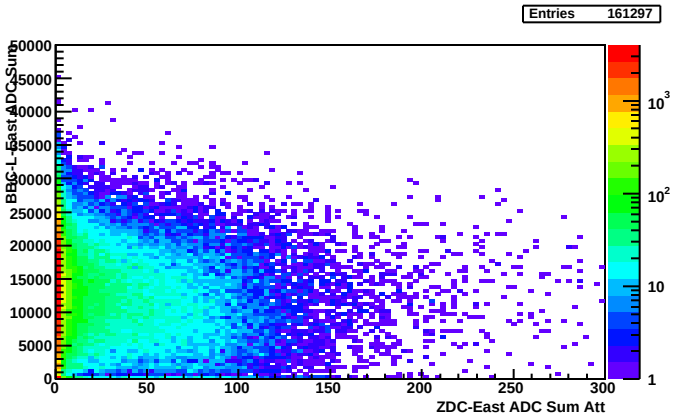

Entries 161297
TOF Mult
BBC-S-West ADC Sum

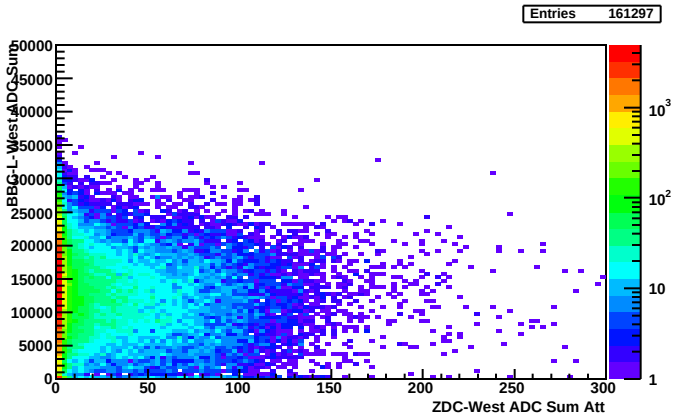

Entries 161297
TOF Mult
ZDC-East ADC Sum Att

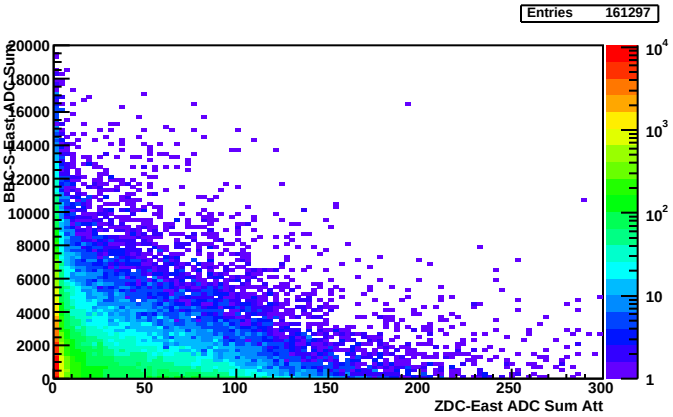

Entries 161297
TOF Mult
ZDC-West ADC Sum Att

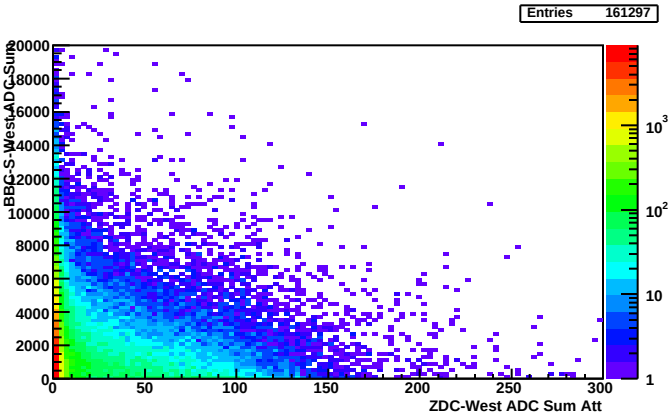

Entries 161297
BBC-L-East ADC Sum
BBC-S-East ADC Sum

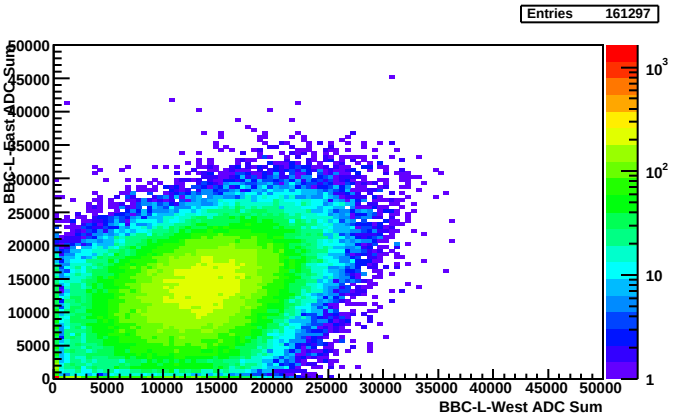

Entries 161297
BBC-L-West ADC Sum
BBC-S-West ADC Sum

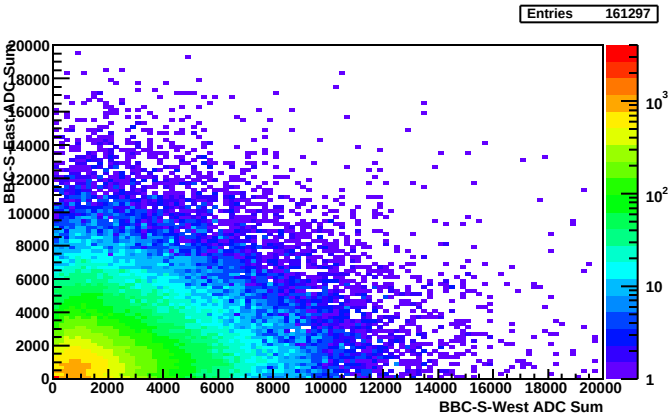

Entries 161297
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

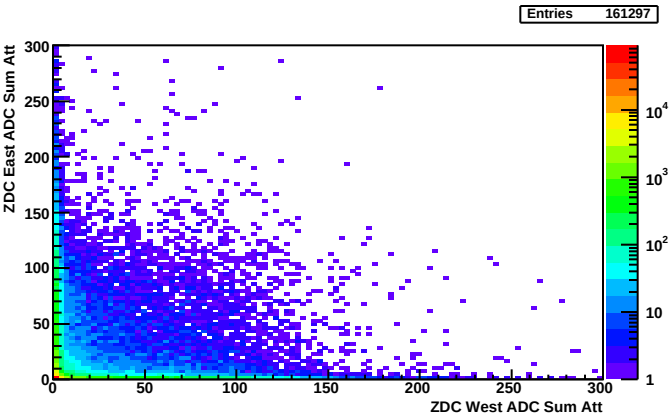

Entries 161297
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

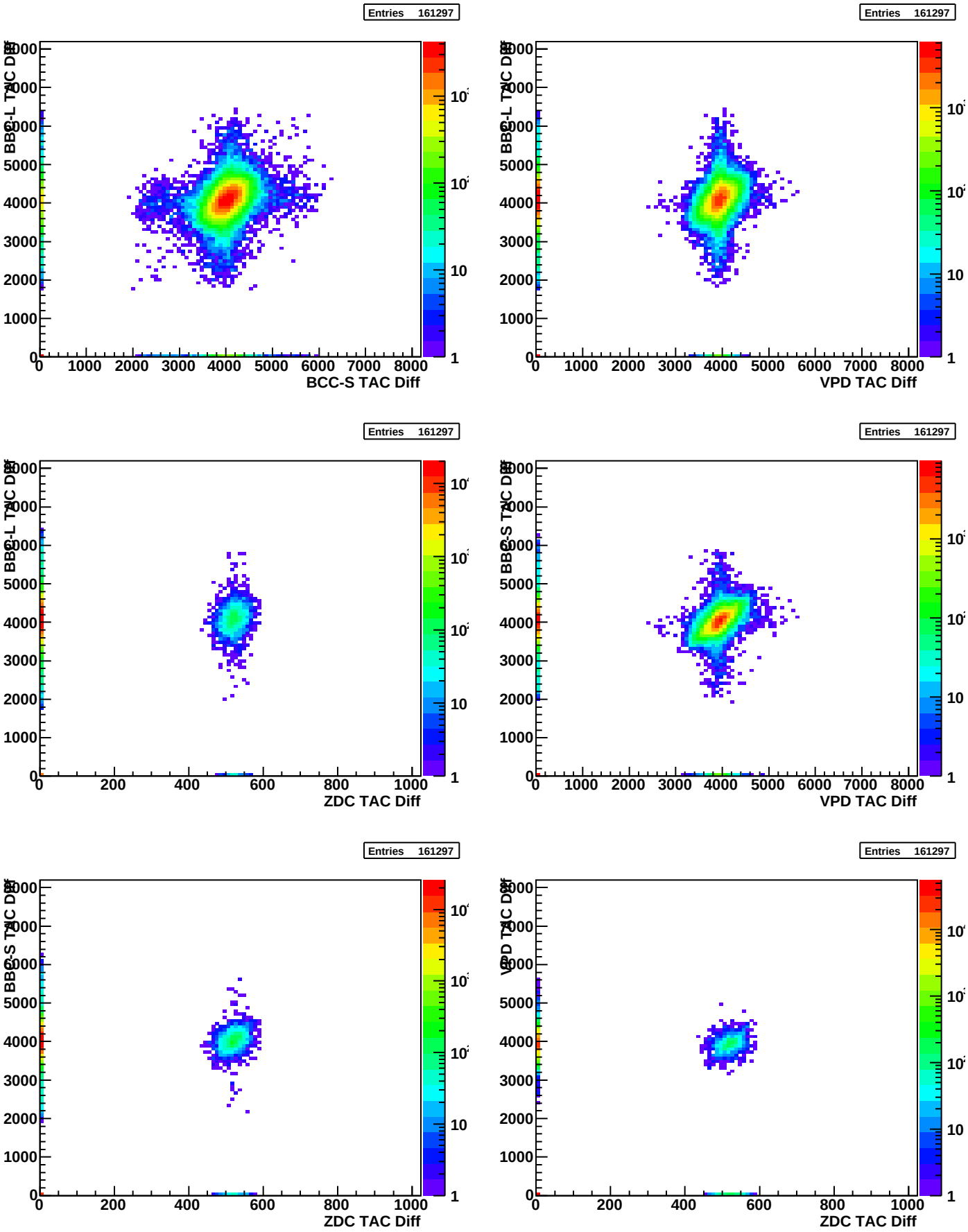

Entries 161297
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

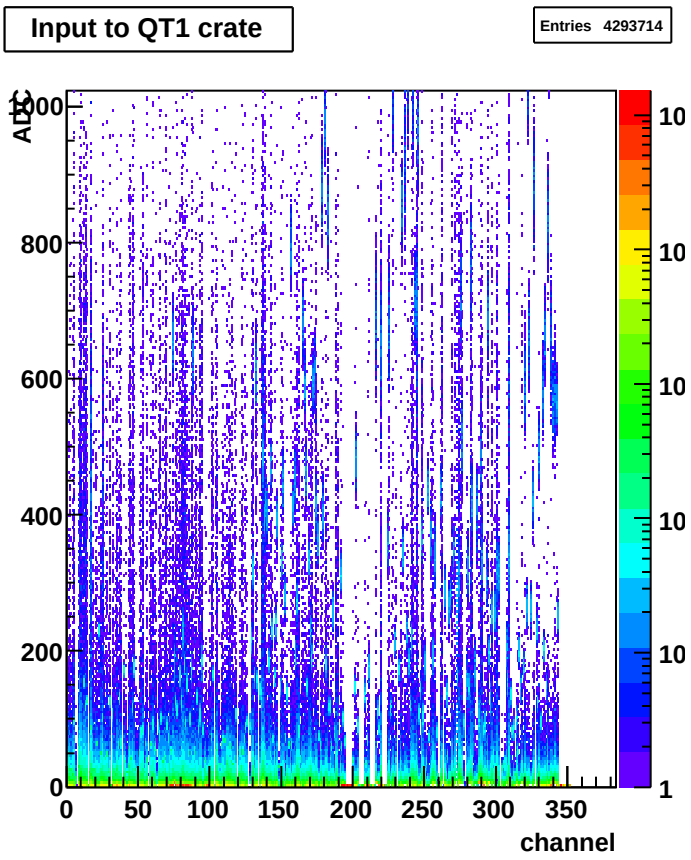

Entries 161297
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

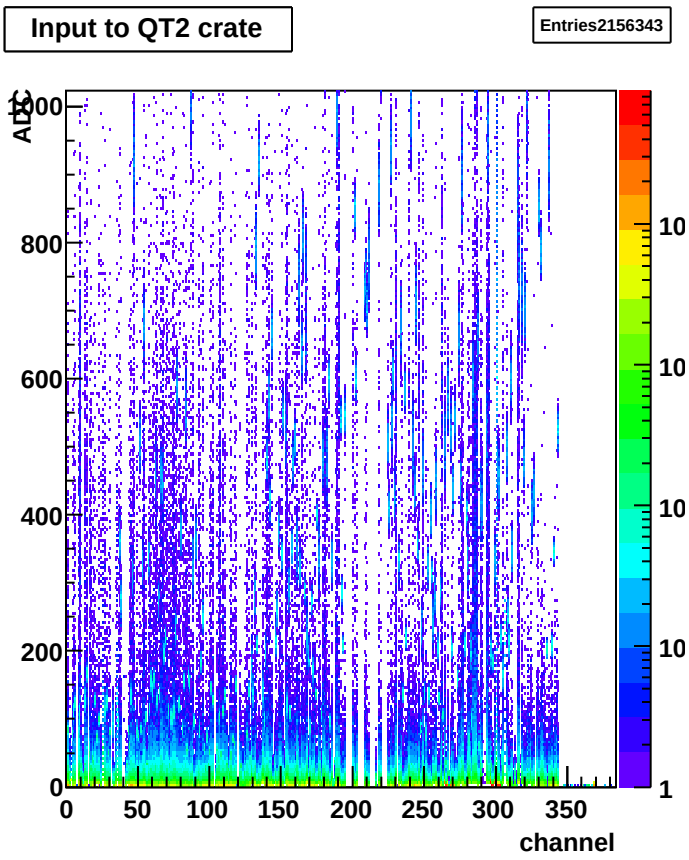

Entries 161297
BBC-L-East ADC Sum
BBC-L-West ADC Sum

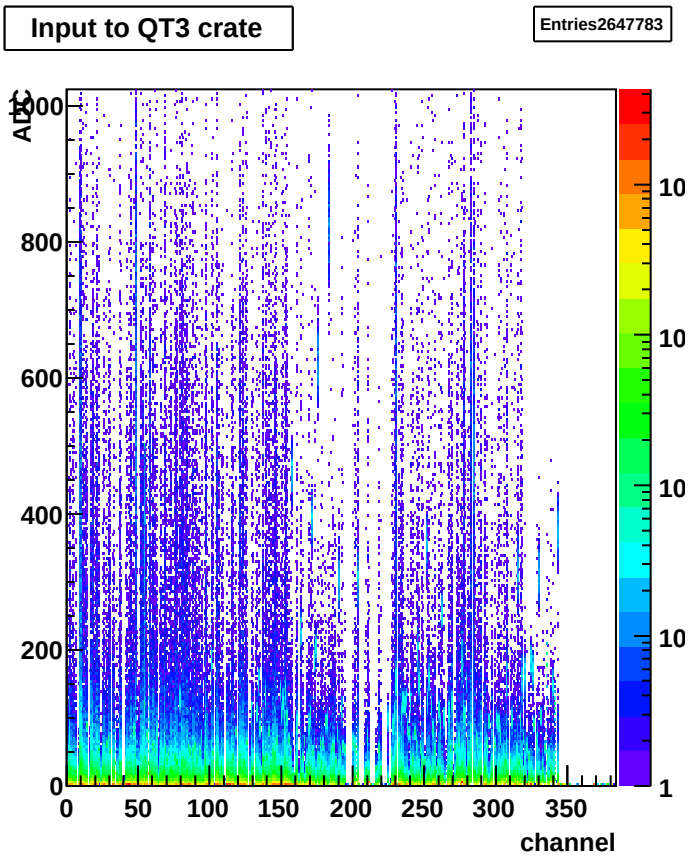

Entries 161297
BBC-S-East ADC Sum
BBC-S-West ADC Sum

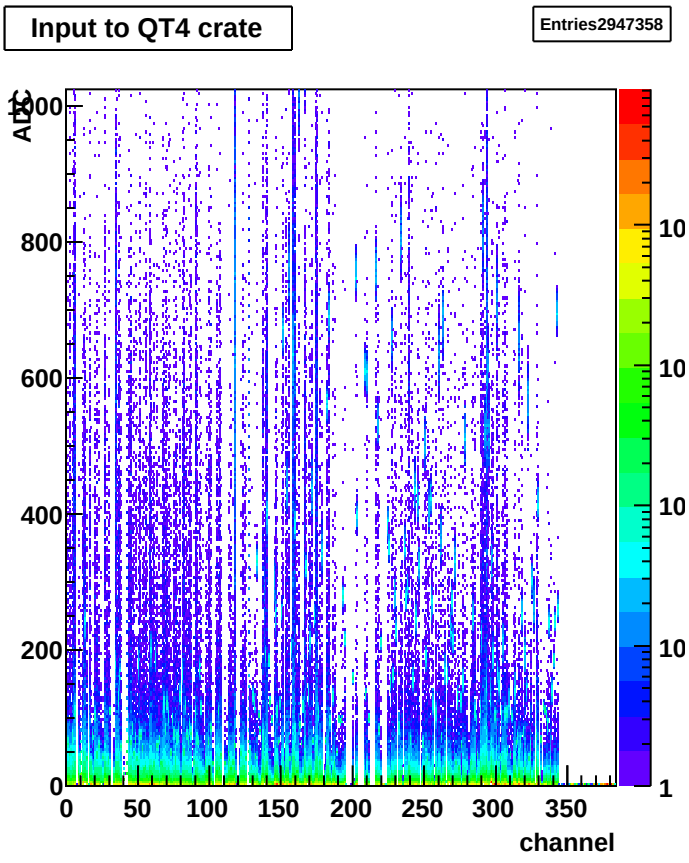

Entries 161297
ZDC East ADC Sum Att
ZDC West ADC Sum Att

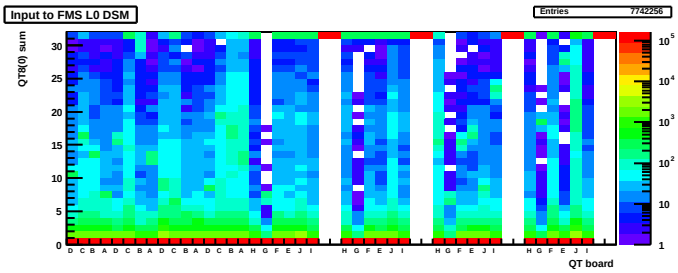

Entries 161297
BBC-L TAC Diff
BCC-S TAC Diff
Entries 161297
BBC-L TAC Diff
VPD TAC Diff
Entries 161297
BBC-L TAC Diff
ZDC TAC Diff
Entries 161297
BBC-S TAC Diff
VPD TAC Diff
Entries 161297
BBC-S TAC Diff
ZDC TAC Diff
Entries 161297
VPD TAC Diff
ZDC TAC Diff

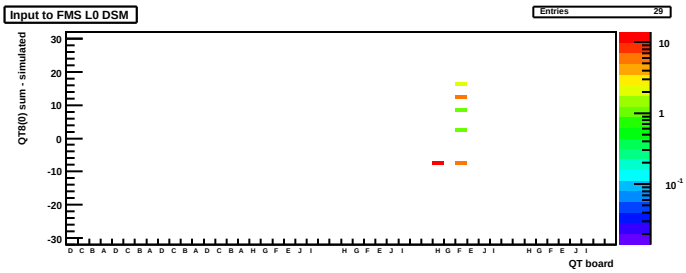

Input to QT1 crate
Entries 4293714
ADC
channel

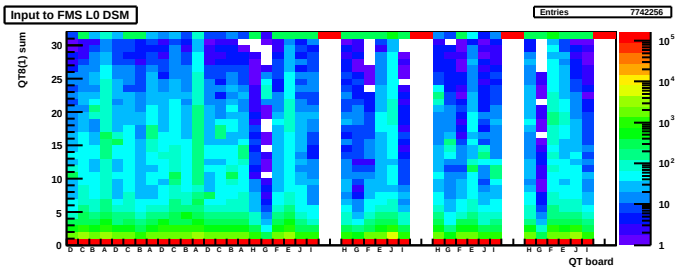

Input to QT2 crate
Entries2156343
ADC
channel

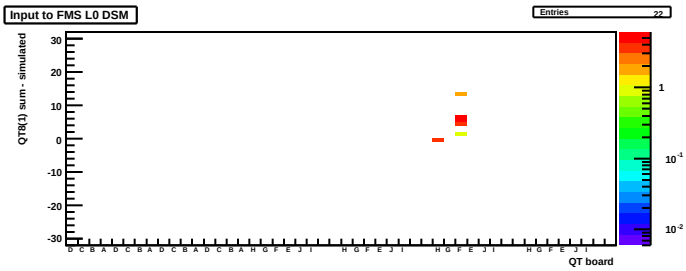

Input to QT3 crate
Entries2647783
ADC
channel

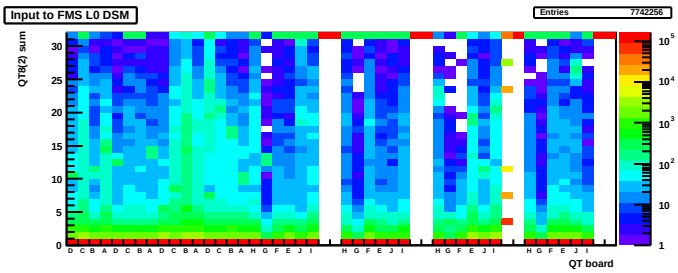

Input to QT4 crate
Entries2947358
ADC
channel

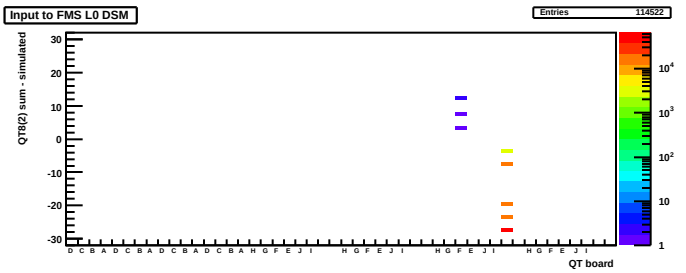

Input to FMS L0 DSM
Entries 7742256
QT8(0) sum
QT board

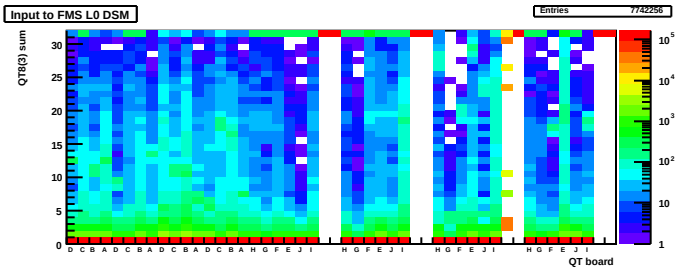

Input to FMS L0 DSM
Entries 29
QT8(0) sum - simulated
QT board

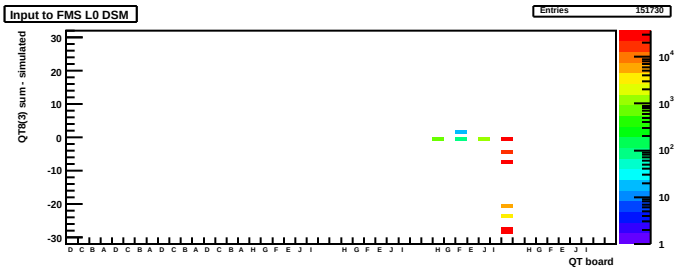

Input to FMS L0 DSM
Entries 7742256
QT8(1) sum
QT board

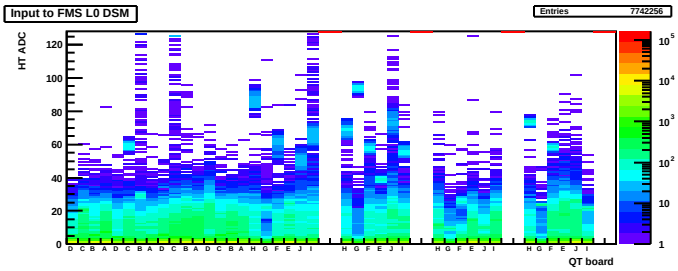

Input to FMS L0 DSM
Entries 22
QT8(1) sum - simulated
QT board

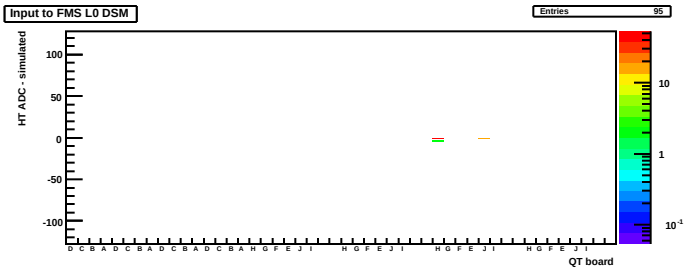

Input to FMS L0 DSM
Entries 7742256
QT8(2) sum
QT board

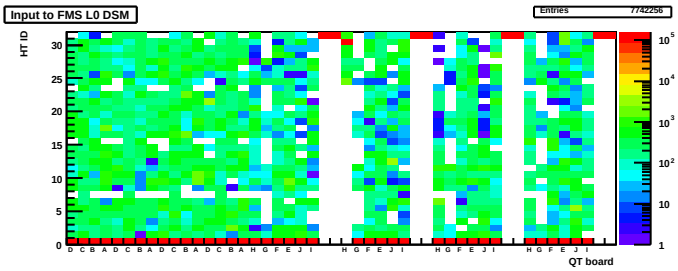

Input to FMS L0 DSM
Entries 114522
QT8(2) sum - simulated
QT board

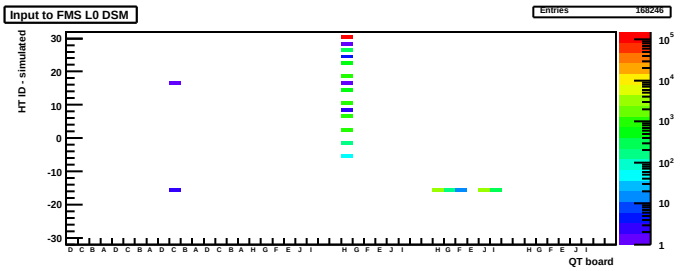

Input to FMS L0 DSM
Entries 7742256
QT8(3) sum
QT board

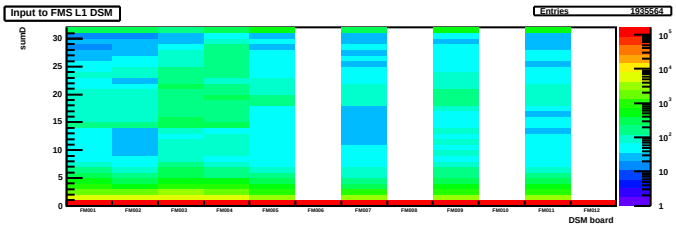

Input to FMS L0 DSM
Entries 151730
QT8(3) sum - simulated
QT board

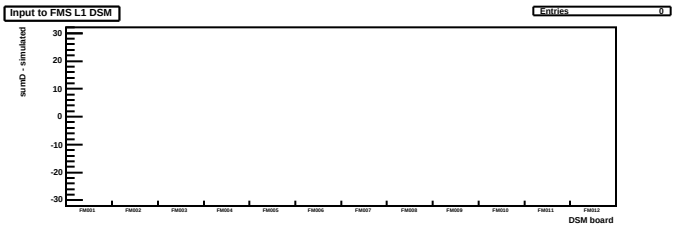

Input to FMS L0 DSM
Entries 7742256
HT ADC
QT board

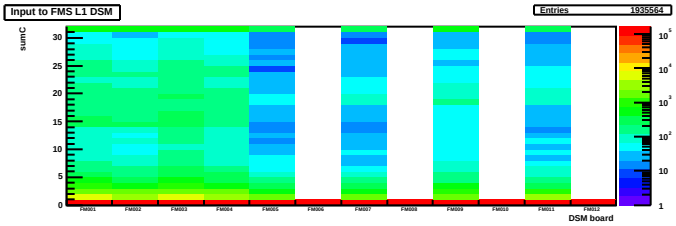

Input to FMS L0 DSM
Entries 95
HT ADC - simulated
QT board

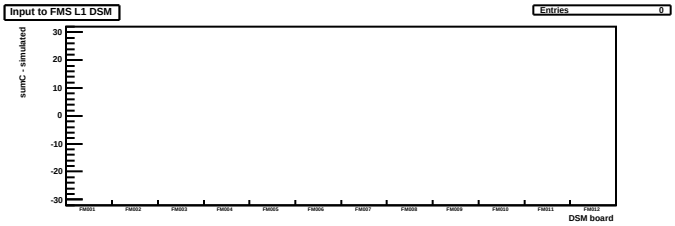

Input to FMS L0 DSM
Entries 7742256
HT ID
QT board

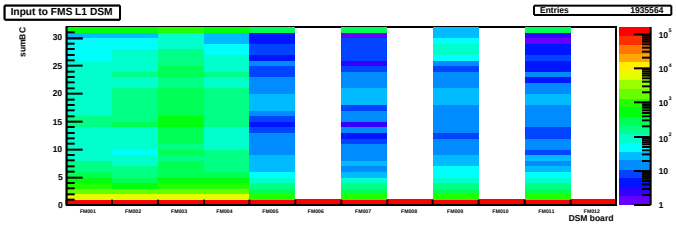

Input to FMS L0 DSM
Entries 168246
HT ID - simulated
QT board

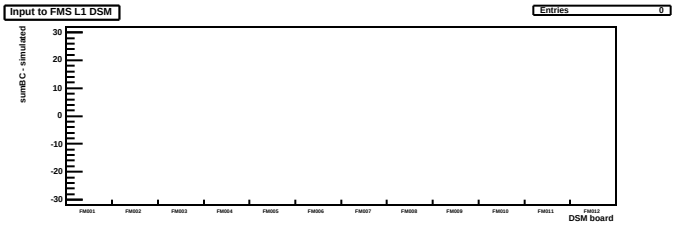

Input to FMS L1 DSM
Entries 1935564
sumD
DSM board

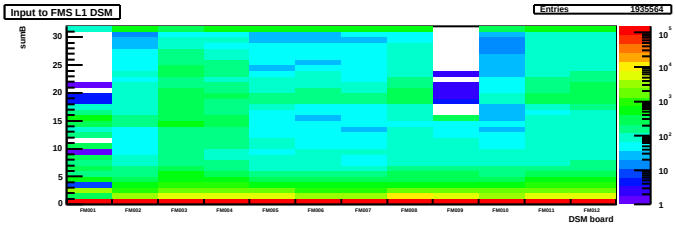

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

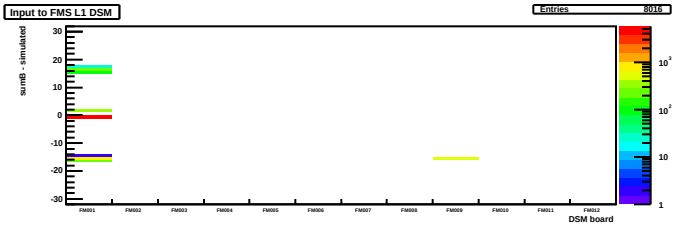

Input to FMS L1 DSM
Entries 1935564
sumC
DSM board

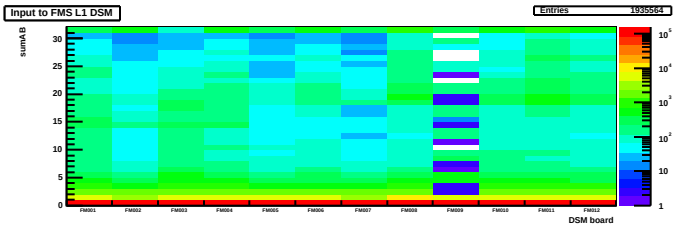

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

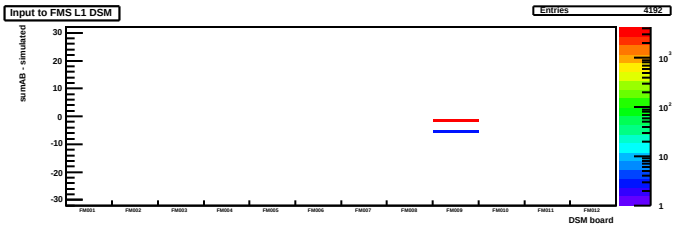

Input to FMS L1 DSM
Entries 1935564
sumBC
DSM board

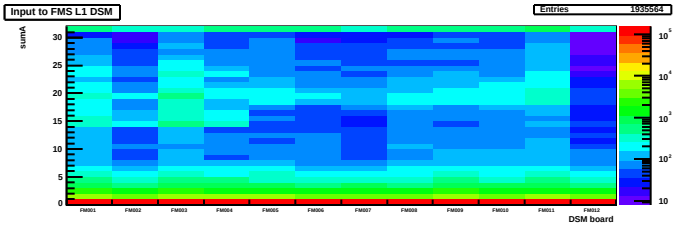

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

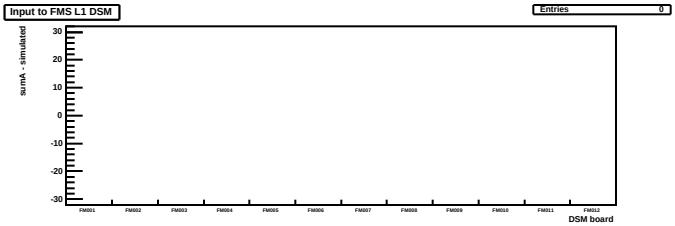

Input to FMS L1 DSM
Entries 1935564
sumB
DSM board

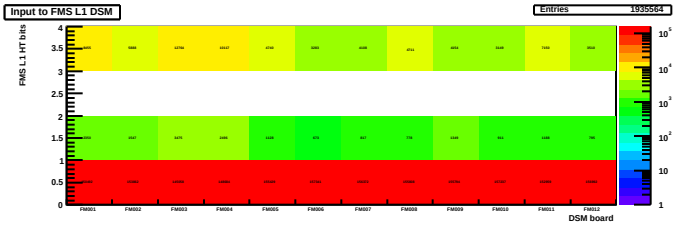

Input to FMS L1 DSM
Entries 8016
sumB - simulated
DSM board

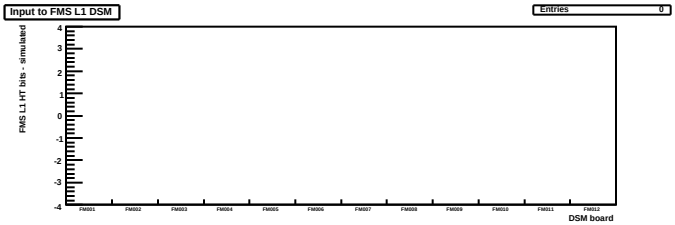

Input to FMS L1 DSM
Entries 1935564
sumAB
DSM board

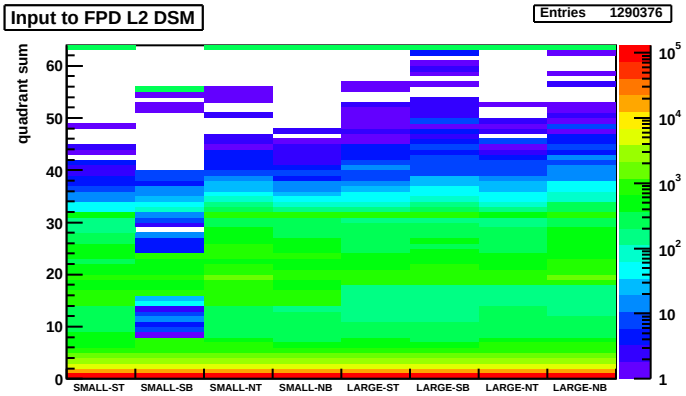

Input to FMS L1 DSM
Entries 4192
sumAB - simulated
DSM board

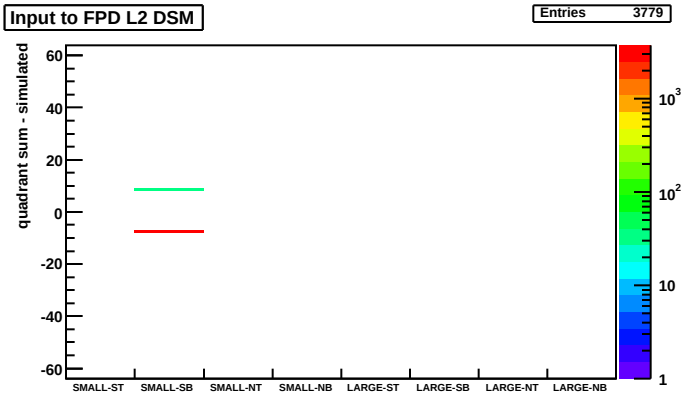

Input to FMS L1 DSM
Entries 1935564
sumA
DSM board

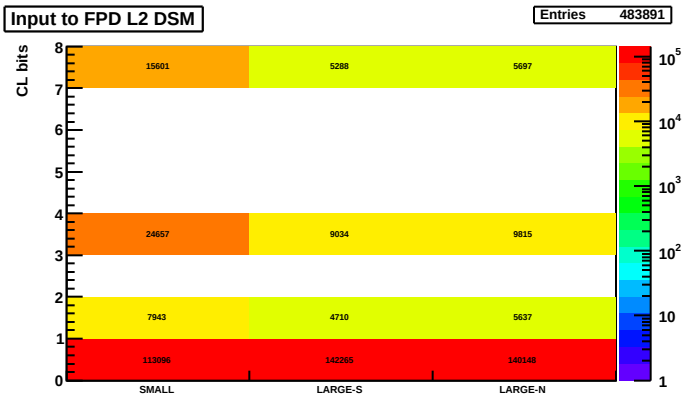

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

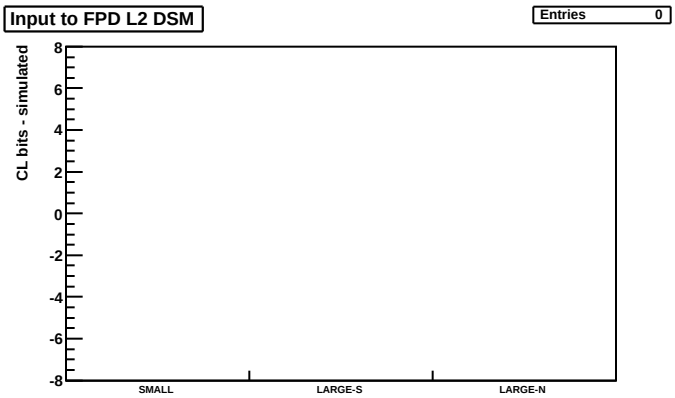

Input to FMS L1 DSM
Entries 1935564
FMS L1 HT bits
DSM board

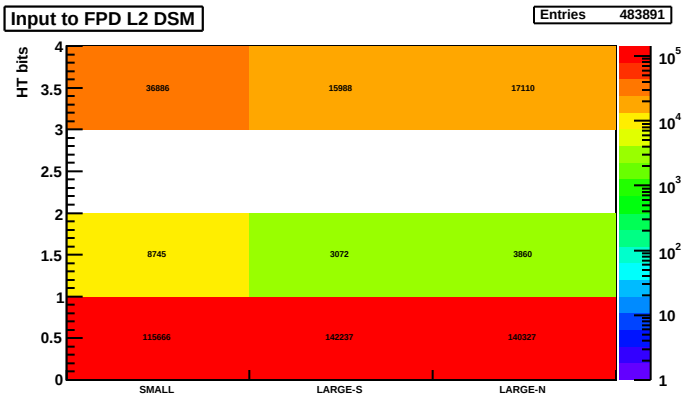

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

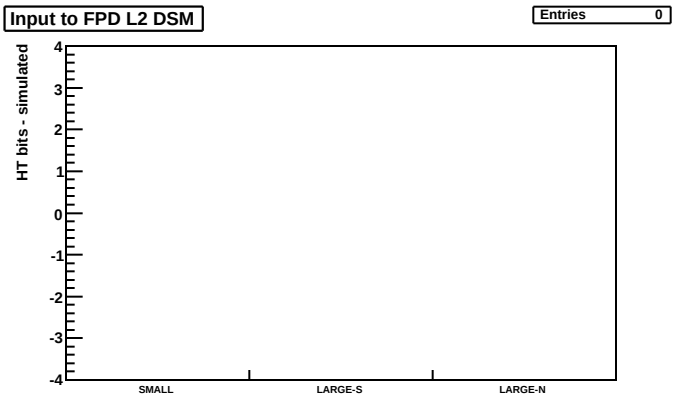

Input to FPD L2 DSM
Entries 1290376
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

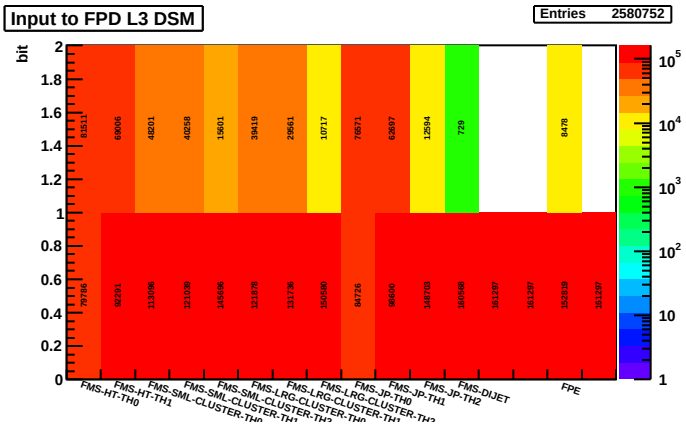

Input to FPD L2 DSM
Entries 3779
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

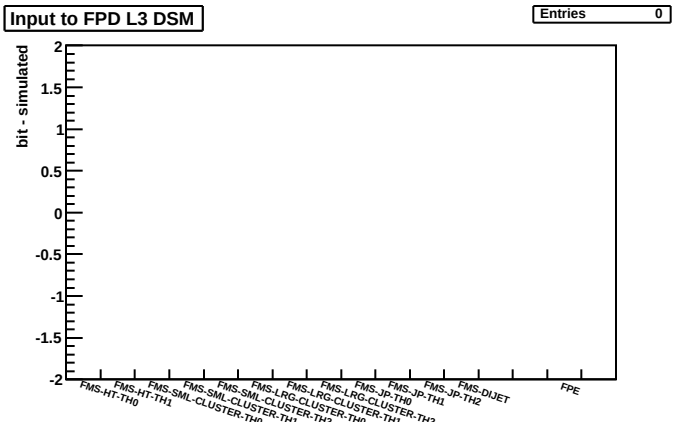

Input to FPD L2 DSM
Entries 483891
CL bits
15601 5288 5697
24657 9034 9815
7943 4710 5637
113096 142265 140148
SMALL LARGE-S LARGE-N

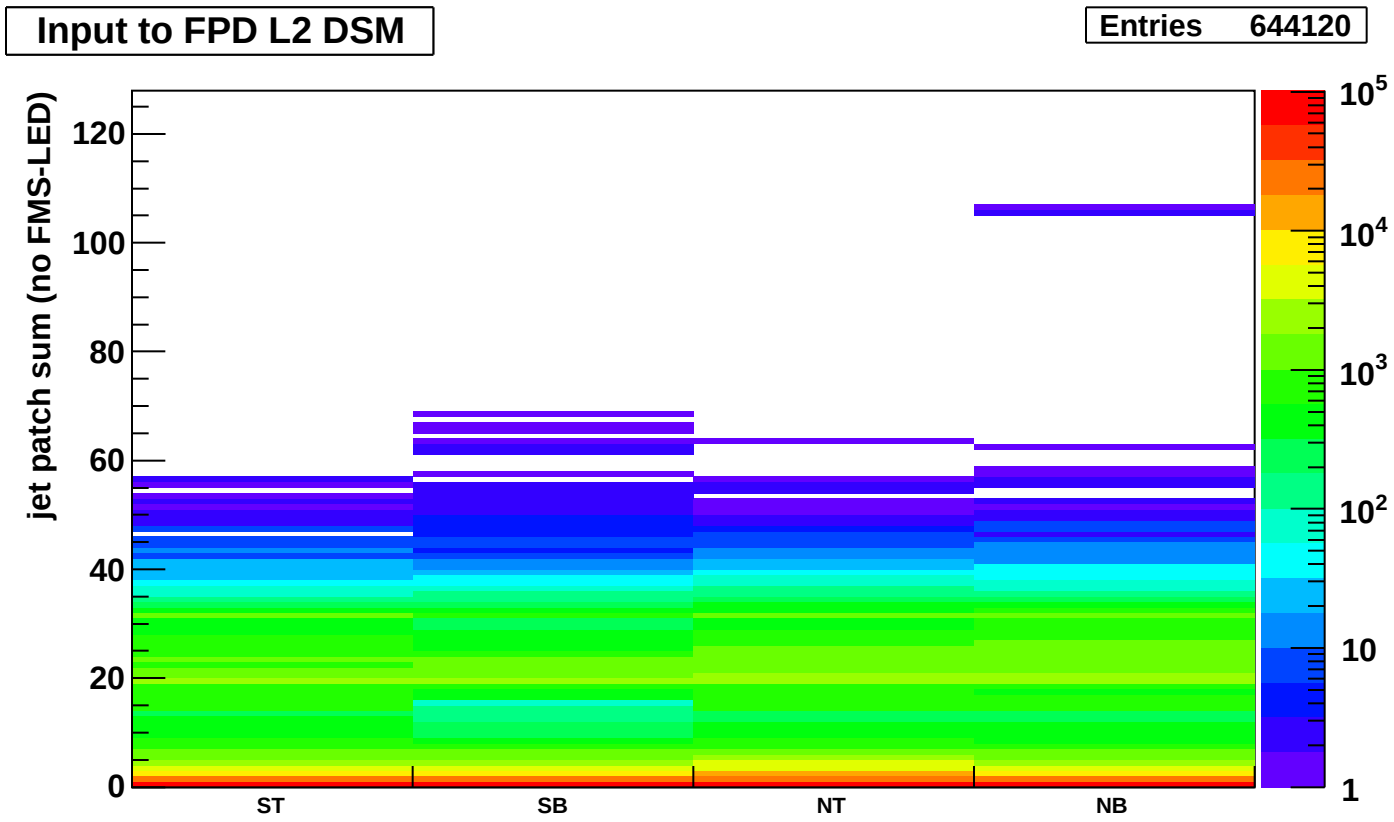

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL LARGE-S LARGE-N

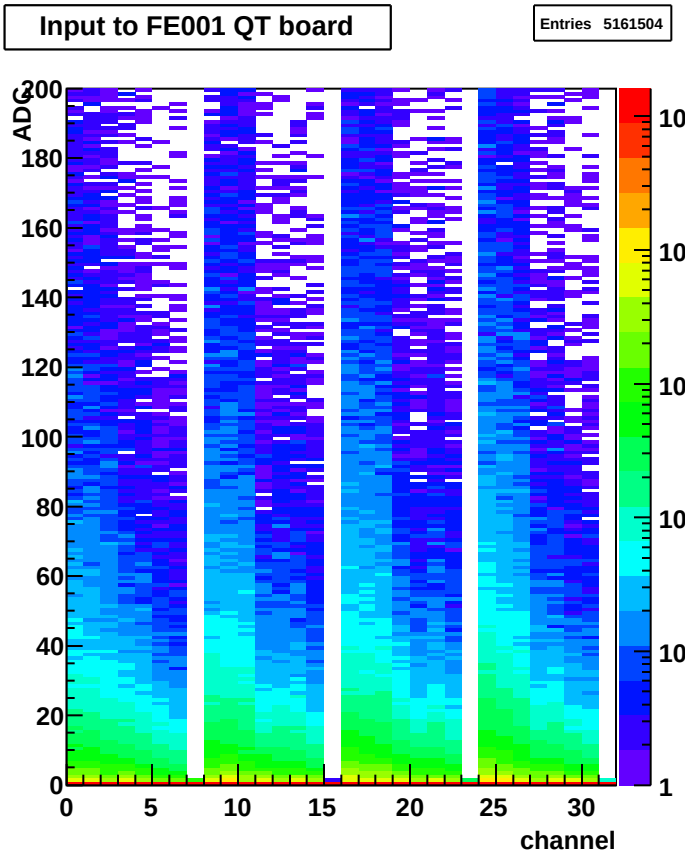

Input to FPD L2 DSM
Entries 483891
HT bits
36886 15988 17110
8745 3072 3860
115666 142237 140327
SMALL LARGE-S LARGE-N

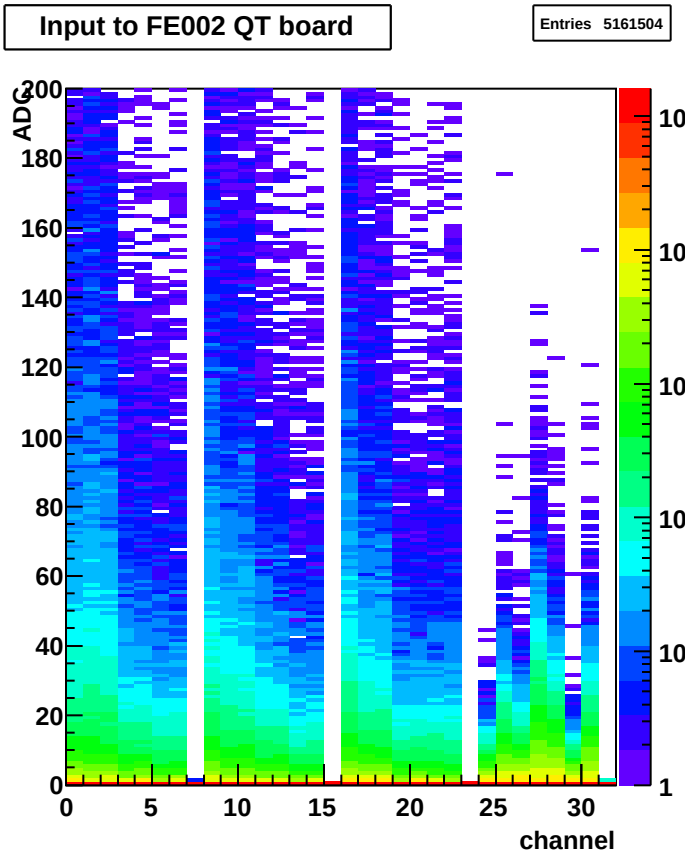

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

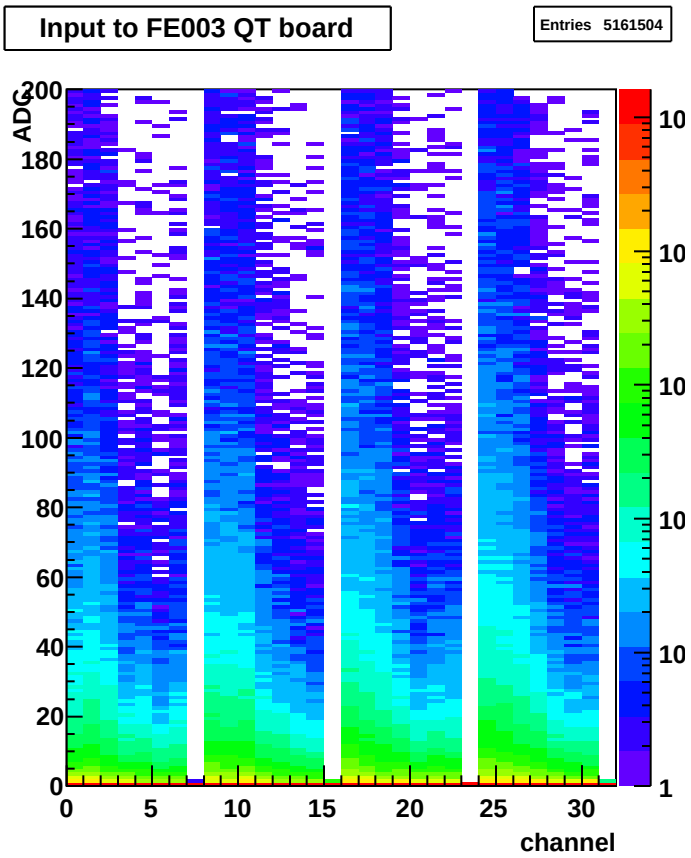

Input to FPD L3 DSM
Entries 2580752
bit
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

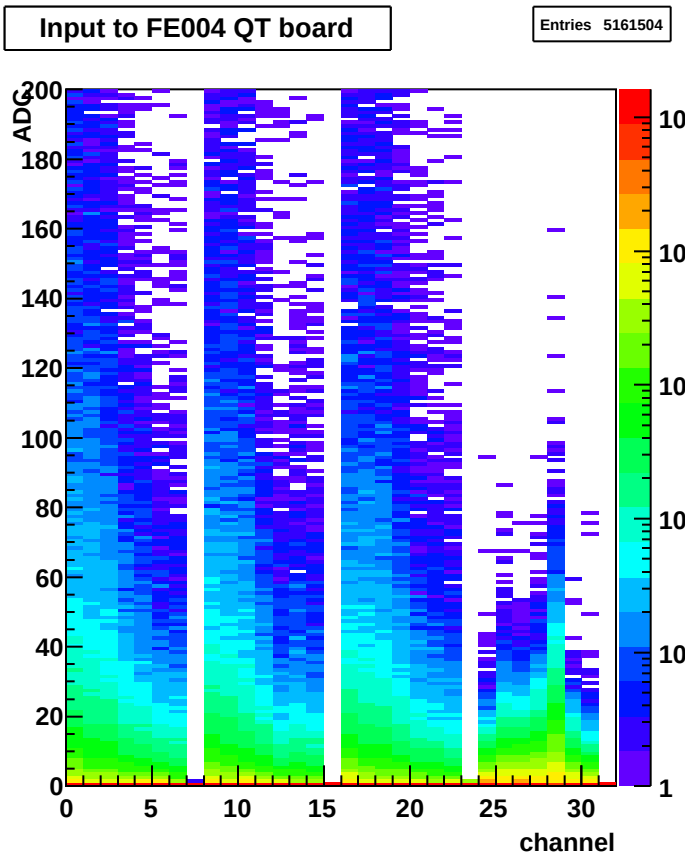

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

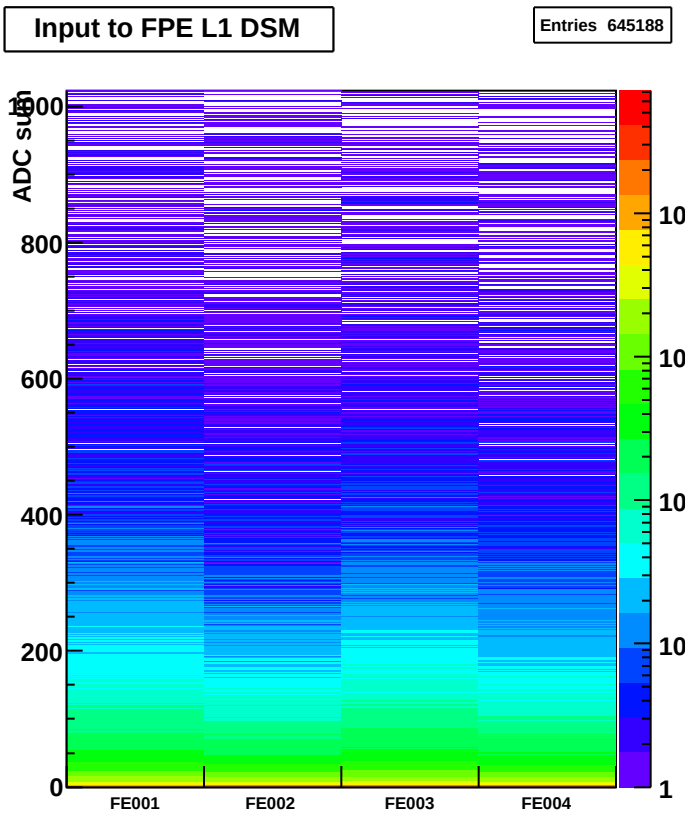

Input to FPD L2 DSM
Entries 644120
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

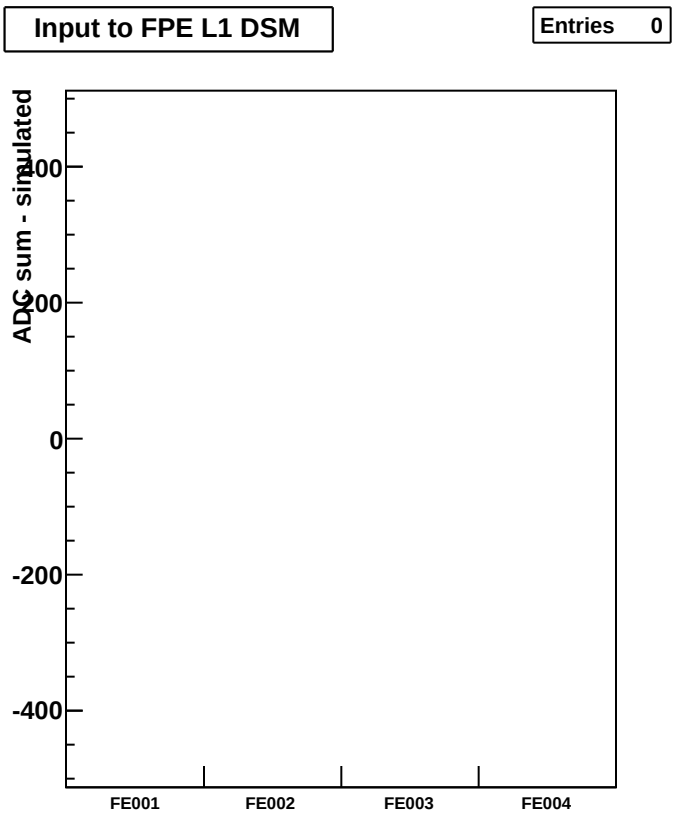

Input to FE001 QT board
Entries 5161504
ADC
channel

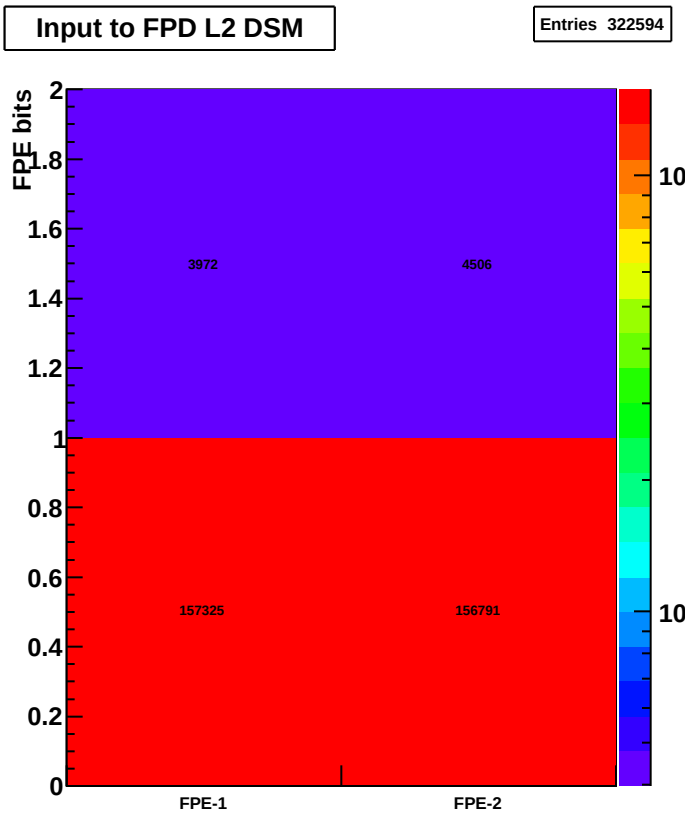

Input to FE002 QT board
Entries 5161504
ADC
channel

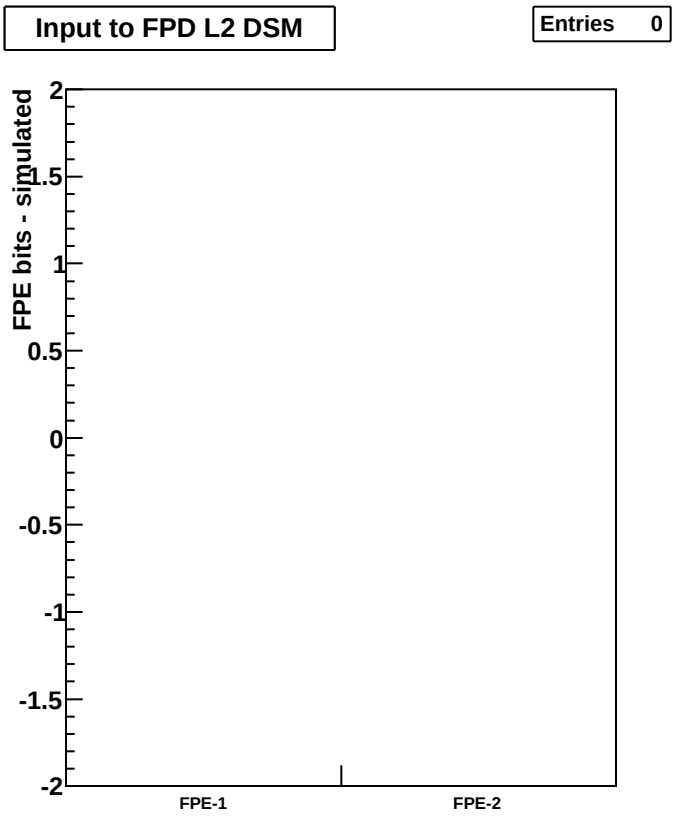

Input to FE003 QT board
Entries 5161504
ADC
channel


Input to FE004 QT board
Entries 5161504
ADC
channel


Input to FPE L1 DSM
Entries 645188
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 322594
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
3972
4506
157325
156791
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2