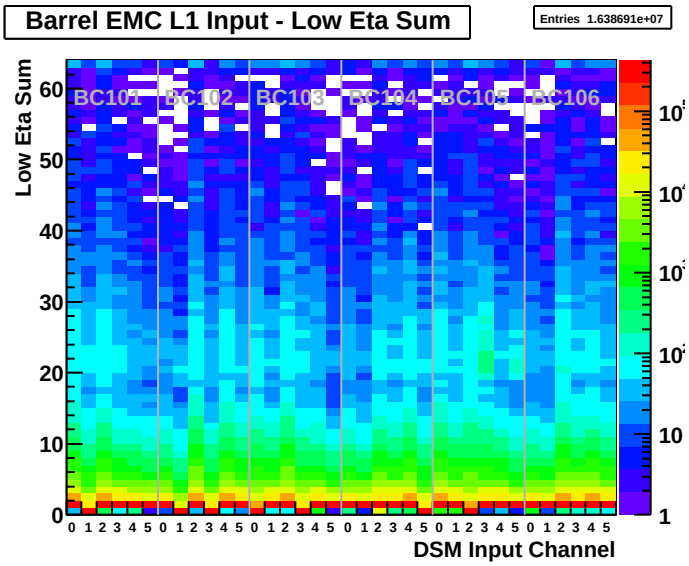
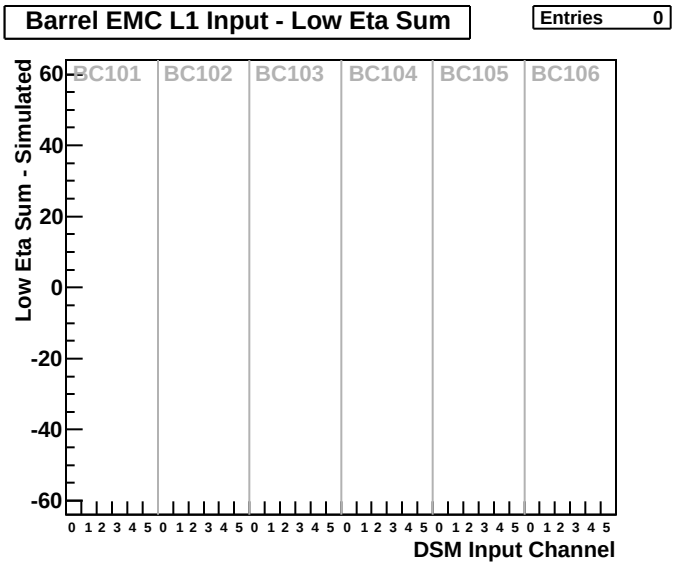
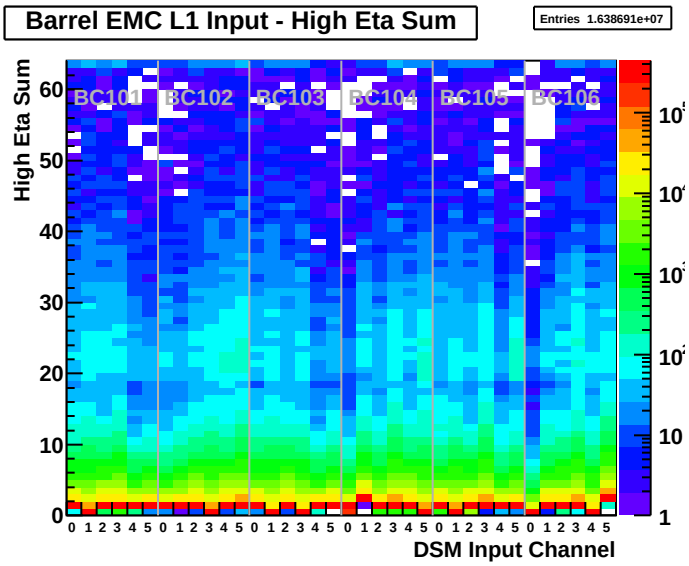
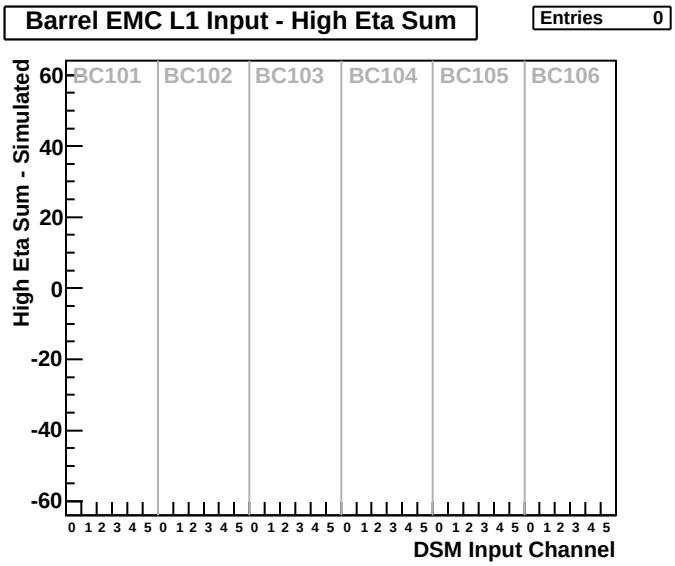
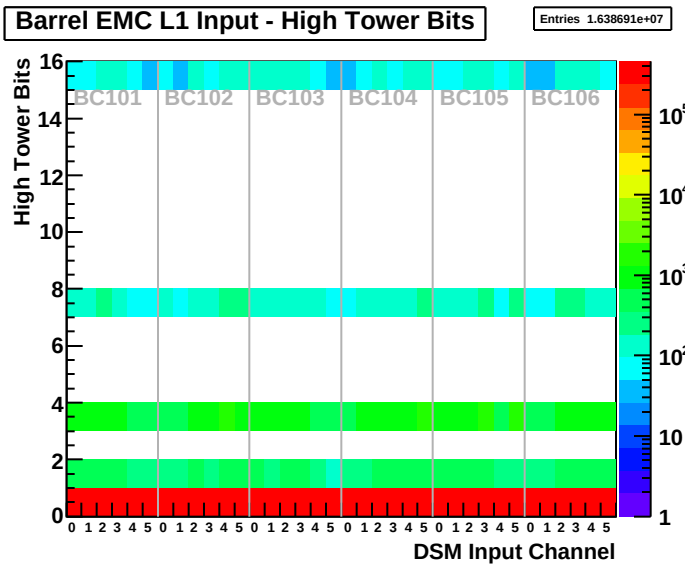
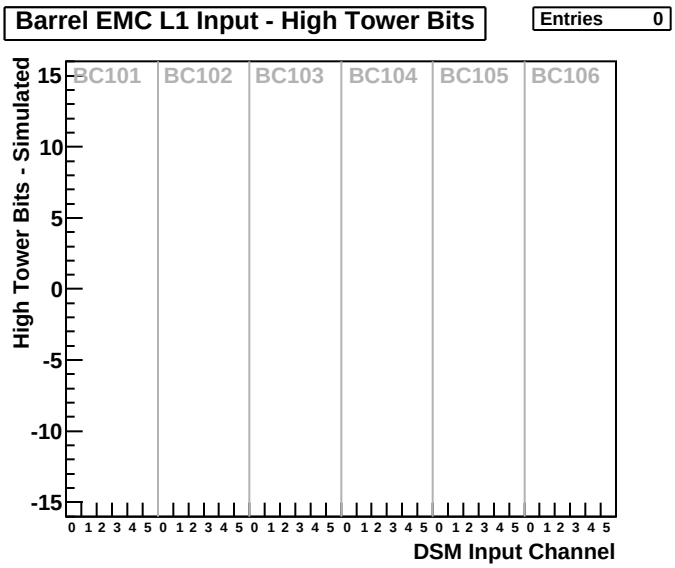

Barrel EMC L1 Input - Low Eta Sum
Entries 1.638691e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

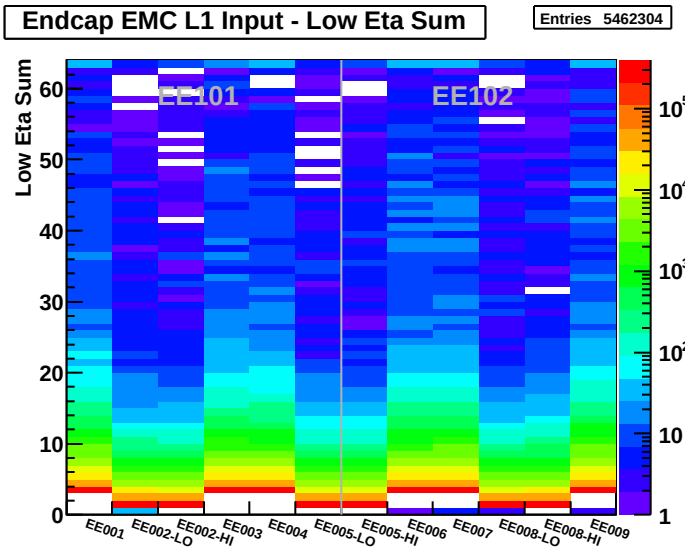

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

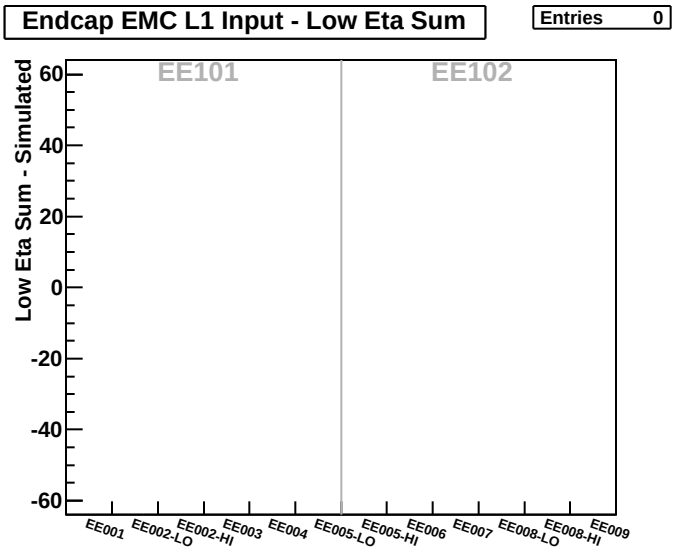

Barrel EMC L1 Input - High Eta Sum
Entries 1.638691e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

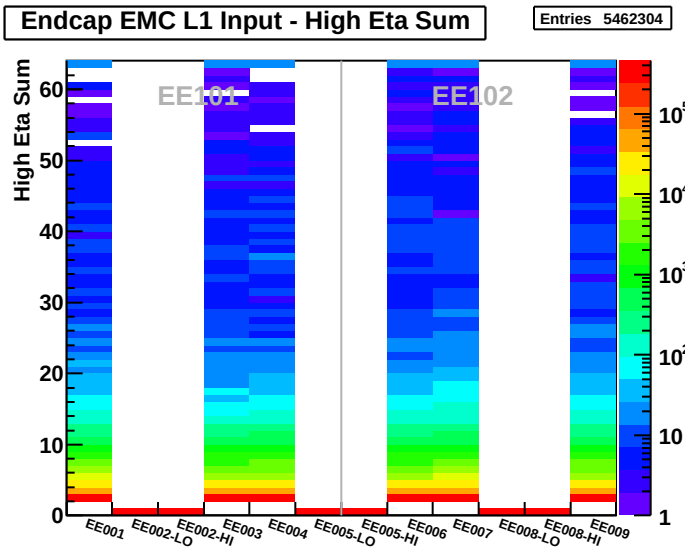

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

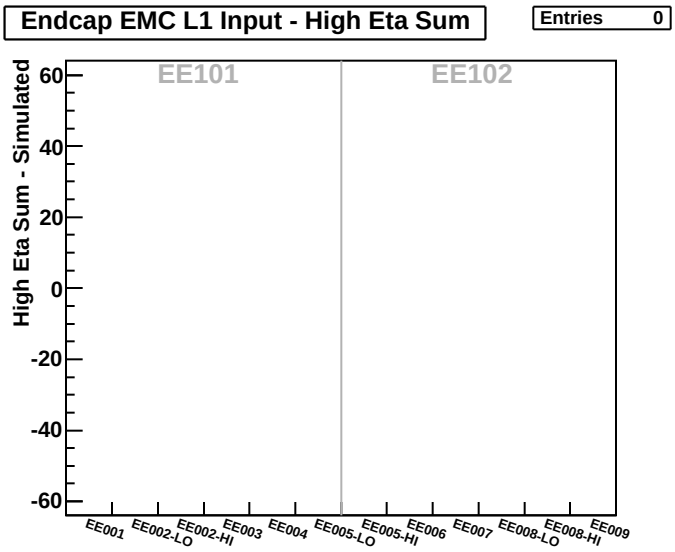

Barrel EMC L1 Input - High Tower Bits
Entries 1.638691e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

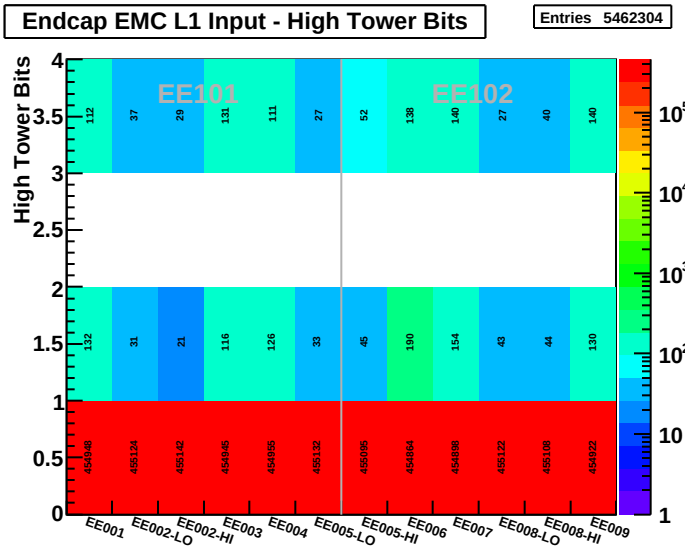

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

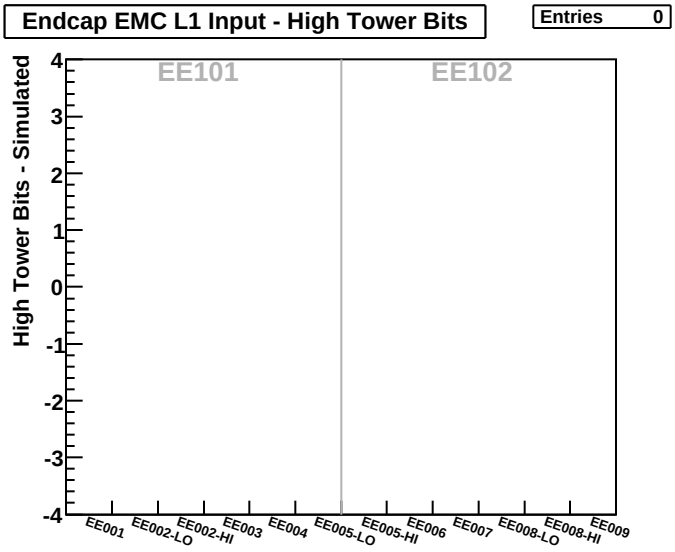

Endcap EMC L1 Input - Low Eta Sum
Entries 5462304
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

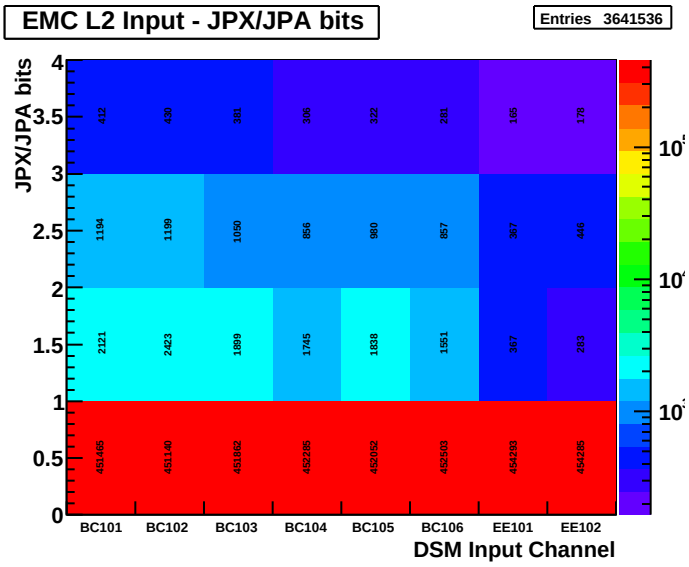

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

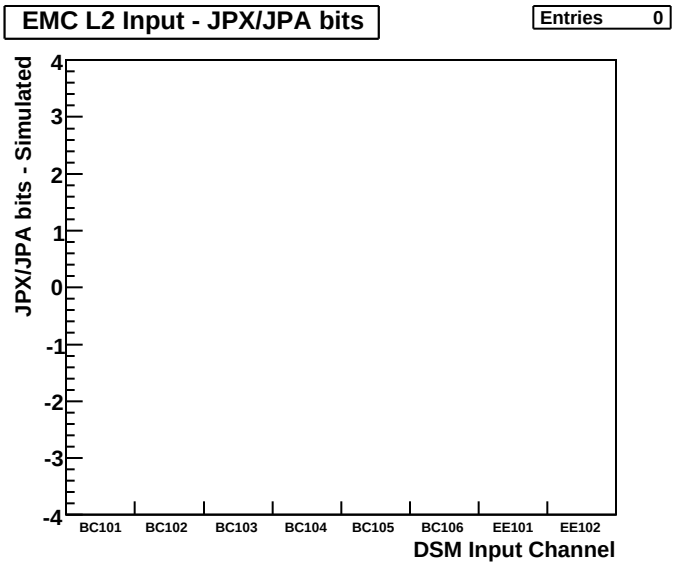

Endcap EMC L1 Input - High Eta Sum
Entries 5462304
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

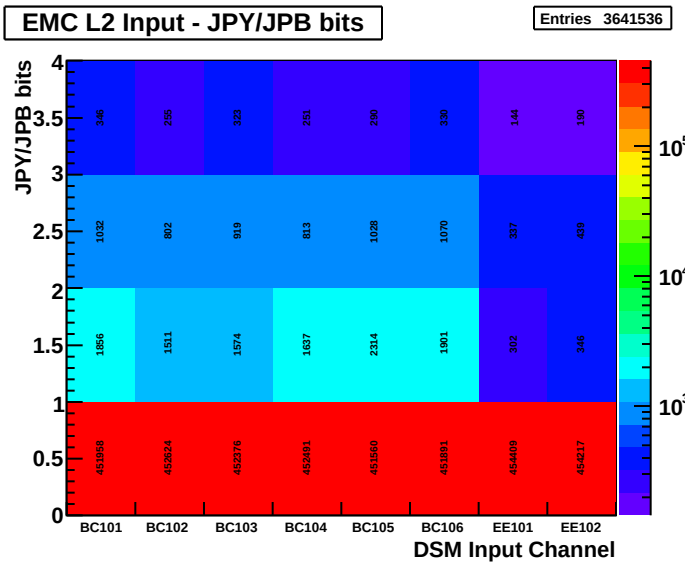

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

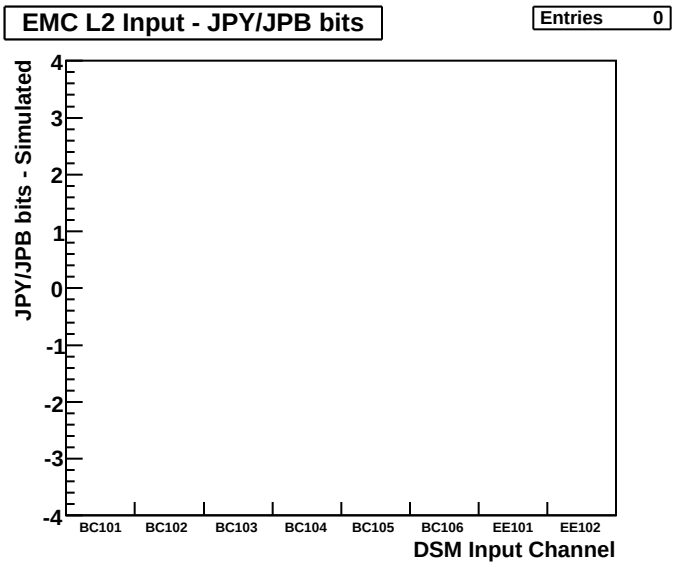

Endcap EMC L1 Input - High Tower Bits
Entries 5462304
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

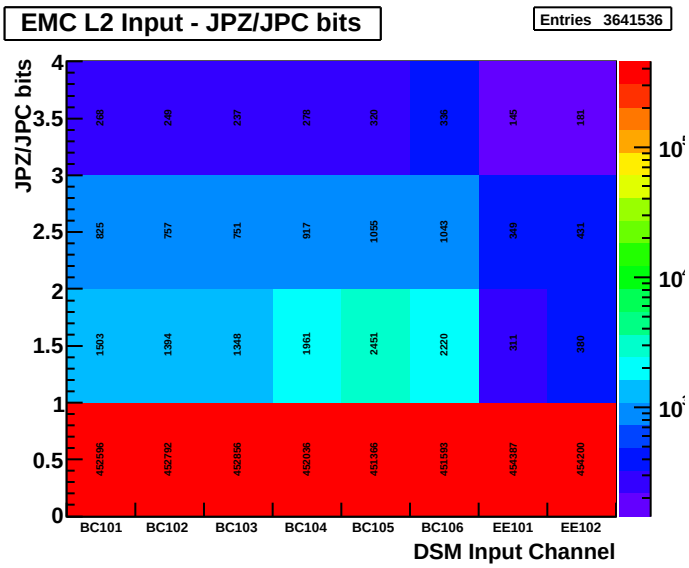

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

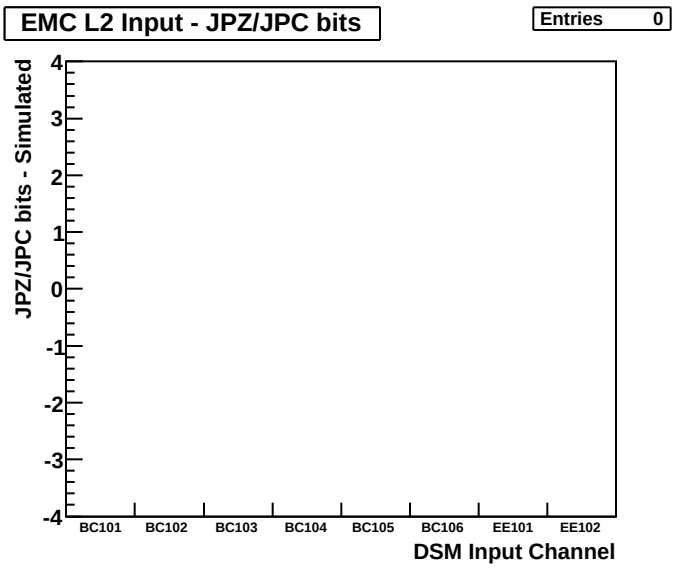

EMC L2 Input - JPX/JPA bits
Entries 3641536
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
412 430 381 306 322 281 165 178
1194 1199 1050 856 980 857 387 446
2121 2423 1899 1745 1838 1551 367 233
451465 451140 451862 452285 452052 452503 454293 454285

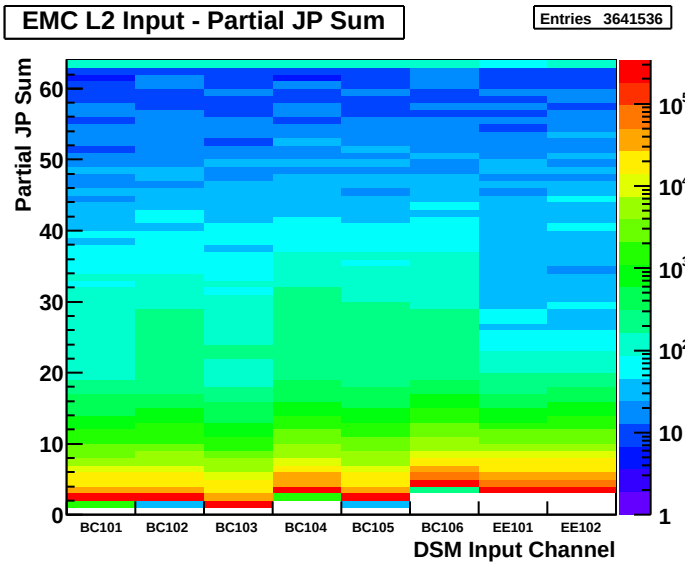

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

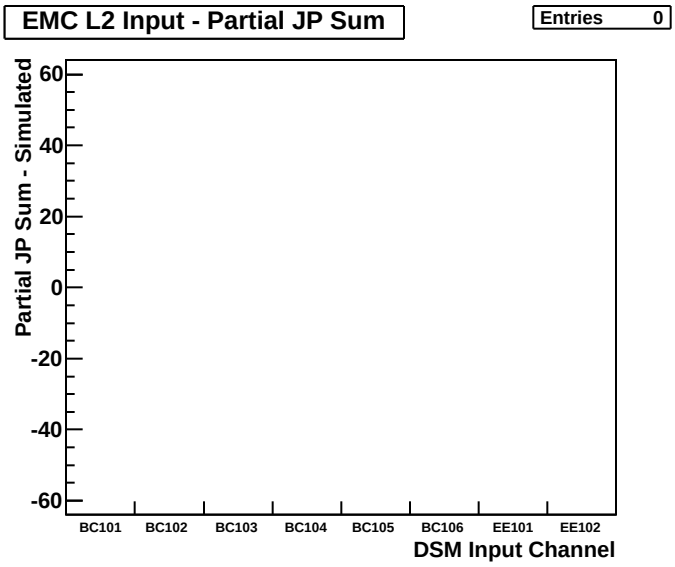

EMC L2 Input - JPY/JPB bits
Entries 3641536
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
346 255 323 251 290 330 144 190
1032 802 919 813 1028 1070 337 439
1856 1511 1574 1637 2314 1901 302 346
451958 452824 452276 452491 451560 451891 454409 454217

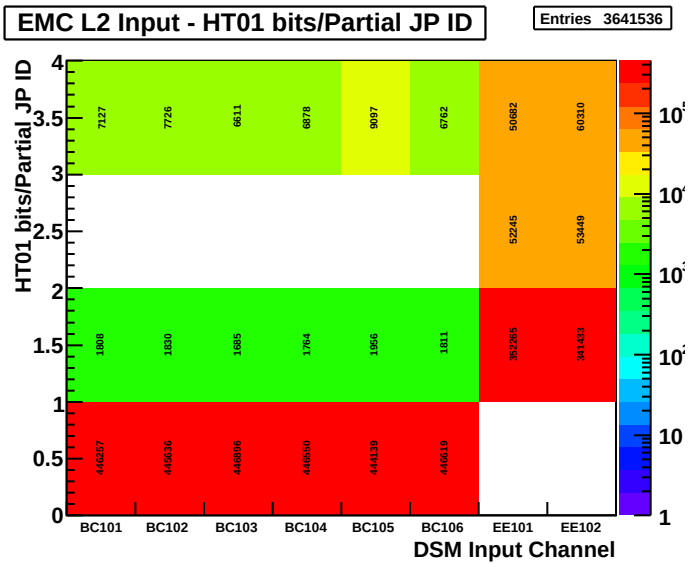

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

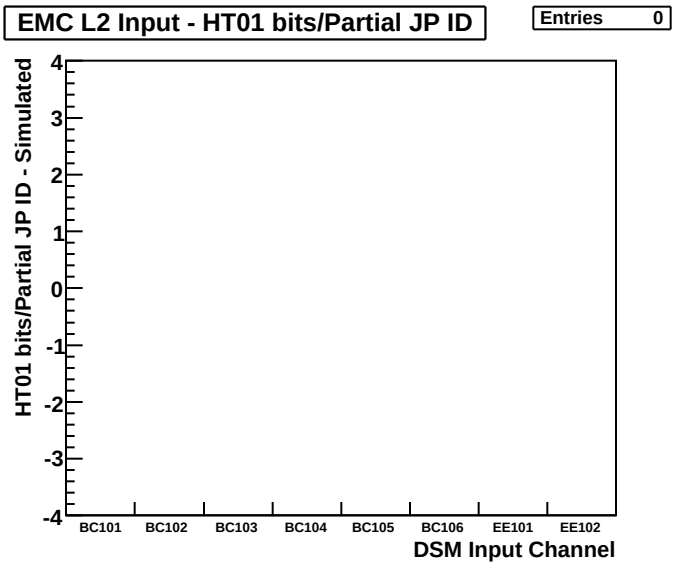

EMC L2 Input - JPZ/JPC bits
Entries 3641536
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
238 249 237 278 320 336 145 181
825 757 751 917 1055 1043 349 431
1503 1394 1348 1961 2451 2220 311 330
452586 452792 452856 452036 451366 451593 454387 454200

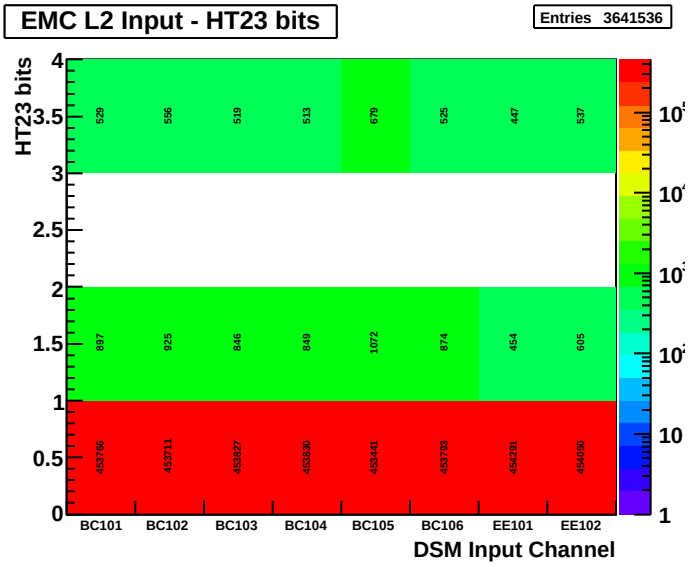

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

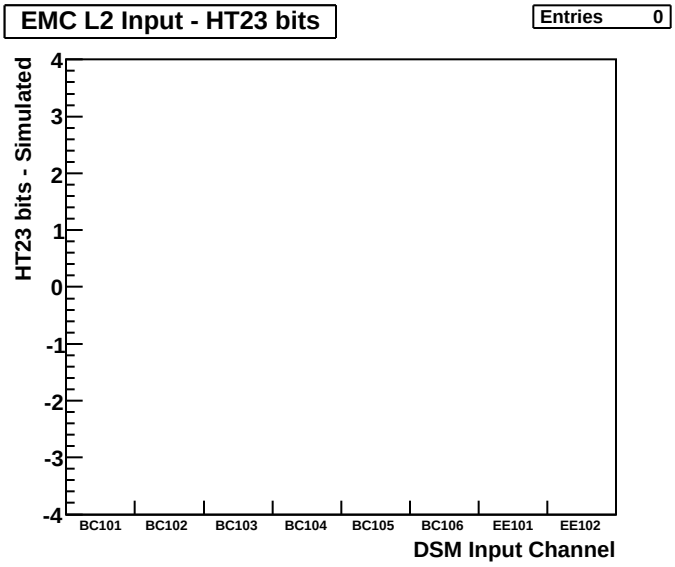

EMC L2 Input - Partial JP Sum
Entries 3641536
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

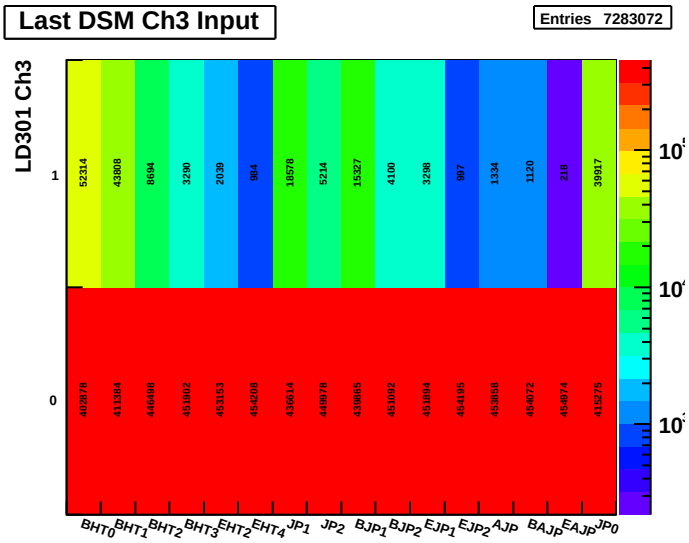

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

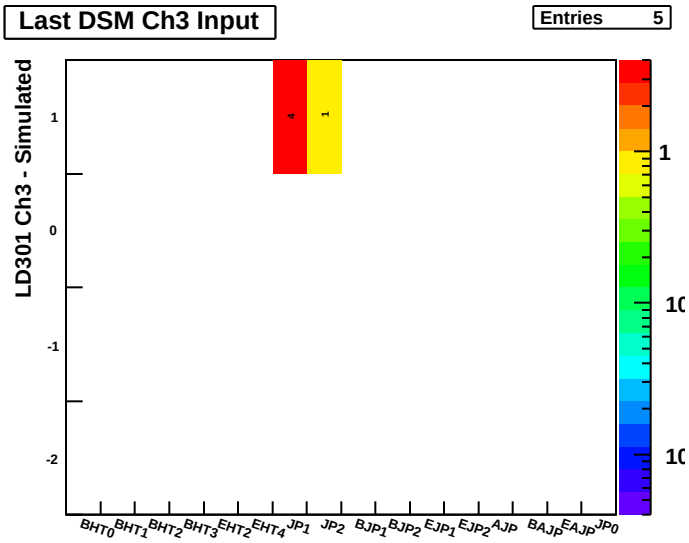

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3641536
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

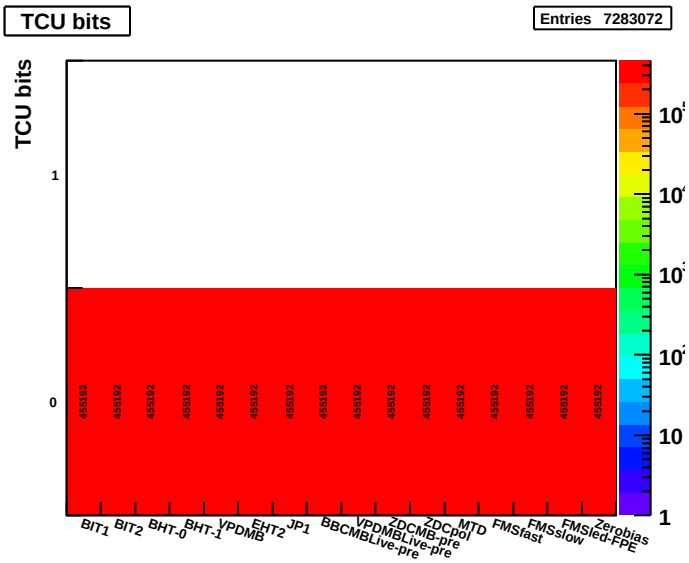

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

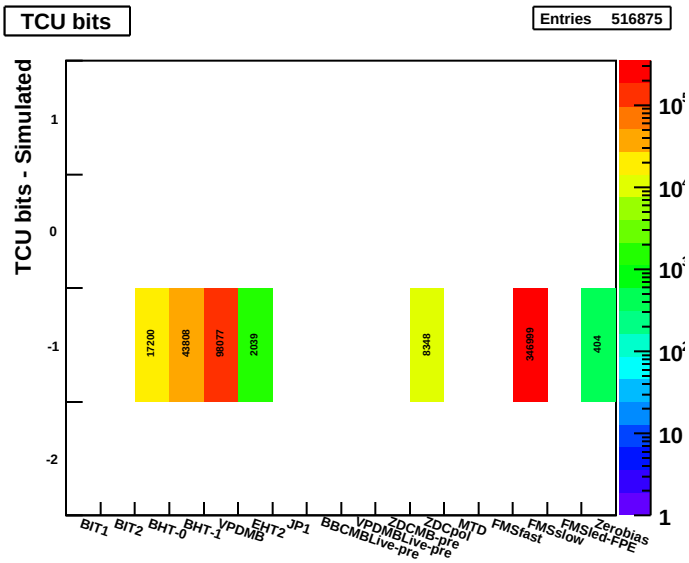

EMC L2 Input - HT23 bits
Entries 3641536
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

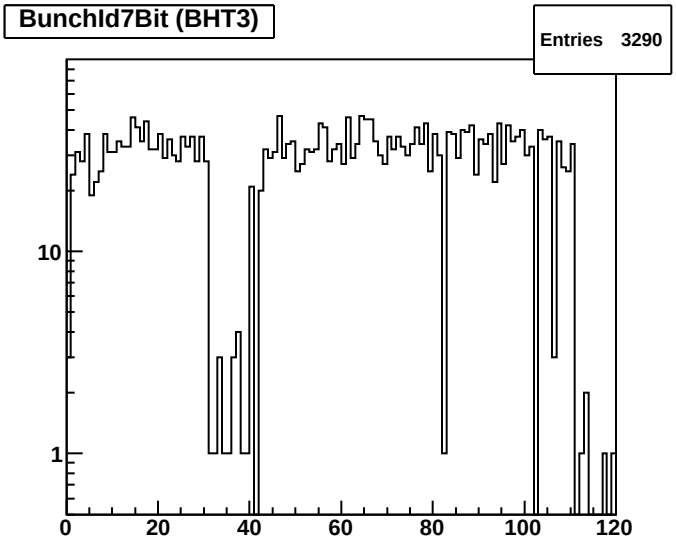

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

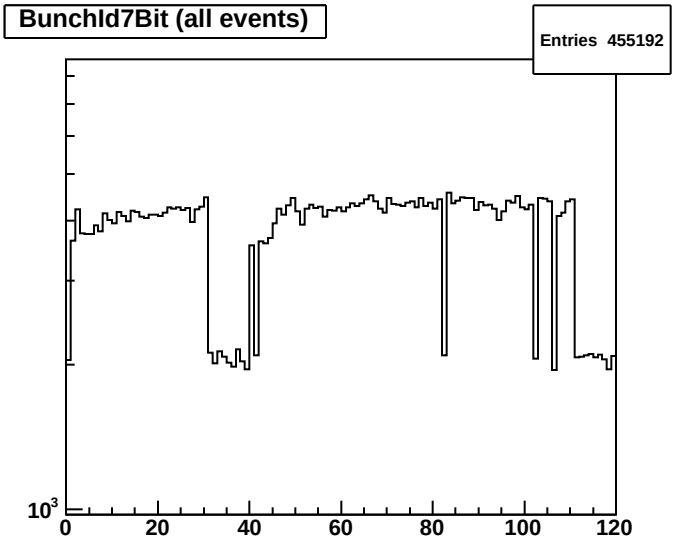

Last DSM Ch3 Input
Entries 7283072
LD301 Ch3
52314 43808 8694 3290 2039 984 18578 5214 15527 4100 3298 997 1334 1120 218 39917
402878 411384 446498 451902 453153 454208 436614 449978 438865 451092 451894 454195 453858 454072 454974 415275
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

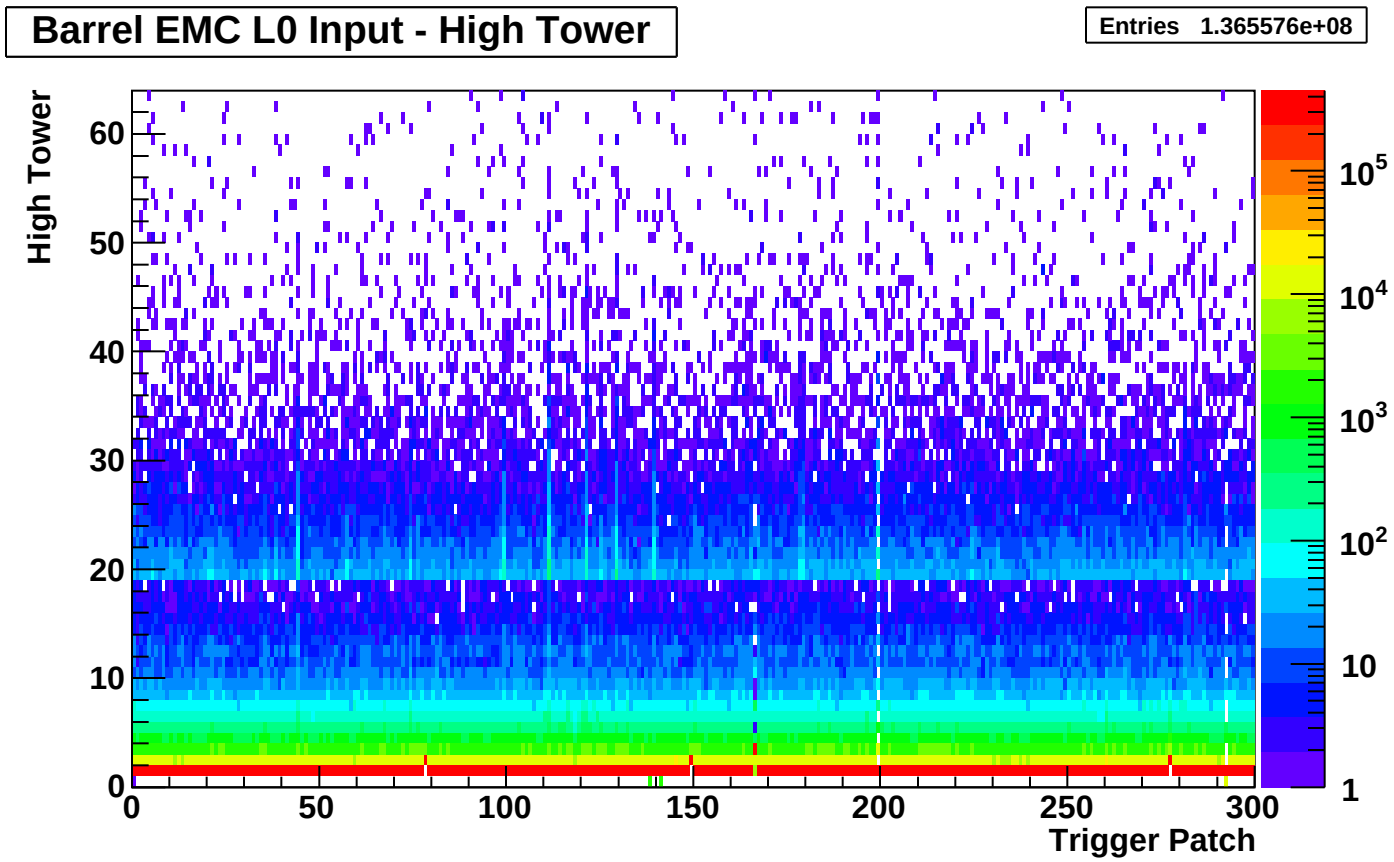

Last DSM Ch3 Input
Entries 5
LD301 Ch3 - Simulated
4 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

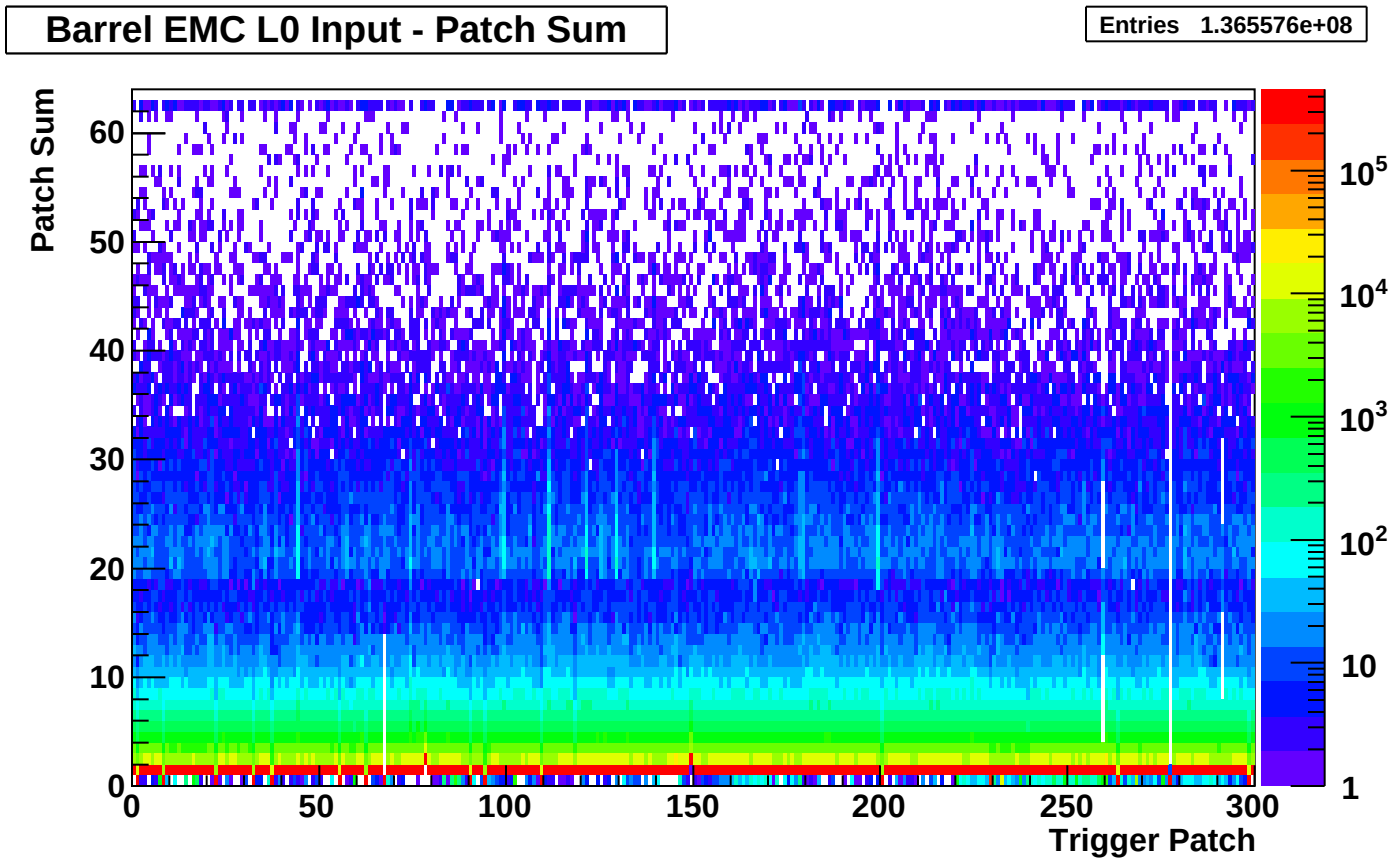

TCU bits
Entries 7283072
TCU bits
455192 455192 455192 455192 455192 455192 455192 455192 455192 455192 455192 455192 455192 455192 455192 455192
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

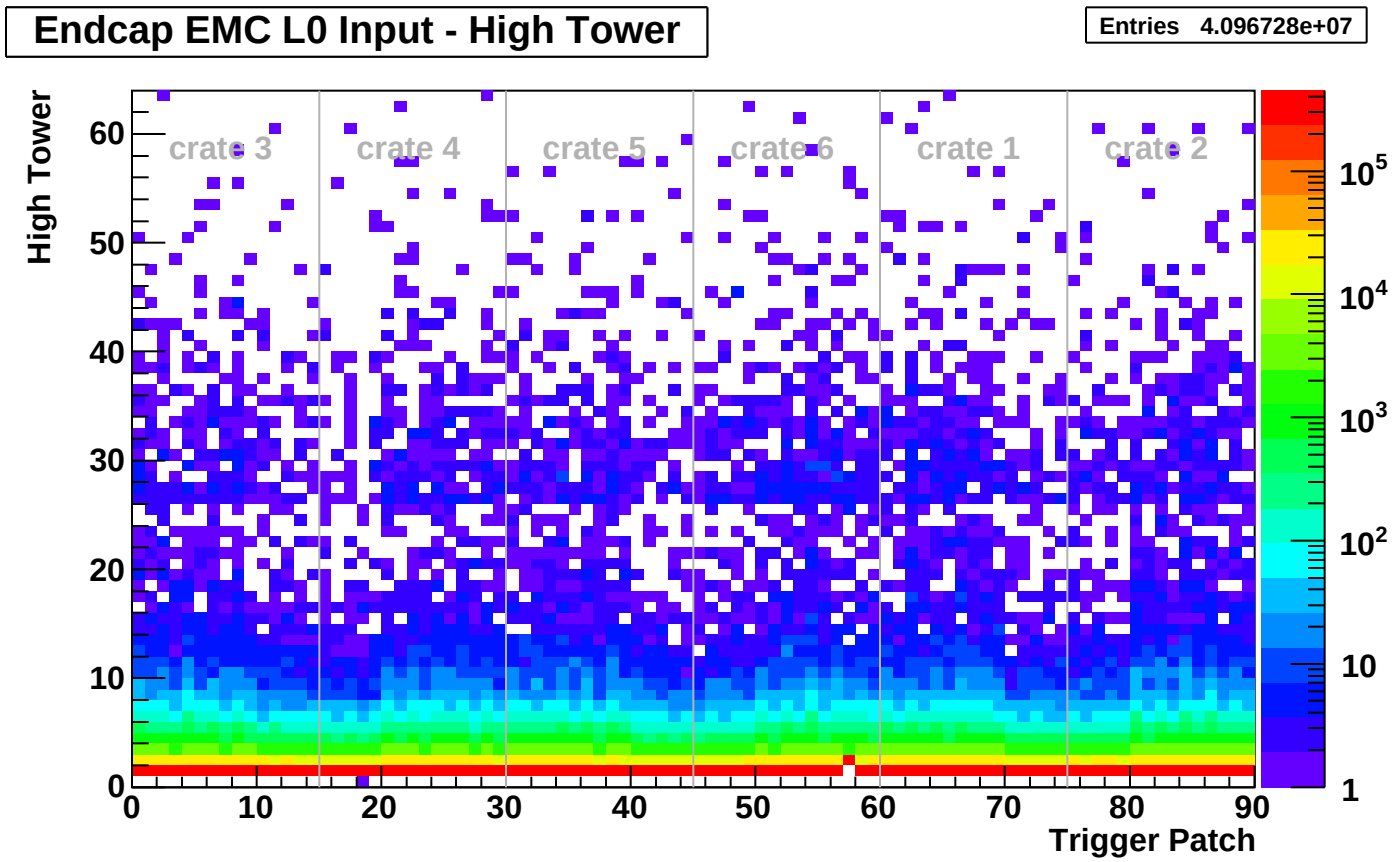

TCU bits
Entries 516875
TCU bits - Simulated
17200 43808 98077 2039 8348 346999 404
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

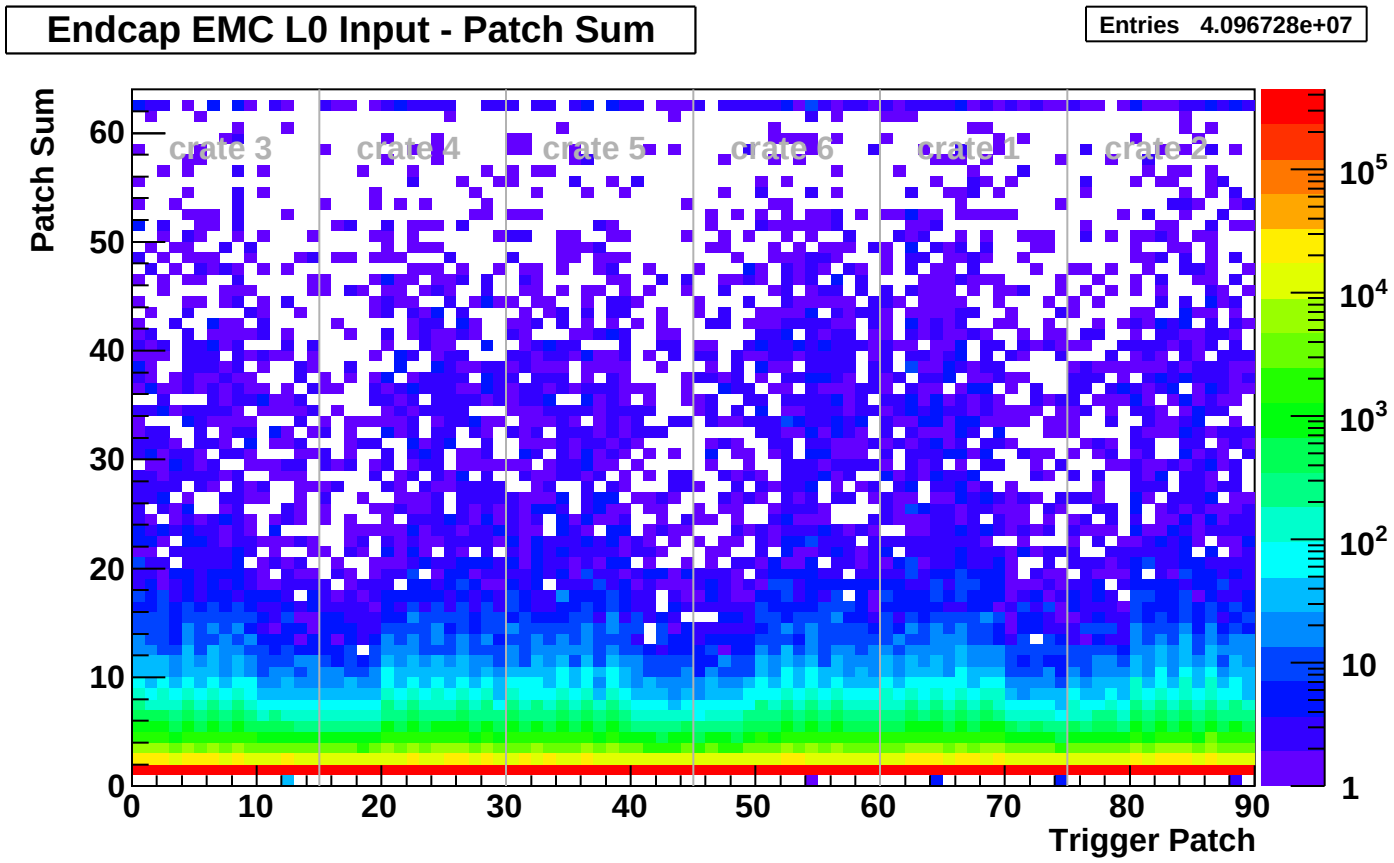

BunchId7Bit (BHT3)
Entries 3290

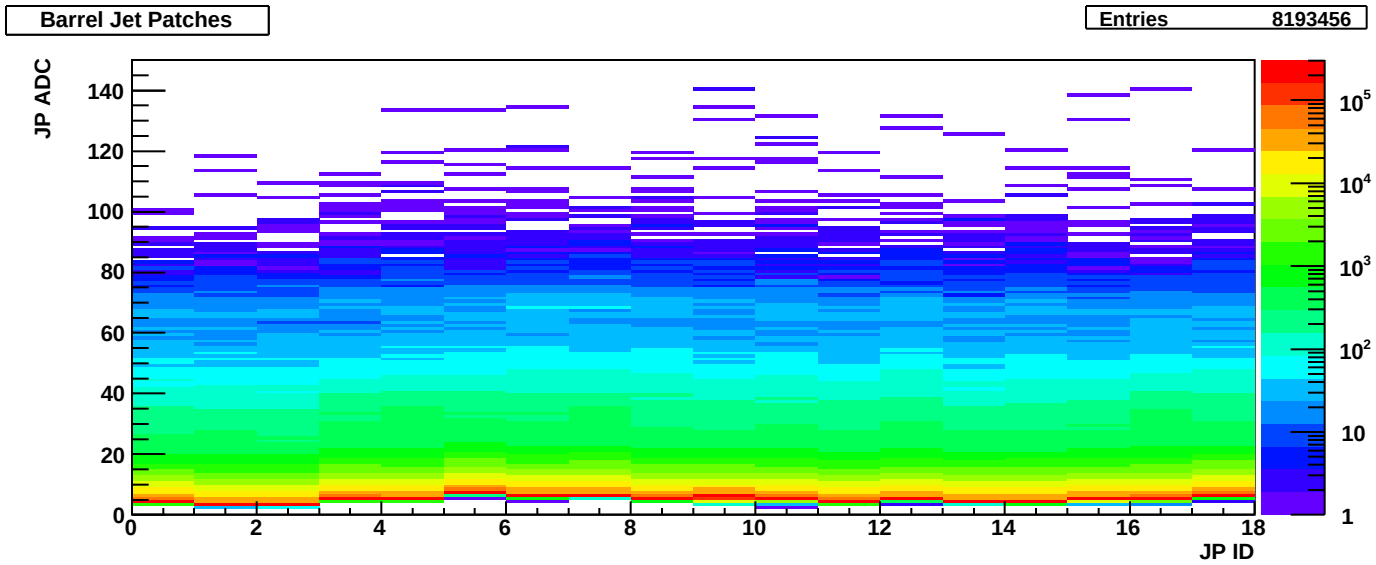

BunchId7Bit (all events)
Entries 455192

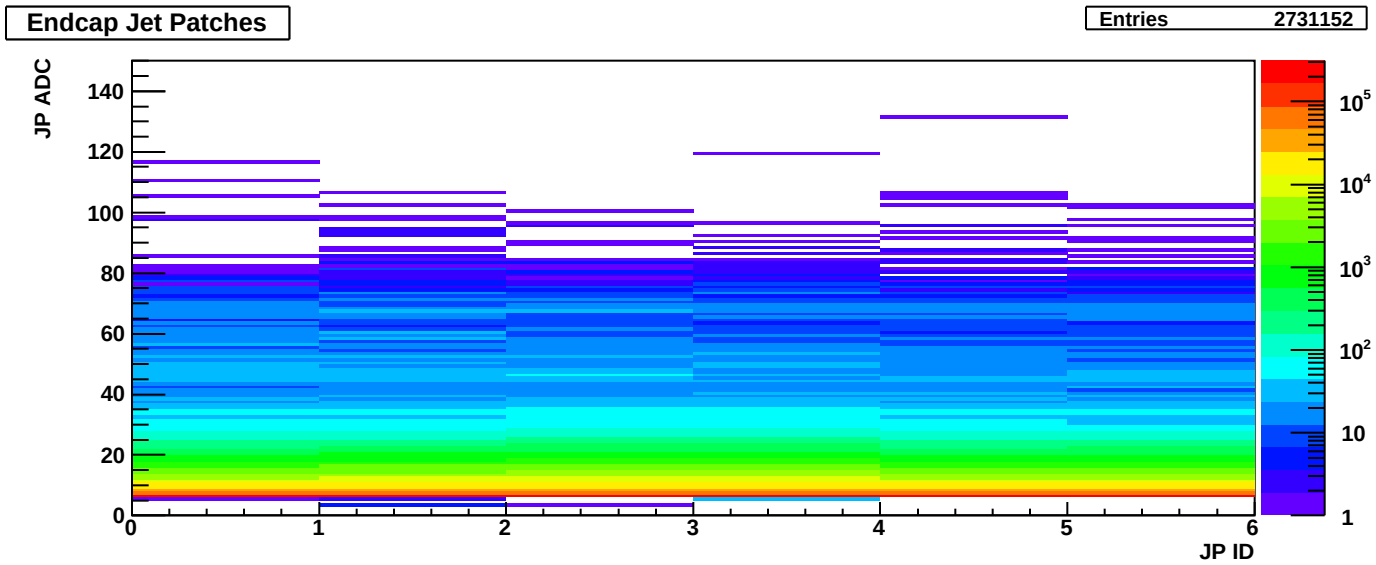

Barrel EMC L0 Input - High Tower
Entries 1.365576e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

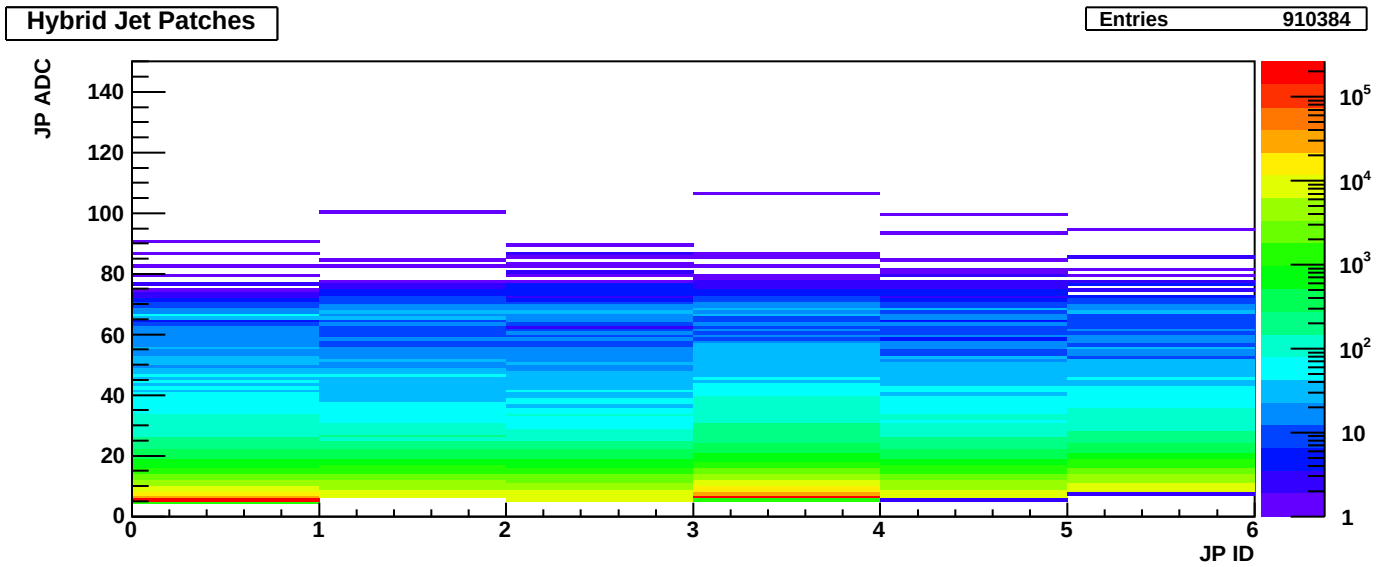

Barrel EMC L0 Input - Patch Sum
Entries 1.365576e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

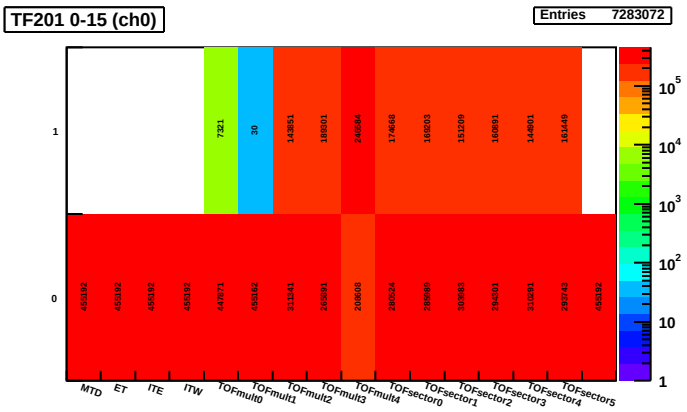

Endcap EMC L0 Input - High Tower
Entries 4.096728e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

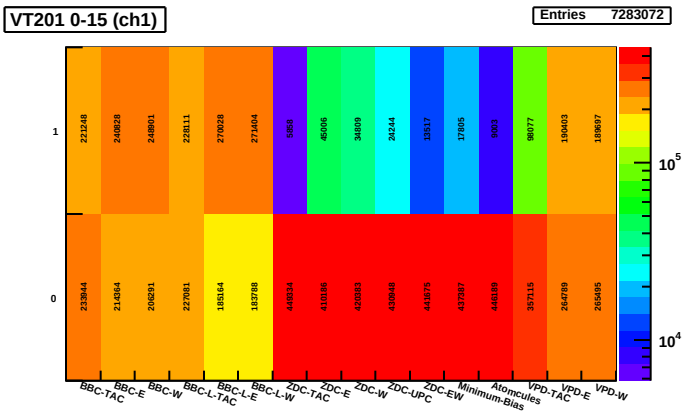

Endcap EMC L0 Input - Patch Sum
Entries 4.096728e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

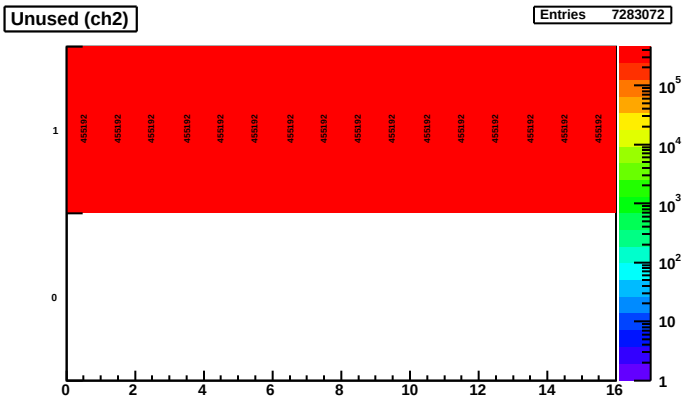

Barrel Jet Patches
Entries 8193456
JP ADC
JP ID

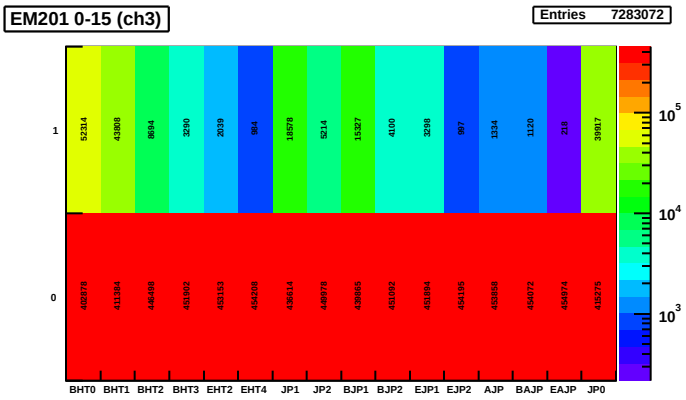

Endcap Jet Patches
Entries 2731152
JP ADC
JP ID

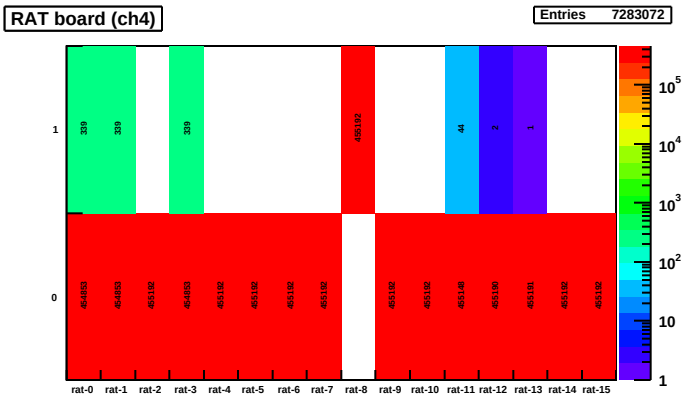

Hybrid Jet Patches
Entries 910384
JP ADC
JP ID

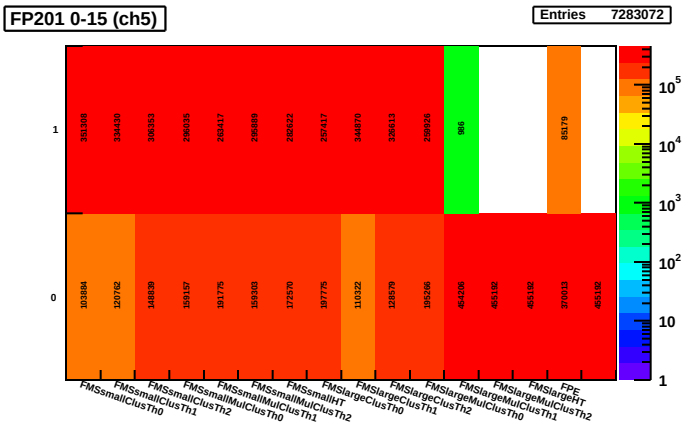

TF201 0-15 (ch0)
Entries 7283072
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

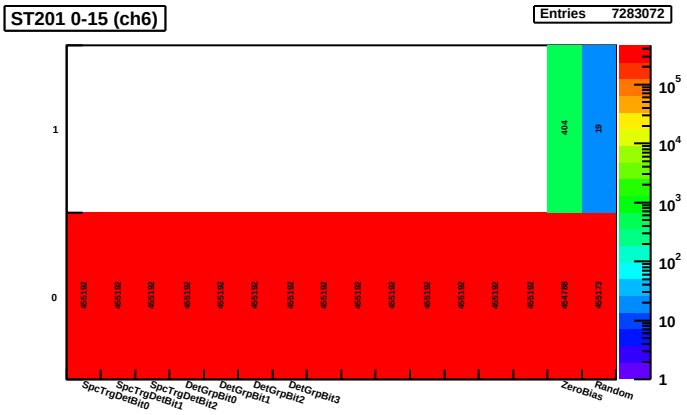

VT201 0-15 (ch1)
Entries 7283072
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

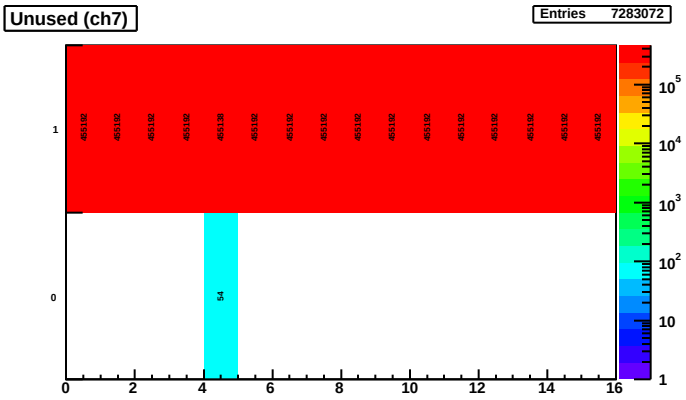

Unused (ch2)
Entries 7283072

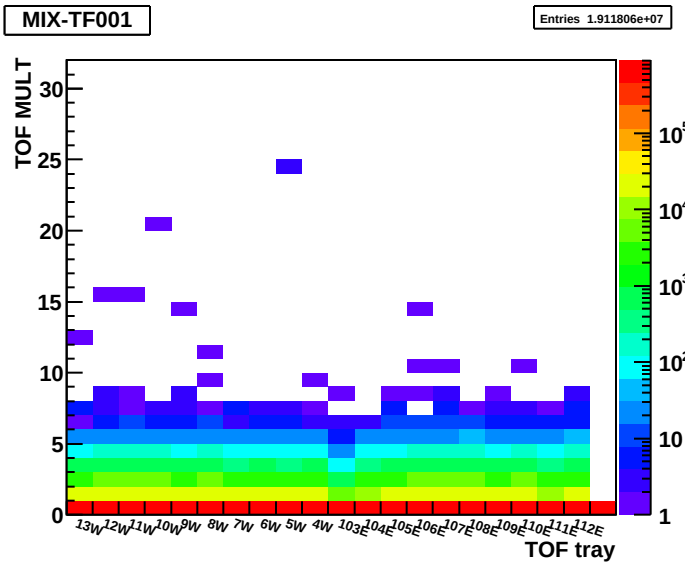

EM201 0-15 (ch3)
Entries 7283072
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

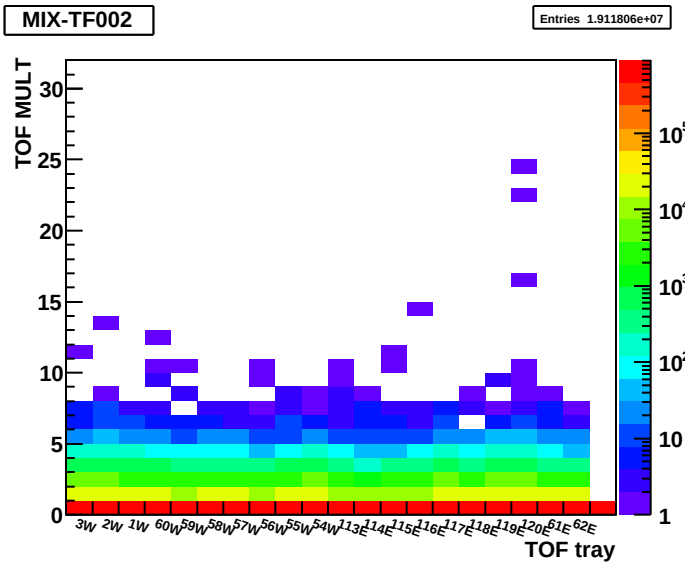

RAT board (ch4)
Entries 7283072
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

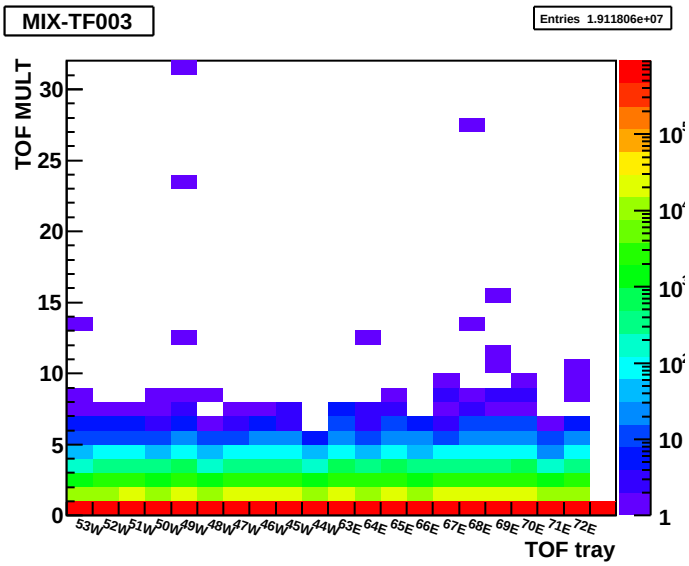

FP201 0-15 (ch5)
Entries 7283072
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

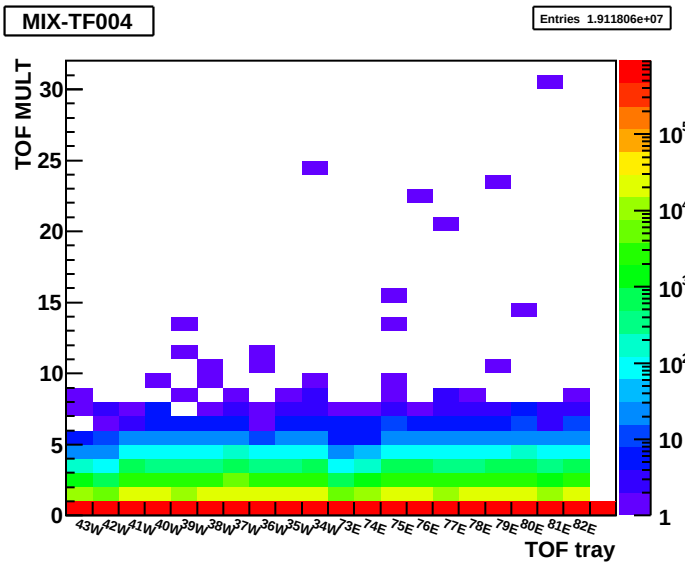

ST201 0-15 (ch6)
Entries 7283072
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

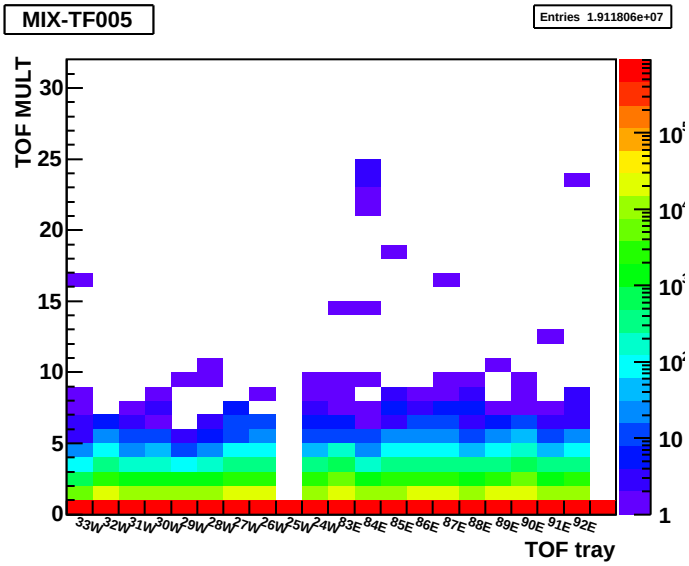

Unused (ch7)
Entries 7283072

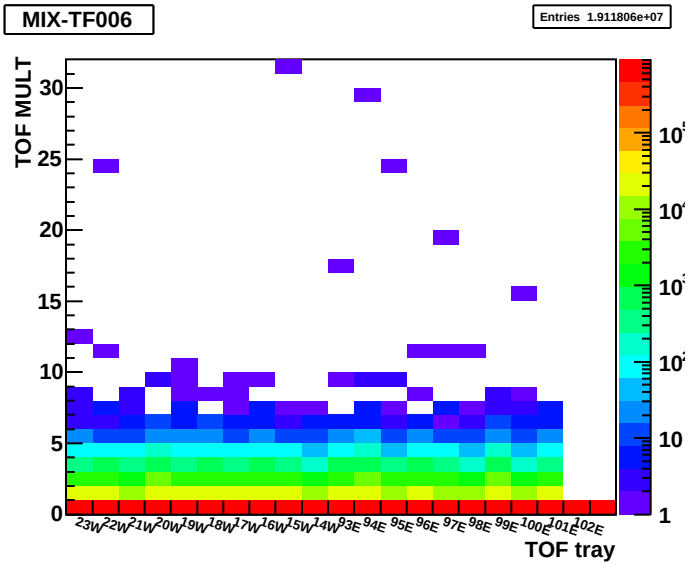

MIX-TF001
Entries 1.911806e+07
TOF MULT
TOF tray

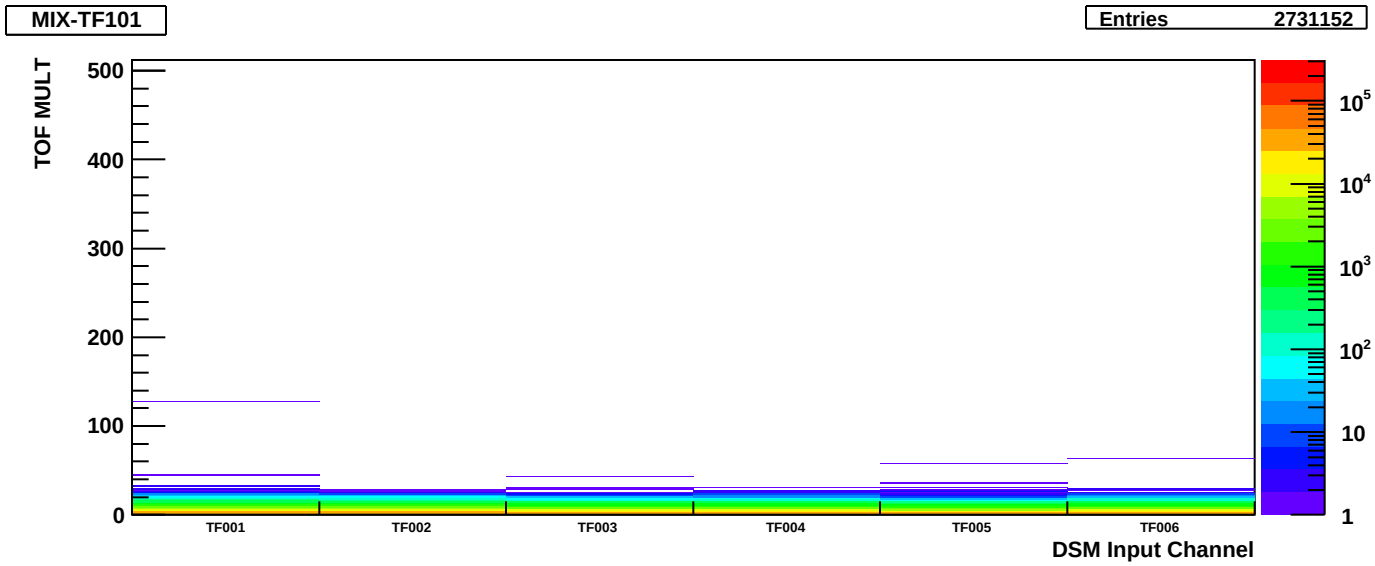

MIX-TF002
Entries 1.911806e+07
TOF MULT
TOF tray

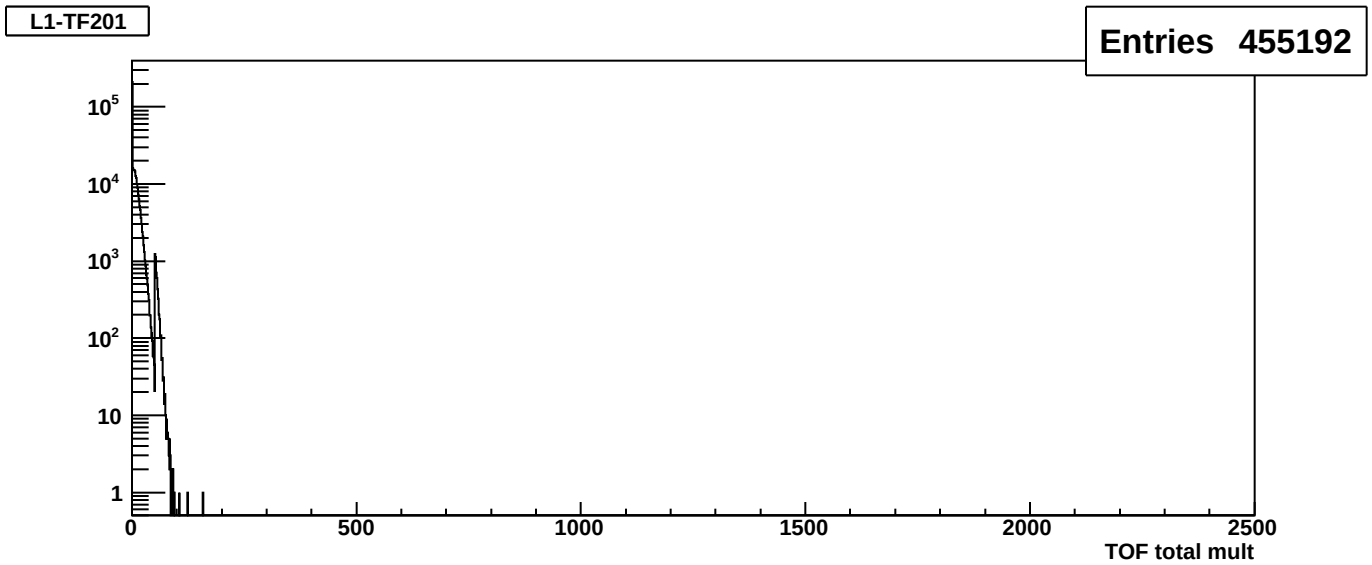

MIX-TF003
Entries 1.911806e+07
TOF MULT
TOF tray

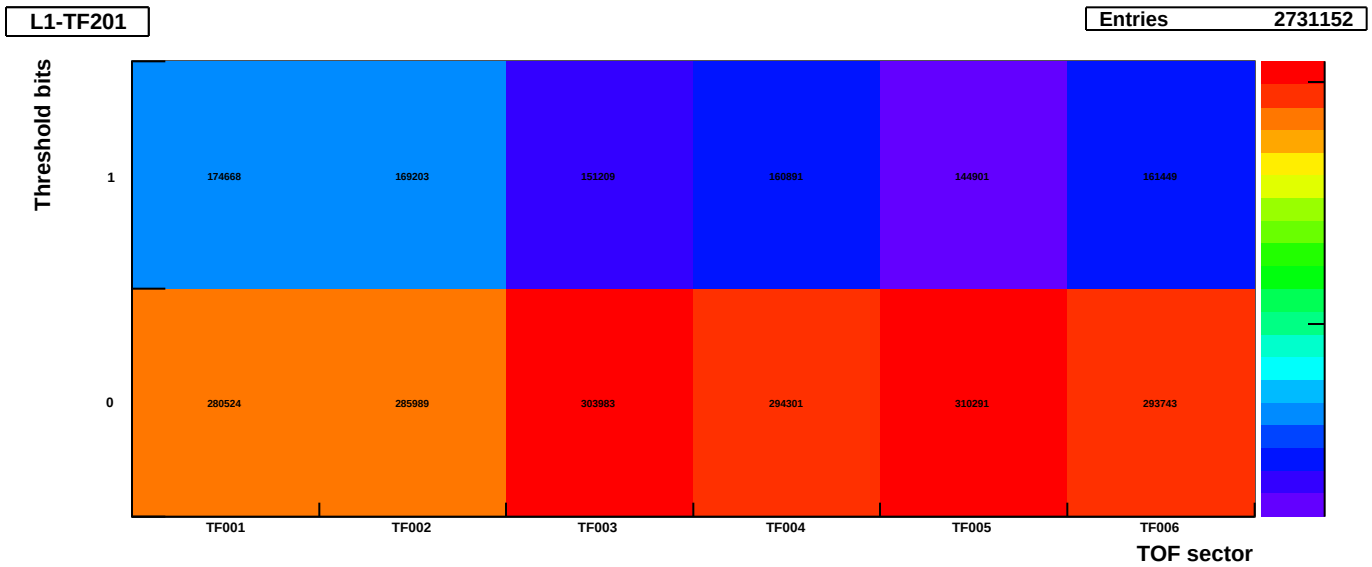

MIX-TF004
Entries 1.911806e+07
TOF MULT
TOF tray

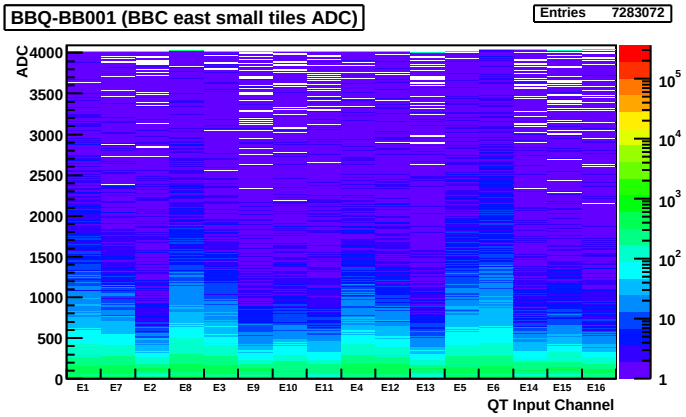

MIX-TF005
Entries 1.911806e+07
TOF MULT
TOF tray

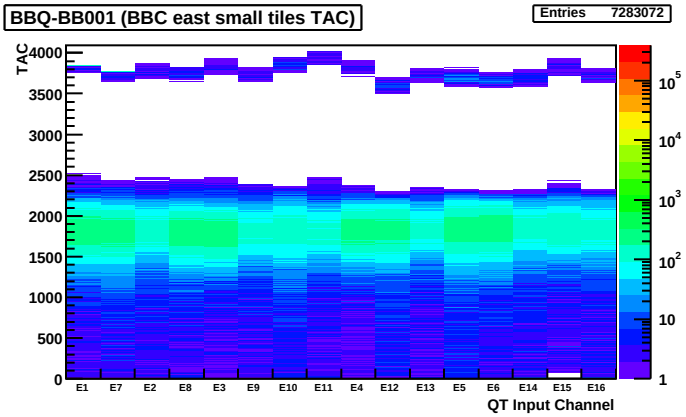

MIX-TF006
Entries 1.911806e+07
TOF MULT
TOF tray

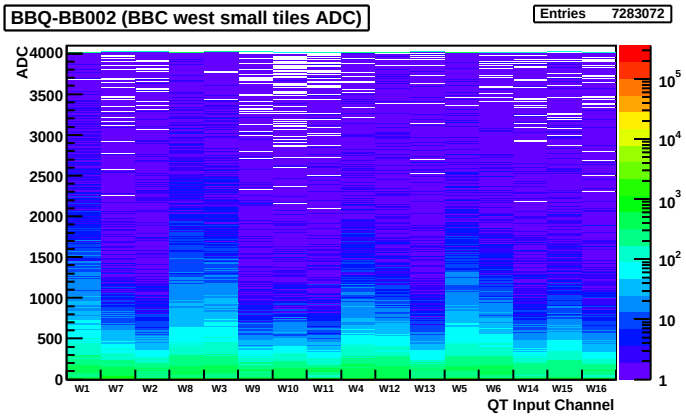

MIX-TF101
Entries 2731152
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

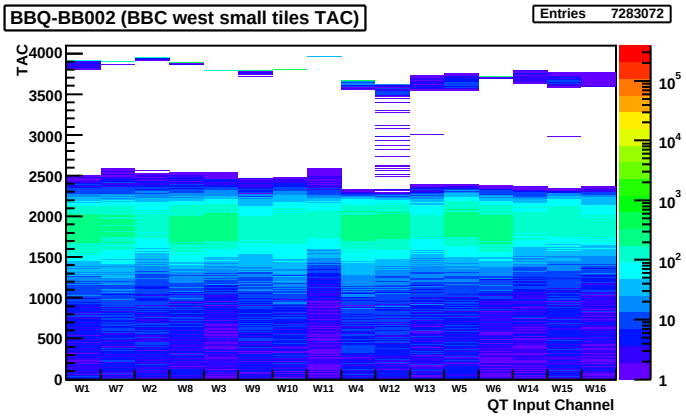

L1-TF201
Entries 455192
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

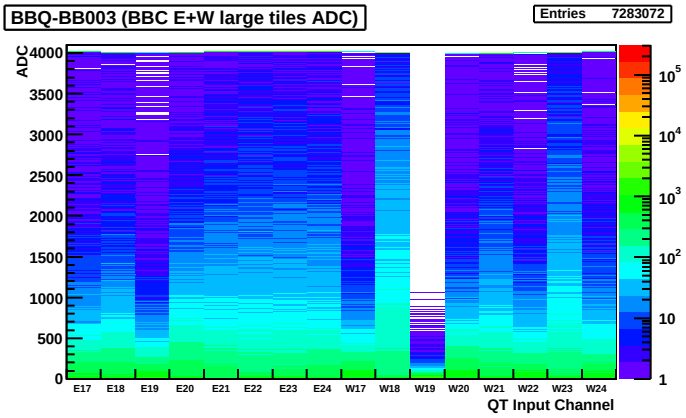

L1-TF201
Entries 2731152
Threshold bits
1
0
174668
169203
151209
160891
144901
161449
280524
285989
303983
294301
310291
293743
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

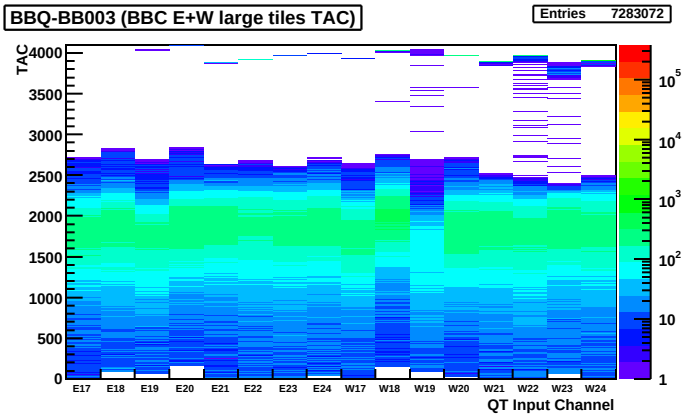

BBQ-BB001 (BBC east small tiles ADC)
Entries 7283072
ADC
QT Input Channel

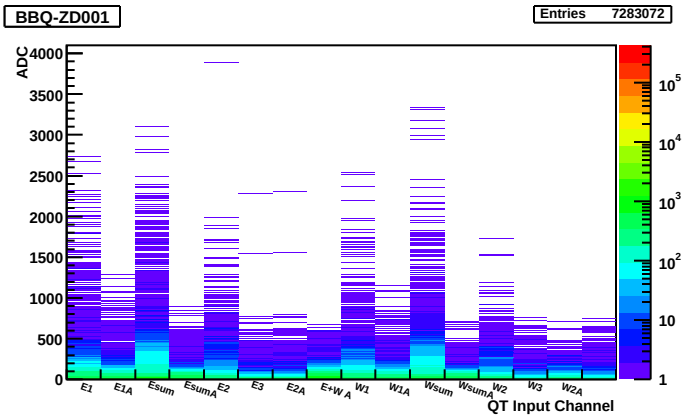

BBQ-BB001 (BBC east small tiles TAC)
Entries 7283072
TAC
QT Input Channel

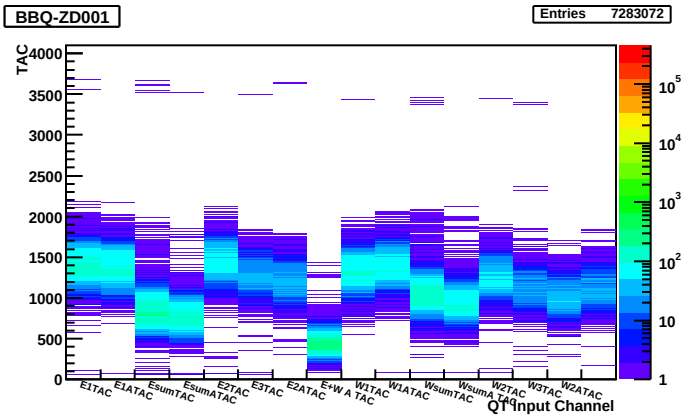

BBQ-BB002 (BBC west small tiles ADC)
Entries 7283072
ADC
QT Input Channel

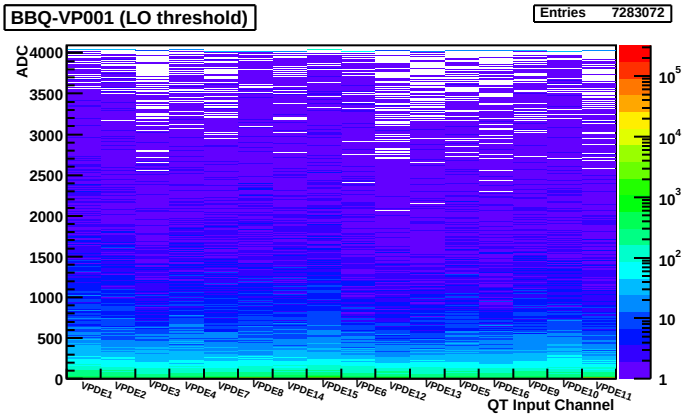

BBQ-BB002 (BBC west small tiles TAC)
Entries 7283072
TAC
QT Input Channel

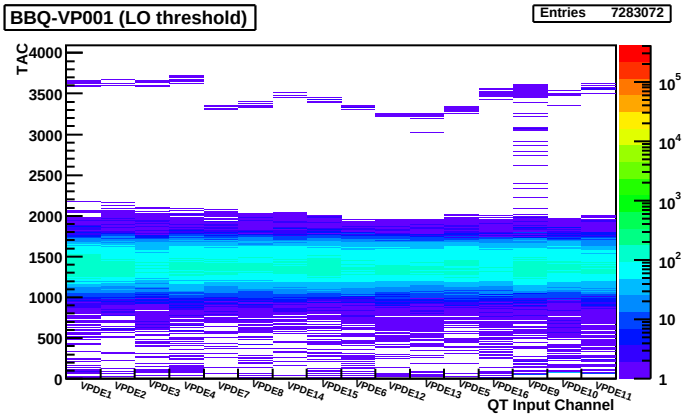

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7283072
ADC
QT Input Channel

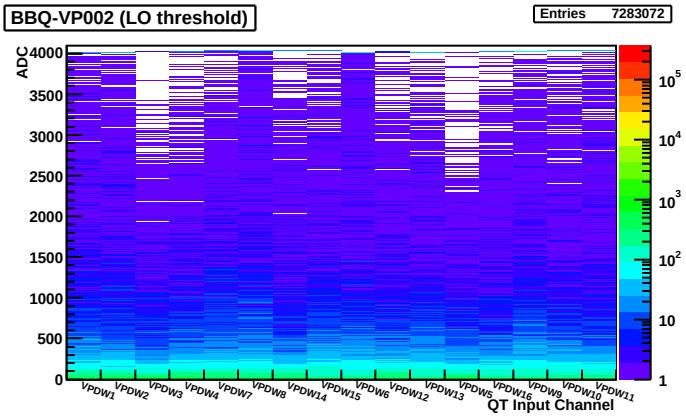

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7283072
TAC
QT Input Channel

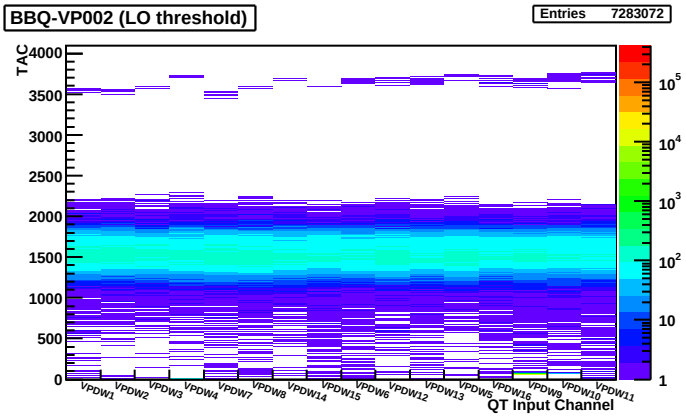

BBQ-ZD001
Entries 7283072
ADC
QT Input Channel

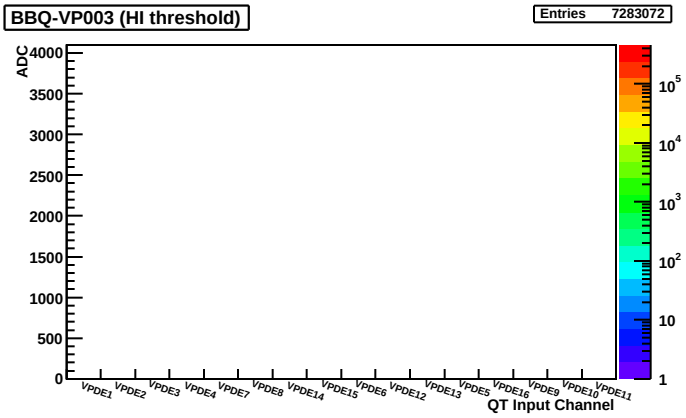

BBQ-ZD001
Entries 7283072
TAC
QT Input Channel

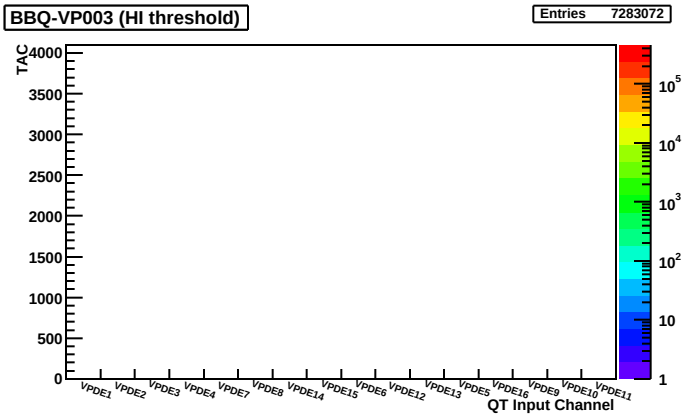

BBQ-VP001 (LO threshold)
Entries 7283072
ADC
QT Input Channel

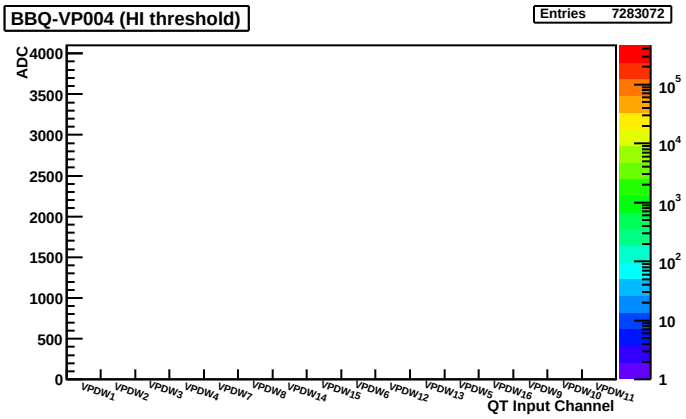

BBQ-VP001 (LO threshold)
Entries 7283072
TAC
QT Input Channel

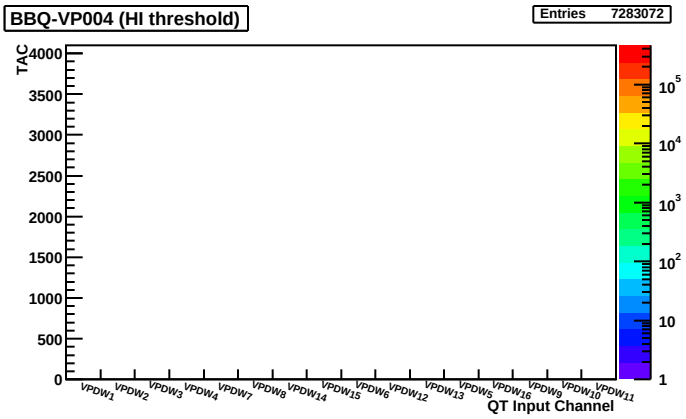

BBQ-VP002 (LO threshold)
Entries 7283072
ADC
QT Input Channel

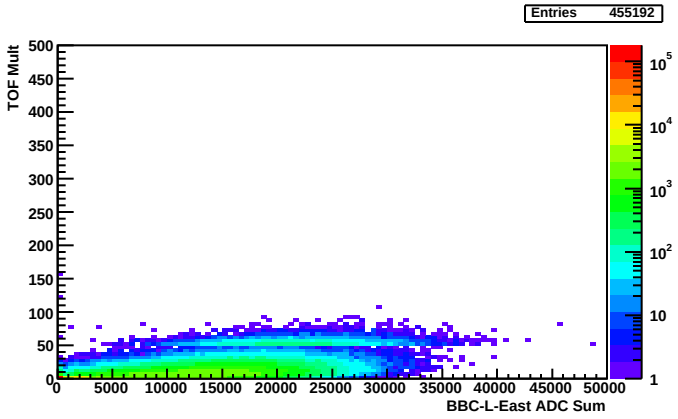

BBQ-VP002 (LO threshold)
Entries 7283072
TAC
QT Input Channel

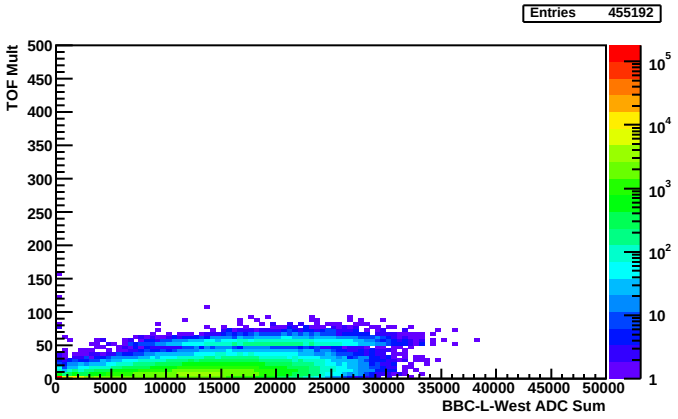

BBQ-VP003 (HI threshold)
Entries 7283072
ADC
QT Input Channel

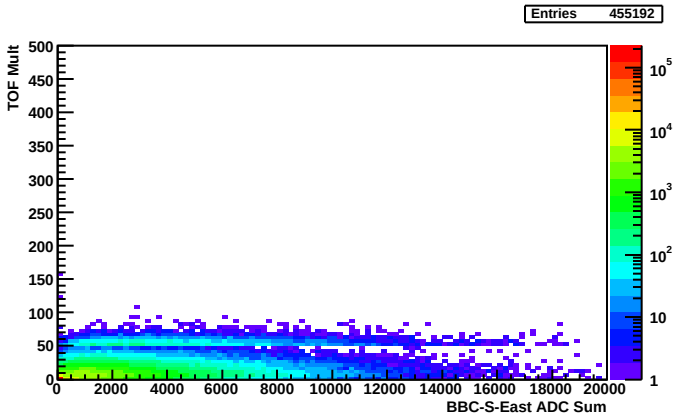

BBQ-VP003 (HI threshold)
Entries 7283072
TAC
QT Input Channel

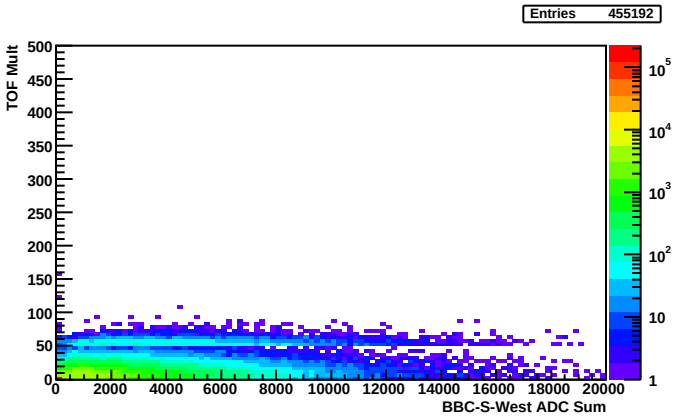

BBQ-VP004 (HI threshold)
Entries 7283072
ADC
QT Input Channel

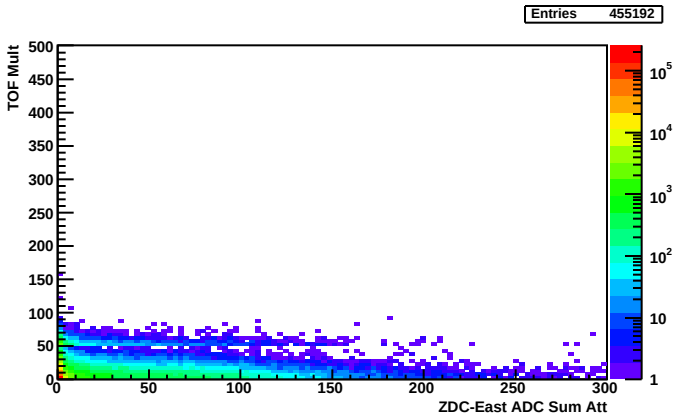

BBQ-VP004 (HI threshold)
Entries 7283072
TAC
QT Input Channel

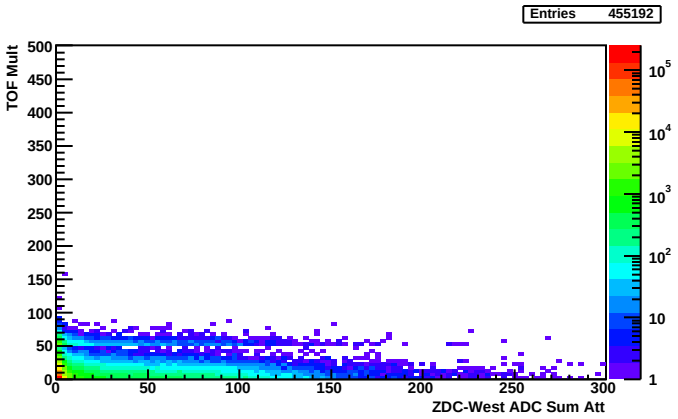

Entries 455192
TOF Mult
BBC-L-East ADC Sum

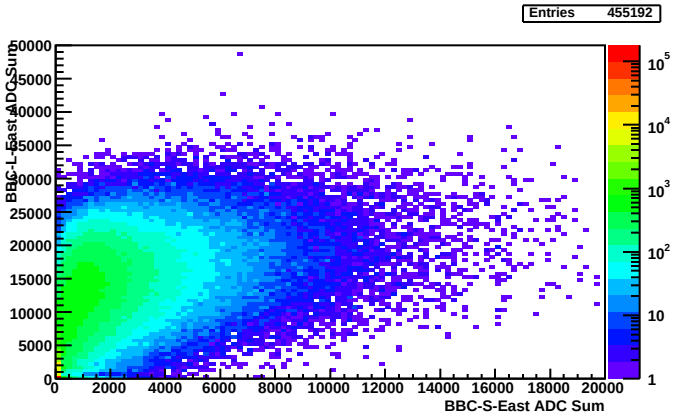

Entries 455192
TOF Mult
BBC-L-West ADC Sum

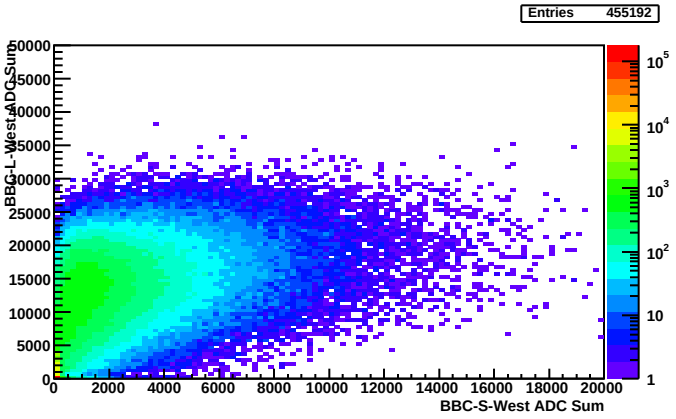

Entries 455192
TOF Mult
BBC-S-East ADC Sum

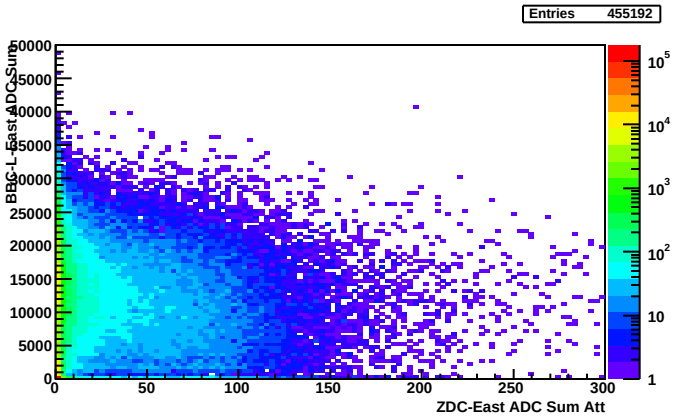

Entries 455192
TOF Mult
BBC-S-West ADC Sum

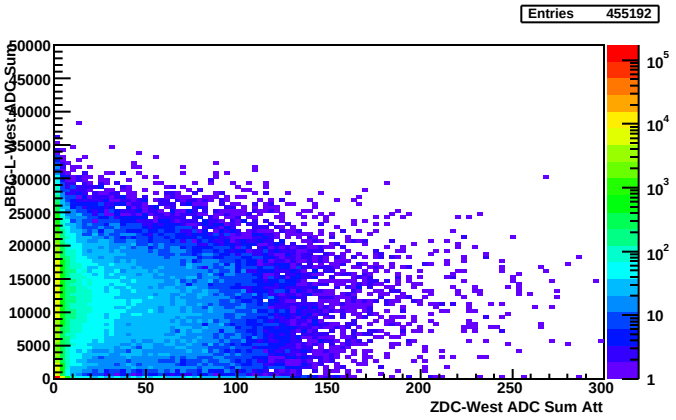

Entries 455192
TOF Mult
ZDC-East ADC Sum Att

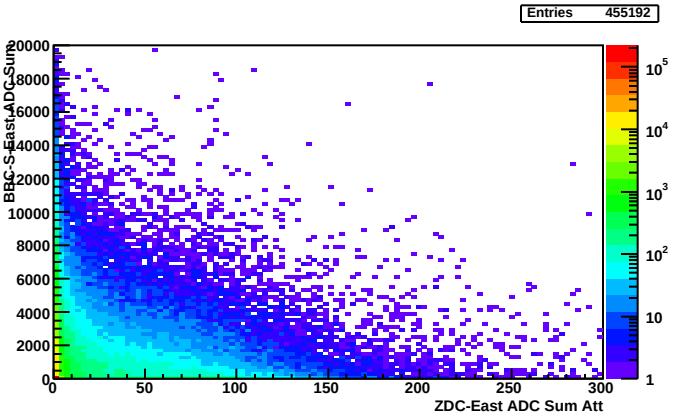

Entries 455192
TOF Mult
ZDC-West ADC Sum Att

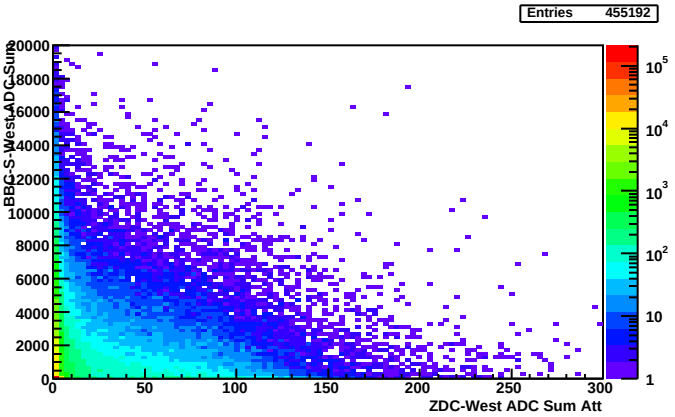

Entries 455192
BBC-L-East ADC Sum
BBC-S-East ADC Sum

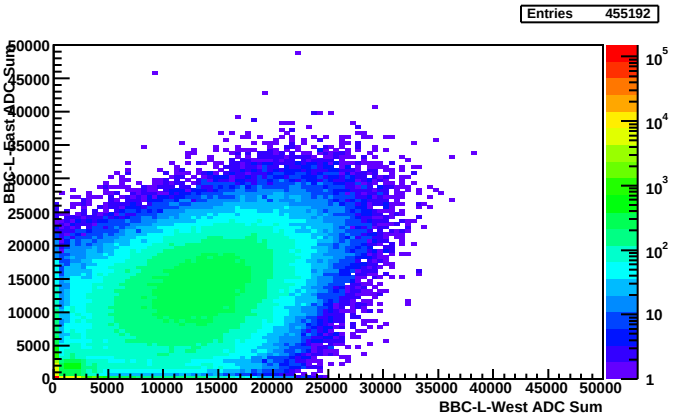

Entries 455192
BBC-L-West ADC Sum
BBC-S-West ADC Sum

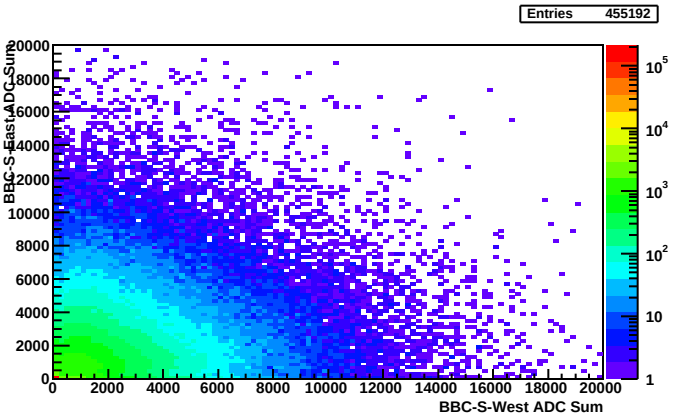

Entries 455192
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

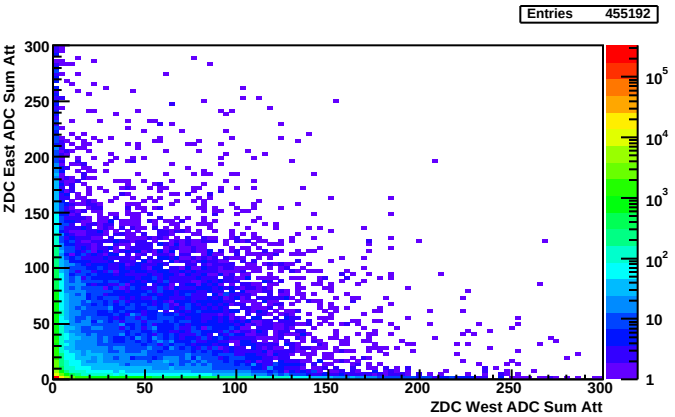

Entries 455192
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

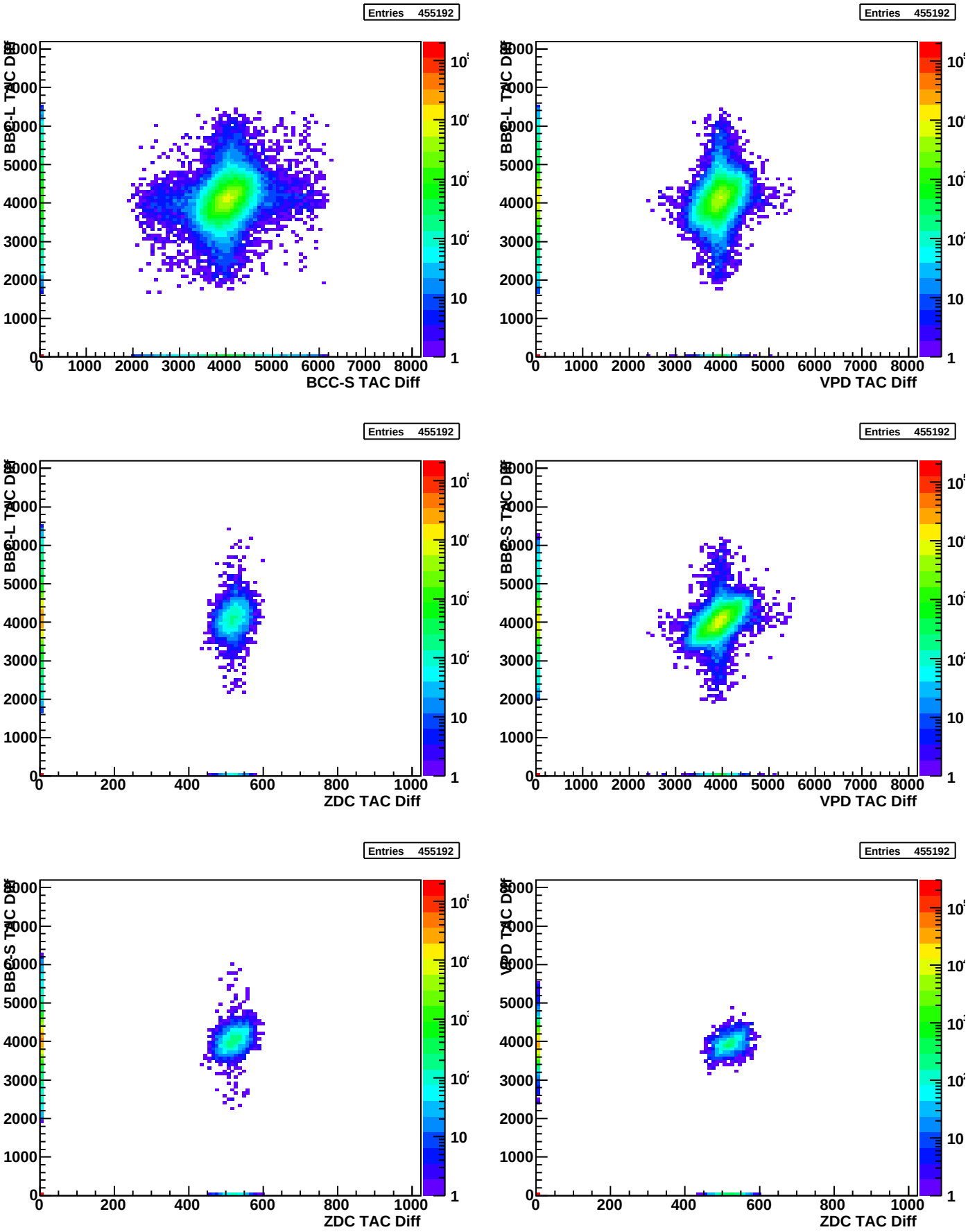

Entries 455192
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

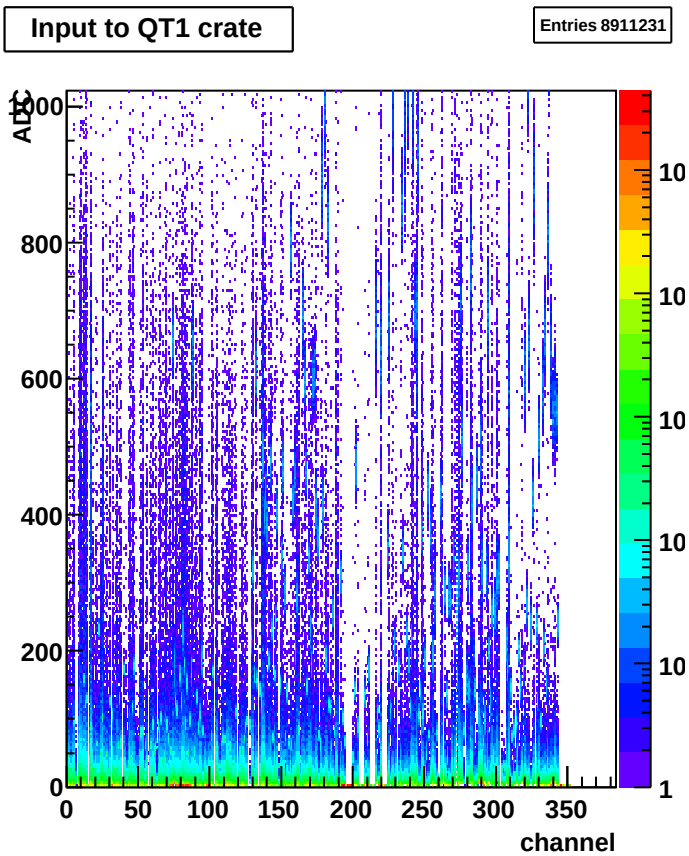

Entries 455192
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

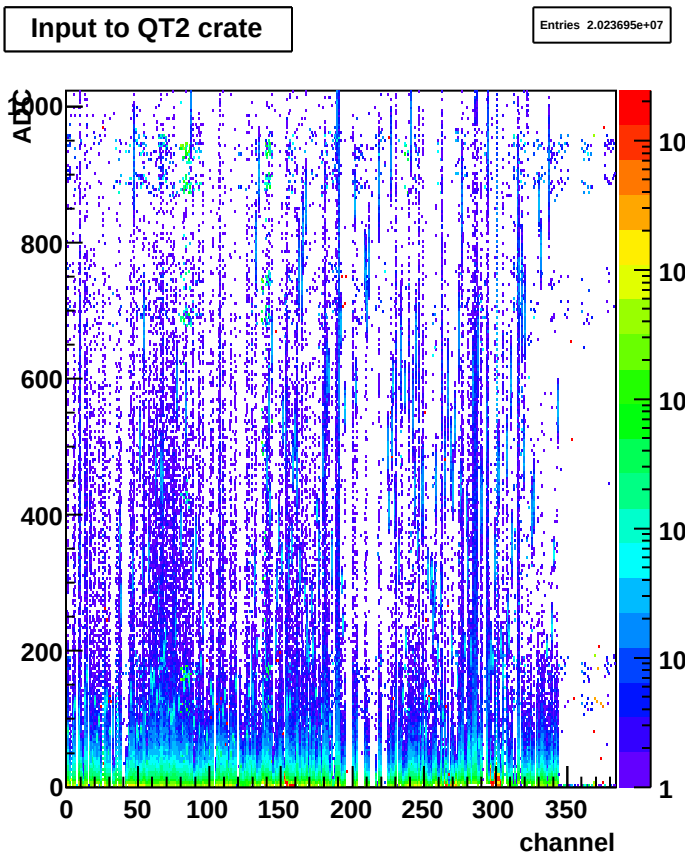

Entries 455192
BBC-L-East ADC Sum
BBC-L-West ADC Sum

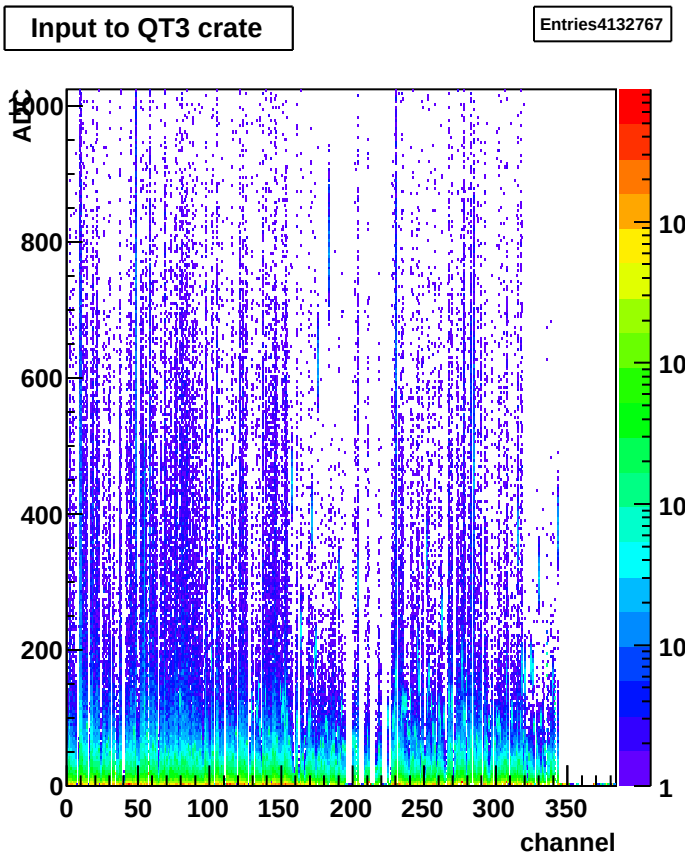

Entries 455192
BBC-S-East ADC Sum
BBC-S-West ADC Sum

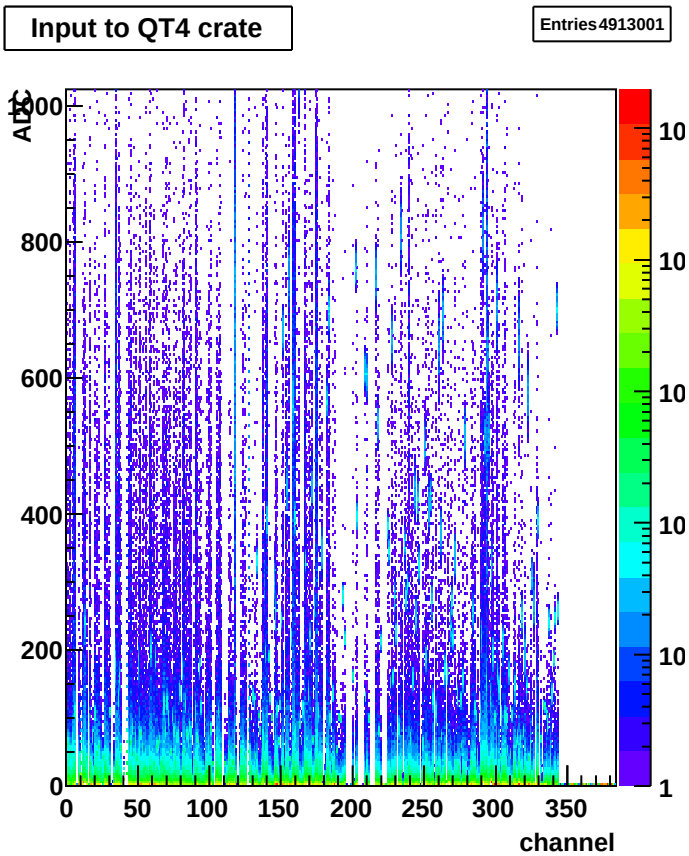

Entries 455192
ZDC East ADC Sum Att
ZDC West ADC Sum Att

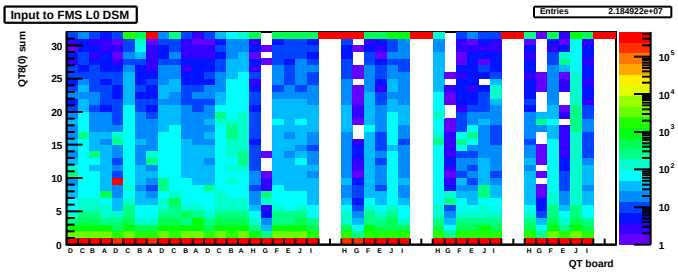

Entries 455192
BBC-L TAC Diff
BCC-S TAC Diff
Entries 455192
BBC-L TAC Diff
VPD TAC Diff
Entries 455192
BBC-L TAC Diff
ZDC TAC Diff
Entries 455192
BBC-S TAC Diff
VPD TAC Diff
Entries 455192
BBC-S TAC Diff
ZDC TAC Diff
Entries 455192
VPD TAC Diff
ZDC TAC Diff

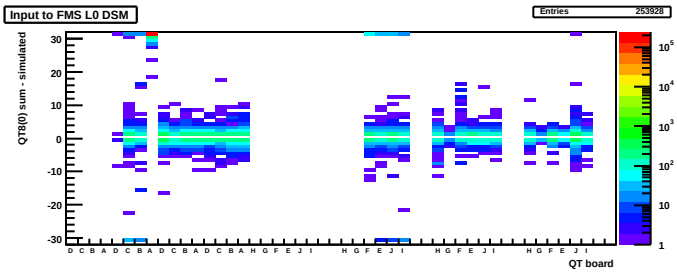

Input to QT1 crate
Entries 8911231
ADC
channel

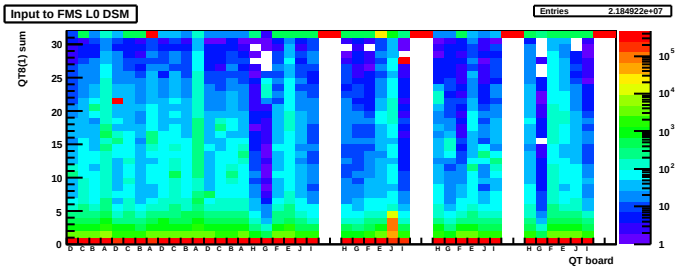

Input to QT2 crate
Entries 2.023695e+07
ADC
channel

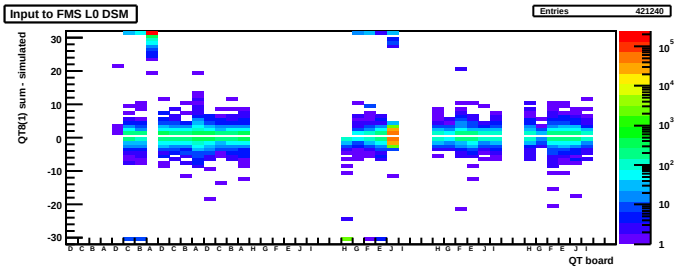

Input to QT3 crate
Entries4132767
ADC
channel

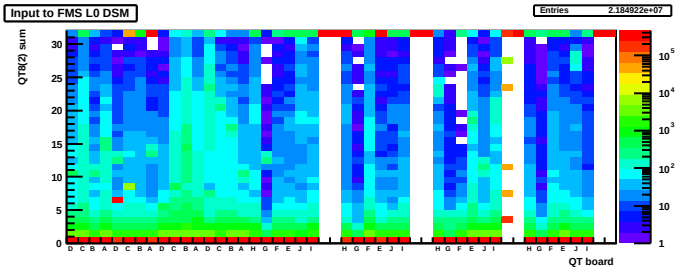

Input to QT4 crate
Entries4913001
ADC
channel

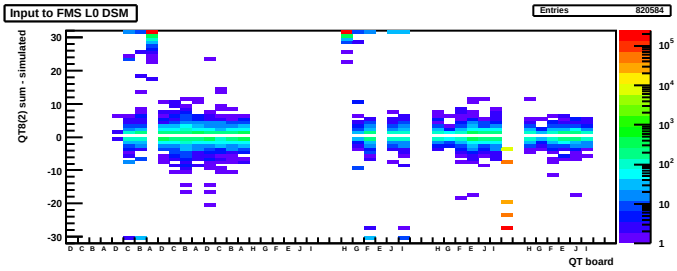

Input to FMS L0 DSM
Entries 2.184922e+07
QT8(0) sum
QT board

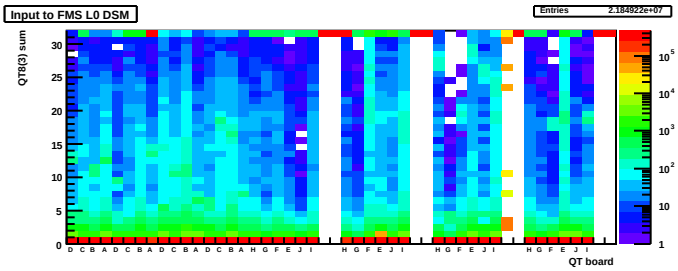

Input to FMS L0 DSM
Entries 253928
QT8(0) sum - simulated
QT board

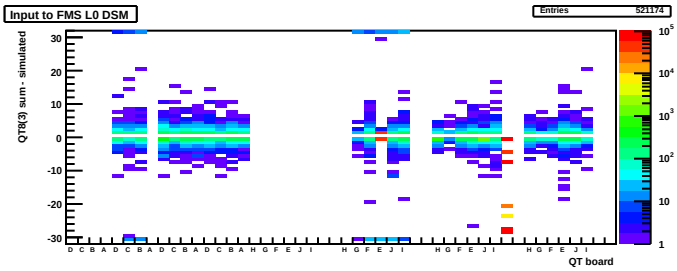

Input to FMS L0 DSM
Entries 2.184922e+07
QT8(1) sum
QT board

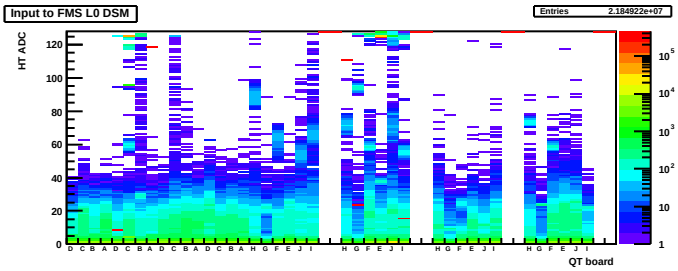

Input to FMS L0 DSM
Entries 421240
QT8(1) sum - simulated
QT board

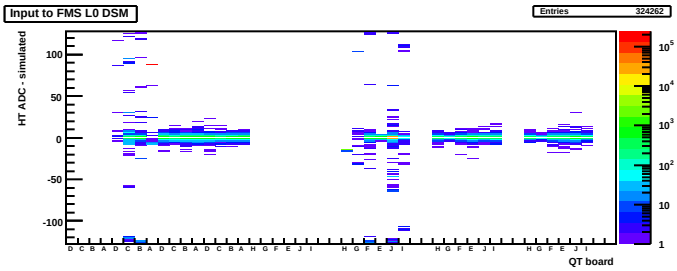

Input to FMS L0 DSM
Entries 2.184922e+07
QT8(2) sum
QT board

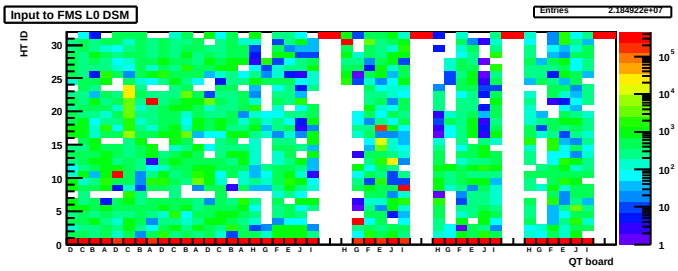

Input to FMS L0 DSM
Entries 820584
QT8(2) sum - simulated
QT board

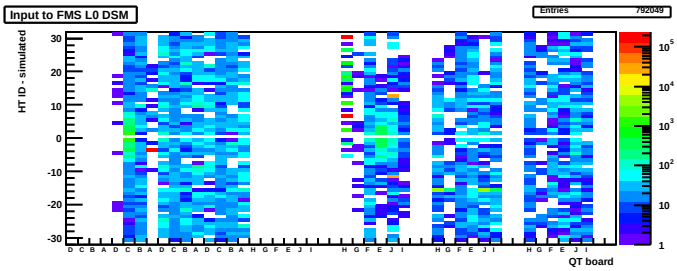

Input to FMS L0 DSM
Entries 2.184922e+07
QT8(3) sum
QT board

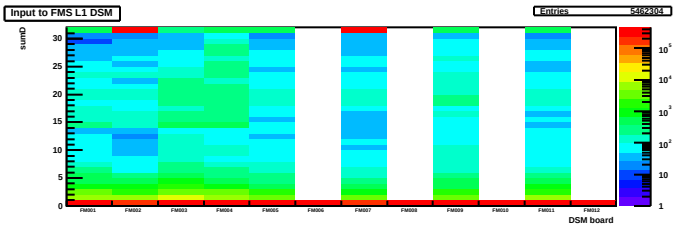

Input to FMS L0 DSM
Entries 521174
QT8(3) sum - simulated
QT board

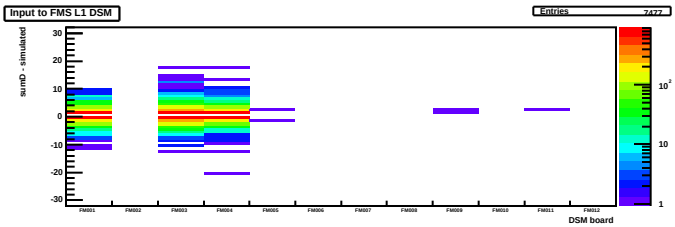

Input to FMS L0 DSM
Entries 2.184922e+07
HT ADC
QT board

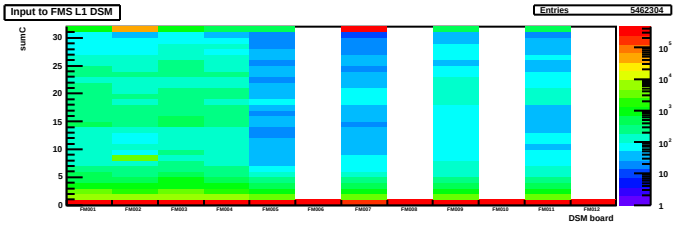

Input to FMS L0 DSM
Entries 324262
HT ADC - simulated
QT board

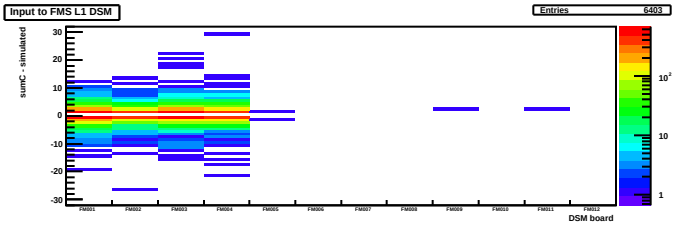

Input to FMS L0 DSM
Entries 2.184922e+07
HT ID
QT board

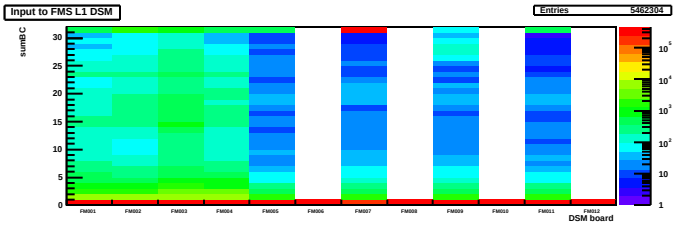

Input to FMS L0 DSM
Entries 792049
HT ID - simulated
QT board

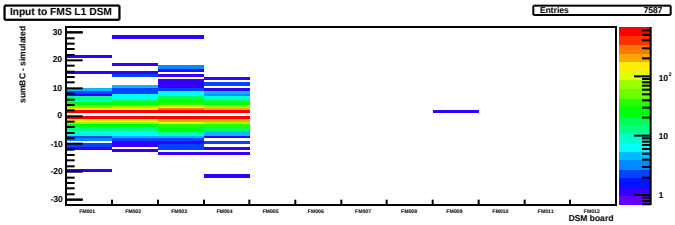

Input to FMS L1 DSM
Entries 5462304
sumD
DSM board

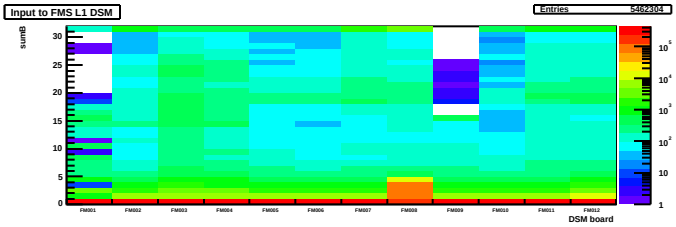

Input to FMS L1 DSM
Entries 7477
sumD - simulated
DSM board

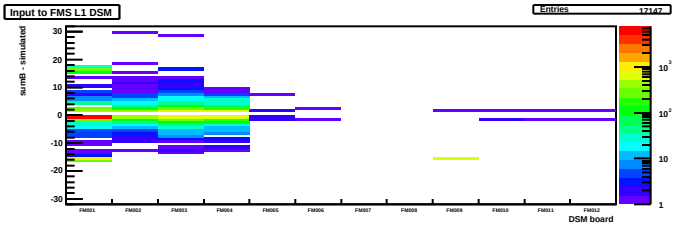

Input to FMS L1 DSM
Entries 5462304
sumC
DSM board

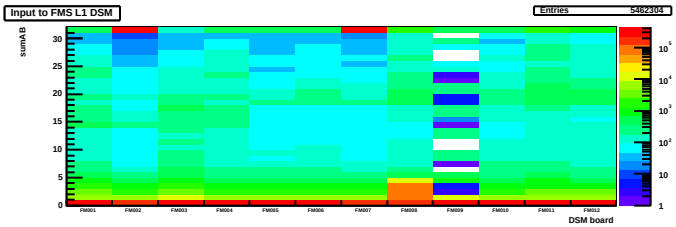

Input to FMS L1 DSM
Entries 6403
sumC - simulated
DSM board

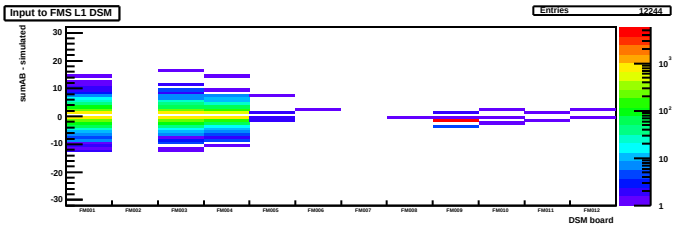

Input to FMS L1 DSM
Entries 5462304
sumBC
DSM board

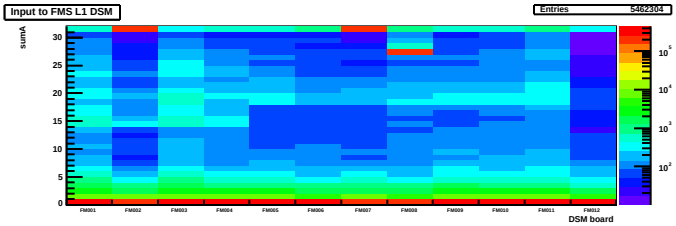

Input to FMS L1 DSM
Entries 7587
sumBC - simulated
DSM board

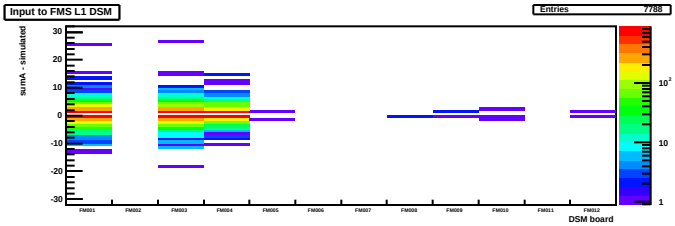

Input to FMS L1 DSM
Entries 5462304
sumB
DSM board

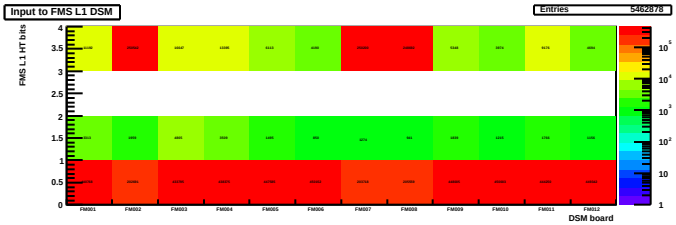

Input to FMS L1 DSM
Entries 17147
sumB - simulated
DSM board

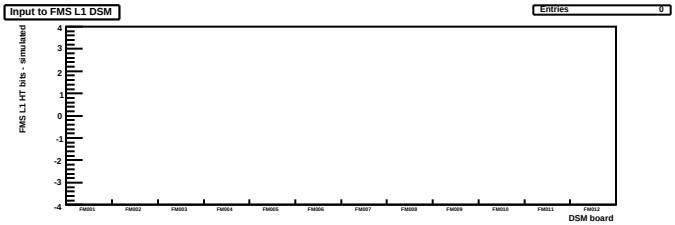

Input to FMS L1 DSM
Entries 5462304
sumAB
DSM board

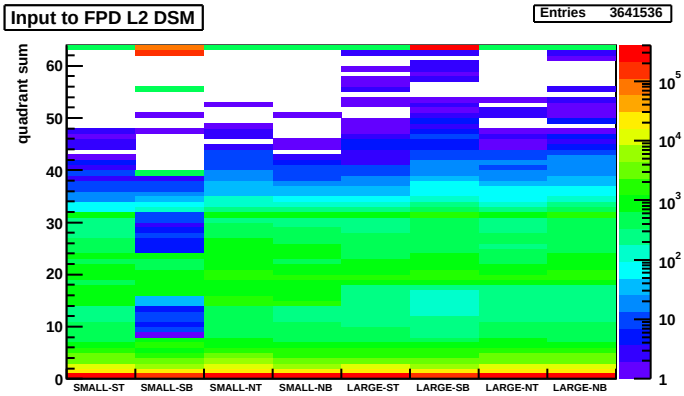

Input to FMS L1 DSM
Entries 12244
sumAB - simulated
DSM board

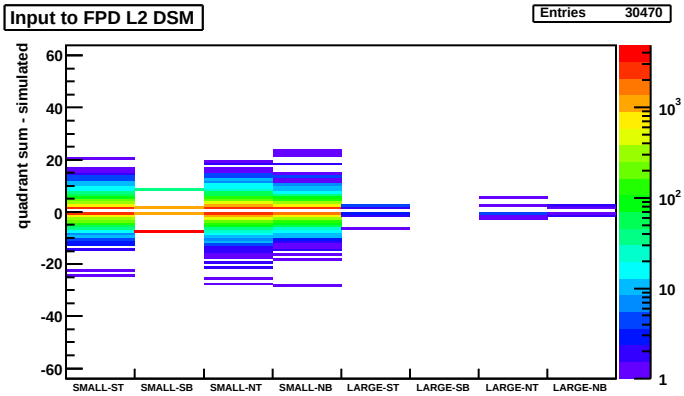

Input to FMS L1 DSM
Entries 5462304
sumA
DSM board

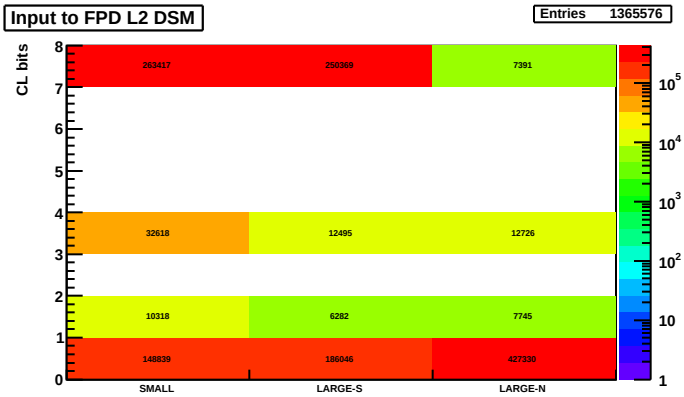

Input to FMS L1 DSM
Entries 7788
sumA - simulated
DSM board

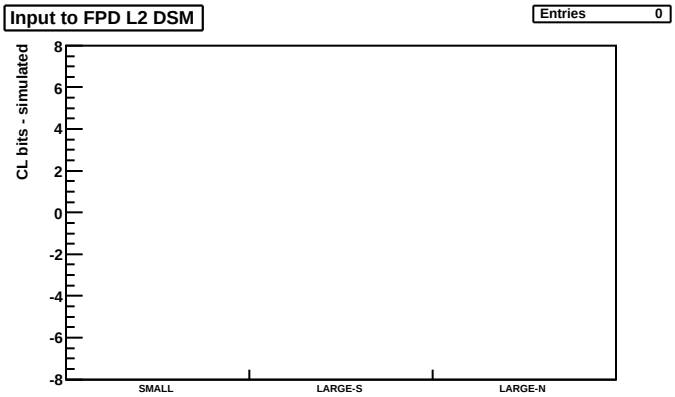

Input to FMS L1 DSM
Entries 5462878
FMS L1 HT bits
DSM board

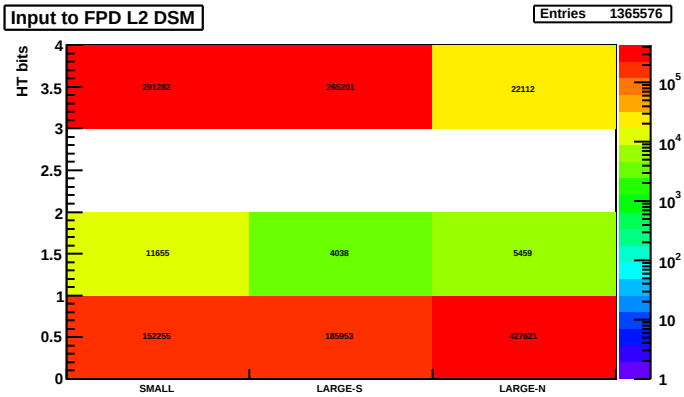

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

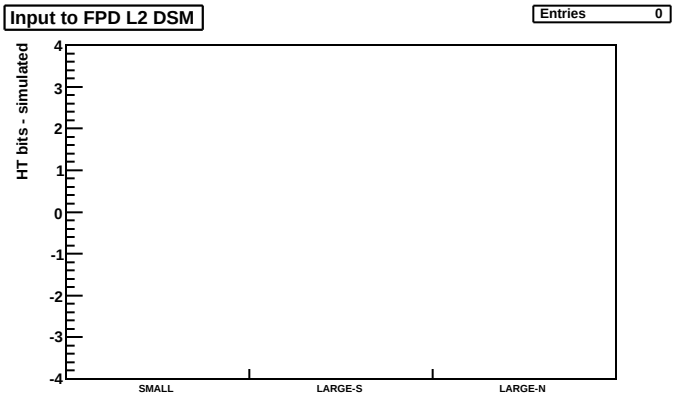

Input to FPD L2 DSM
Entries 3641536
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

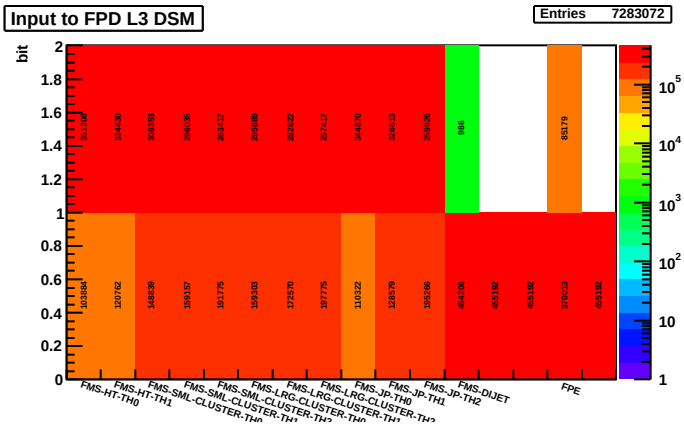

Input to FPD L2 DSM
Entries 30470
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

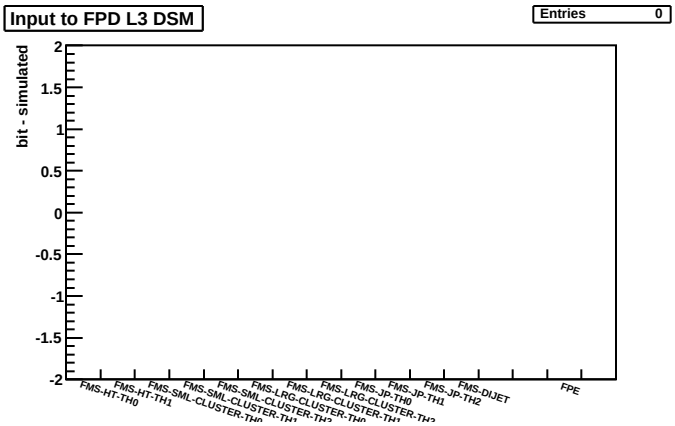

Input to FPD L2 DSM
Entries 1365576
CL bits
263417
250369
7391
32618
12495
12726
10318
6282
7745
148839
186046
427330
SMALL
LARGE-S
LARGE-N

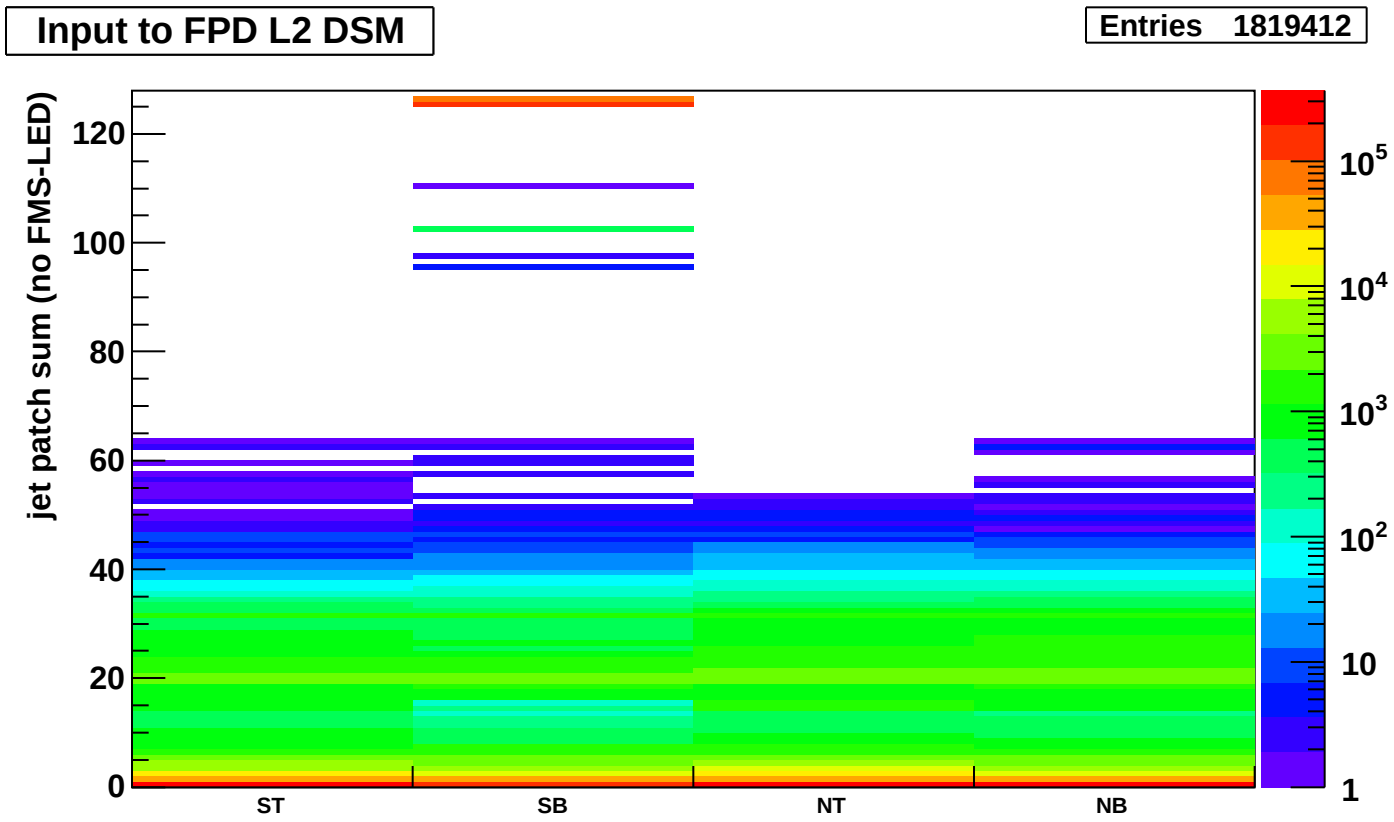

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

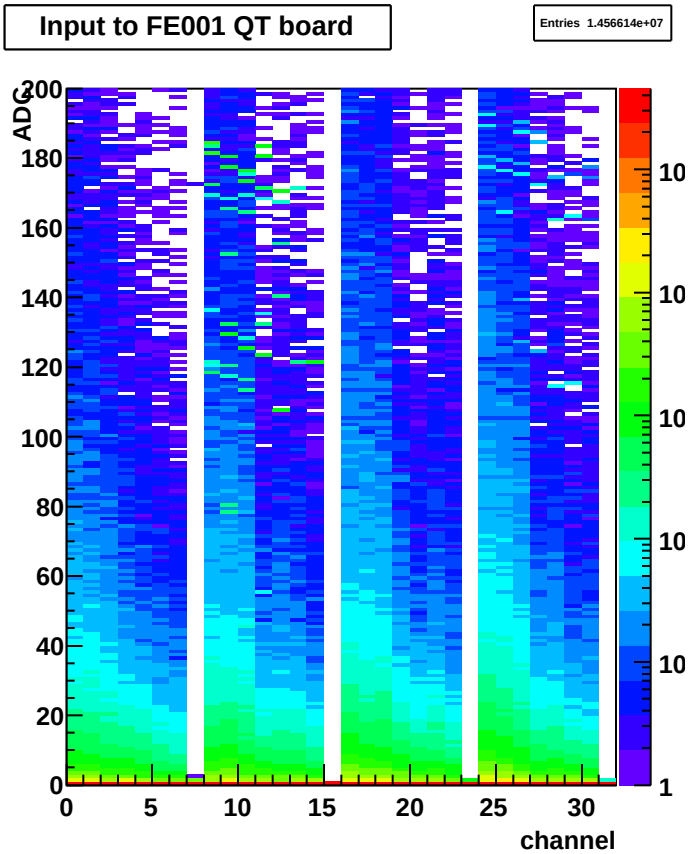

Input to FPD L2 DSM
Entries 1365576
HT bits
291282
265201
22112
11655
4038
5459
152255
185953
427621
SMALL
LARGE-S
LARGE-N

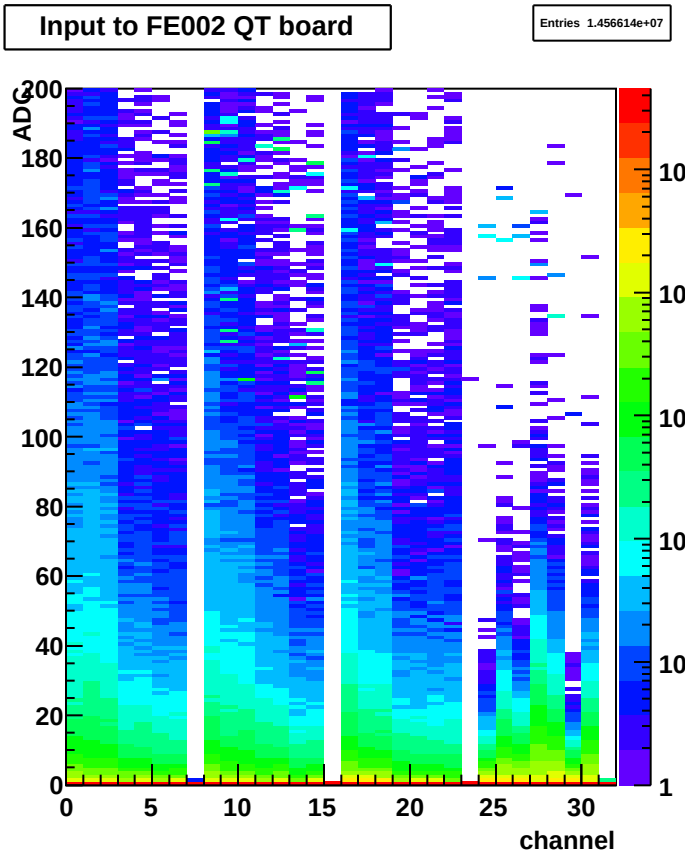

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

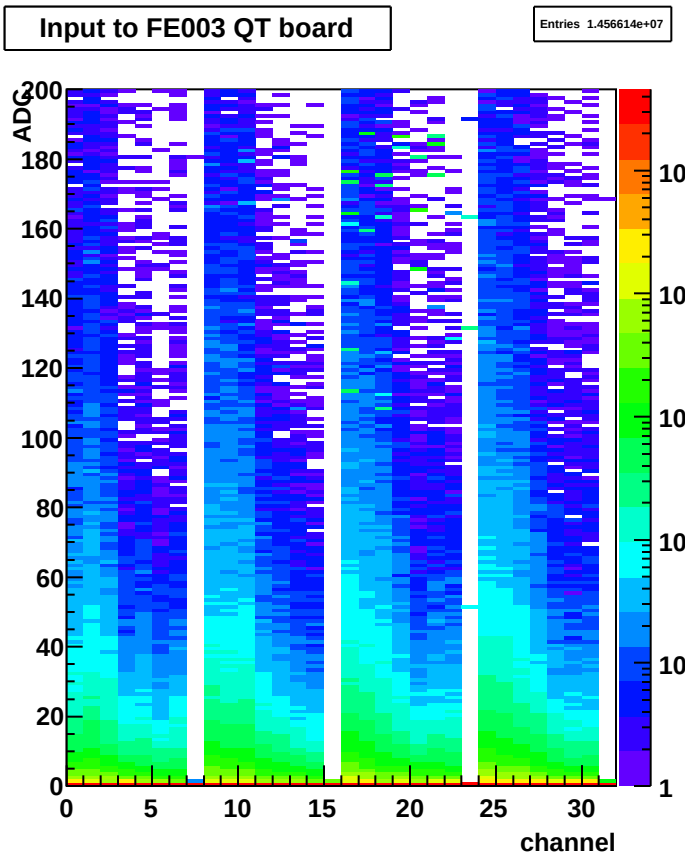

Input to FPD L3 DSM
Entries 7283072
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

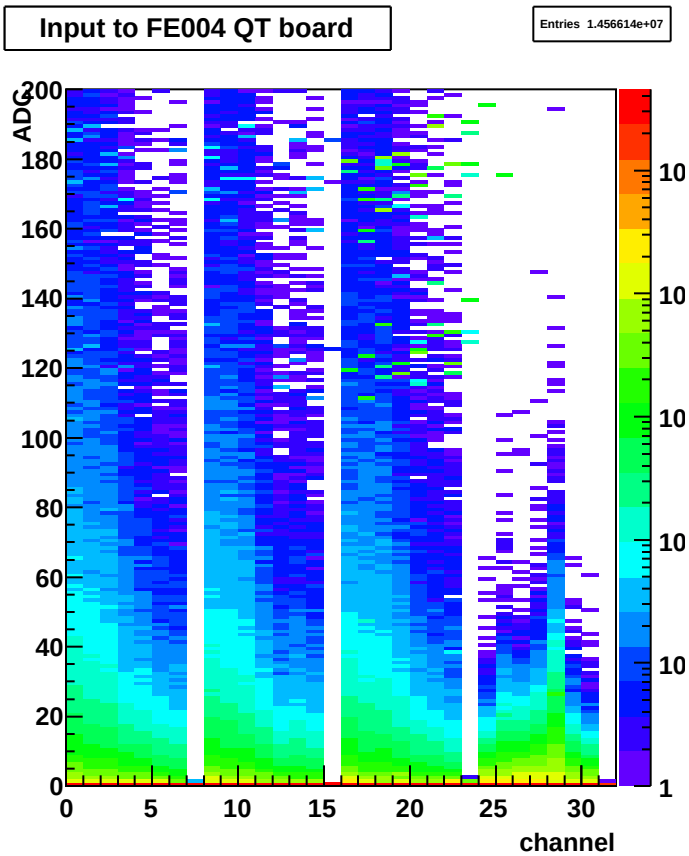

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

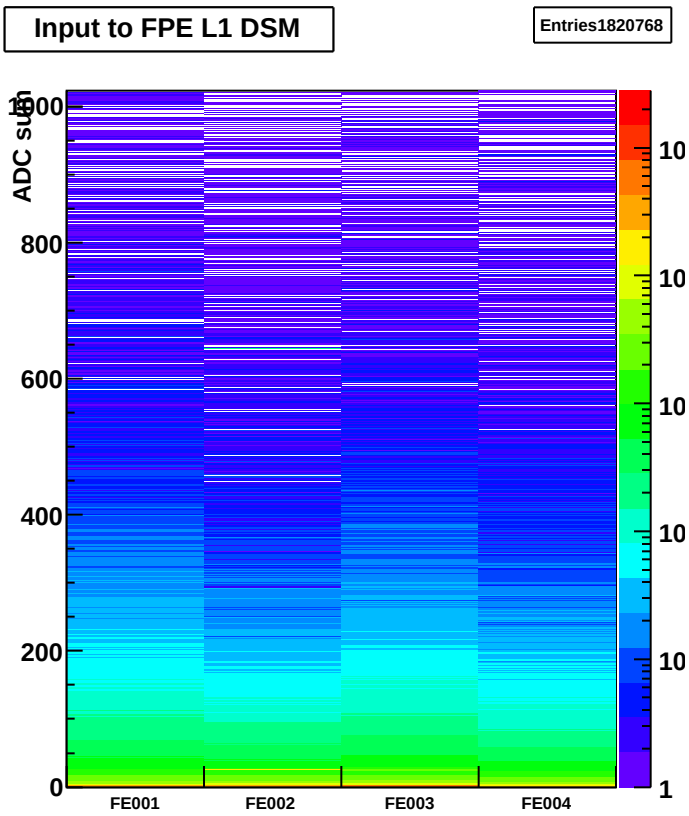

Input to FPD L2 DSM
Entries 1819412
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

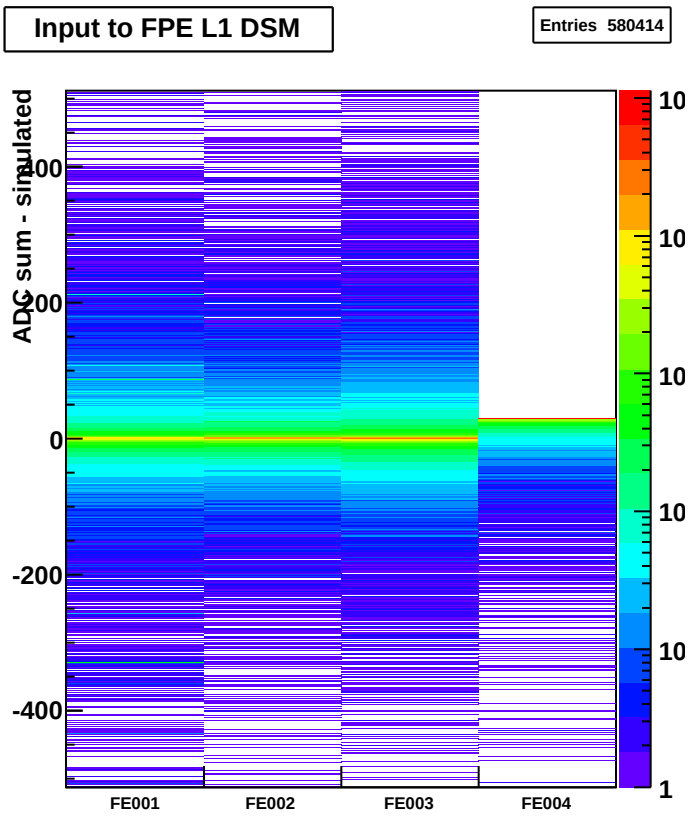

Input to FE001 QT board
Entries 1.456614e+07
ADC
channel

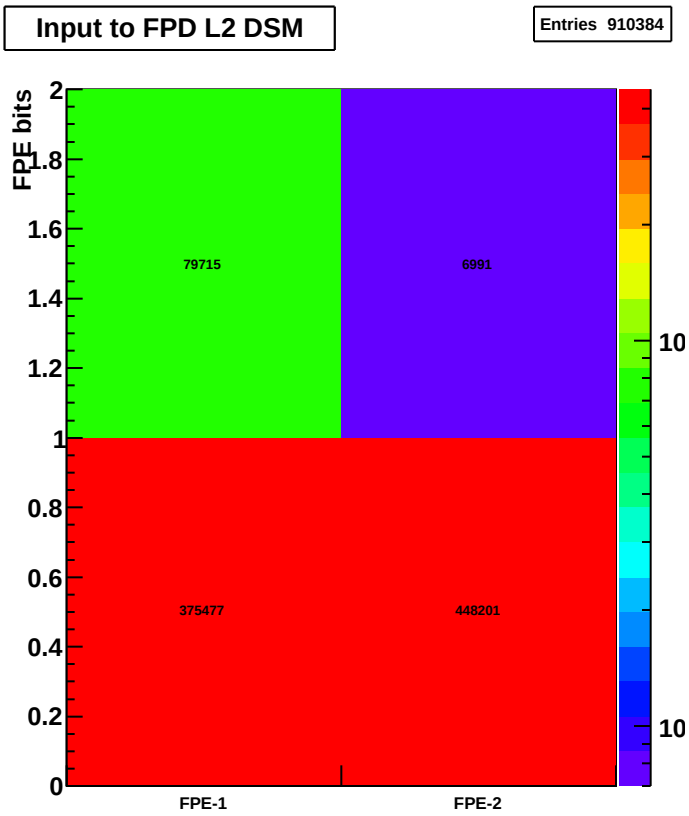

Input to FE002 QT board
Entries 1.456614e+07
ADC
channel

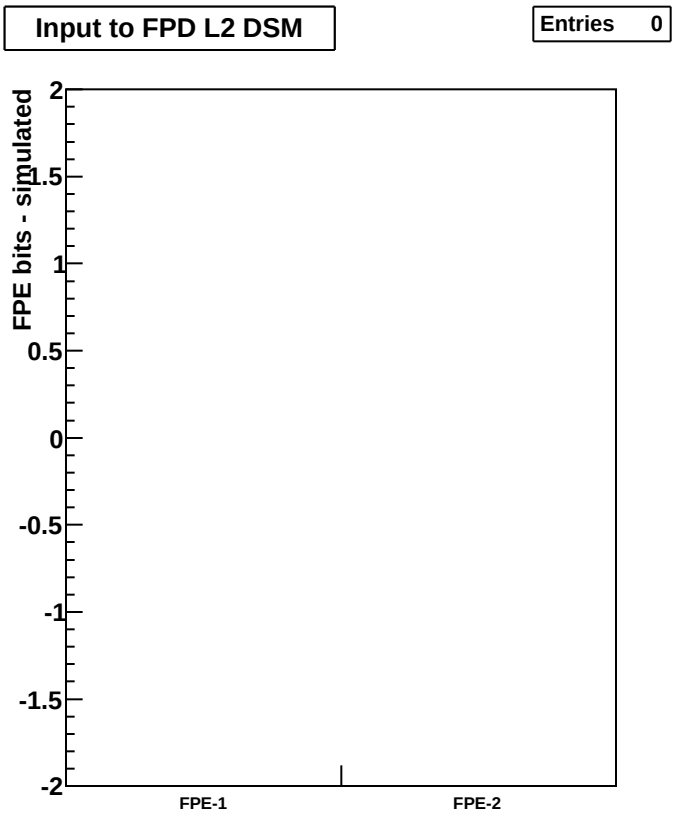

Input to FE003 QT board
Entries 1.456614e+07
ADC
channel


Input to FE004 QT board
Entries 1.456614e+07
ADC
channel


Input to FPE L1 DSM
Entries1820768
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 580414
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 910384
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
79715
6991
375477
448201
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2