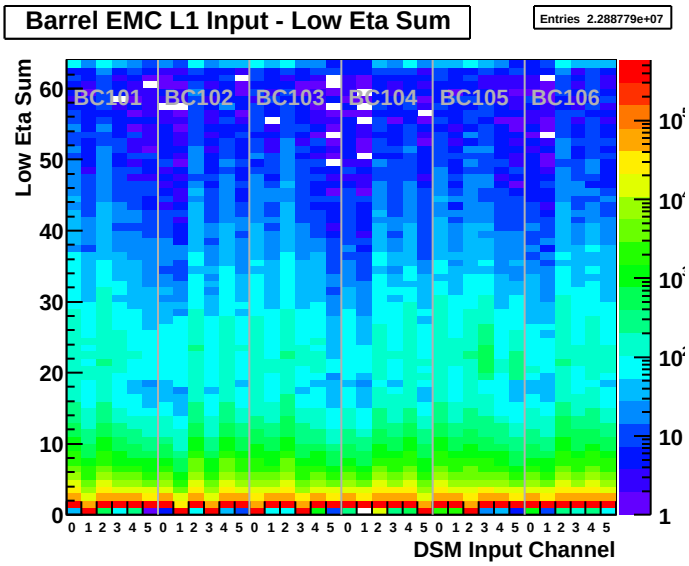
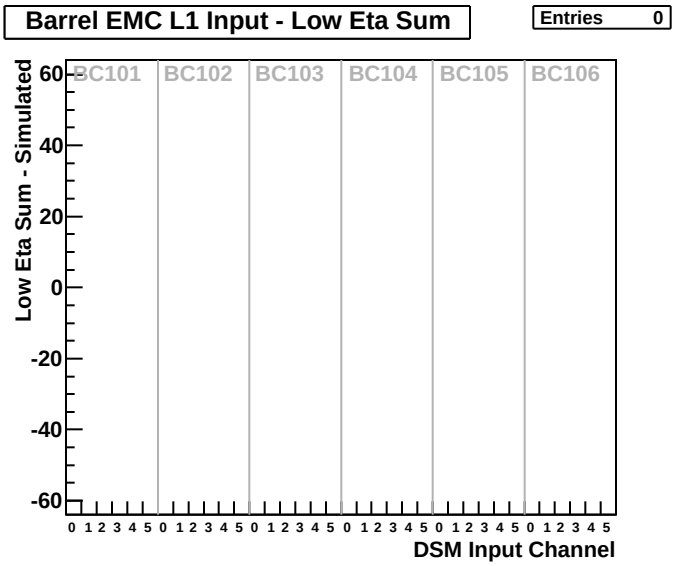
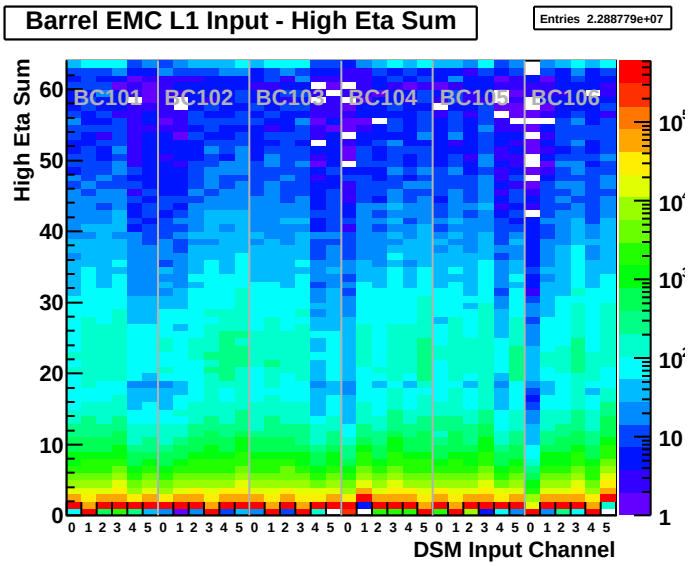
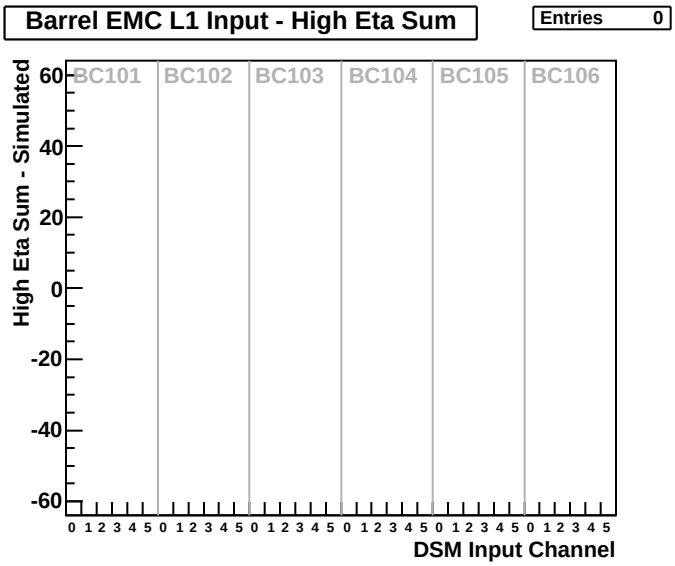
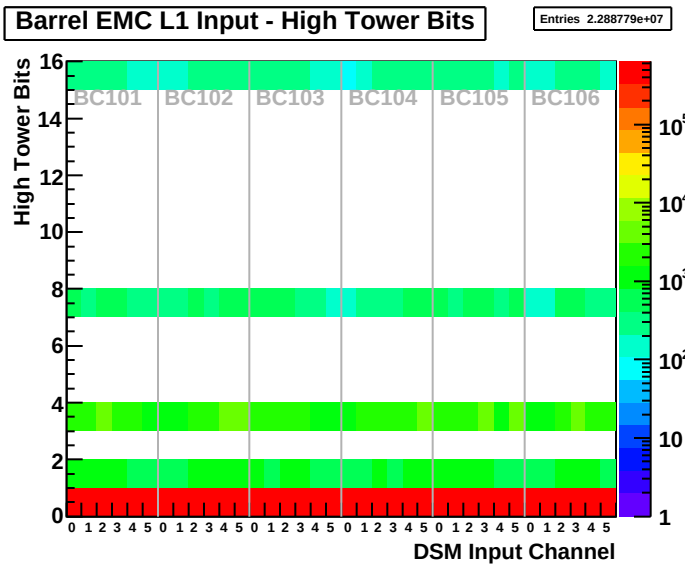
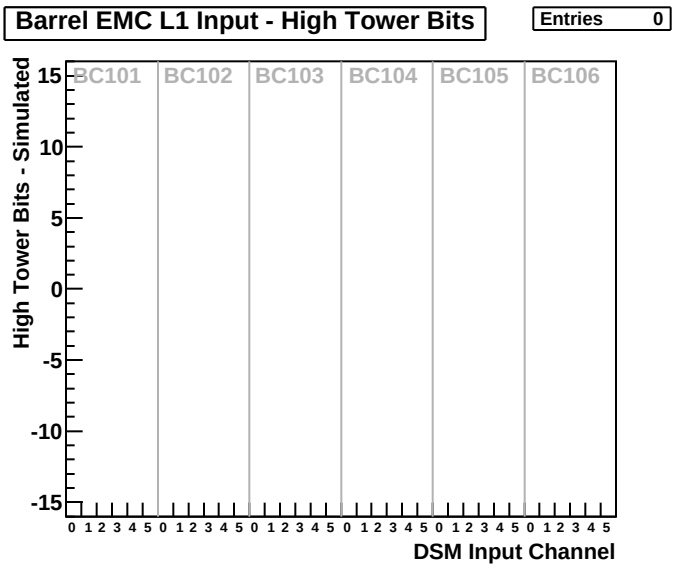

Barrel EMC L1 Input - Low Eta Sum
Entries 2.288779e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

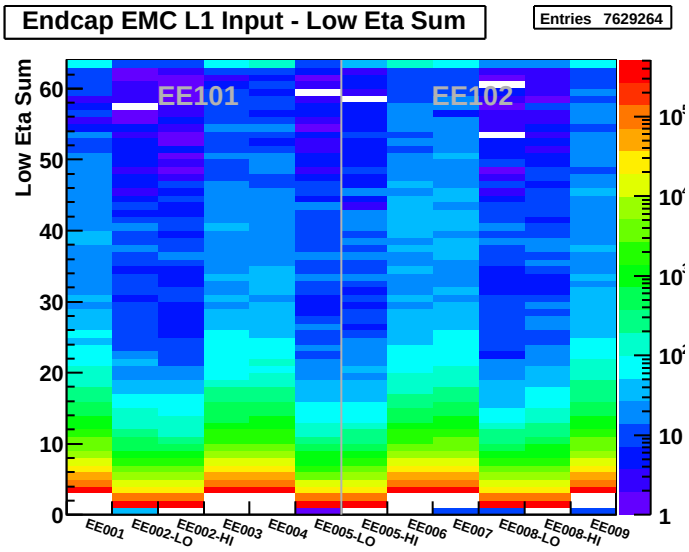

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

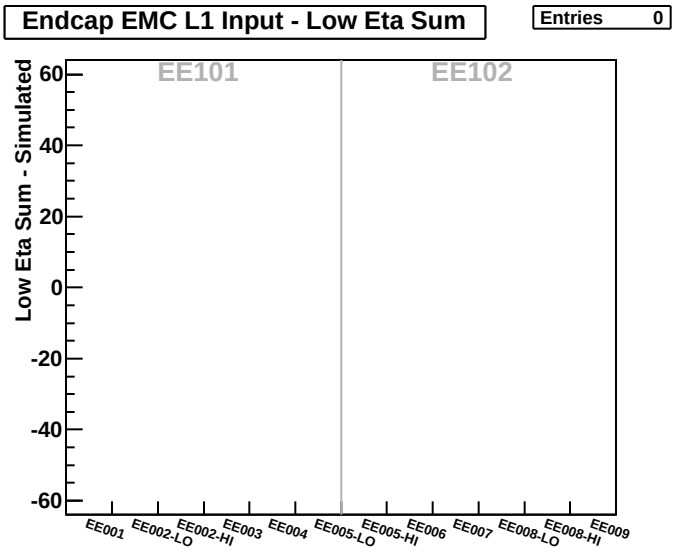

Barrel EMC L1 Input - High Eta Sum
Entries 2.288779e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

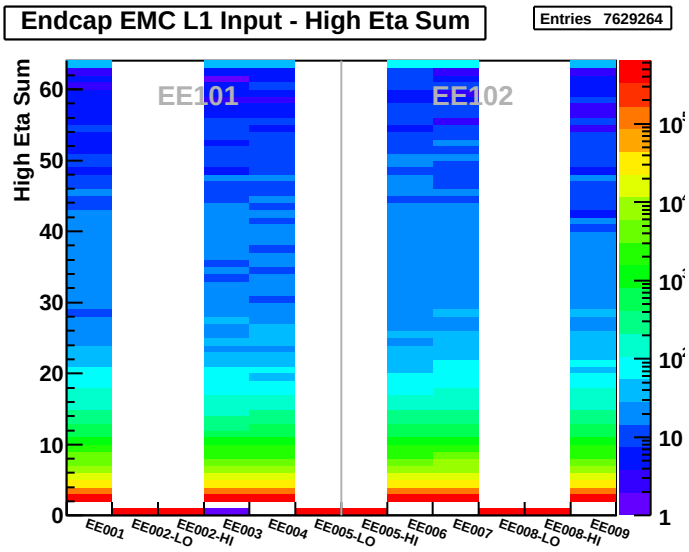

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

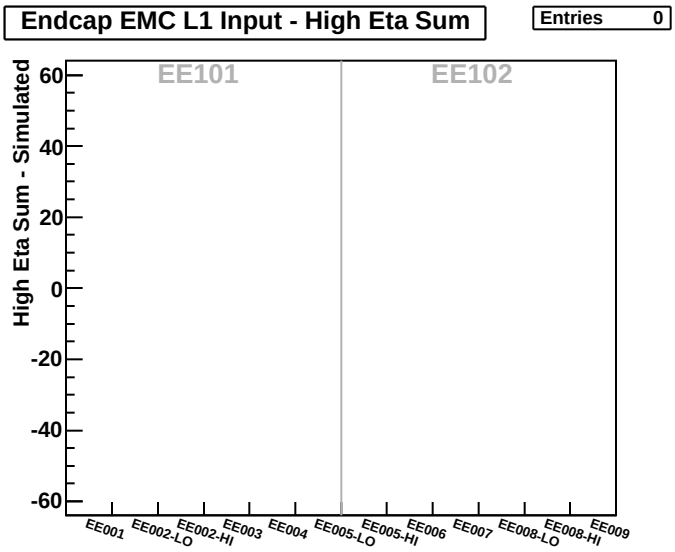

Barrel EMC L1 Input - High Tower Bits
Entries 2.288779e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

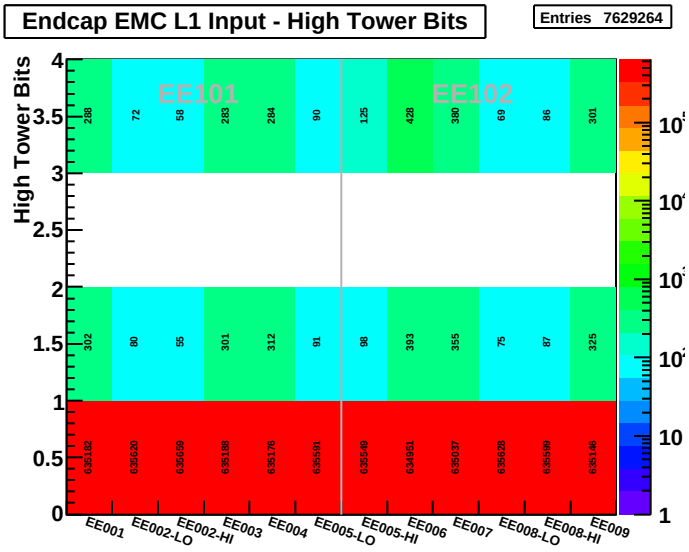

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

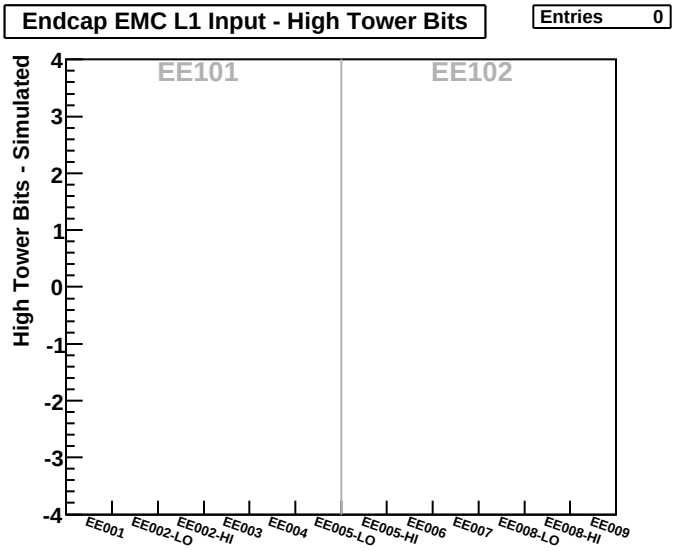

Endcap EMC L1 Input - Low Eta Sum
Entries 7629264
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

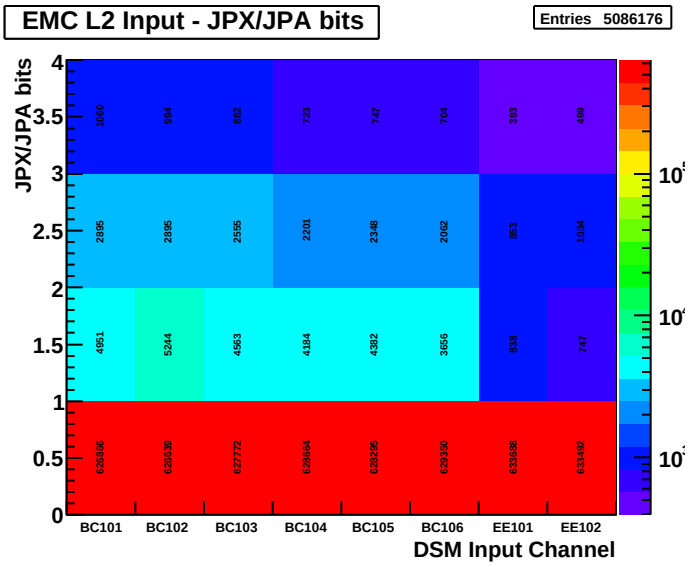

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

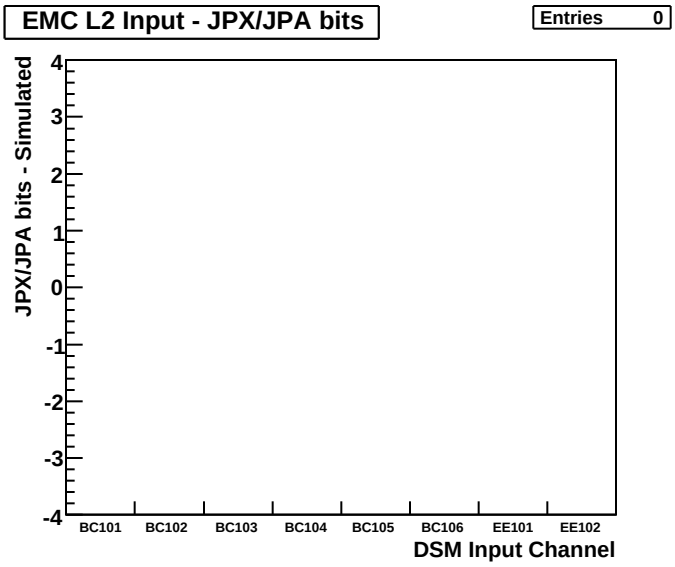

Endcap EMC L1 Input - High Eta Sum
Entries 7629264
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

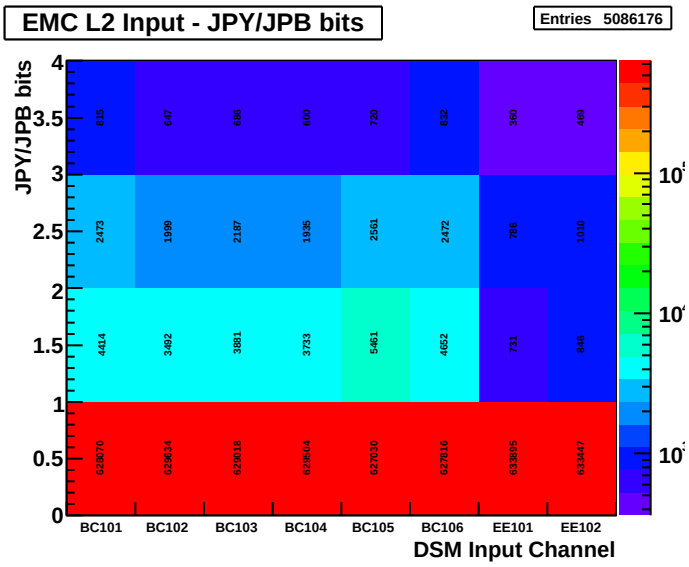

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

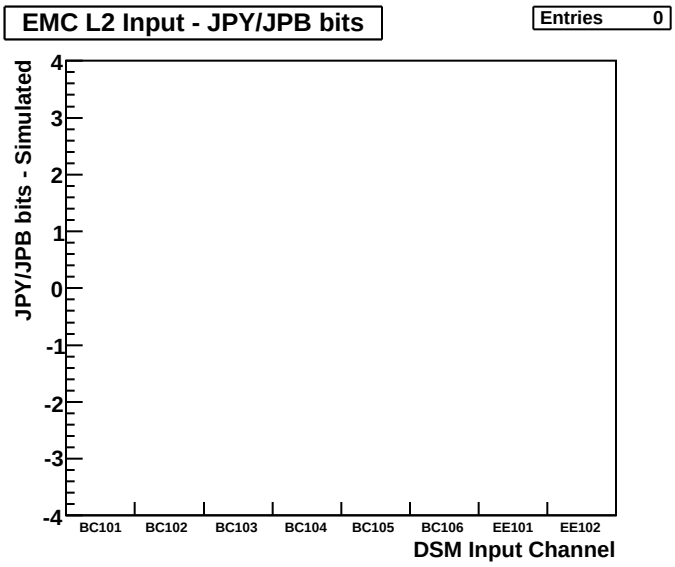

Endcap EMC L1 Input - High Tower Bits
Entries 7629264
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

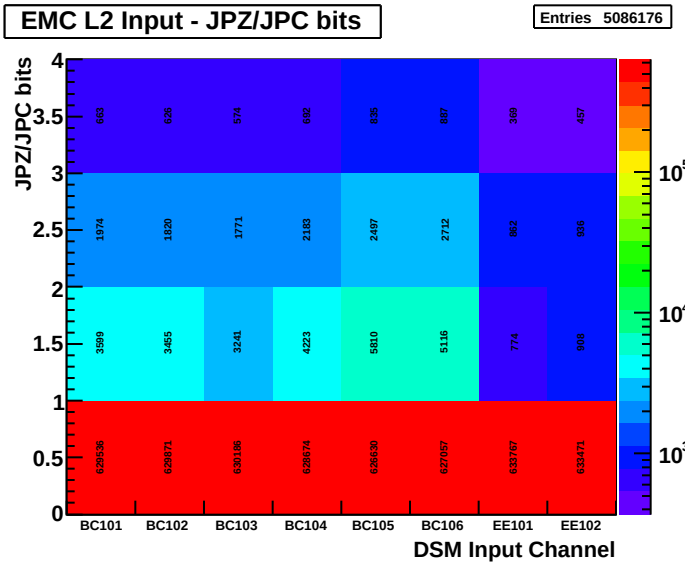

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

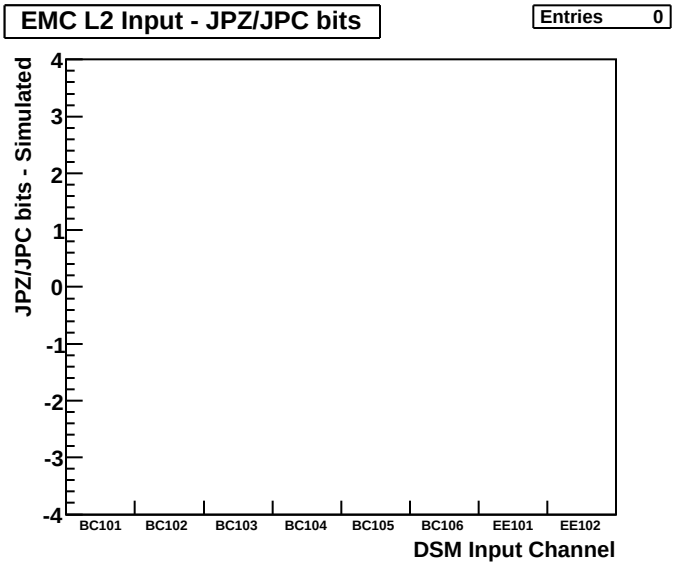

EMC L2 Input - JPX/JPA bits
Entries 5086176
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1060 994 882 723 747 704 393 499
2895 2895 2555 2201 2348 2062 853 1034
4951 5244 4563 4184 4382 3656 838 747
626866 626639 627772 628664 629295 629350 633688 633492

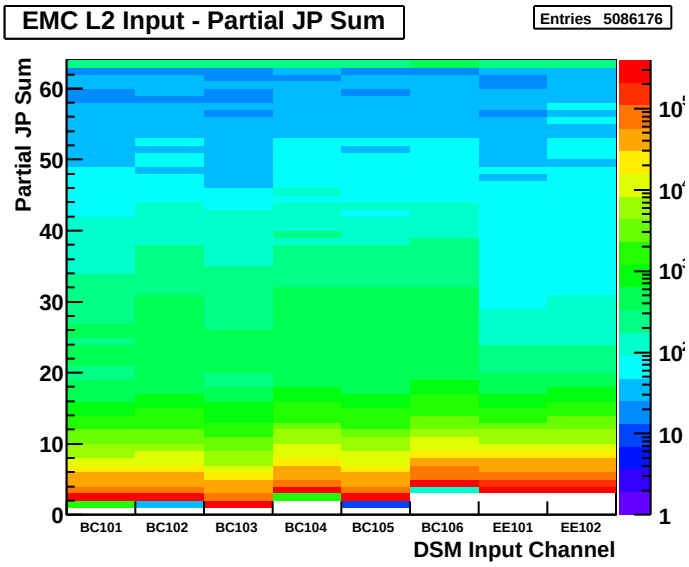

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

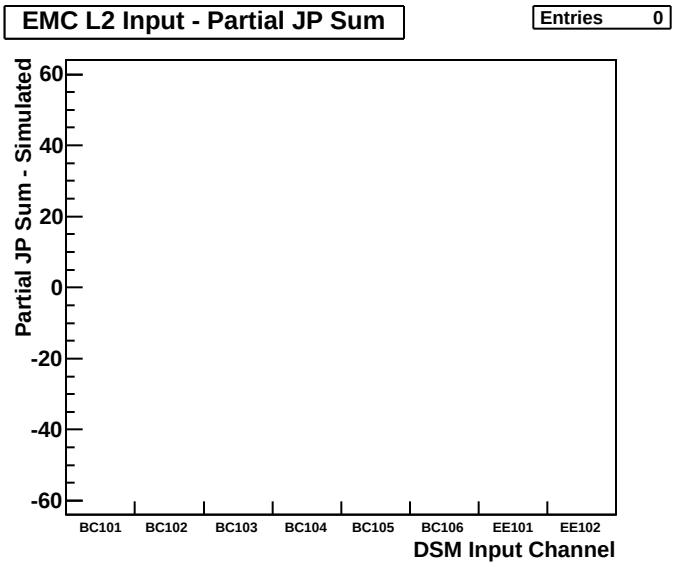

EMC L2 Input - JPY/JPB bits
Entries 5086176
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
615 647 686 600 720 832 360 469
2473 1999 2187 1935 2561 2472 788 1010
4414 3492 3881 3733 5461 4652 731 846
626070 626634 628018 628504 627030 627816 633895 633447

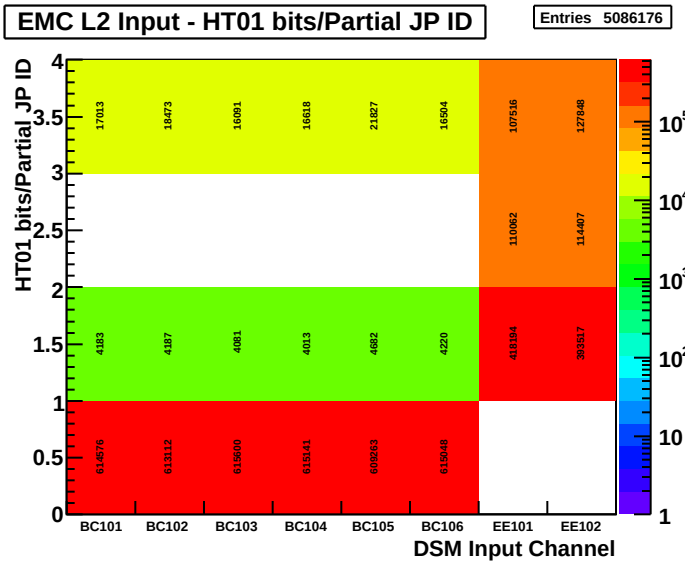

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

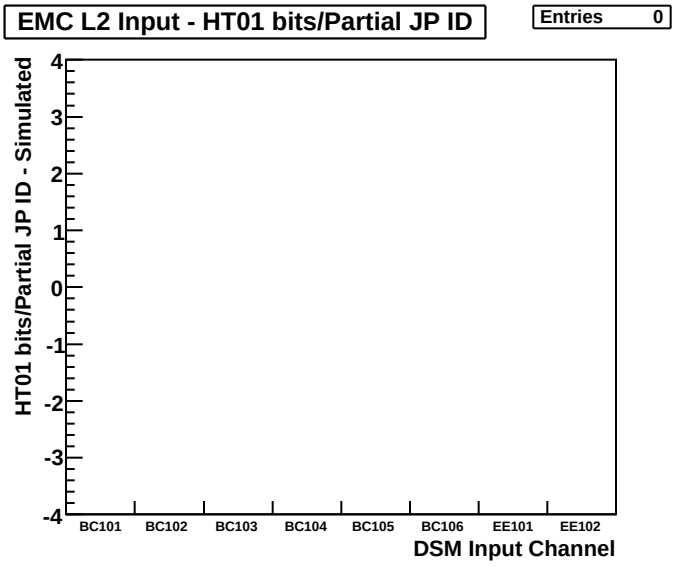

EMC L2 Input - JPZ/JPC bits
Entries 5086176
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
663 626 574 692 835 887 369 457
1974 1820 1771 2183 2497 2712 862 936
3999 3465 3241 4223 5810 5116 774 908
629536 629871 630186 628874 626530 627057 633767 633471

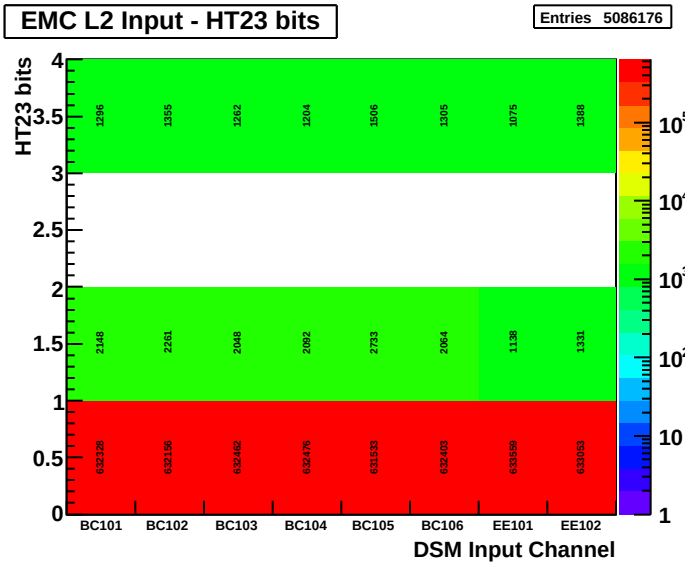

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

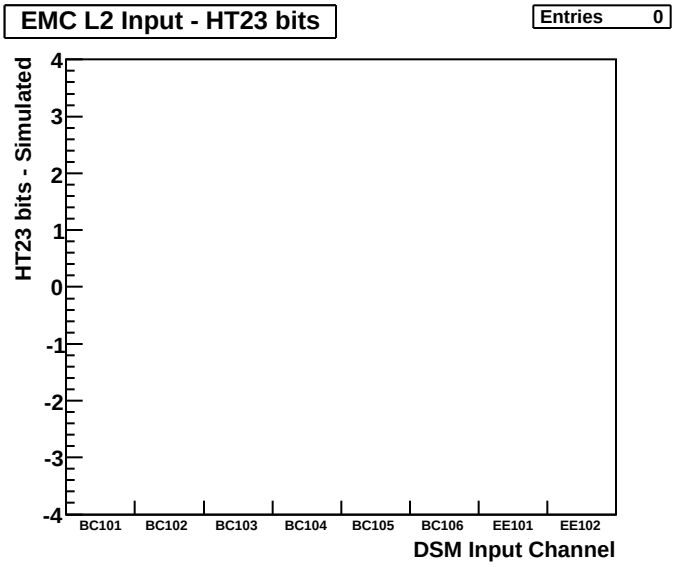

EMC L2 Input - Partial JP Sum
Entries 5086176
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

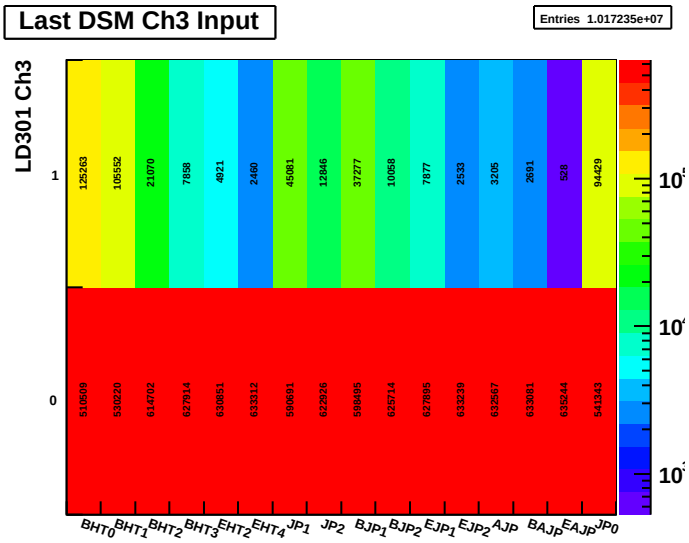

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

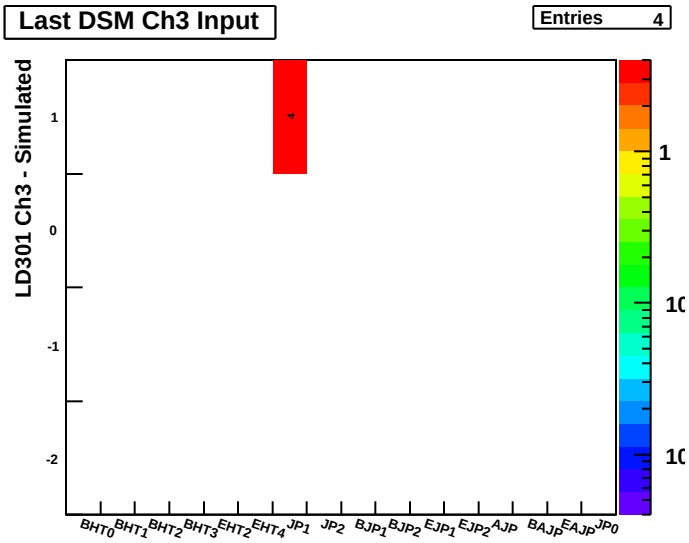

EMC L2 Input - HT01 bits/Partial JP ID
Entries 5086176
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

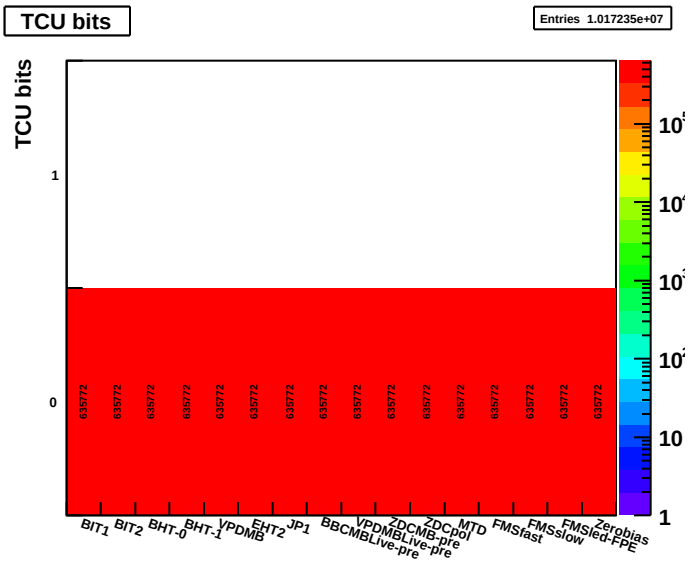

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

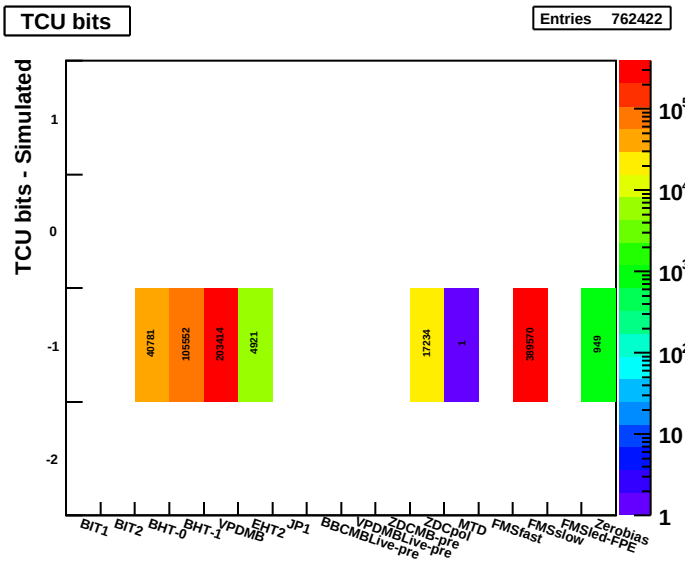

EMC L2 Input - HT23 bits
Entries 5086176
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

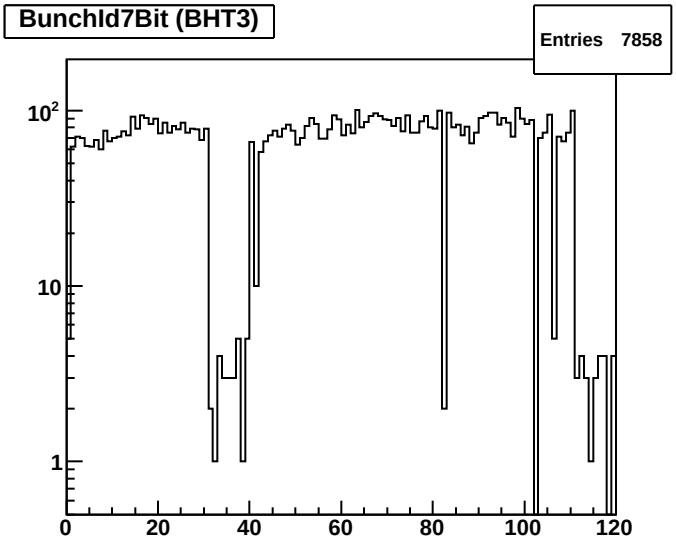

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

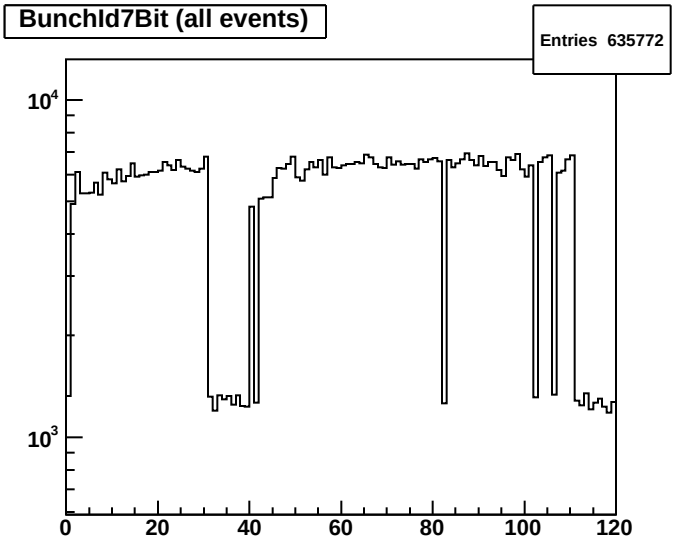

Last DSM Ch3 Input
Entries 1.017235e+07
LD301 Ch3
125363 105552 21070 7858 4921 2460 45081 12846 37277 10058 7877 2533 3205 2691 528 94429
510509 530220 614702 627914 630851 633312 590691 622926 598495 625714 627895 632239 632567 633081 635244 541343
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

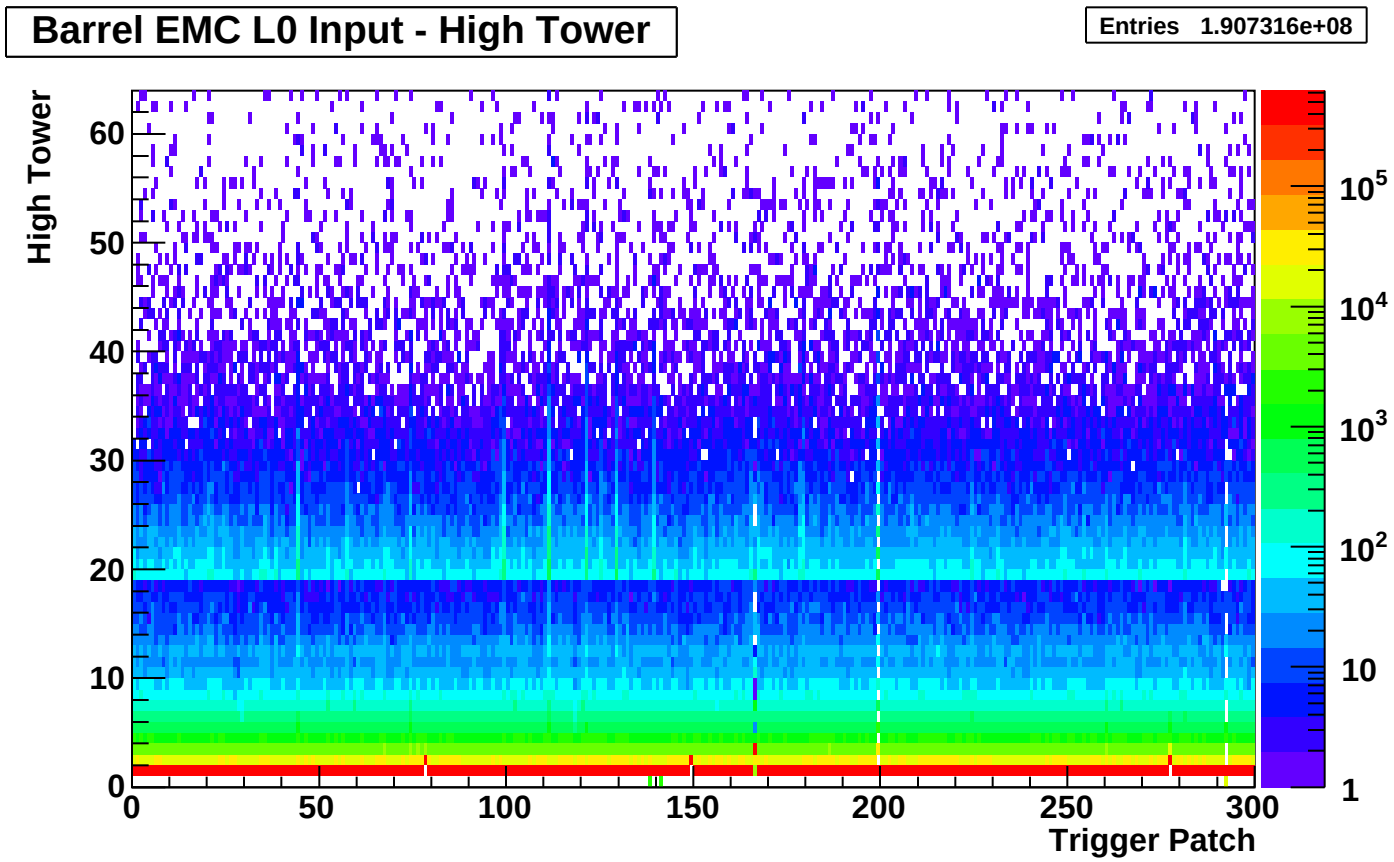

Last DSM Ch3 Input
Entries 4
LD301 Ch3 - Simulated
4
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

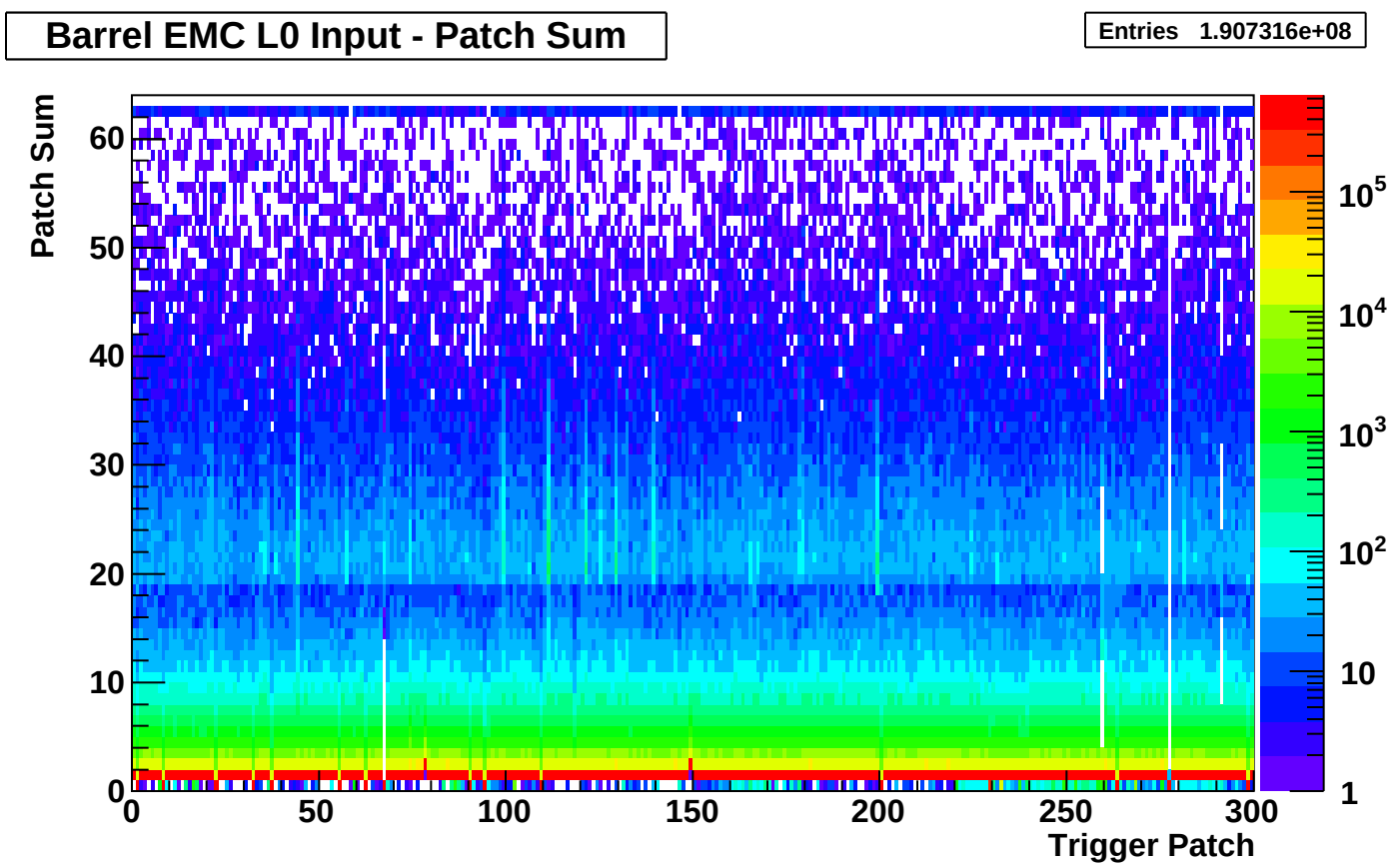

TCU bits
Entries 1.017235e+07
TCU bits
635772
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

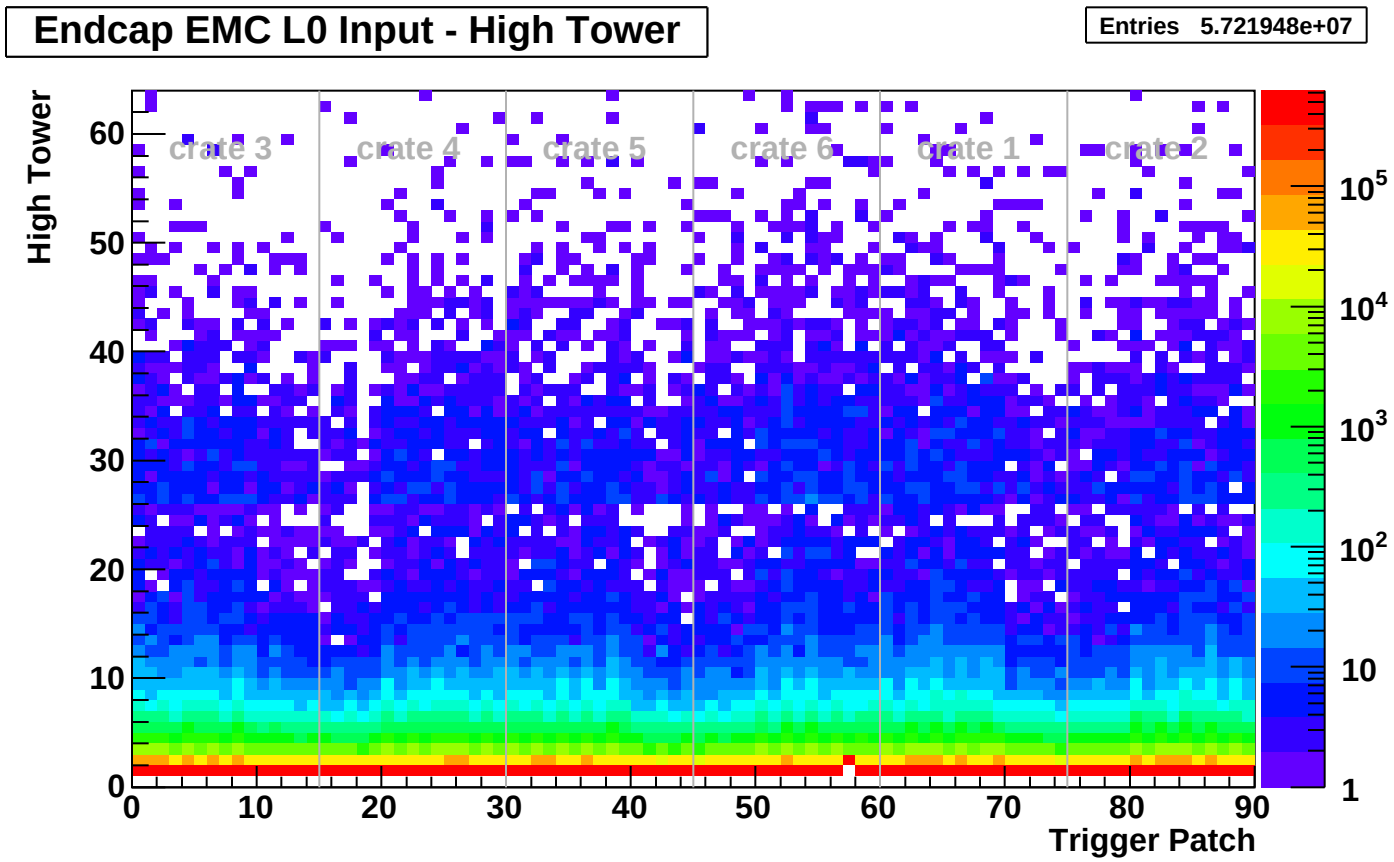

TCU bits
Entries 762422
TCU bits - Simulated
40781 105552 203414 4921 17234 1 389570 949
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

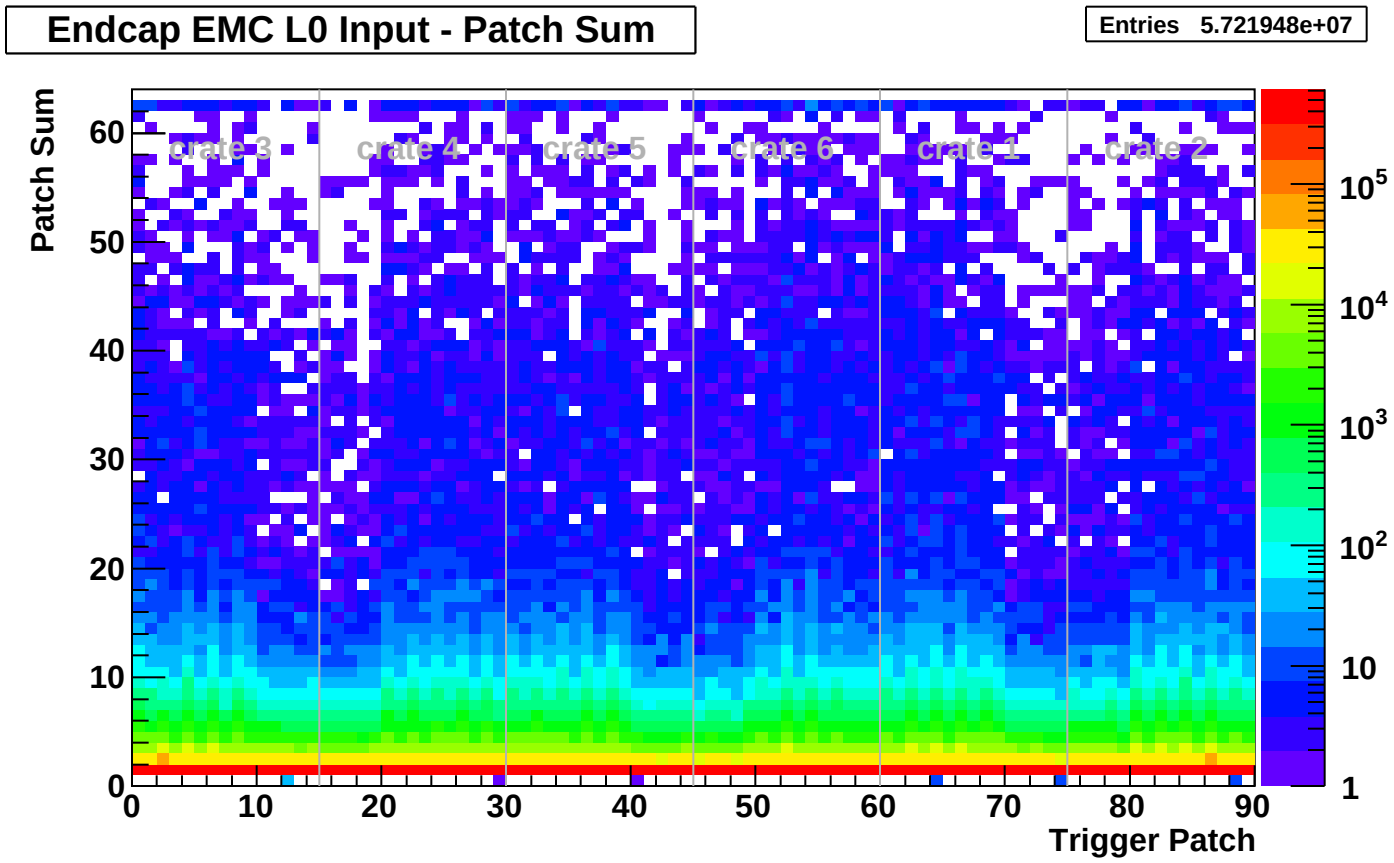

BunchId7Bit (BHT3)
Entries 7858

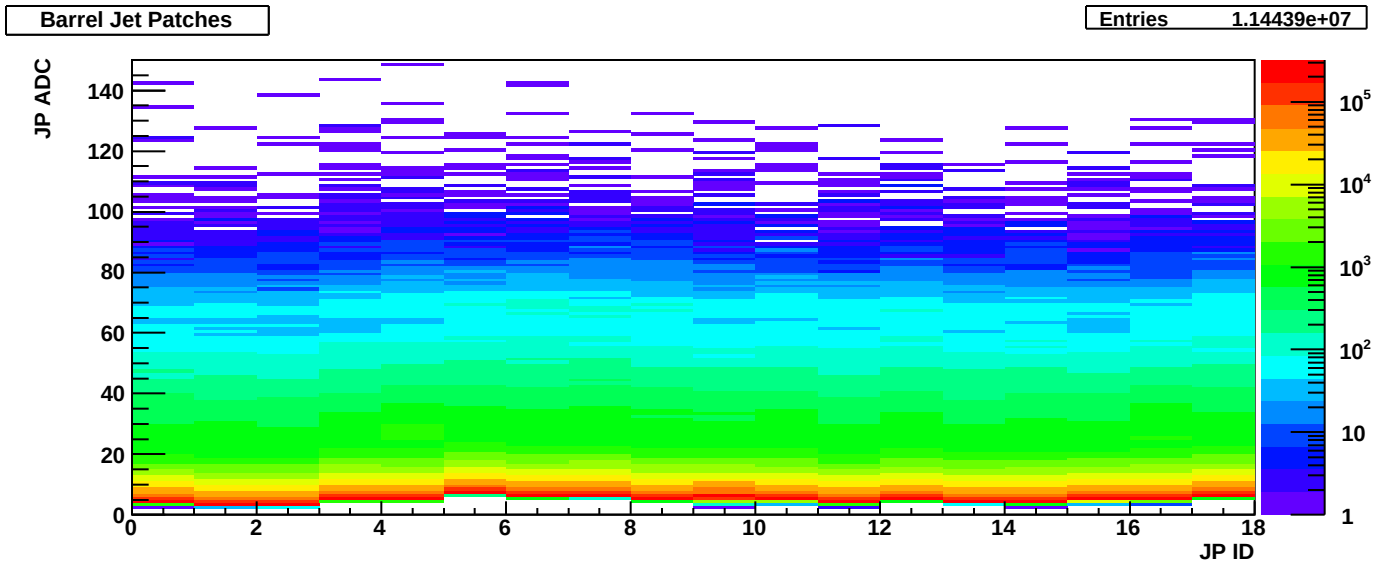

BunchId7Bit (all events)
Entries 635772

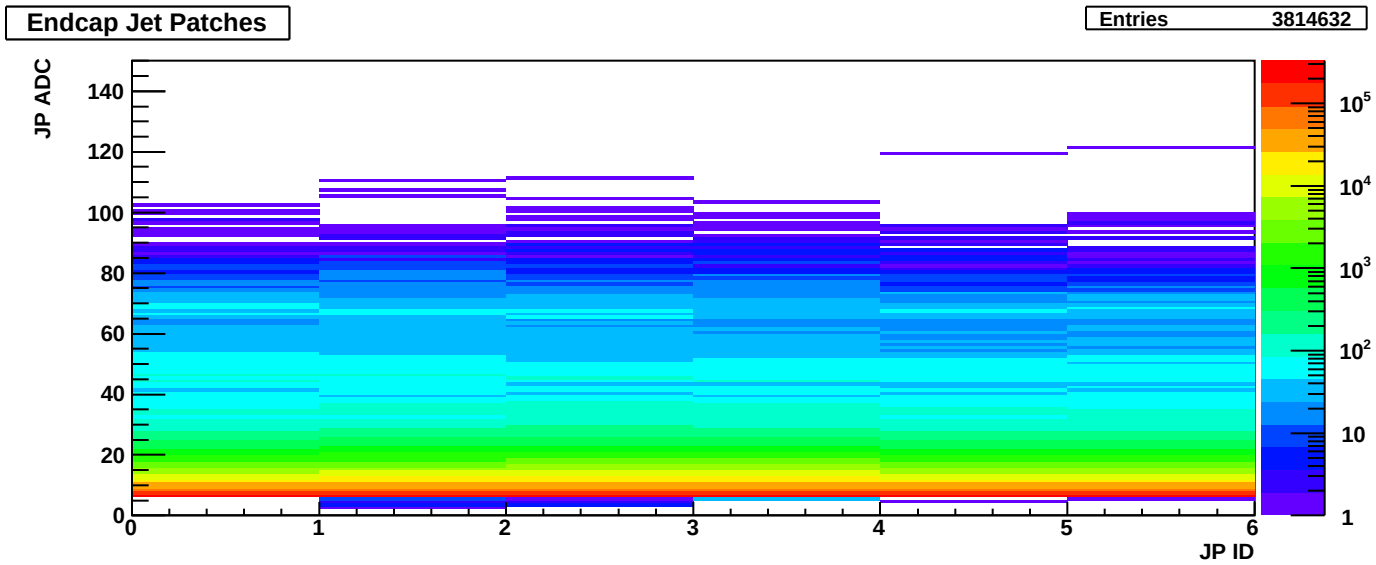

Barrel EMC L0 Input - High Tower
Entries 1.907316e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

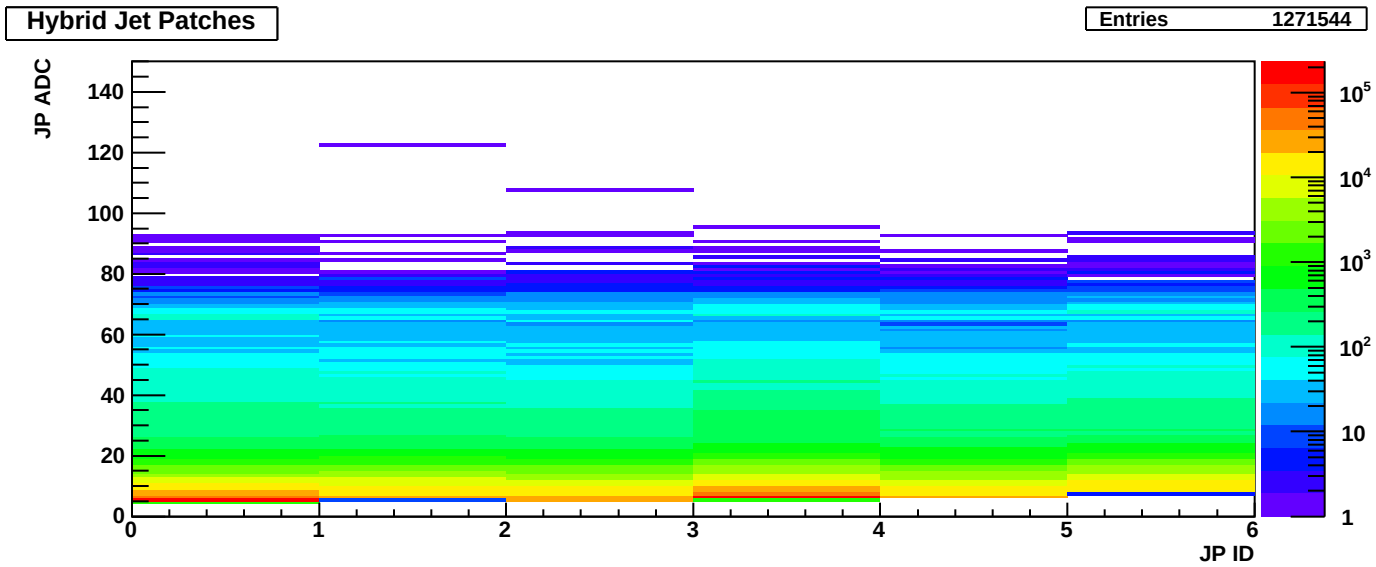

Barrel EMC L0 Input - Patch Sum
Entries 1.907316e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

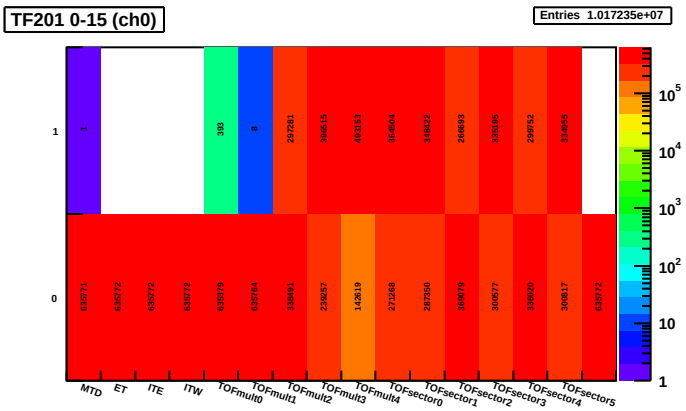

Endcap EMC L0 Input - High Tower
Entries 5.721948e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

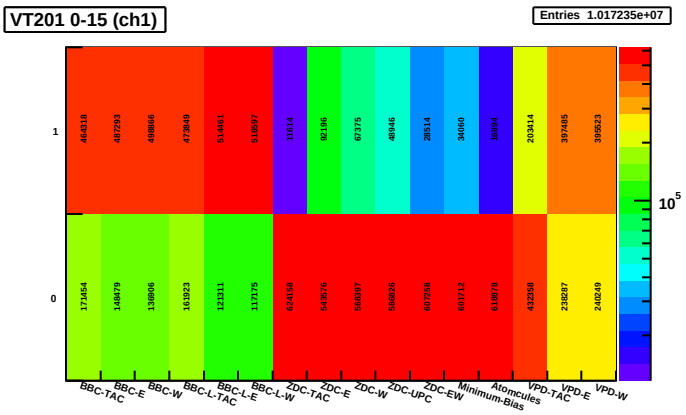

Endcap EMC L0 Input - Patch Sum
Entries 5.721948e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

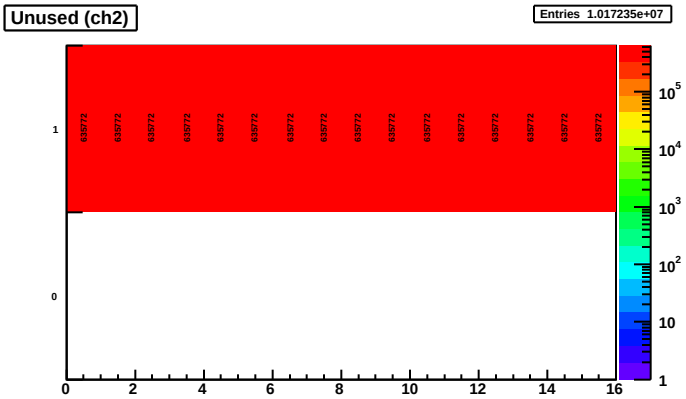

Barrel Jet Patches
Entries 1.14439e+07
JP ADC
JP ID

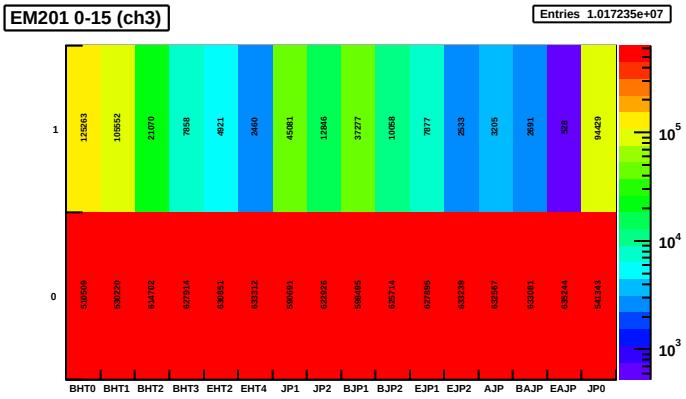

Endcap Jet Patches
Entries 3814632
JP ADC
JP ID

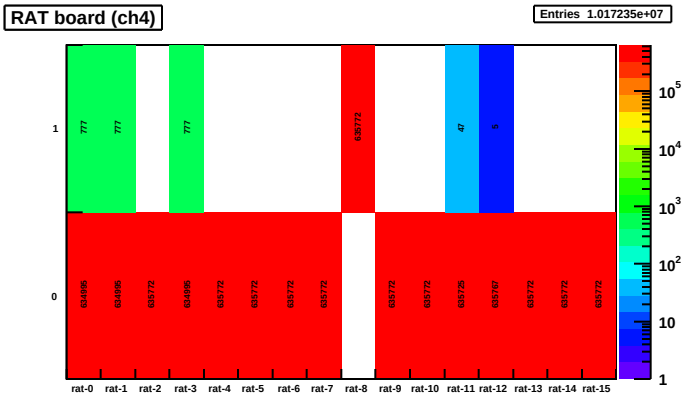

Hybrid Jet Patches
Entries 1271544
JP ADC
JP ID

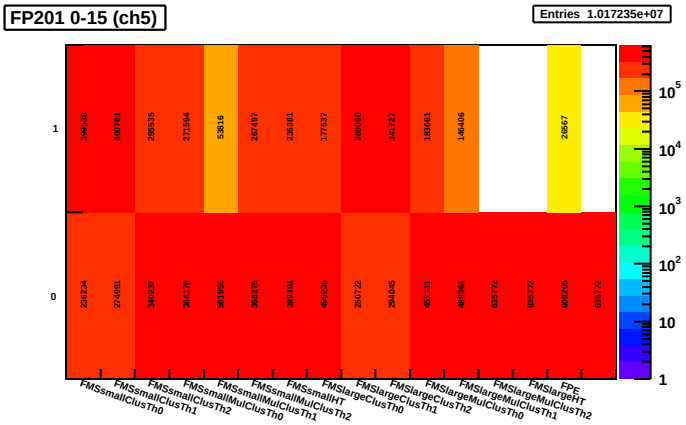

TF201 0-15 (ch0)
Entries 1.017235e+07
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

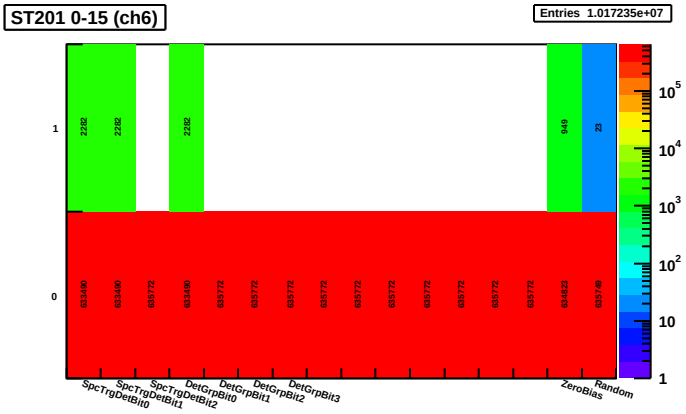

VT201 0-15 (ch1)
Entries 1.017235e+07
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

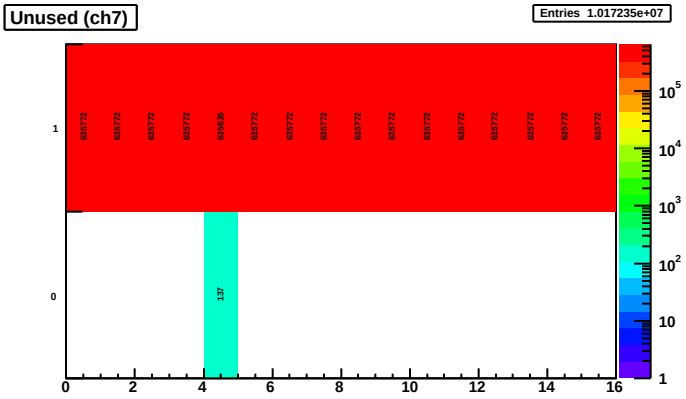

Unused (ch2)
Entries 1.017235e+07

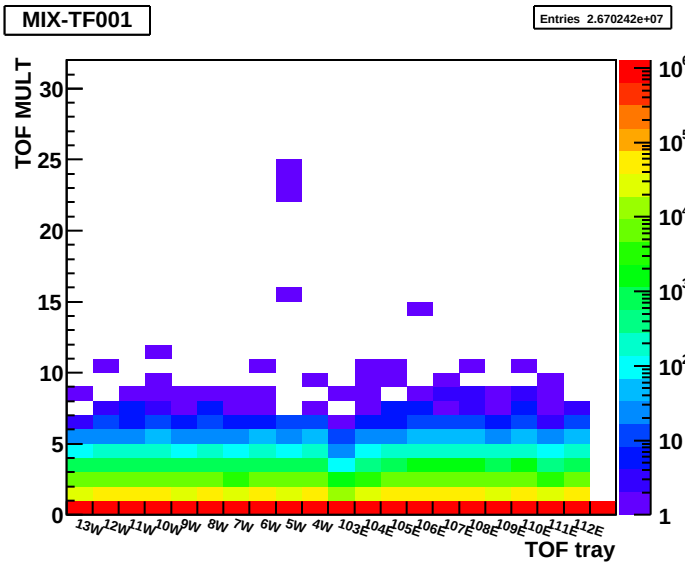

EM201 0-15 (ch3)
Entries 1.017235e+07
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

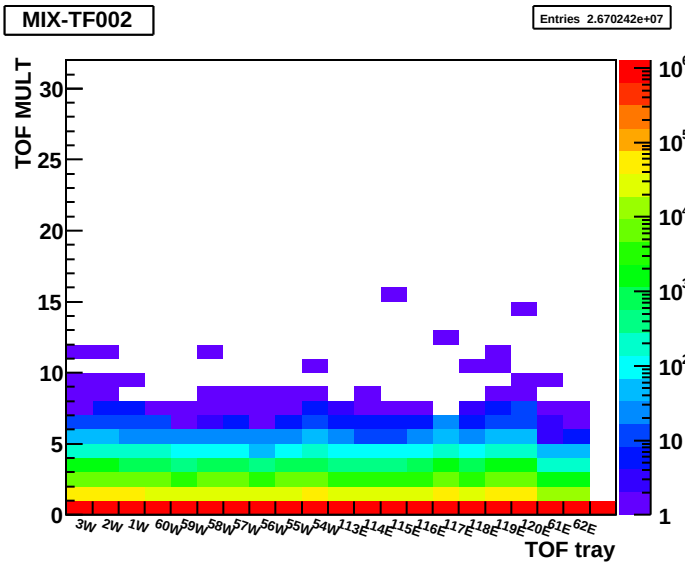

RAT board (ch4)
Entries 1.017235e+07
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

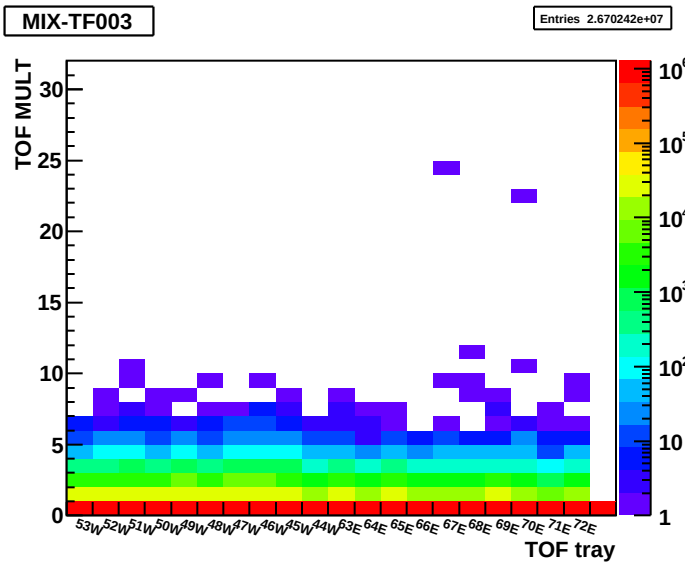

FP201 0-15 (ch5)
Entries 1.017235e+07
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

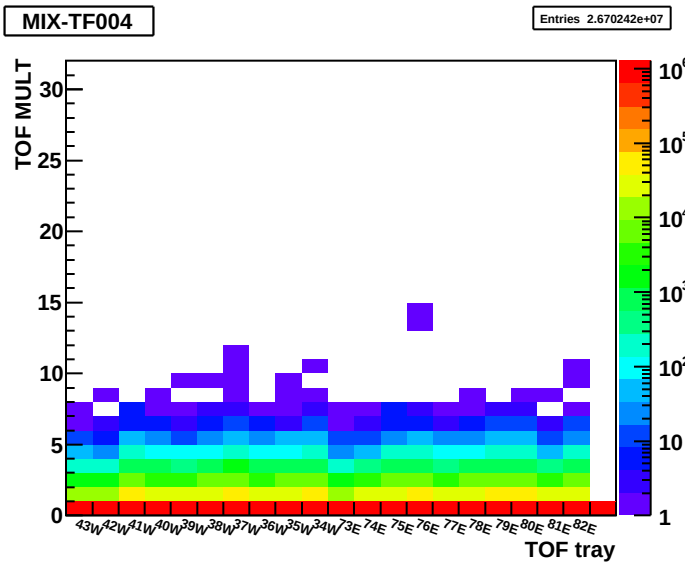

ST201 0-15 (ch6)
Entries 1.017235e+07
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

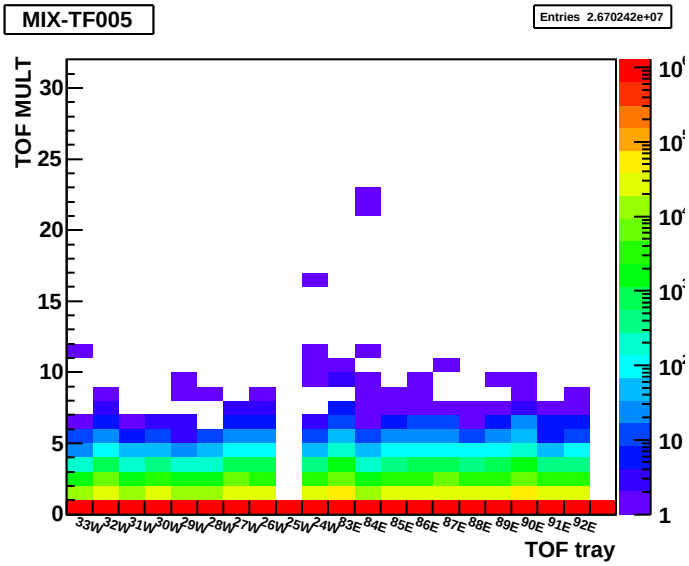

Unused (ch7)
Entries 1.017235e+07

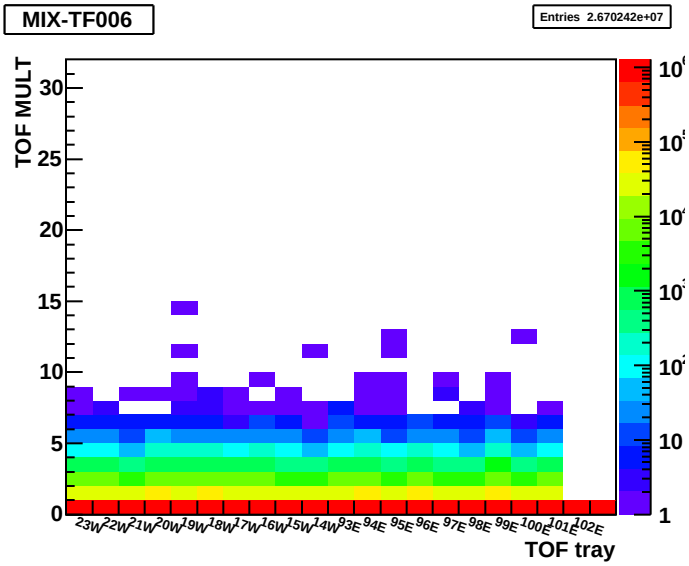

MIX-TF001
Entries 2.670242e+07
TOF MULT
TOF tray

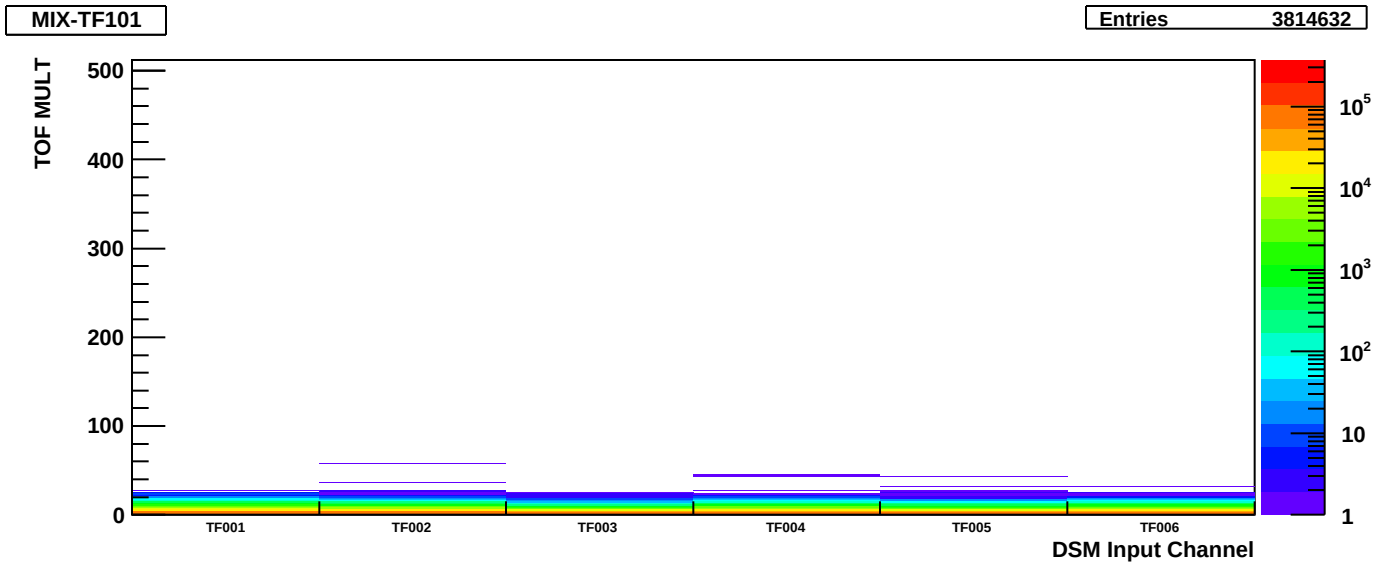

MIX-TF002
Entries 2.670242e+07
TOF MULT
TOF tray

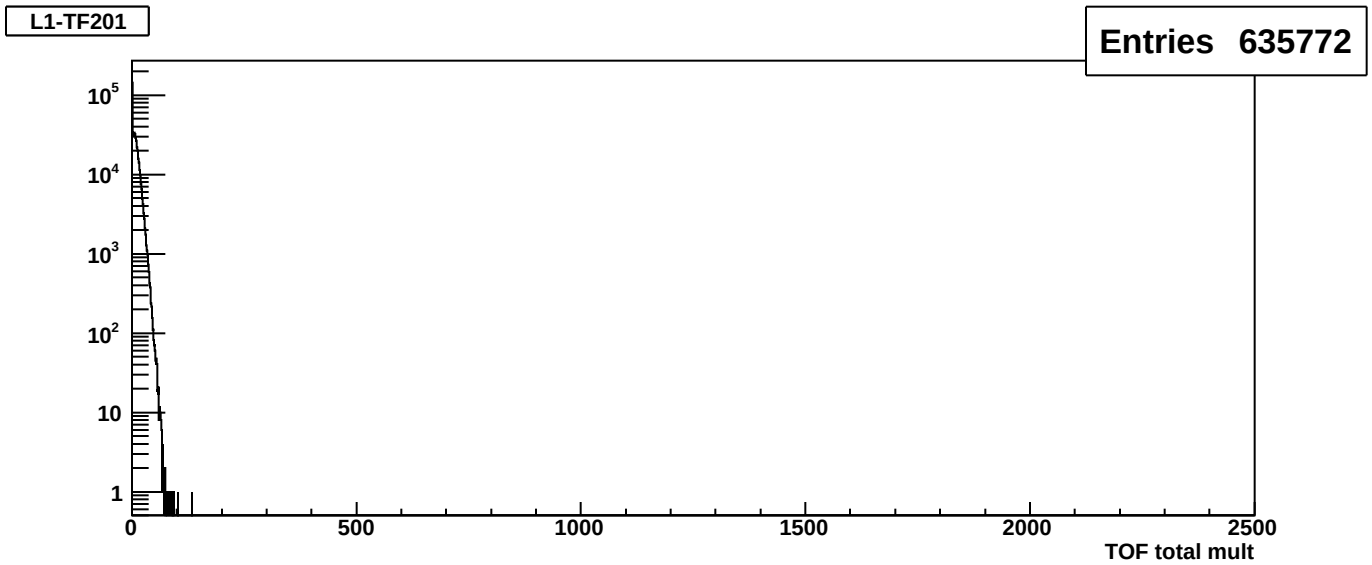

MIX-TF003
Entries 2.670242e+07
TOF MULT
TOF tray

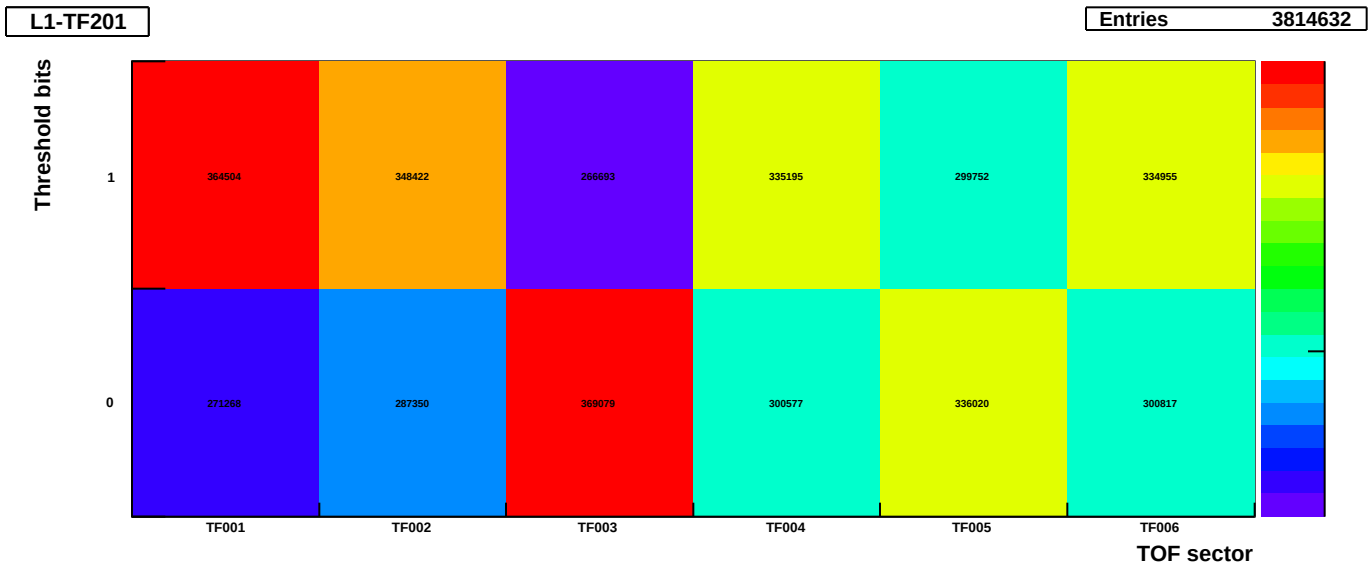

MIX-TF004
Entries 2.670242e+07
TOF MULT
TOF tray

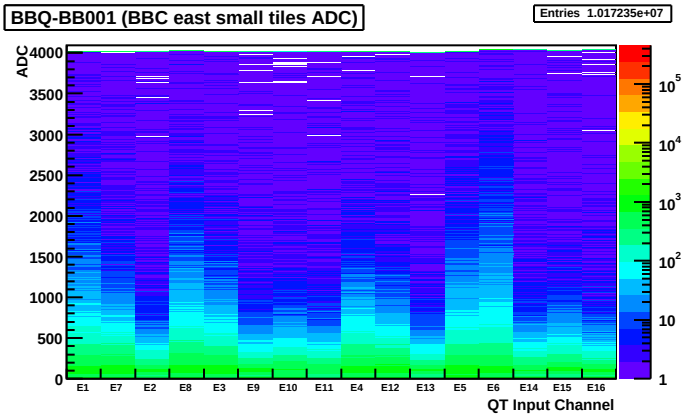

MIX-TF005
Entries 2.670242e+07
TOF MULT
TOF tray

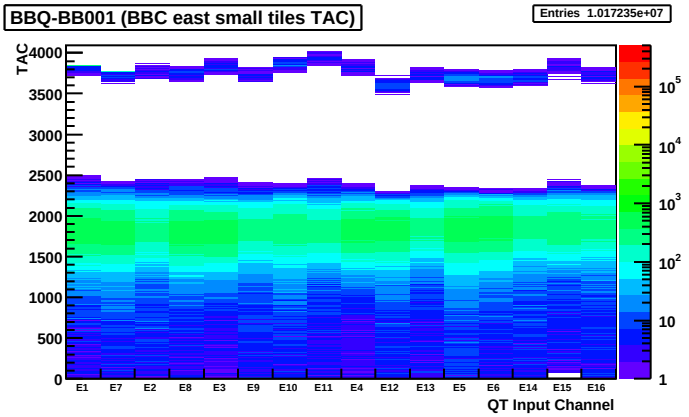

MIX-TF006
Entries 2.670242e+07
TOF MULT
TOF tray

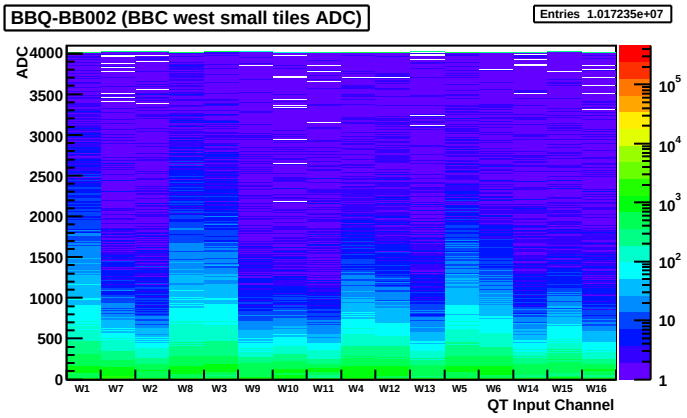

MIX-TF101
Entries 3814632
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

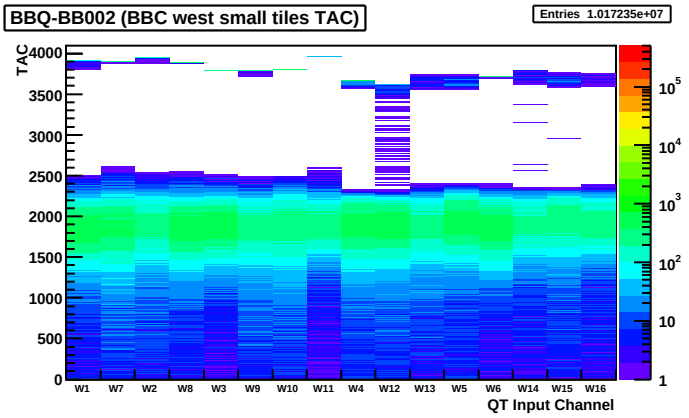

L1-TF201
Entries 635772
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

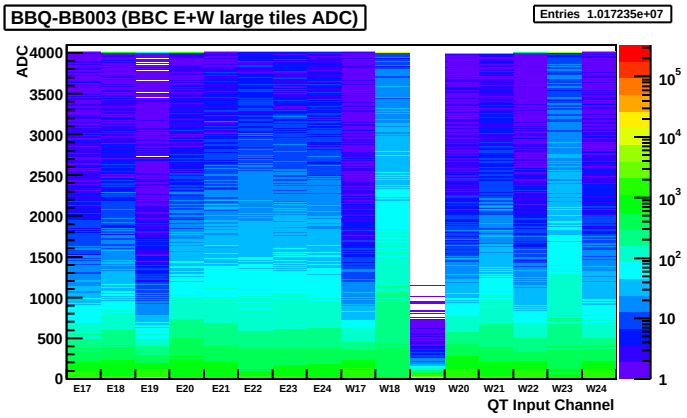

L1-TF201
Entries 3814632
Threshold bits
1
0
364504
348422
266693
335195
299752
334955
271268
287350
369079
300577
336020
300817
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

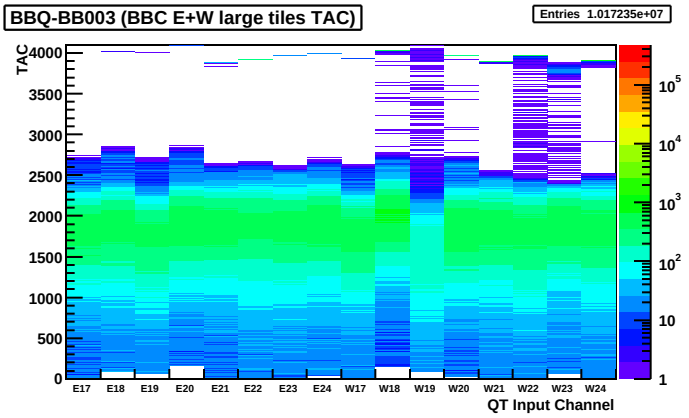

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.017235e+07
ADC
QT Input Channel

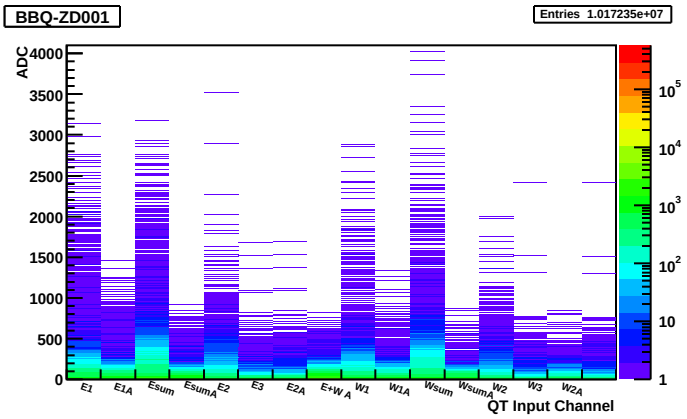

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.017235e+07
TAC
QT Input Channel

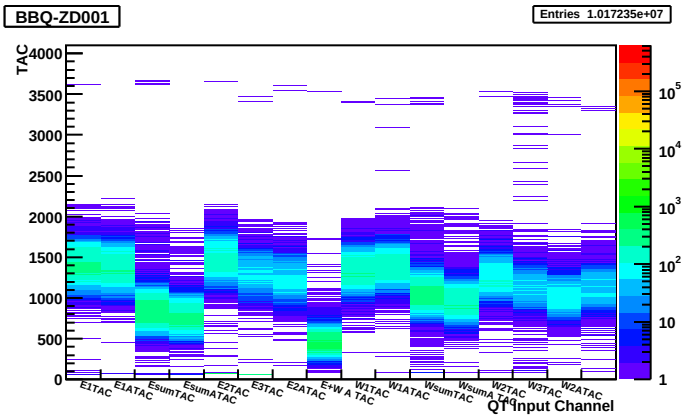

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.017235e+07
ADC
QT Input Channel

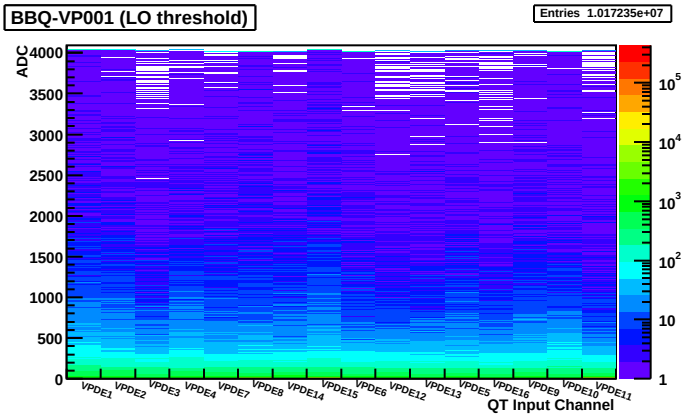

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.017235e+07
TAC
QT Input Channel

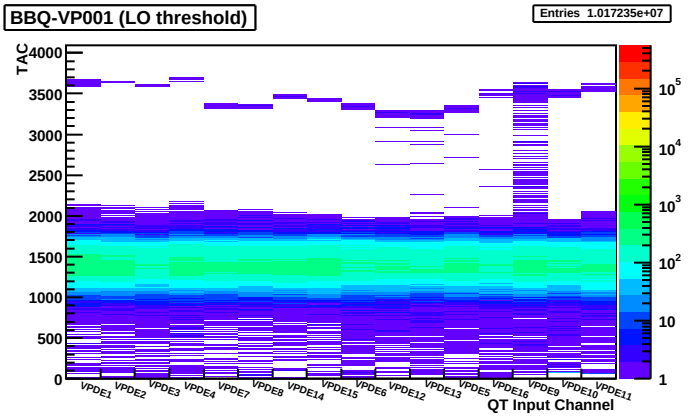

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.017235e+07
ADC
QT Input Channel

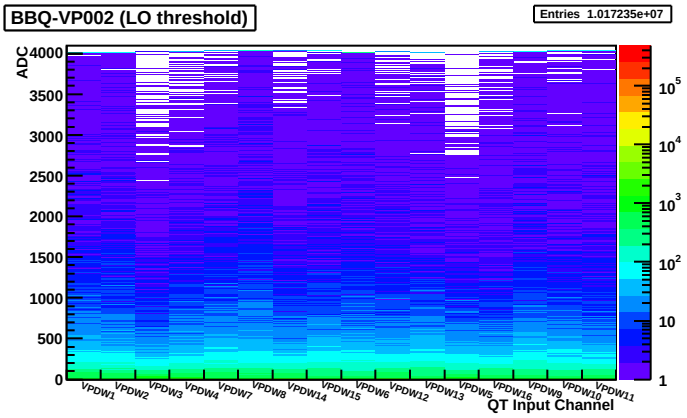

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.017235e+07
TAC
QT Input Channel

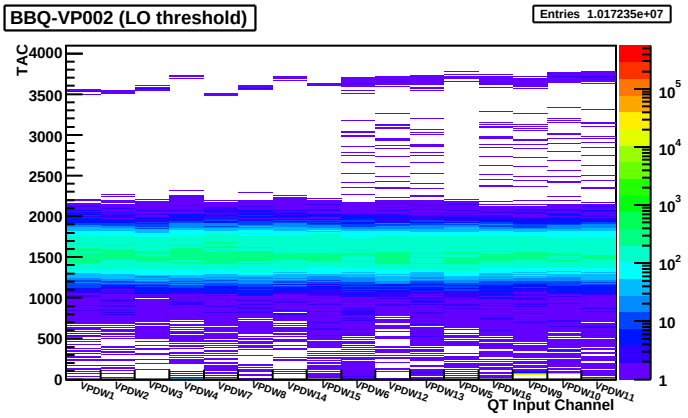

BBQ-ZD001
Entries 1.017235e+07
ADC
QT Input Channel

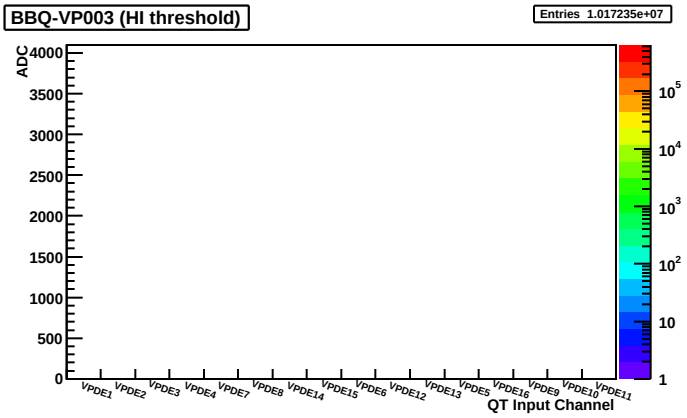

BBQ-ZD001
Entries 1.017235e+07
TAC
QT Input Channel

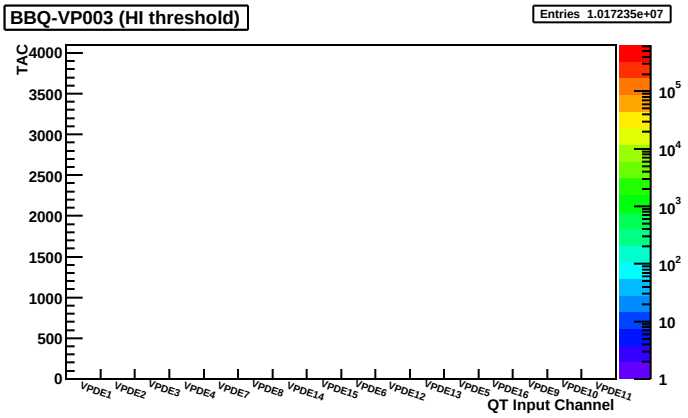

BBQ-VP001 (LO threshold)
Entries 1.017235e+07
ADC
QT Input Channel

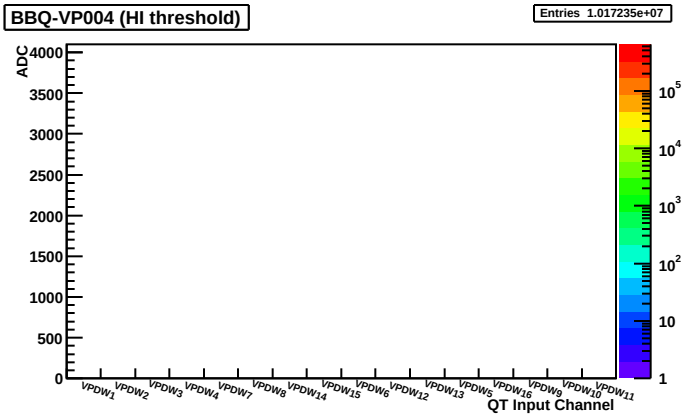

BBQ-VP001 (LO threshold)
Entries 1.017235e+07
TAC
QT Input Channel

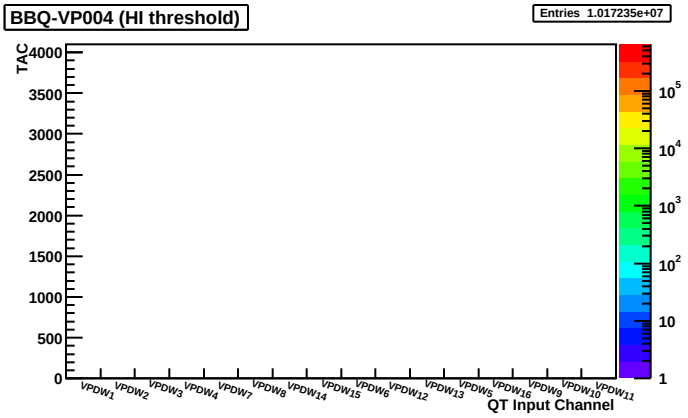

BBQ-VP002 (LO threshold)
Entries 1.017235e+07
ADC
QT Input Channel

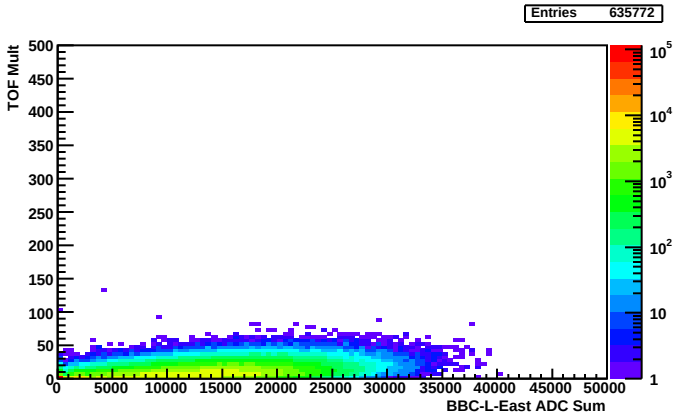

BBQ-VP002 (LO threshold)
Entries 1.017235e+07
TAC
QT Input Channel

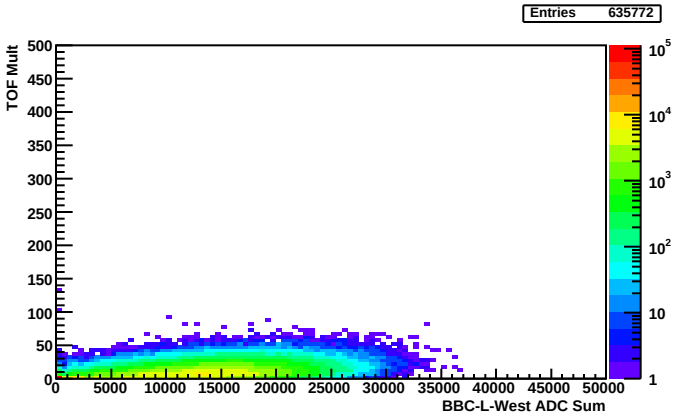

BBQ-VP003 (HI threshold)
Entries 1.017235e+07
ADC
QT Input Channel

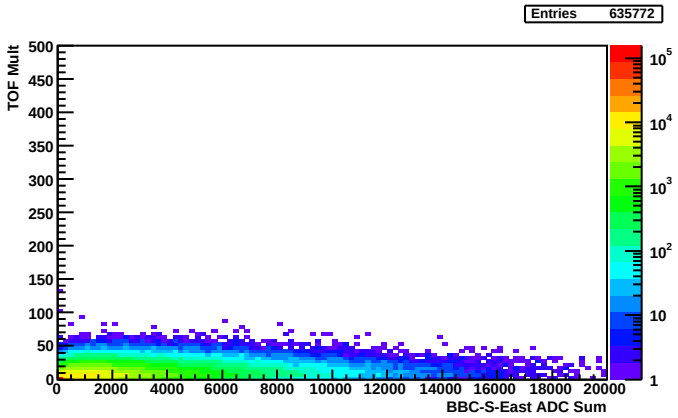

BBQ-VP003 (HI threshold)
Entries 1.017235e+07
TAC
QT Input Channel

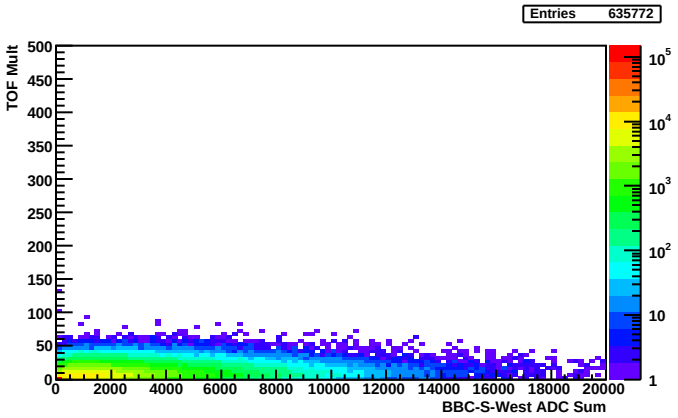

BBQ-VP004 (HI threshold)
Entries 1.017235e+07
ADC
QT Input Channel

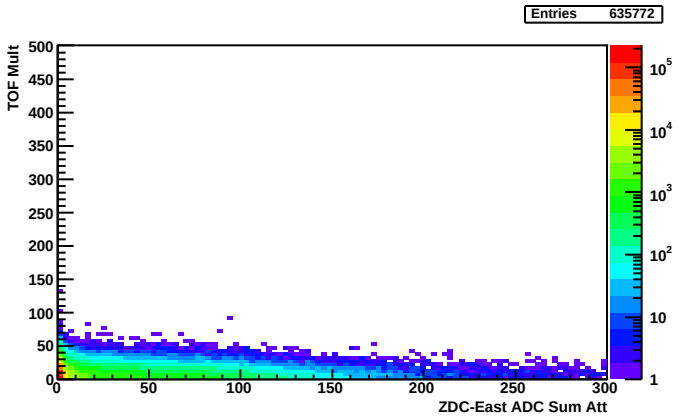

BBQ-VP004 (HI threshold)
Entries 1.017235e+07
TAC
QT Input Channel

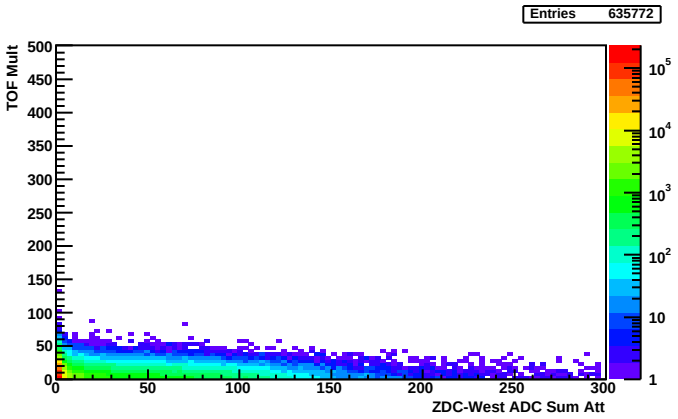

Entries 635772
TOF Mult
BBC-L-East ADC Sum

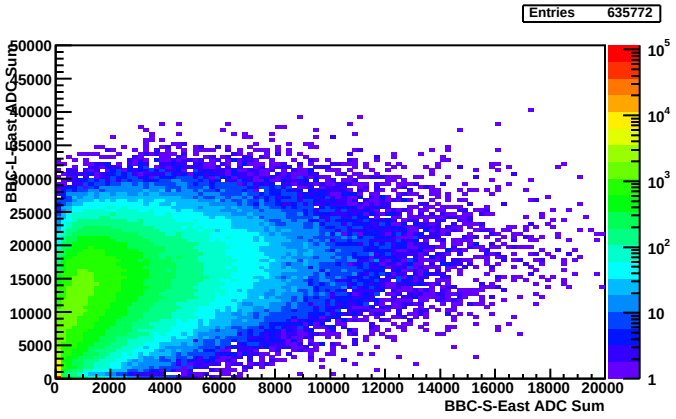

Entries 635772
TOF Mult
BBC-L-West ADC Sum

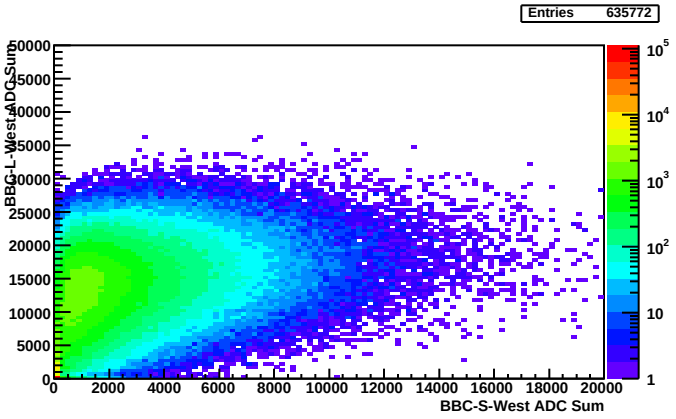

Entries 635772
TOF Mult
BBC-S-East ADC Sum

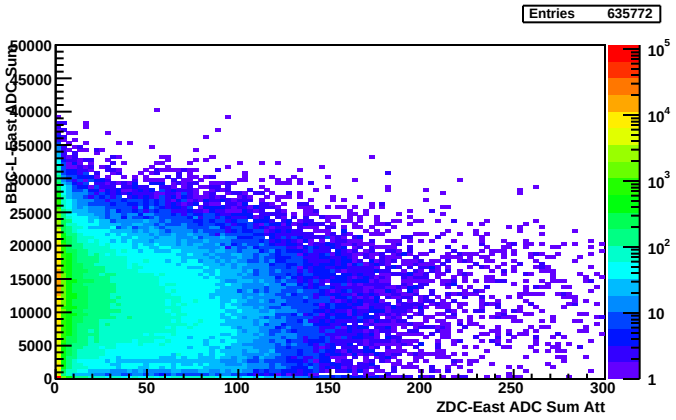

Entries 635772
TOF Mult
BBC-S-West ADC Sum

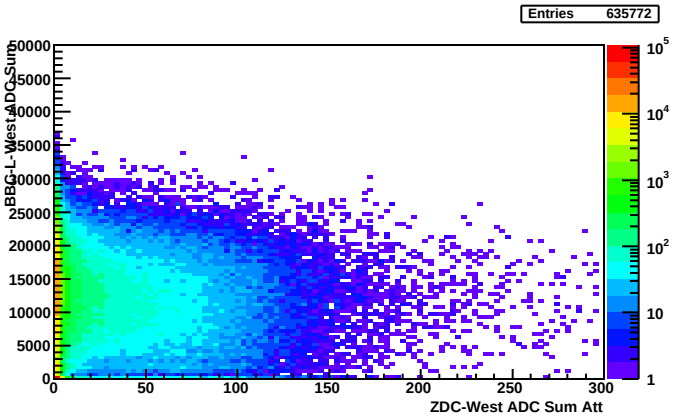

Entries 635772
TOF Mult
ZDC-East ADC Sum Att

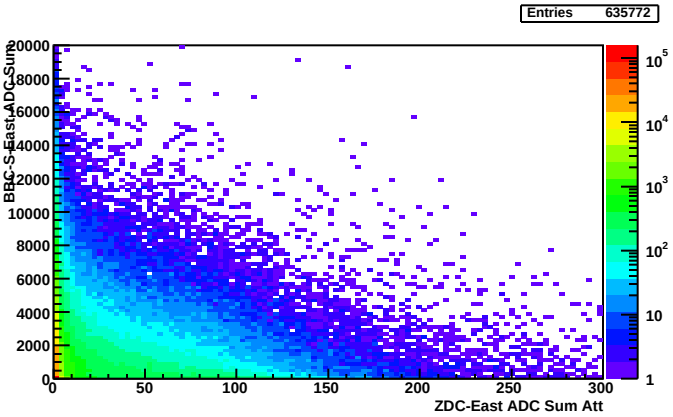

Entries 635772
TOF Mult
ZDC-West ADC Sum Att

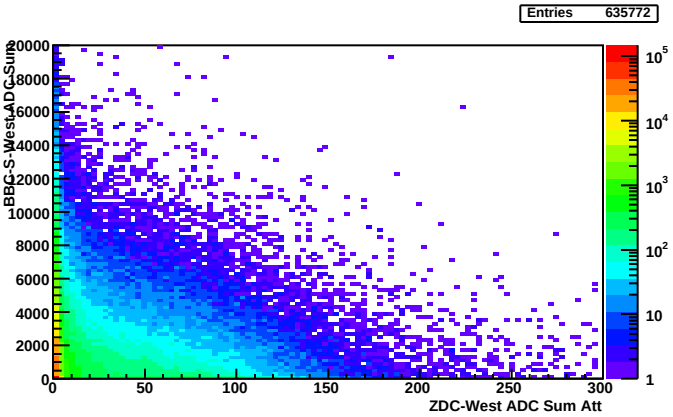

Entries 635772
BBC-L-East ADC Sum
BBC-S-East ADC Sum

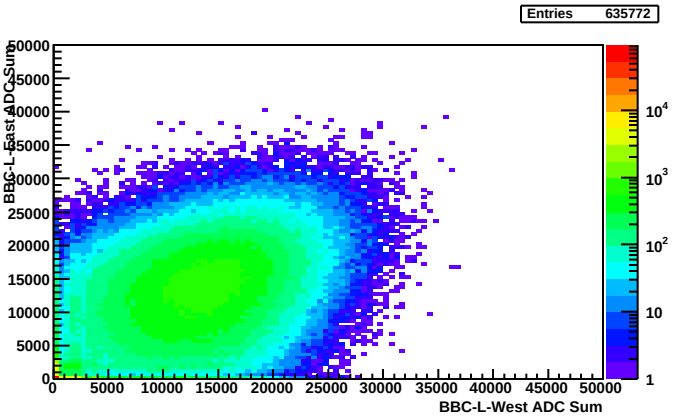

Entries 635772
BBC-L-West ADC Sum
BBC-S-West ADC Sum

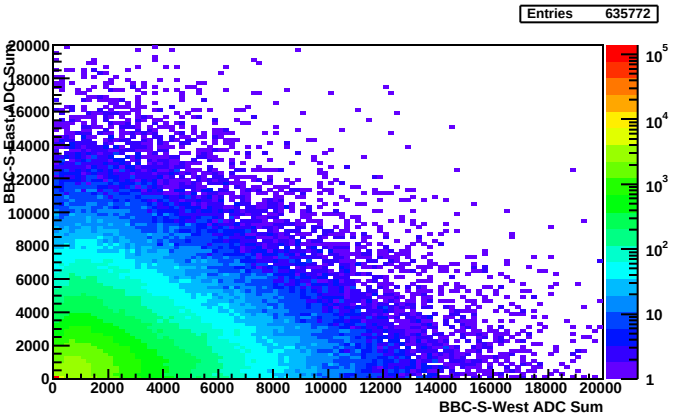

Entries 635772
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

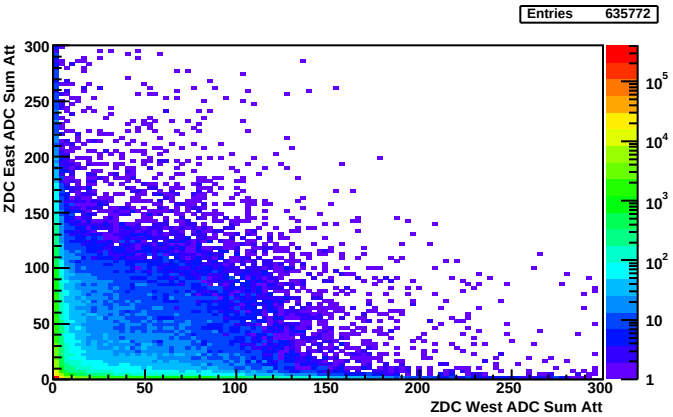

Entries 635772
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

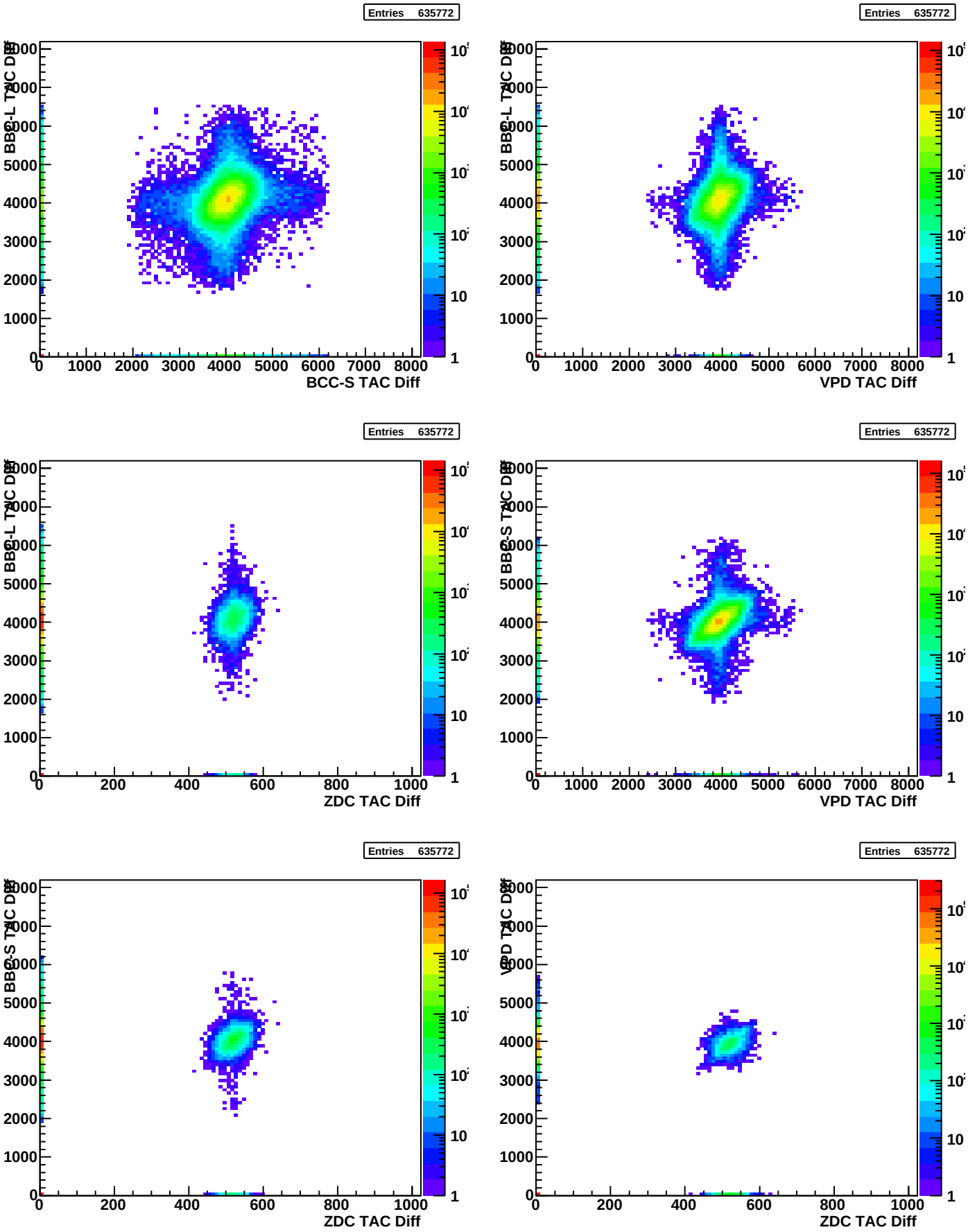

Entries 635772
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

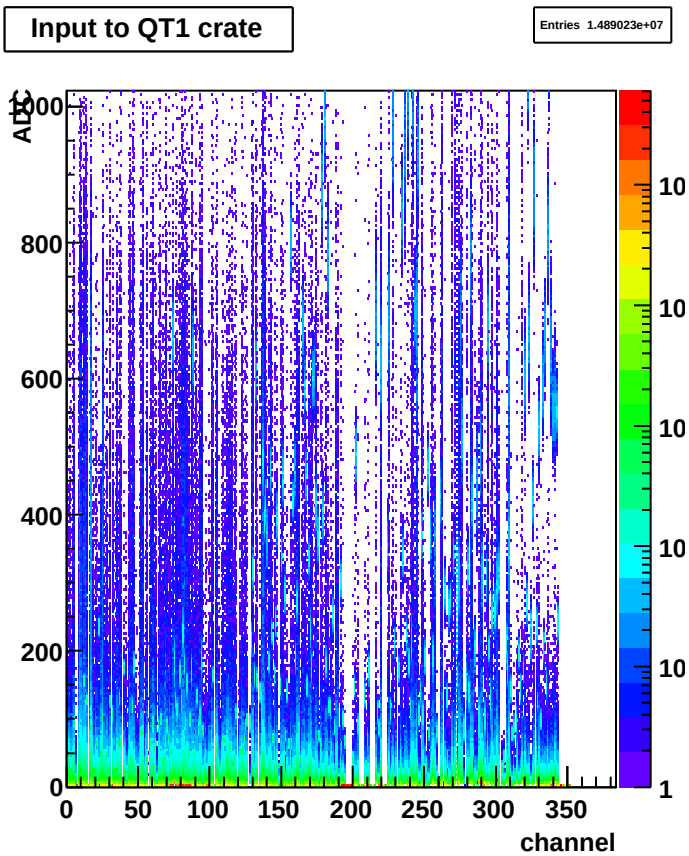

Entries 635772
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

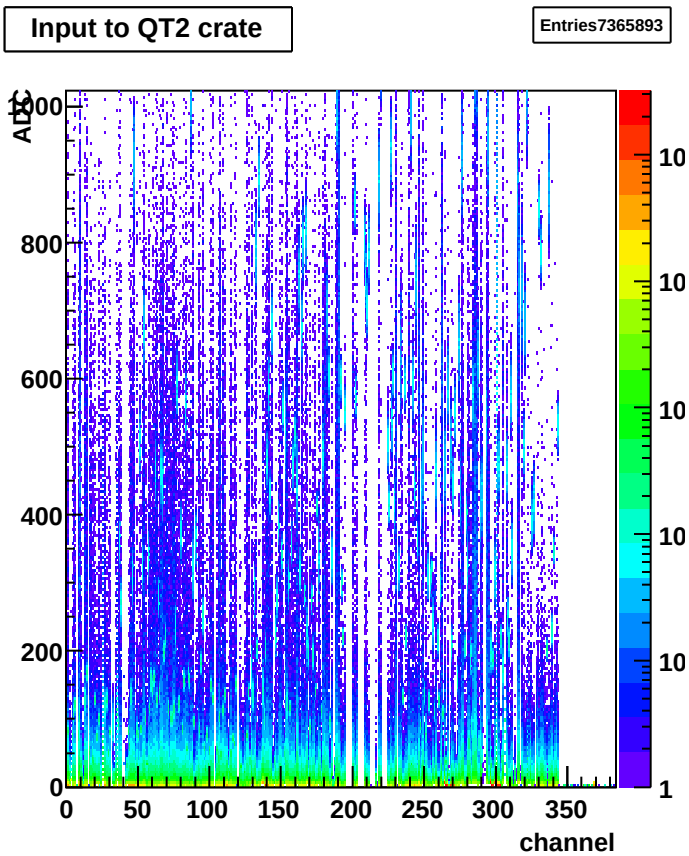

Entries 635772
BBC-L-East ADC Sum
BBC-L-West ADC Sum

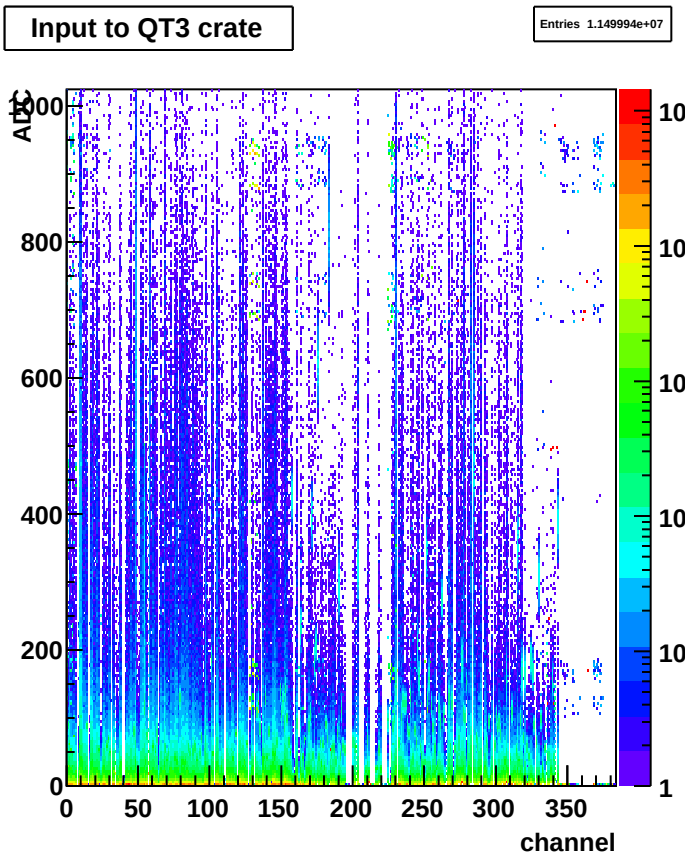

Entries 635772
BBC-S-East ADC Sum
BBC-S-West ADC Sum

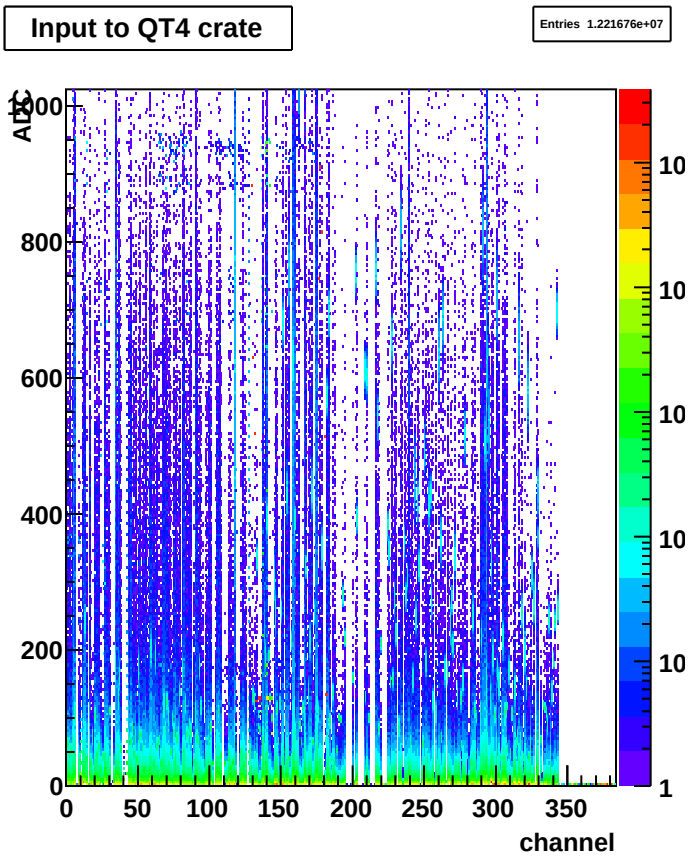

Entries 635772
ZDC East ADC Sum Att
ZDC West ADC Sum Att

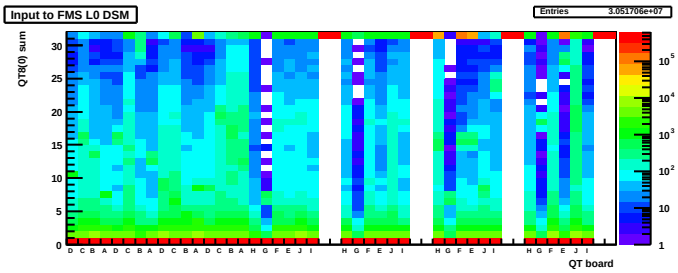

Entries 635772
BBC-L TAC Diff
BCC-S TAC Diff
Entries 635772
BBC-L TAC Diff
VPD TAC Diff
Entries 635772
BBC-L TAC Diff
ZDC TAC Diff
Entries 635772
BBC-S TAC Diff
VPD TAC Diff
Entries 635772
BBC-S TAC Diff
ZDC TAC Diff
Entries 635772
VPD TAC Diff
ZDC TAC Diff

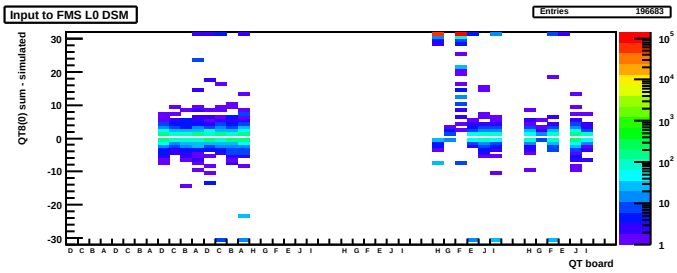

Input to QT1 crate
Entries 1.489023e+07
ADC
channel

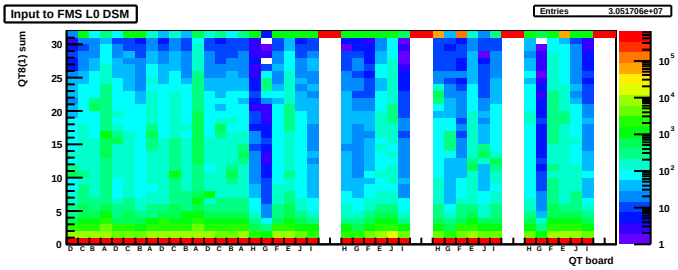

Input to QT2 crate
Entries7365893
ADC
channel

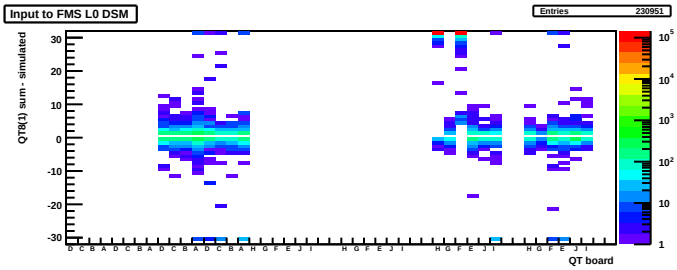

Input to QT3 crate
Entries 1.149994e+07
ADC
channel

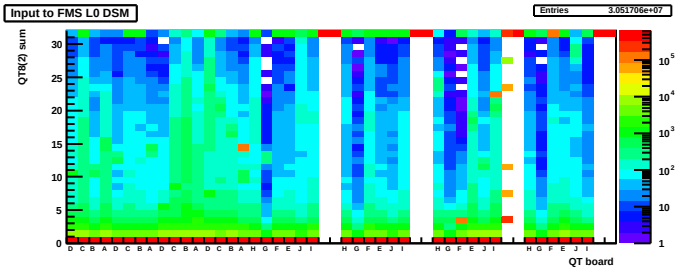

Input to QT4 crate
Entries 1.221676e+07
ADC
channel

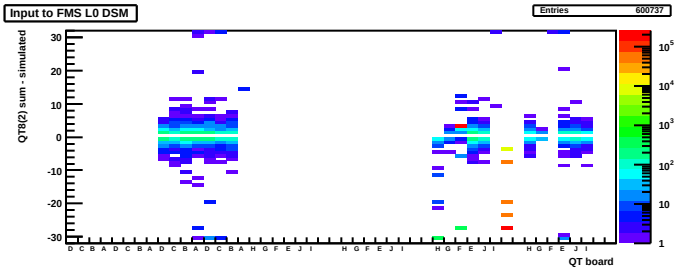

Input to FMS L0 DSM
Entries 3.051706e+07
QT8(0) sum
QT board

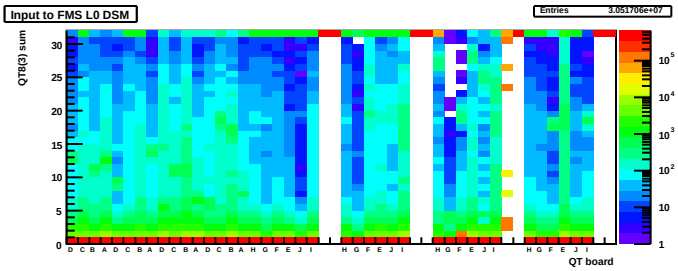

Input to FMS L0 DSM
Entries 196683
QT8(0) sum - simulated
QT board

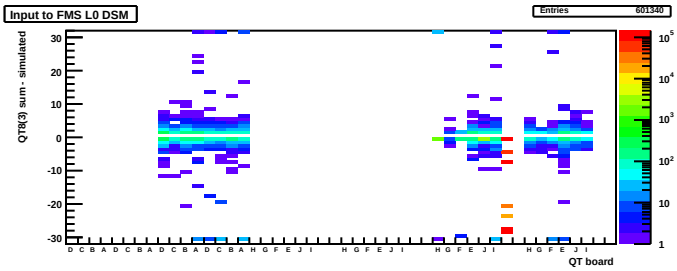

Input to FMS L0 DSM
Entries 3.051706e+07
QT8(1) sum
QT board

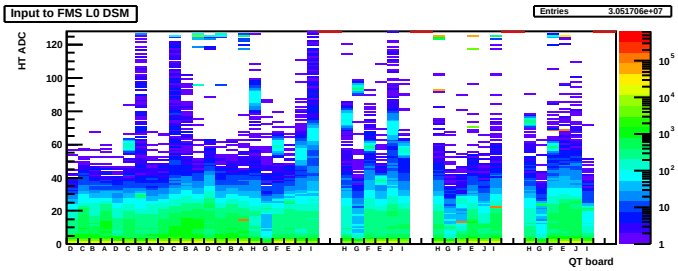

Input to FMS L0 DSM
Entries 230951
QT8(1) sum - simulated
QT board

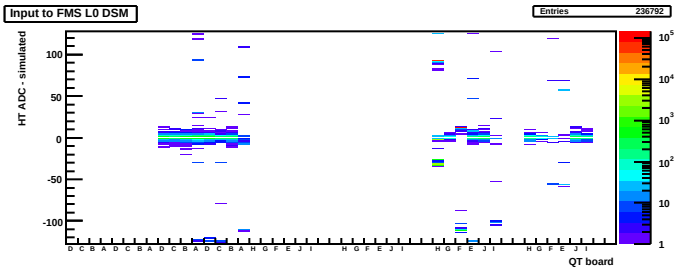

Input to FMS L0 DSM
Entries 3.051706e+07
QT8(2) sum
QT board

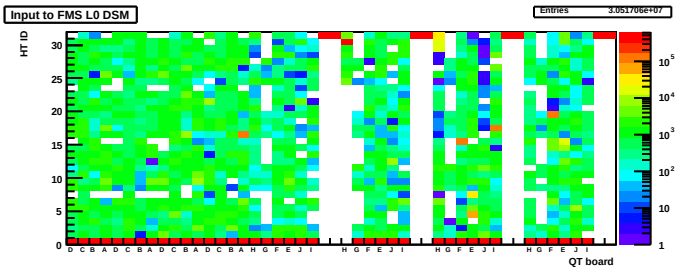

Input to FMS L0 DSM
Entries 600737
QT8(2) sum - simulated
QT board

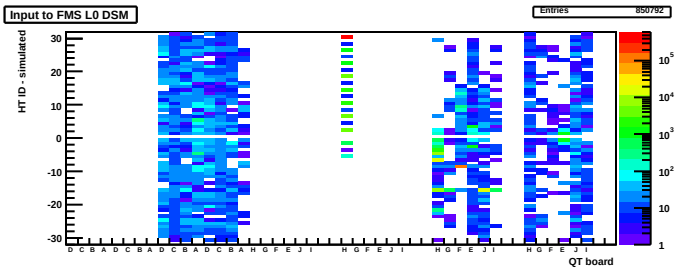

Input to FMS L0 DSM
Entries 3.051706e+07
QT8(3) sum
QT board

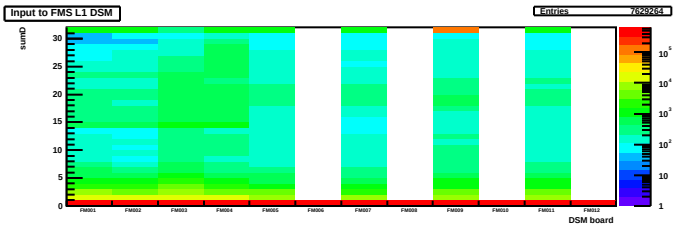

Input to FMS L0 DSM
Entries 601340
QT8(3) sum - simulated
QT board

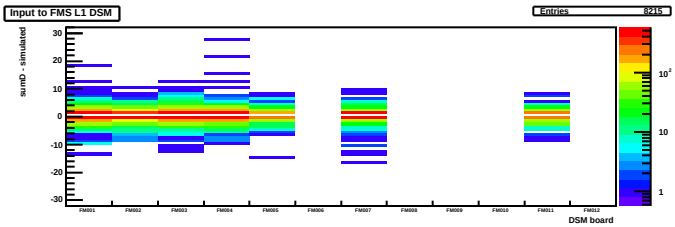

Input to FMS L0 DSM
Entries 3.051706e+07
HT ADC
QT board

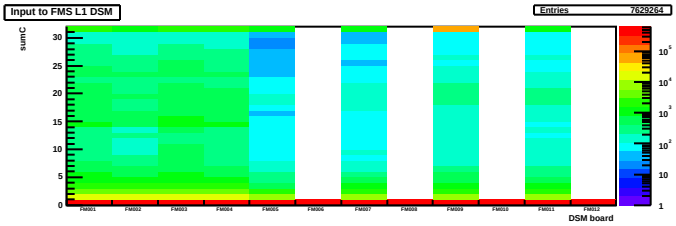

Input to FMS L0 DSM
Entries 236792
HT ADC - simulated
QT board

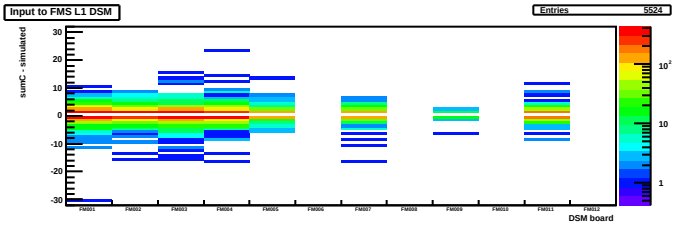

Input to FMS L0 DSM
Entries 3.051706e+07
HT ID
QT board

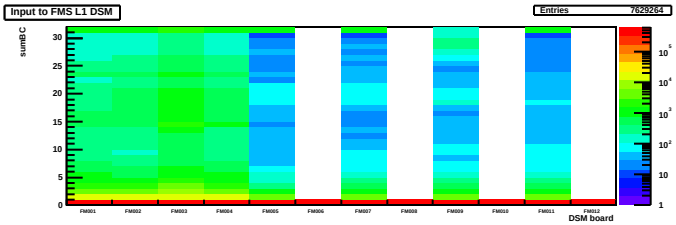

Input to FMS L0 DSM
Entries 850792
HT ID - simulated
QT board

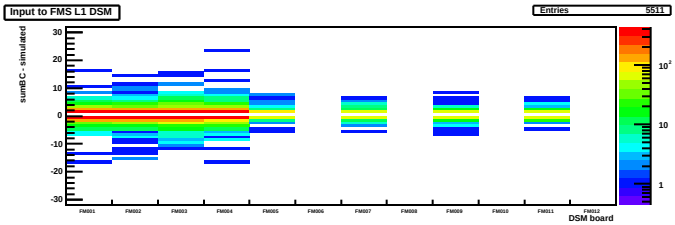

Input to FMS L1 DSM
Entries 7629264
sumD
DSM board

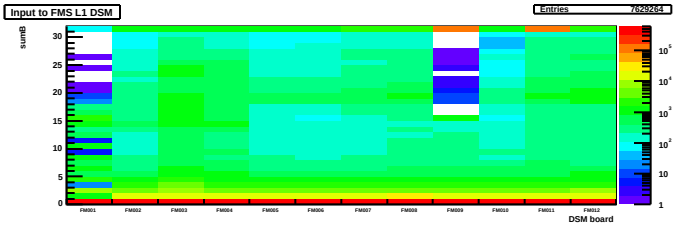

Input to FMS L1 DSM
Entries 8215
sumD - simulated
DSM board

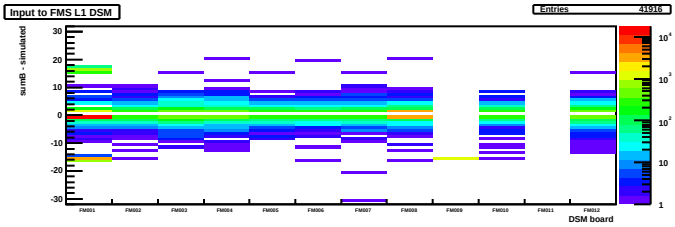

Input to FMS L1 DSM
Entries 7629264
sumC
DSM board

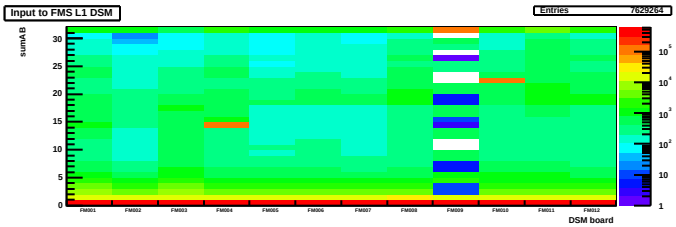

Input to FMS L1 DSM
Entries 5524
sumC - simulated
DSM board

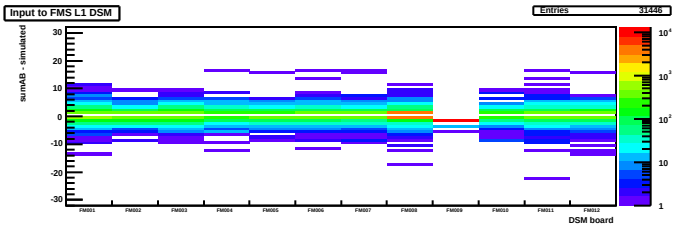

Input to FMS L1 DSM
Entries 7629264
sumBC
DSM board

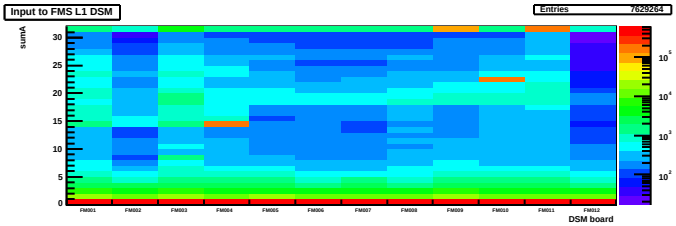

Input to FMS L1 DSM
Entries 5511
sumBC - simulated
DSM board

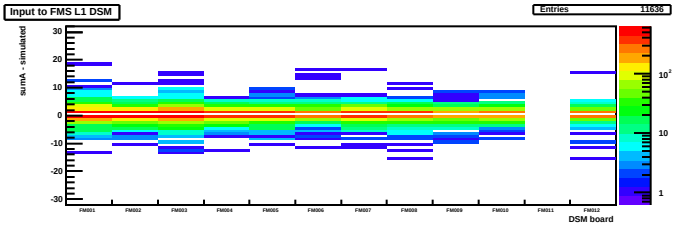

Input to FMS L1 DSM
Entries 7629264
sumB
DSM board

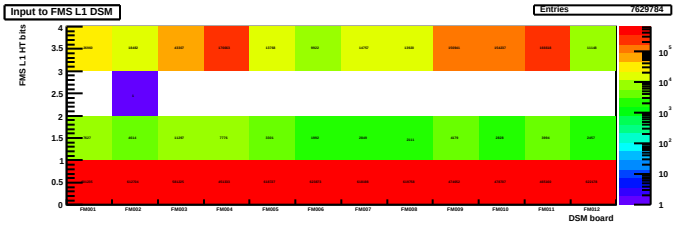

Input to FMS L1 DSM
Entries 41916
sumB - simulated
DSM board

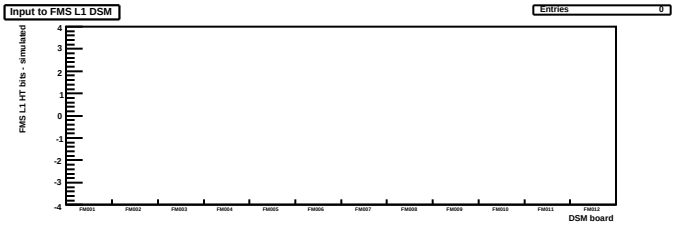

Input to FMS L1 DSM
Entries 7629264
sumAB
DSM board

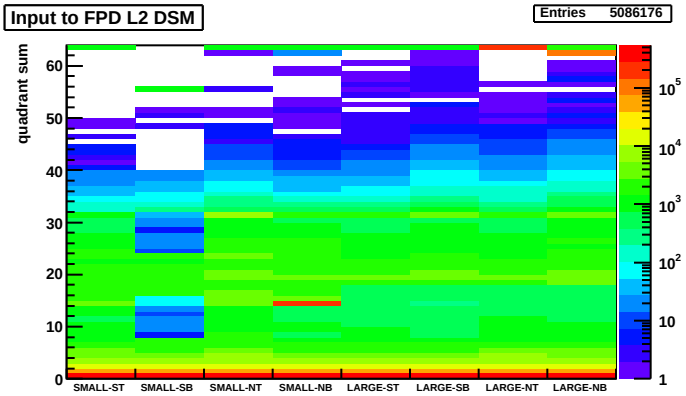

Input to FMS L1 DSM
Entries 31446
sumAB - simulated
DSM board

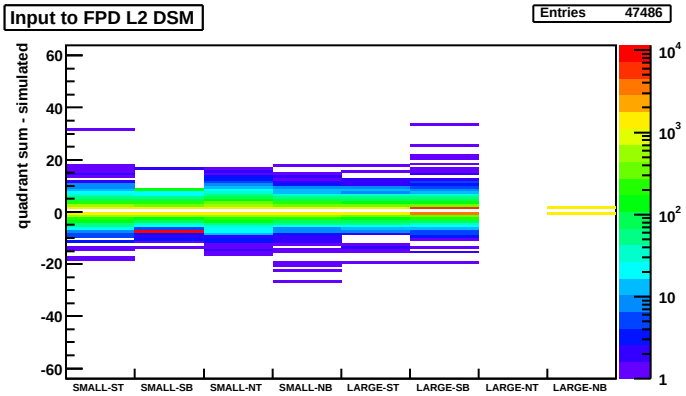

Input to FMS L1 DSM
Entries 7629264
sumA
DSM board

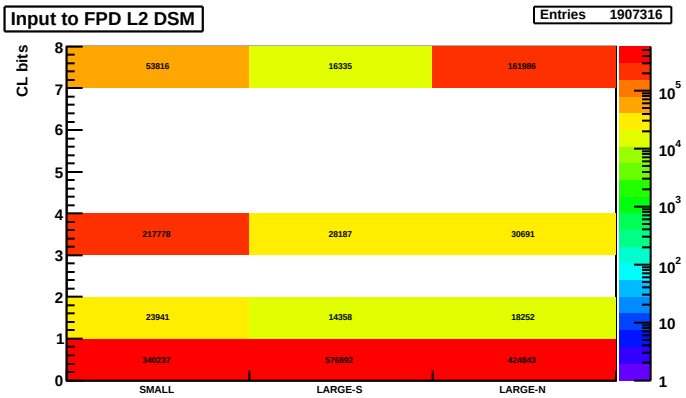

Input to FMS L1 DSM
Entries 11636
sumA - simulated
DSM board

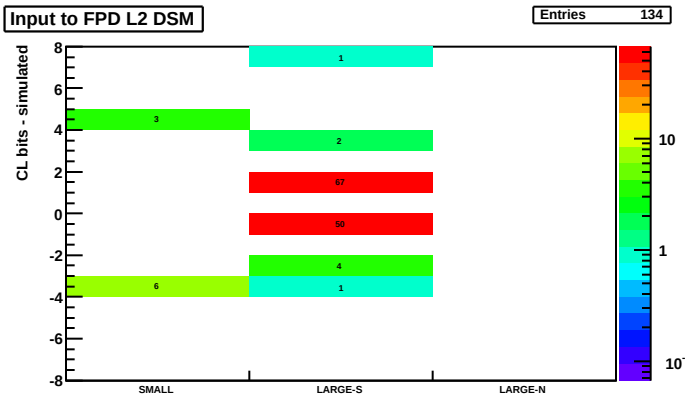

Input to FMS L1 DSM
Entries 7629784
FMS L1 HT bits
DSM board

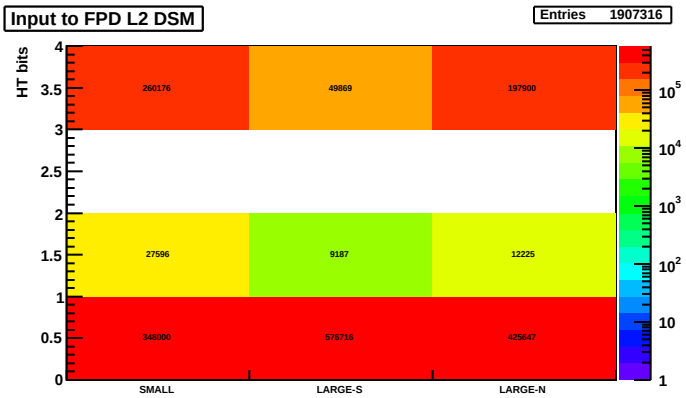

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

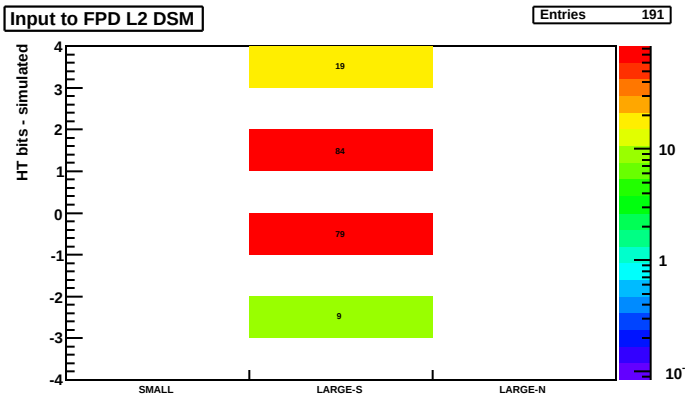

Input to FPD L2 DSM
Entries 5086176
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

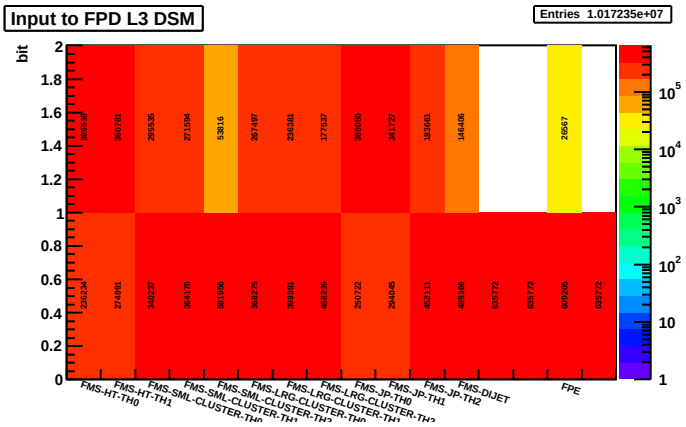

Input to FPD L2 DSM
Entries 47486
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

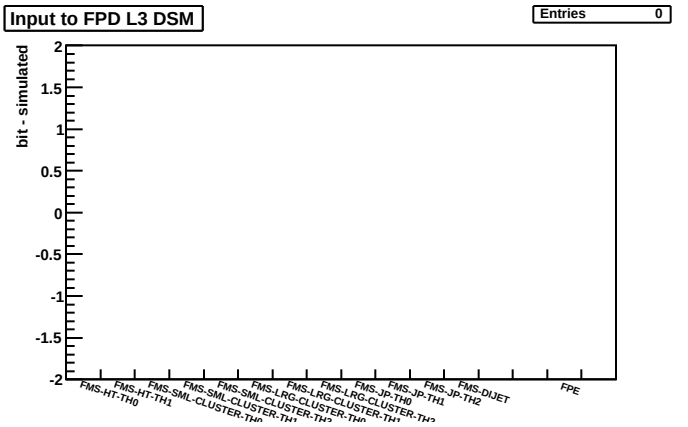

Input to FPD L2 DSM
Entries 1907316
CL bits
53816
16335
161986
217778
28187
30691
23941
14358
18252
340237
576892
424843
SMALL
LARGE-S
LARGE-N

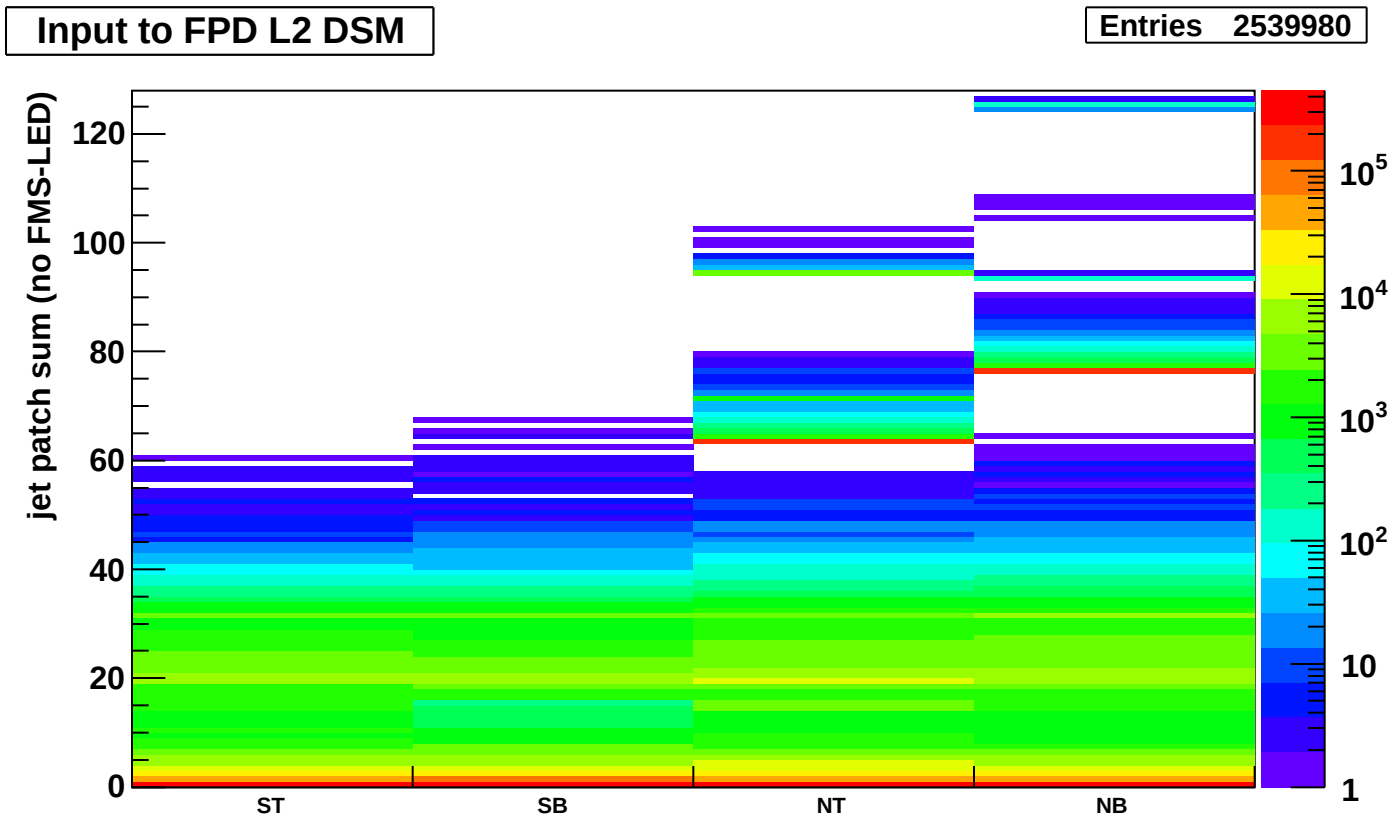

Input to FPD L2 DSM
Entries 134
CL bits - simulated
1
3
2
67
50
4
6
1
SMALL
LARGE-S
LARGE-N

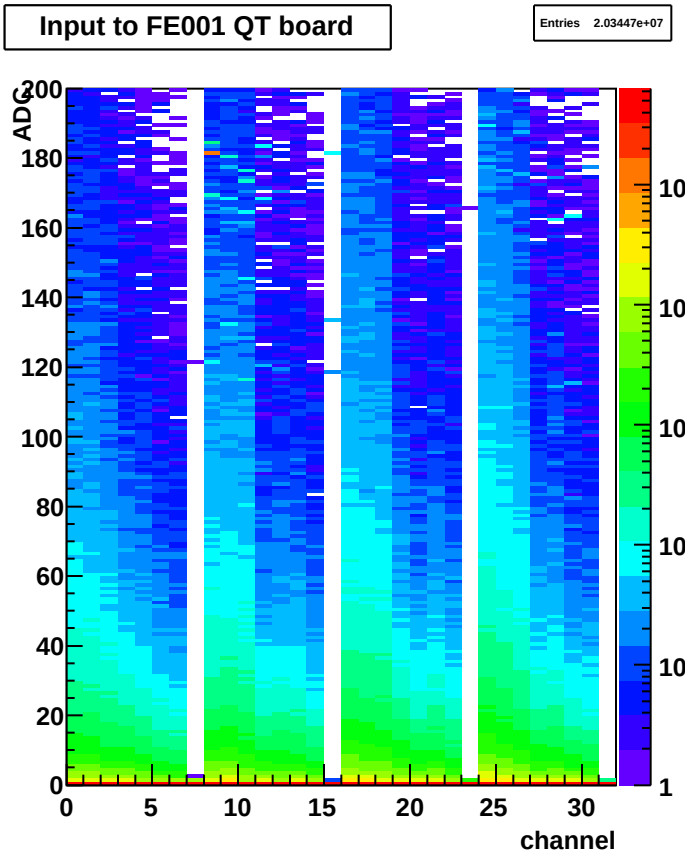

Input to FPD L2 DSM
Entries 1907316
HT bits
260176
49869
197900
27596
9187
12225
348000
576716
425647
SMALL
LARGE-S
LARGE-N

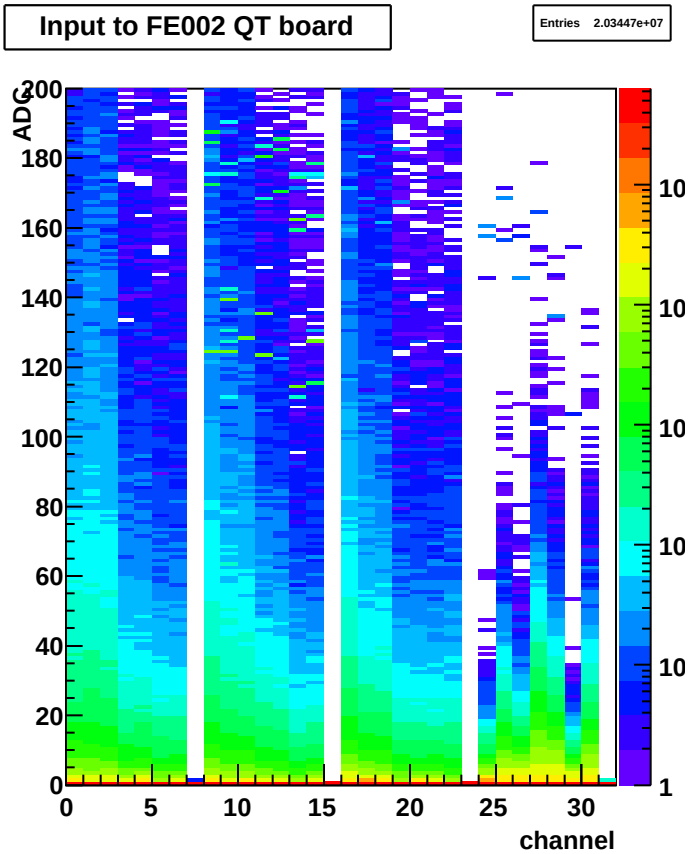

Input to FPD L2 DSM
Entries 191
HT bits - simulated
19
84
79
9
SMALL
LARGE-S
LARGE-N

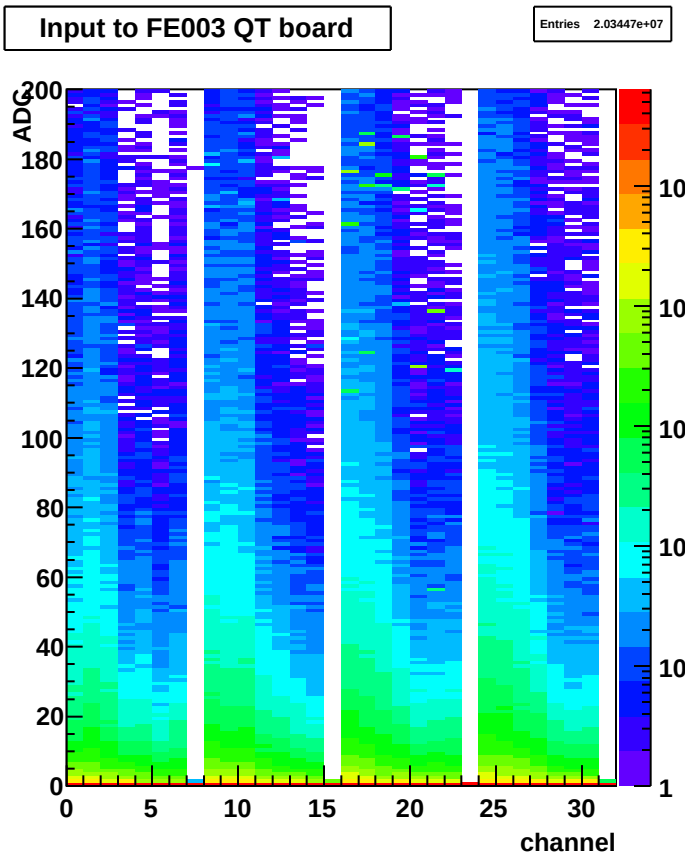

Input to FPD L3 DSM
Entries 1.017235e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

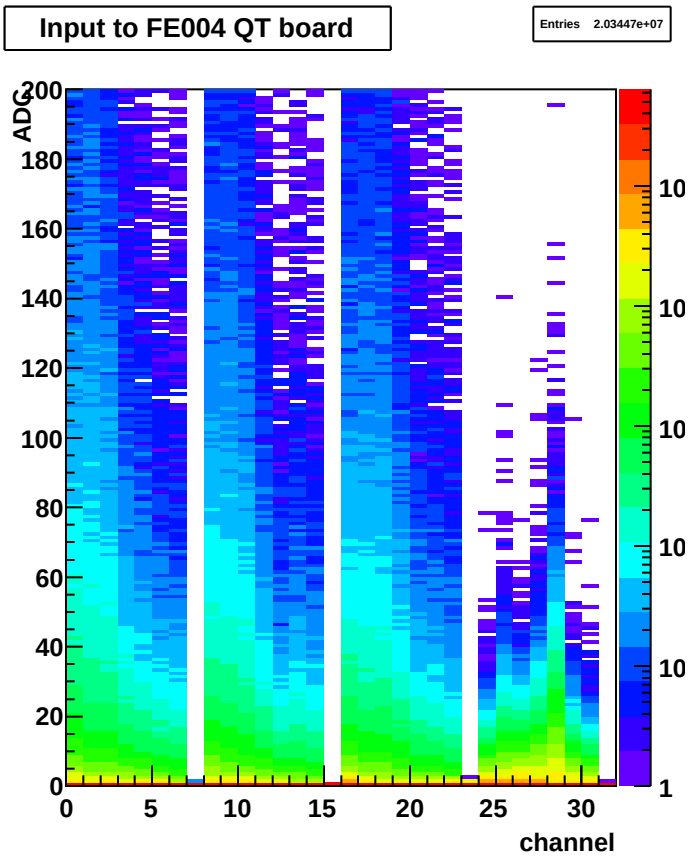

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

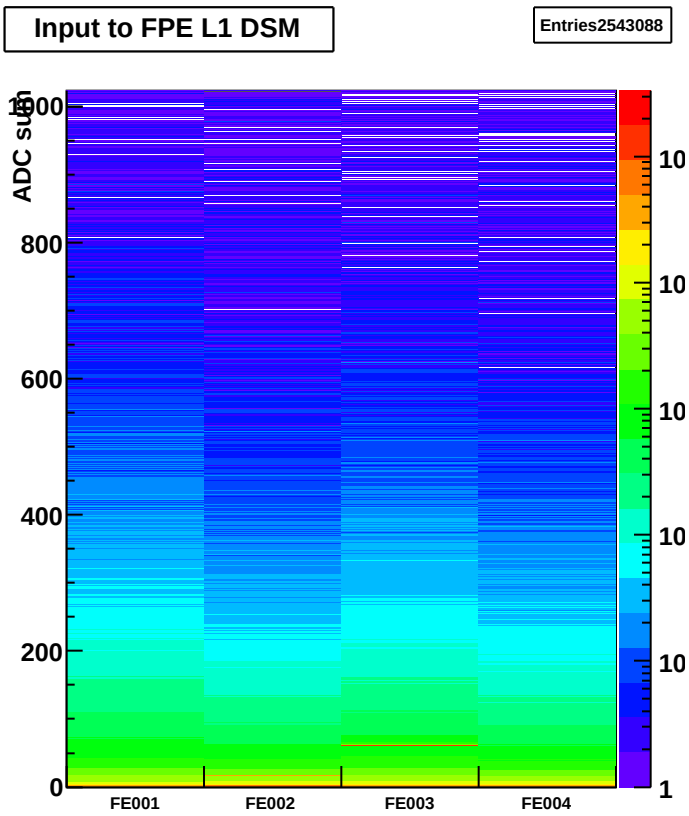

Input to FPD L2 DSM
Entries 2539980
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

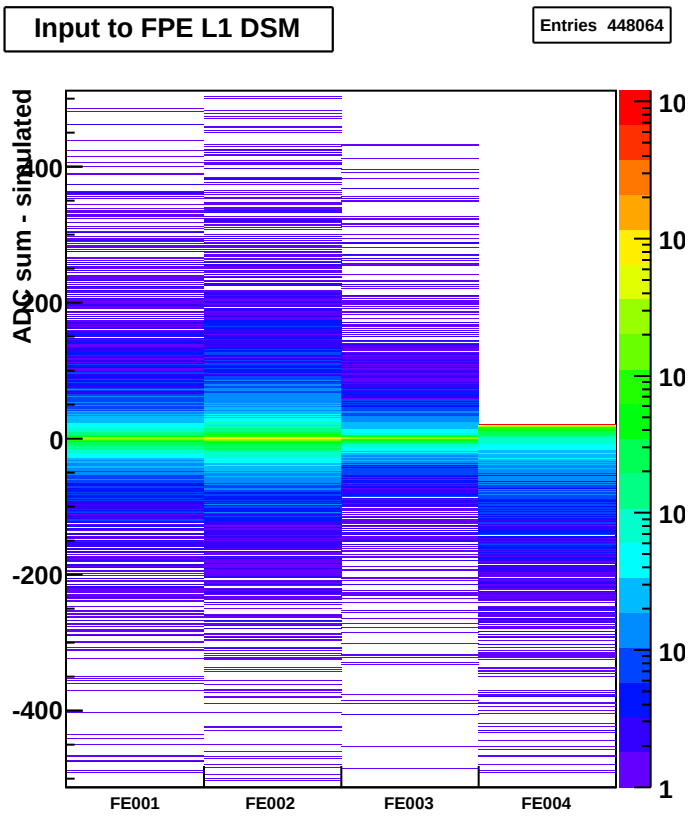

Input to FE001 QT board
Entries 2.03447e+07
ADC
channel

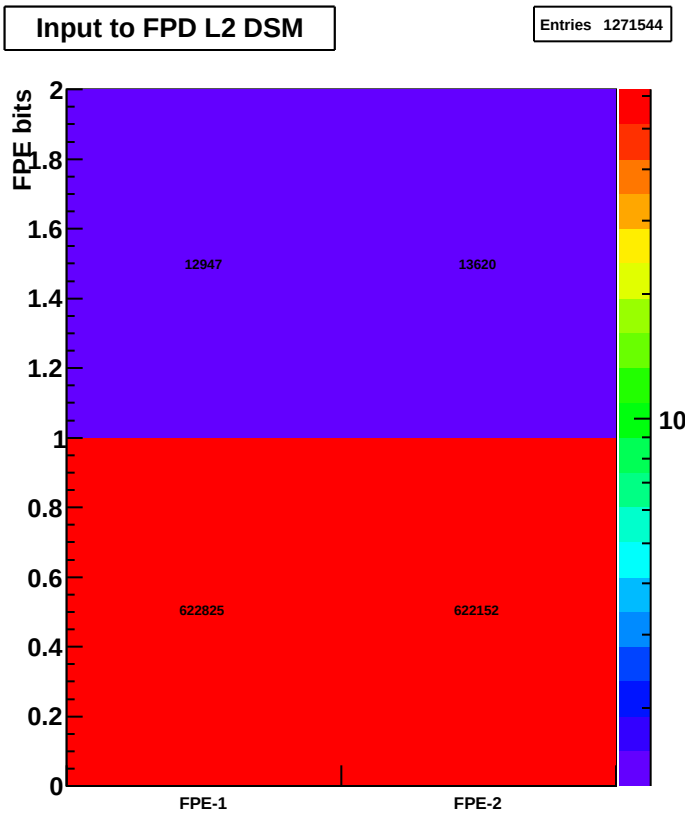

Input to FE002 QT board
Entries 2.03447e+07
ADC
channel

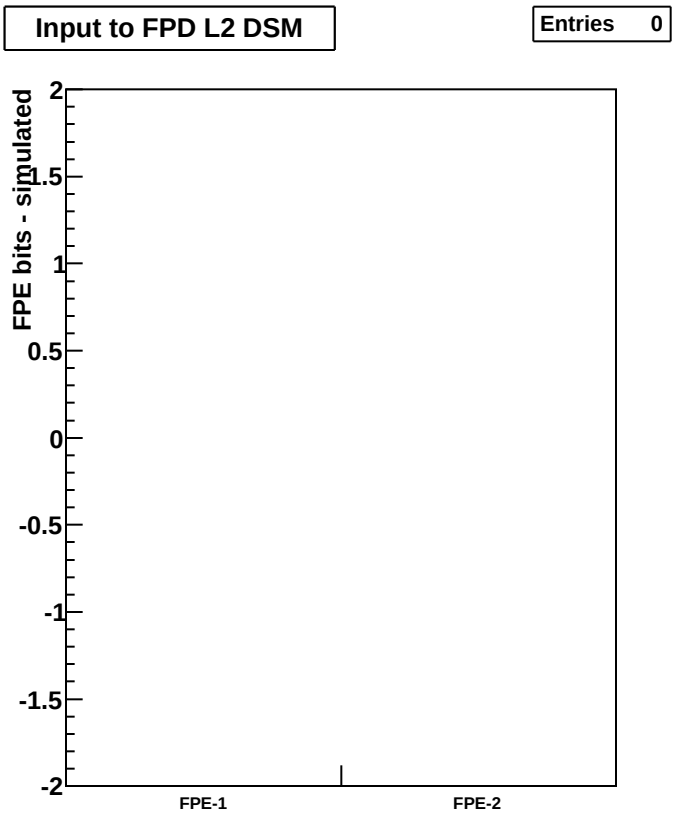

Input to FE003 QT board
Entries 2.03447e+07
ADC
channel


Input to FE004 QT board
Entries 2.03447e+07
ADC
channel


Input to FPE L1 DSM
Entries2543088
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 448064
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 1271544
FPE bits
12947
13620
622825
622152
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2