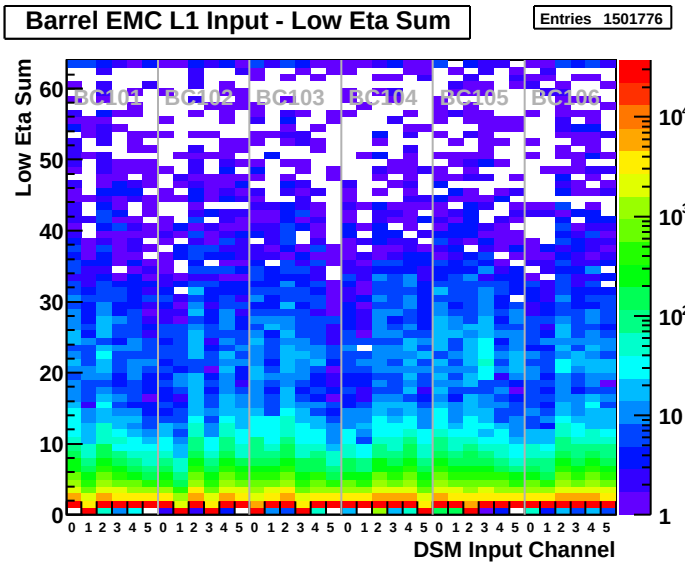
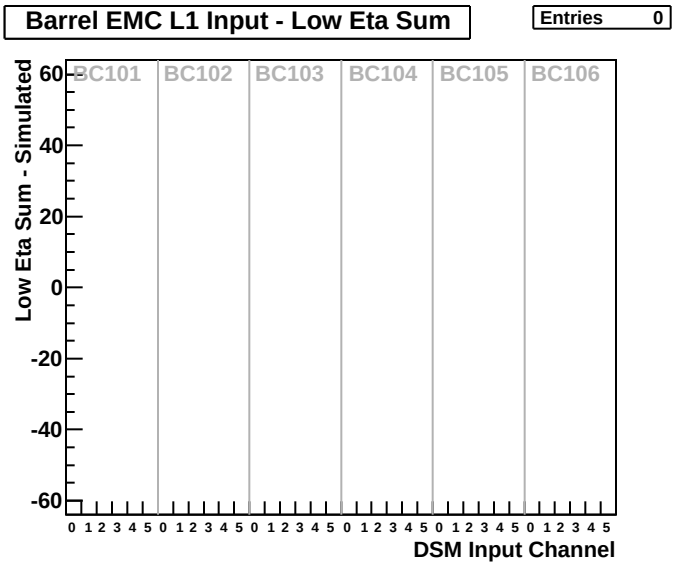
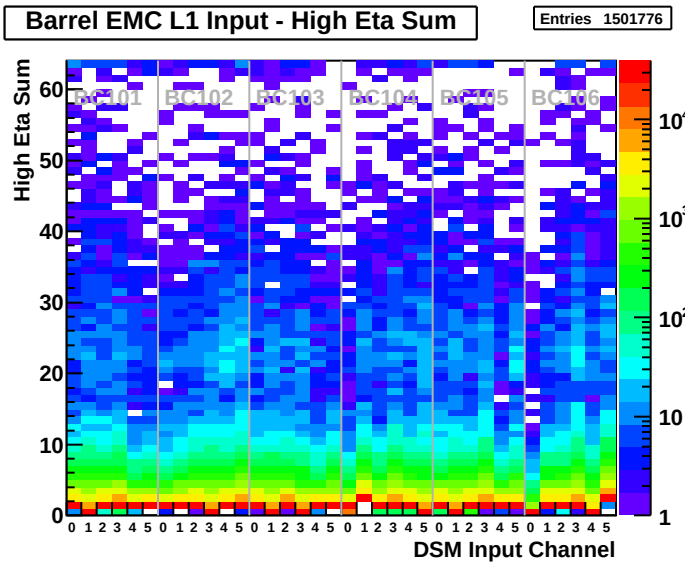
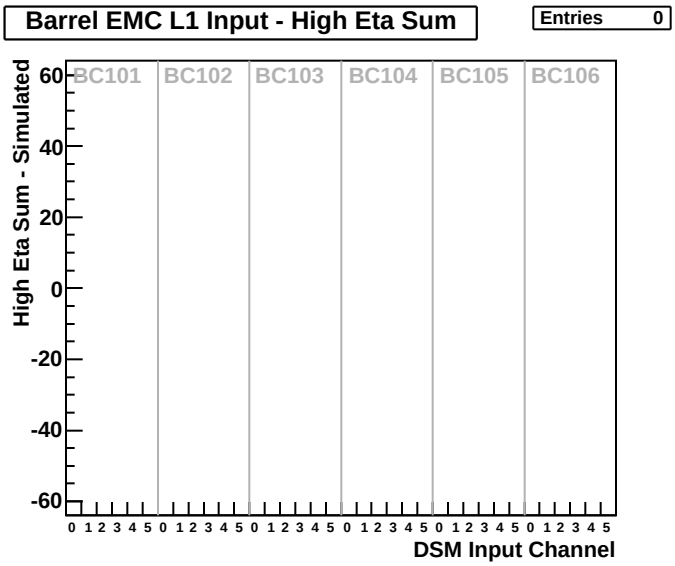
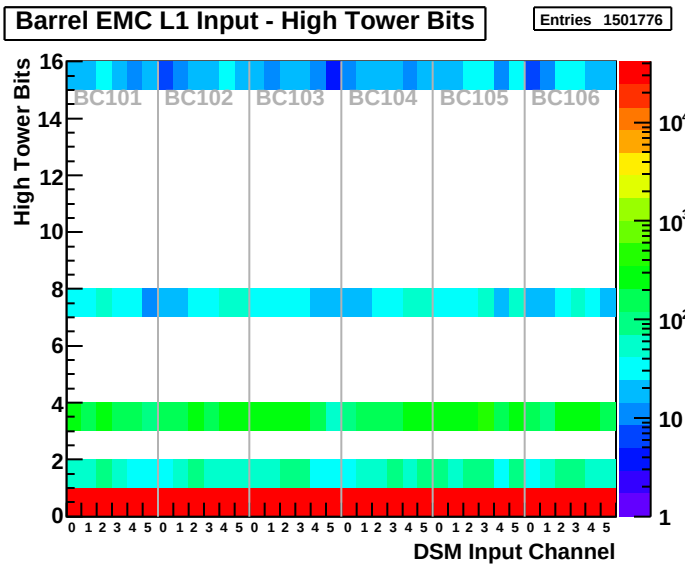
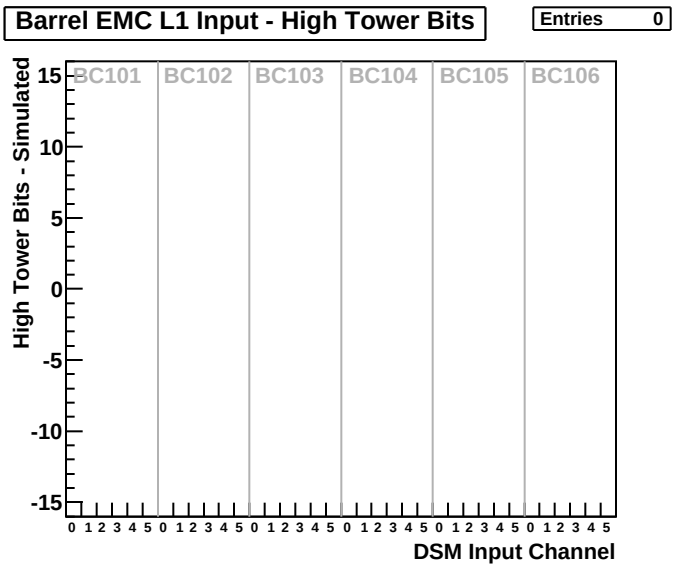

Barrel EMC L1 Input - Low Eta Sum
Entries 1501776
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

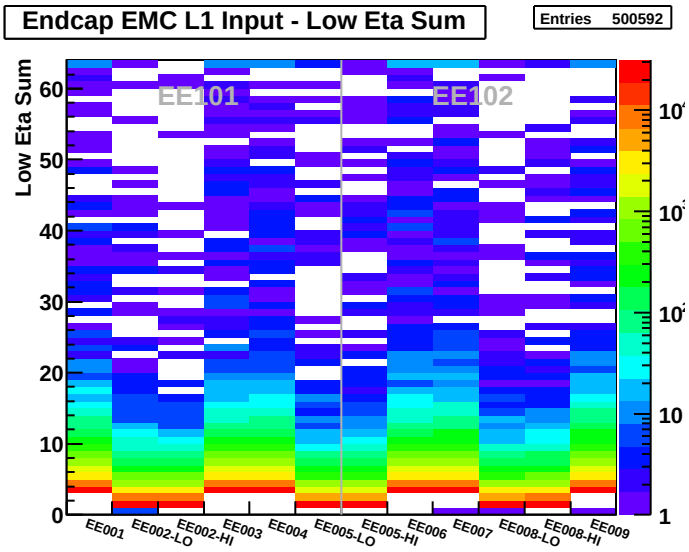

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

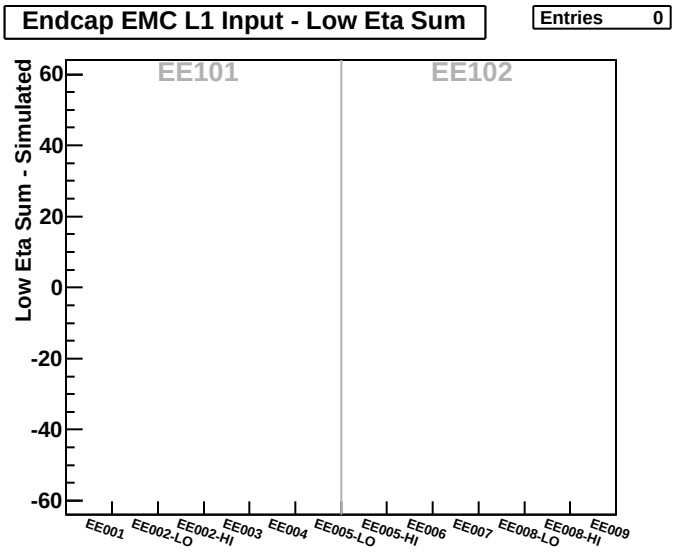

Barrel EMC L1 Input - High Eta Sum
Entries 1501776
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

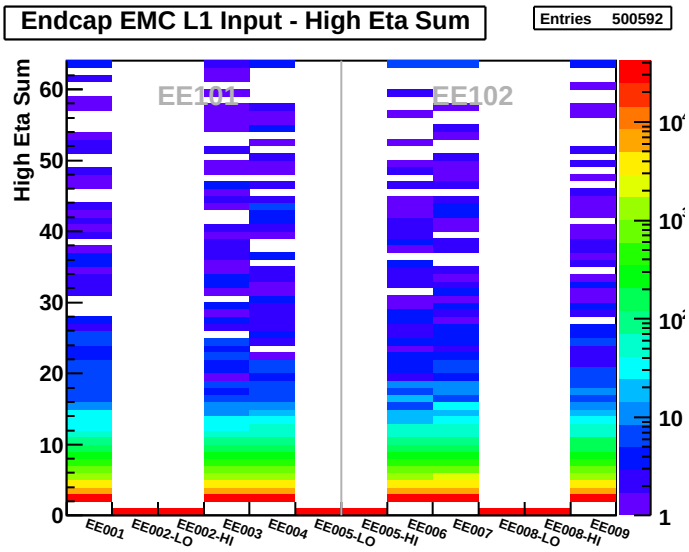

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

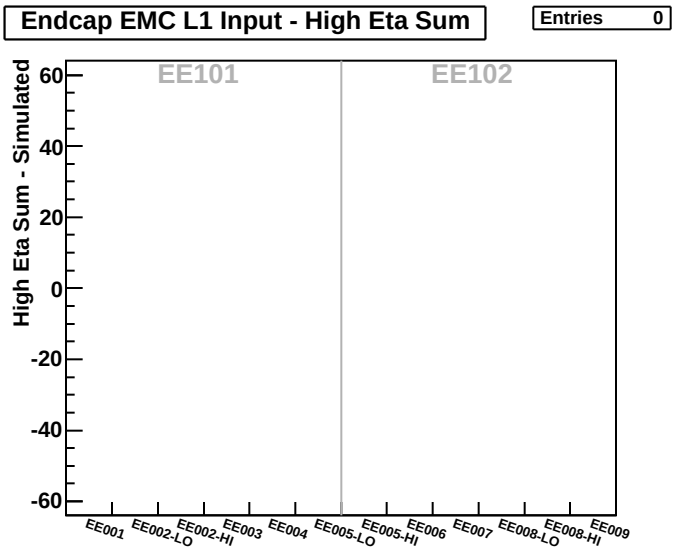

Barrel EMC L1 Input - High Tower Bits
Entries 1501776
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

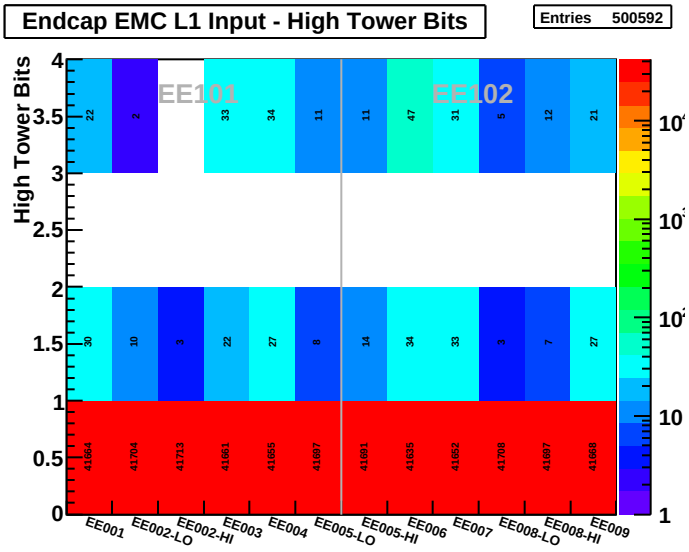

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

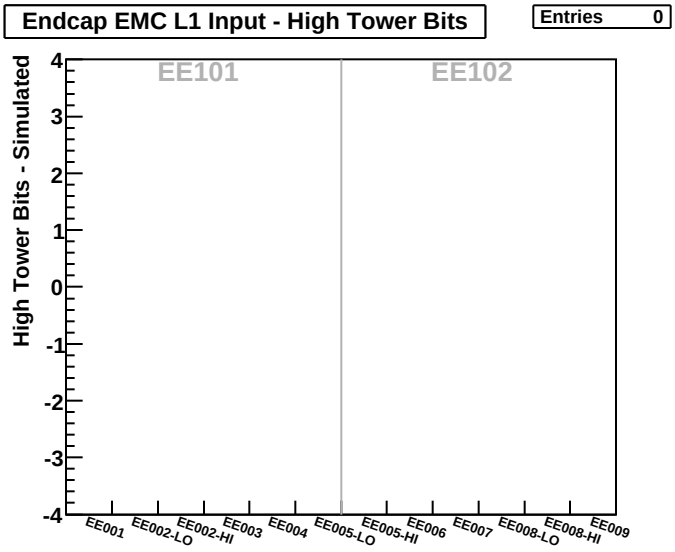

Endcap EMC L1 Input - Low Eta Sum
Entries 500592
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

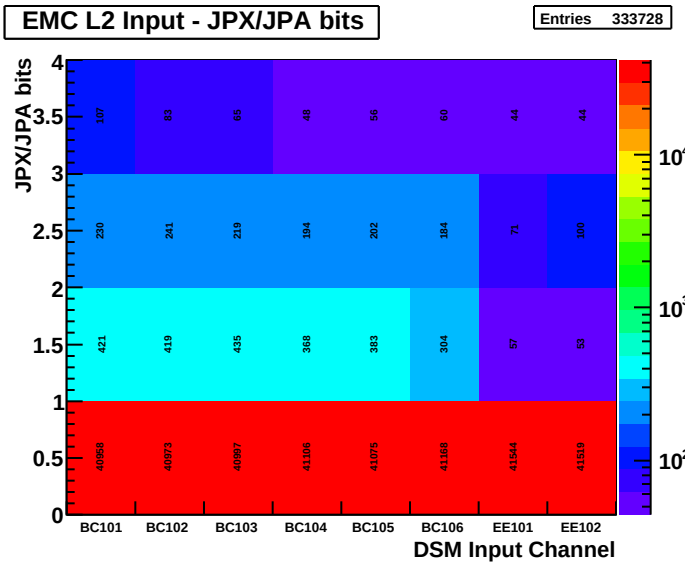

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

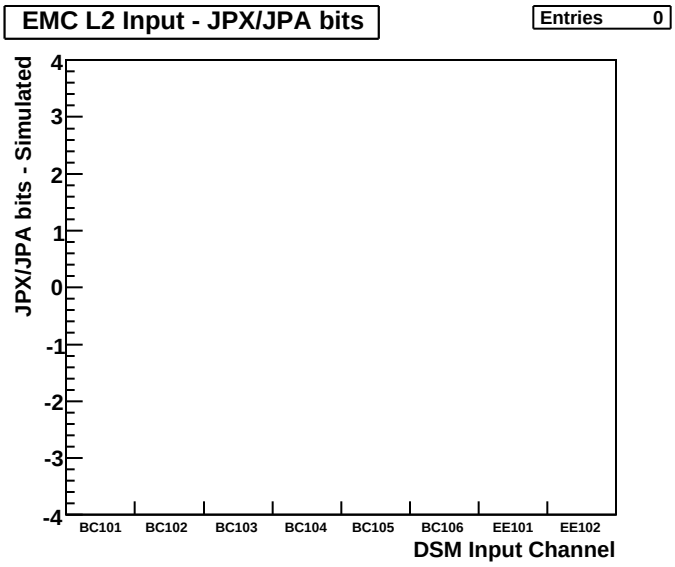

Endcap EMC L1 Input - High Eta Sum
Entries 500592
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

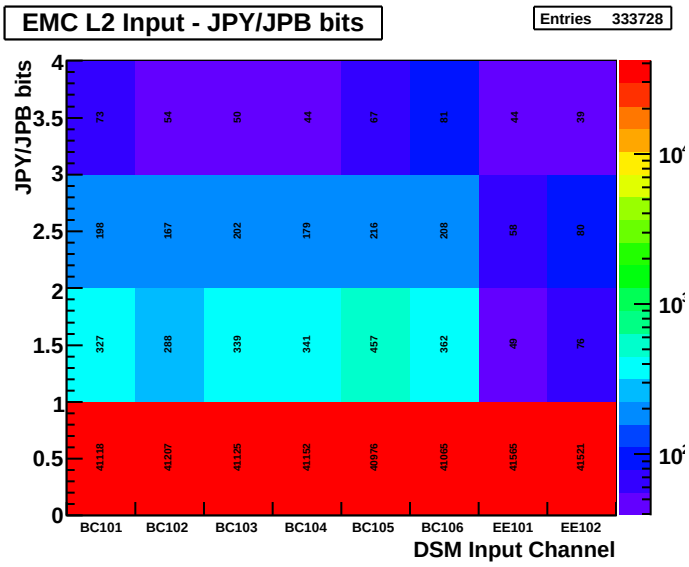

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

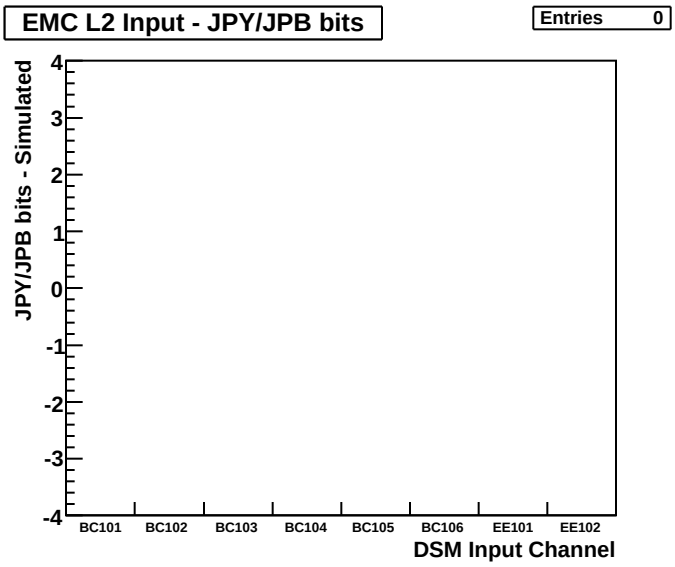

Endcap EMC L1 Input - High Tower Bits
Entries 500592
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

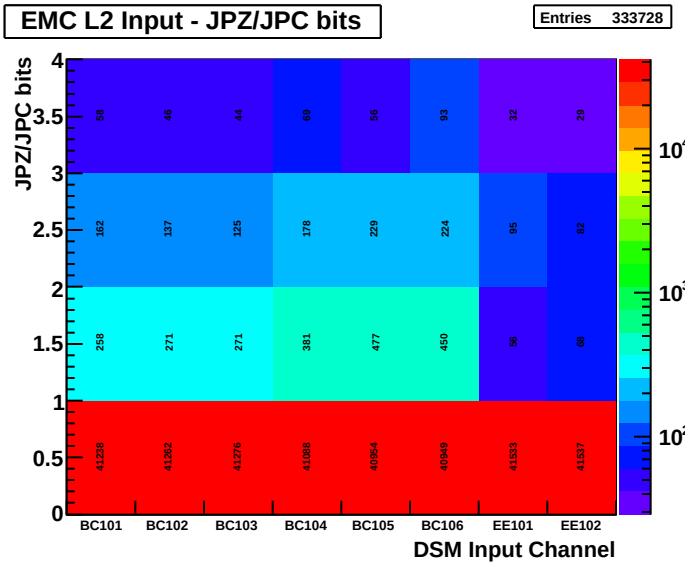

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

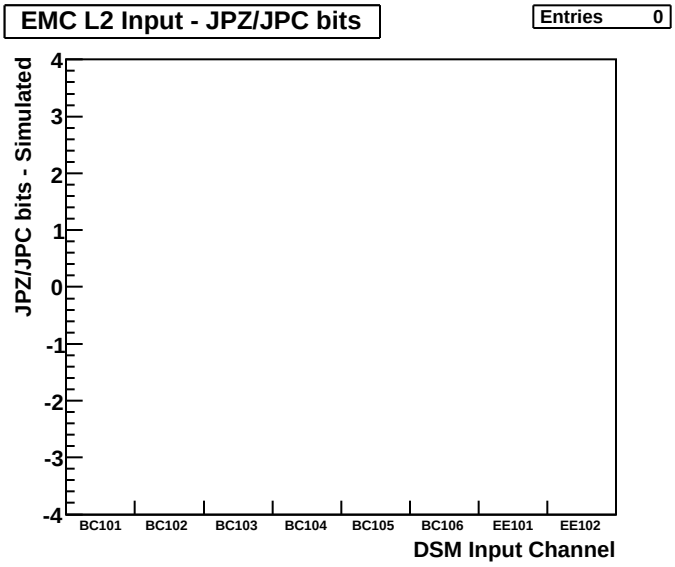

EMC L2 Input - JPX/JPA bits
Entries 333728
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
107 83 65 48 56 60 44 44
230 241 219 194 202 184 71 100
421 419 435 368 383 304 57 53
40958 40973 40997 41106 41075 41168 41544 41519

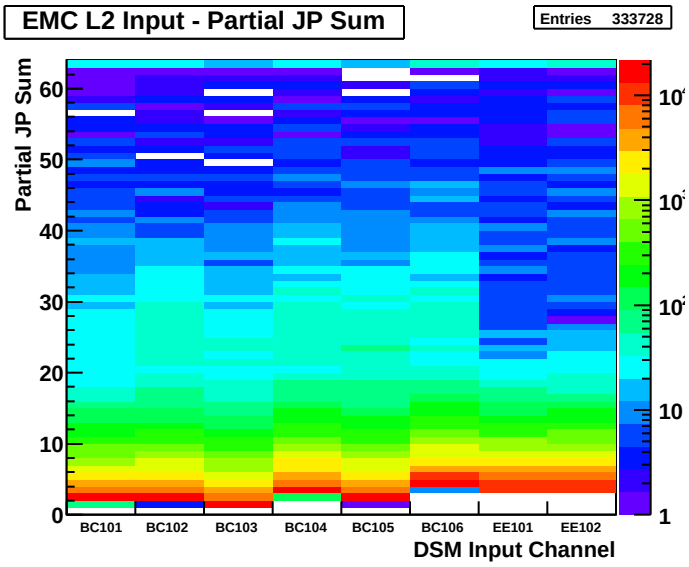

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

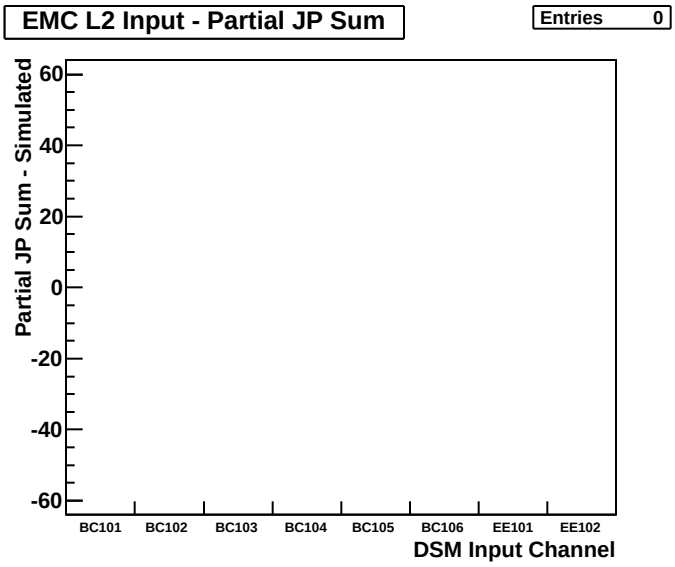

EMC L2 Input - JPY/JPB bits
Entries 333728
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
73 54 50 44 67 81 44 39
198 167 202 179 216 208 58 60
327 288 339 341 457 362 49 76
41118 41207 41125 41152 40976 41065 41565 41521

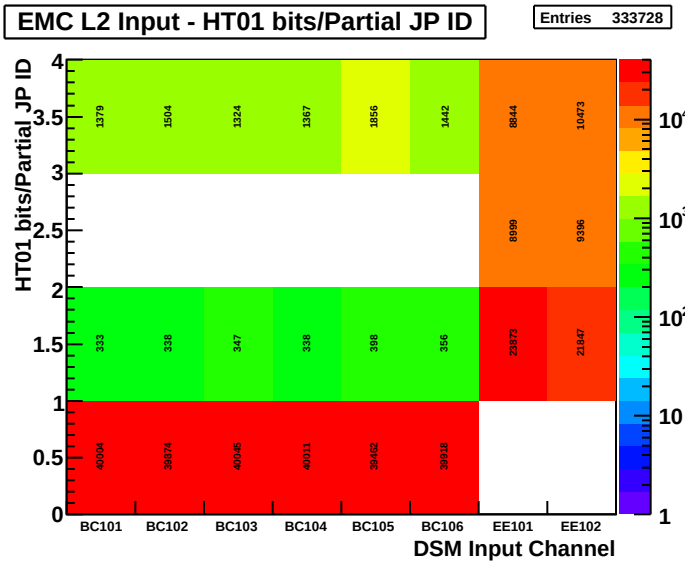

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

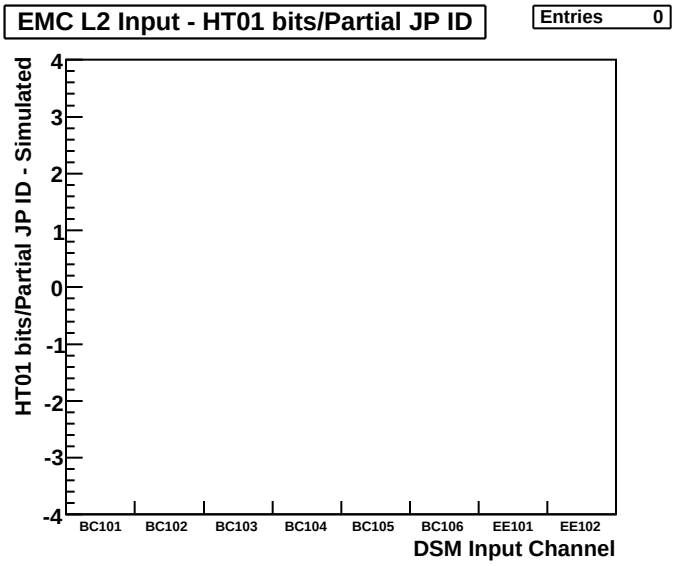

EMC L2 Input - JPZ/JPC bits
Entries 333728
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
58 46 44 69 56 93 32 29
162 137 125 178 229 224 95 82
258 271 271 381 477 450 56 68
41238 41262 41276 41088 40954 40949 41533 41537

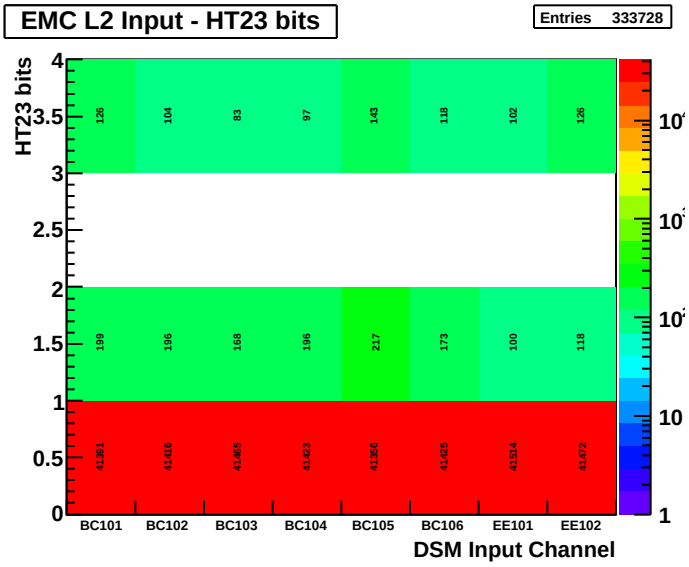

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

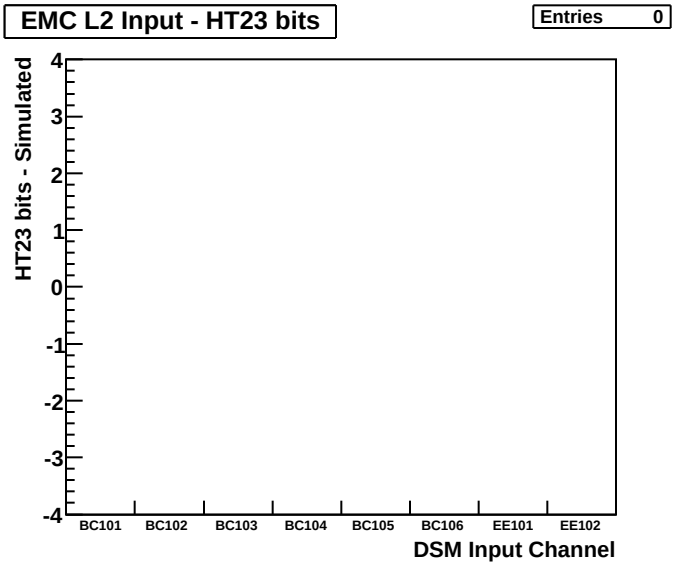

EMC L2 Input - Partial JP Sum
Entries 333728
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

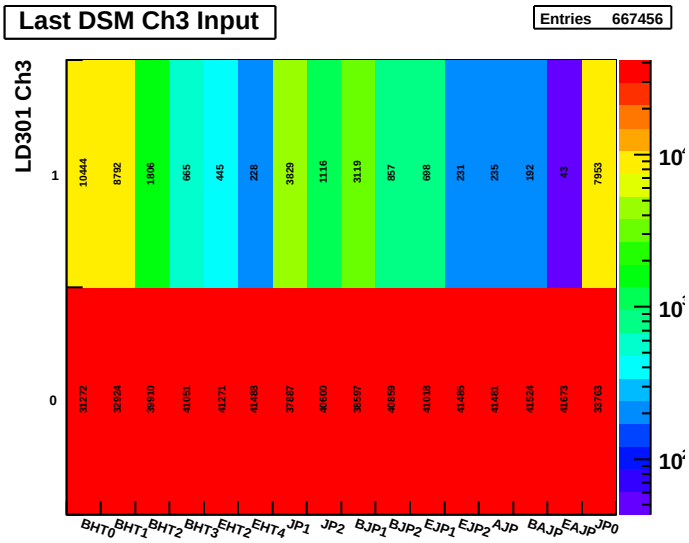

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

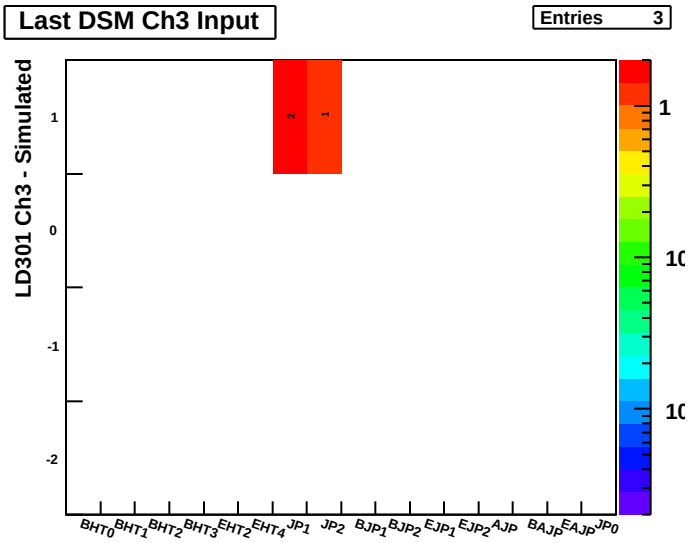

EMC L2 Input - HT01 bits/Partial JP ID
Entries 333728
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

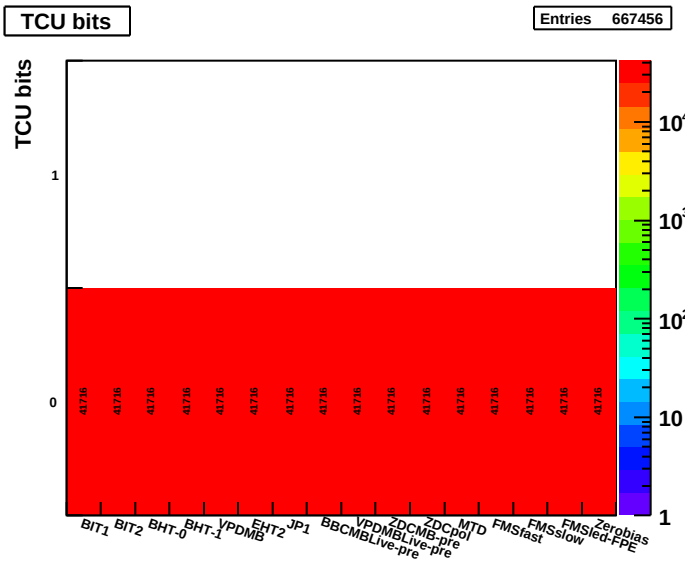

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

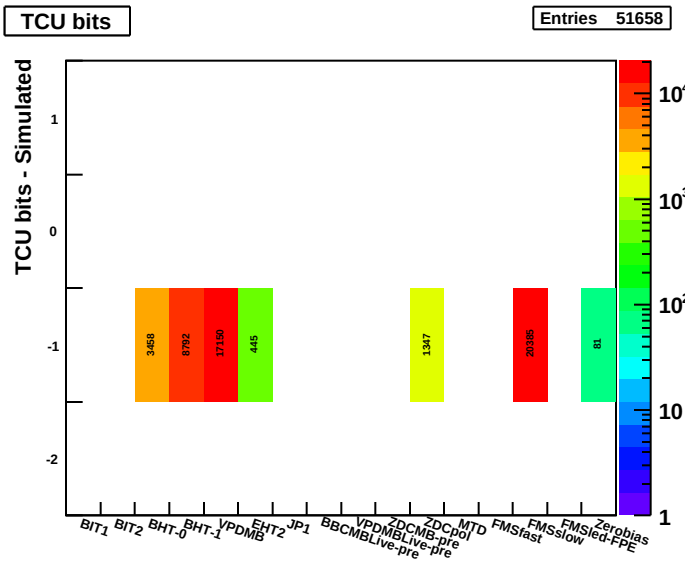

EMC L2 Input - HT23 bits
Entries 333728
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

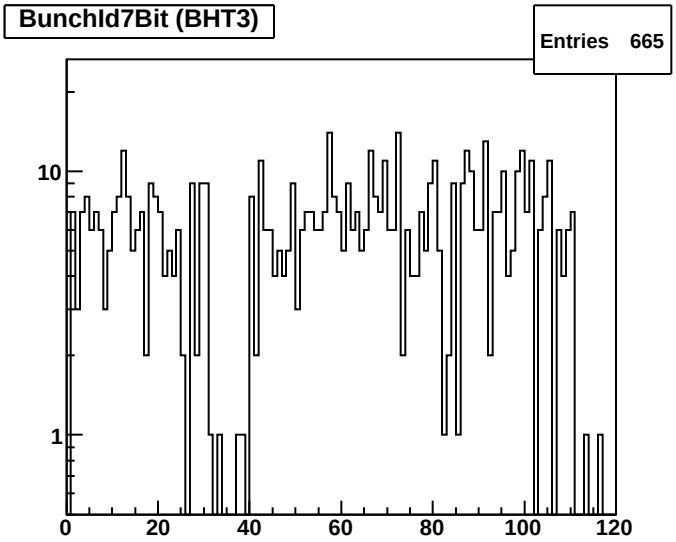

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

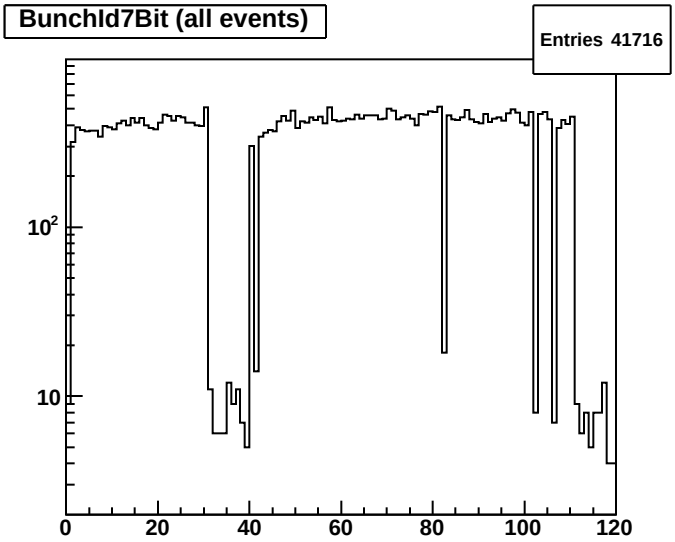

Last DSM Ch3 Input
Entries 667456
LD301 Ch3
10444 8792 1806 665 445 228 3829 1116 3119 857 698 231 235 192 43 7953
31272 32924 39910 41051 41271 41468 37887 40600 38597 40859 41018 41485 41481 41524 41673 33763
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

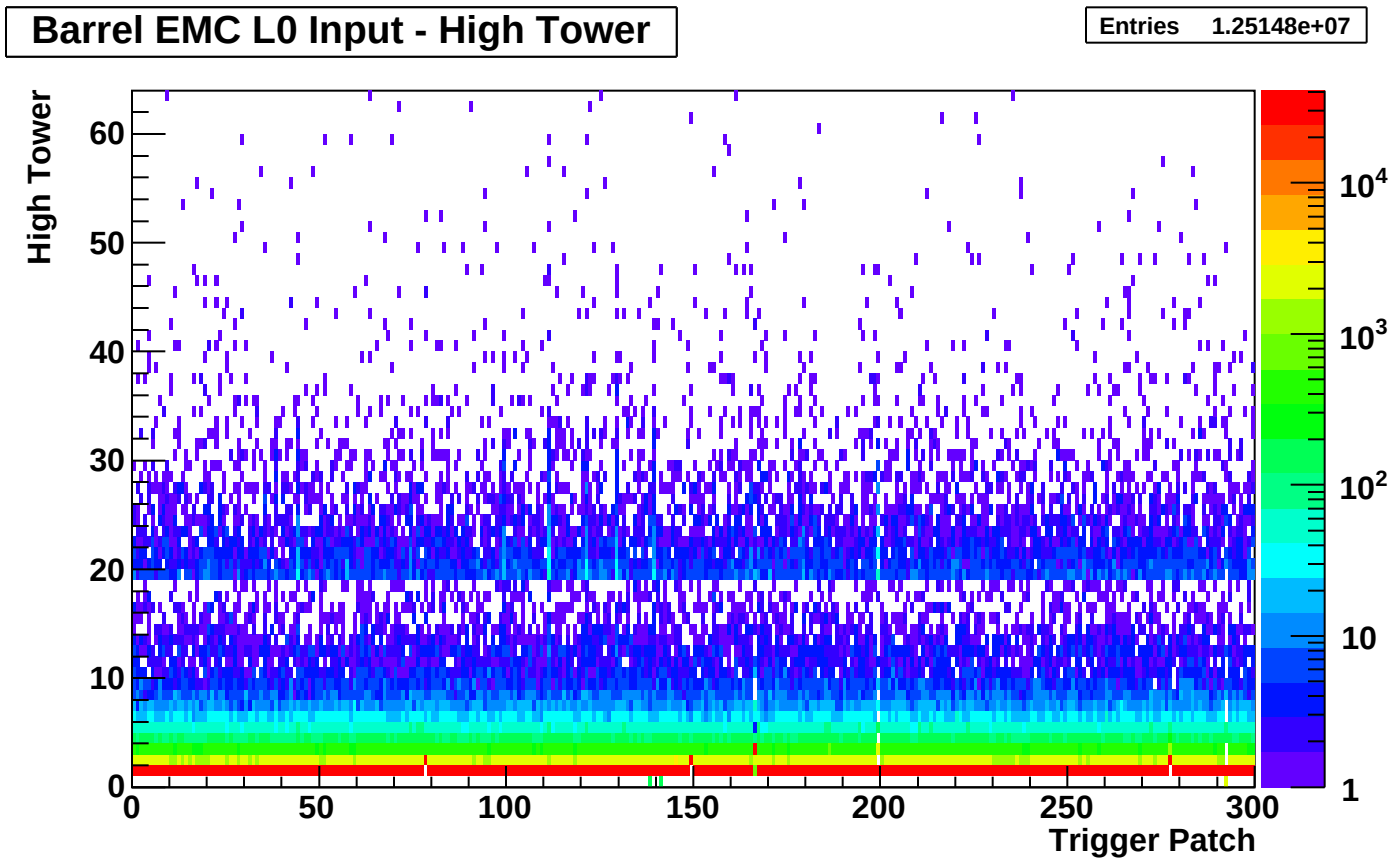

Last DSM Ch3 Input
Entries 3
LD301 Ch3 - Simulated
2 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

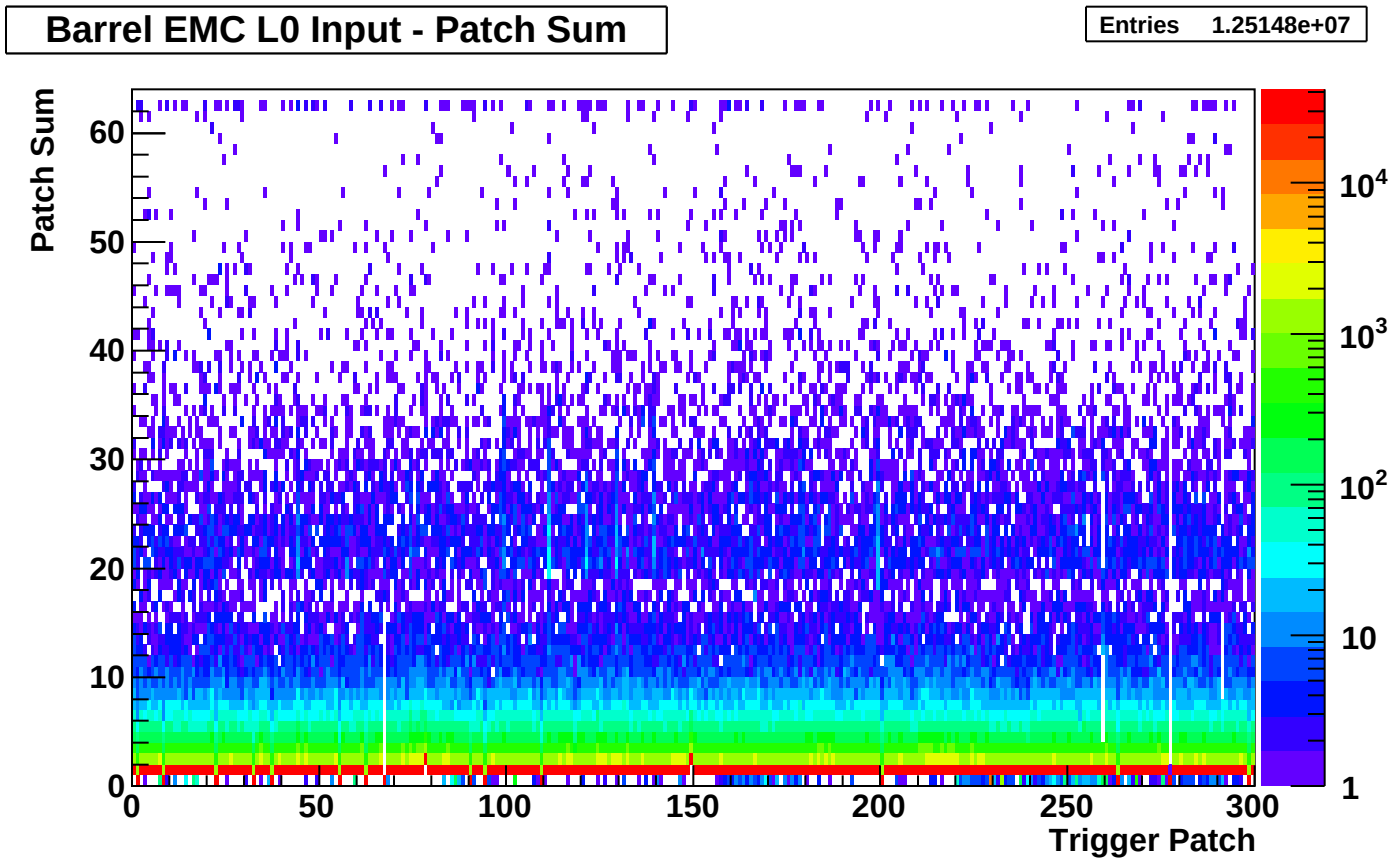

TCU bits
Entries 667456
TCU bits
41716 41716 41716 41716 41716 41716 41716 41716 41716 41716 41716 41716 41716 41716 41716 41716
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

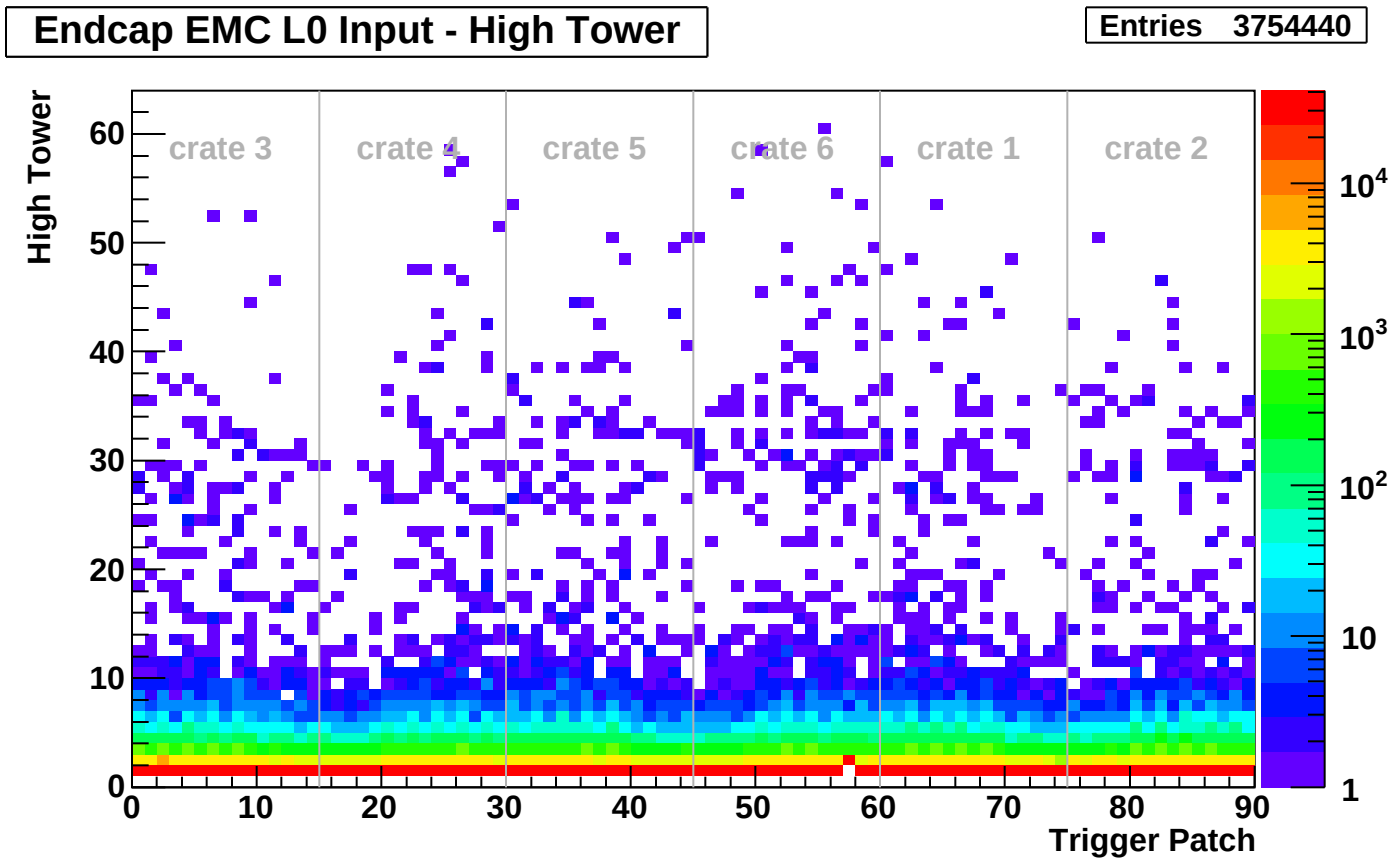

TCU bits
Entries 51658
TCU bits - Simulated
3458 8792 17150 445 1347 20385 81
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

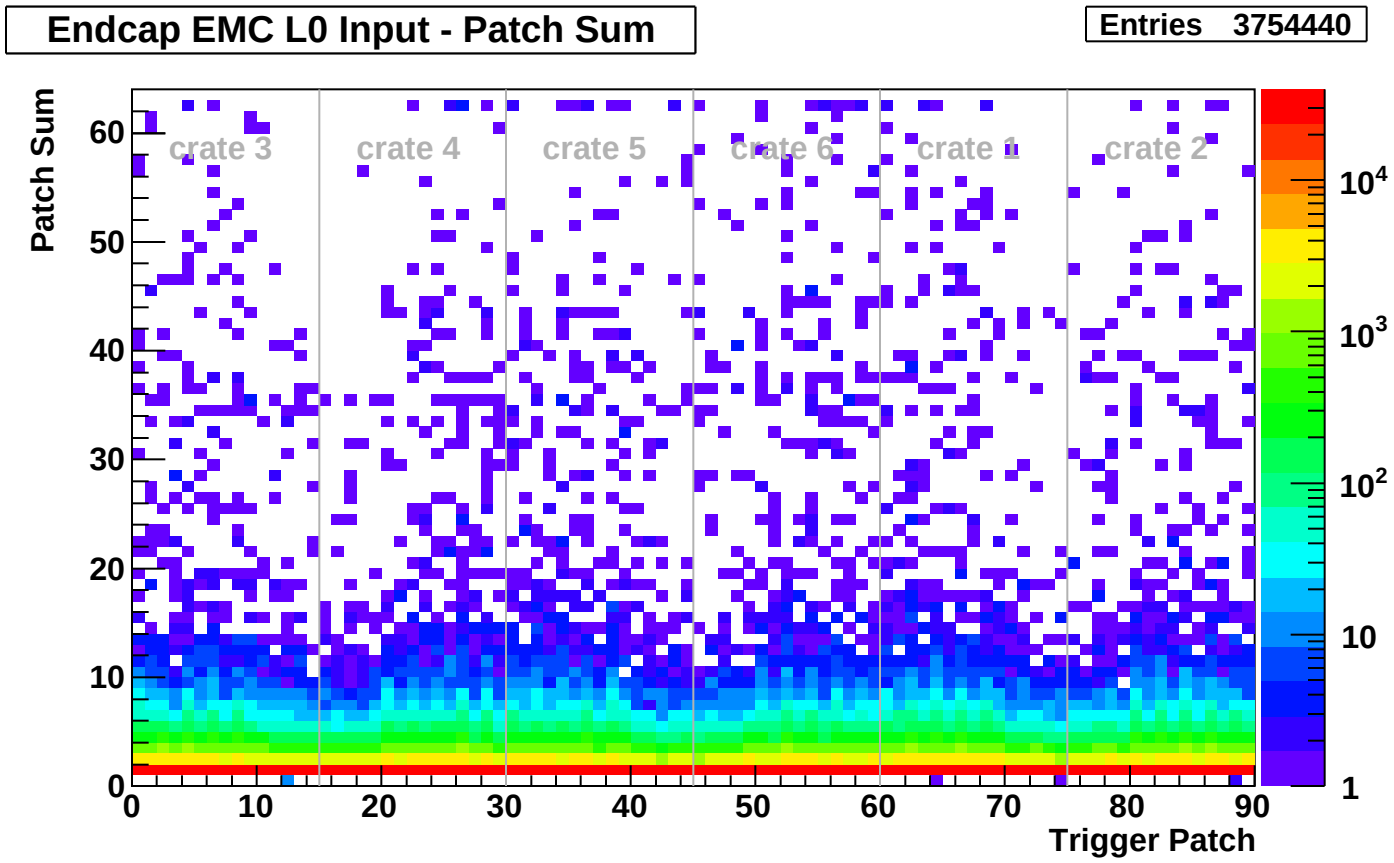

BunchId7Bit (BHT3)
Entries 665

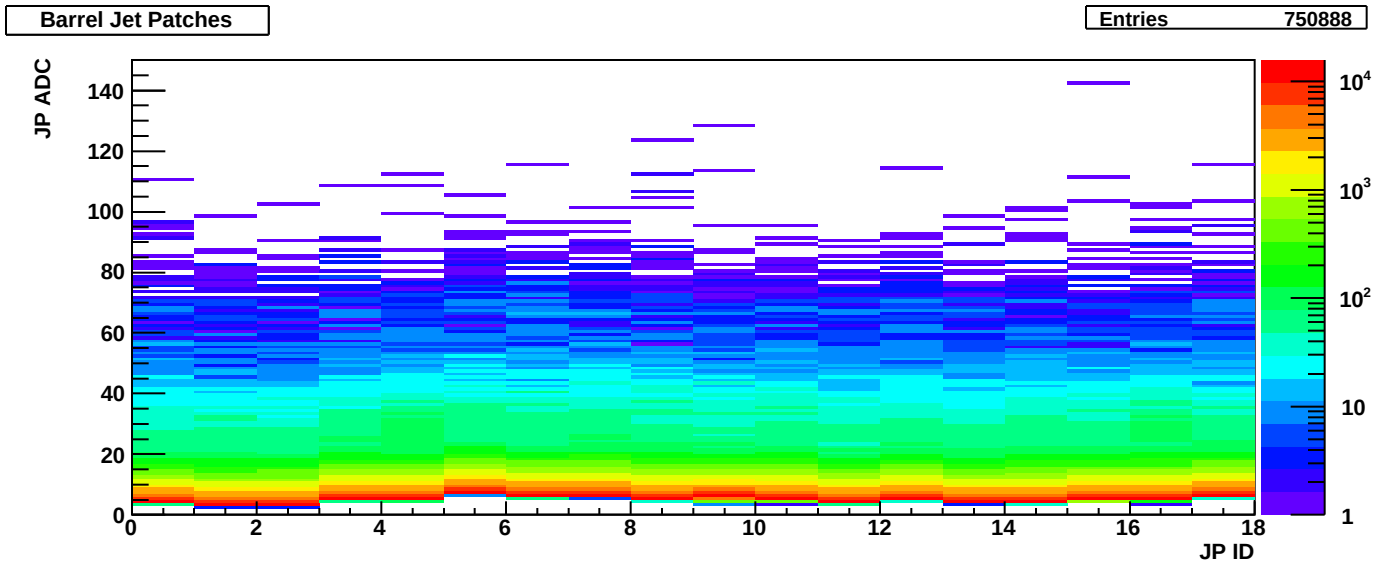

BunchId7Bit (all events)
Entries 41716

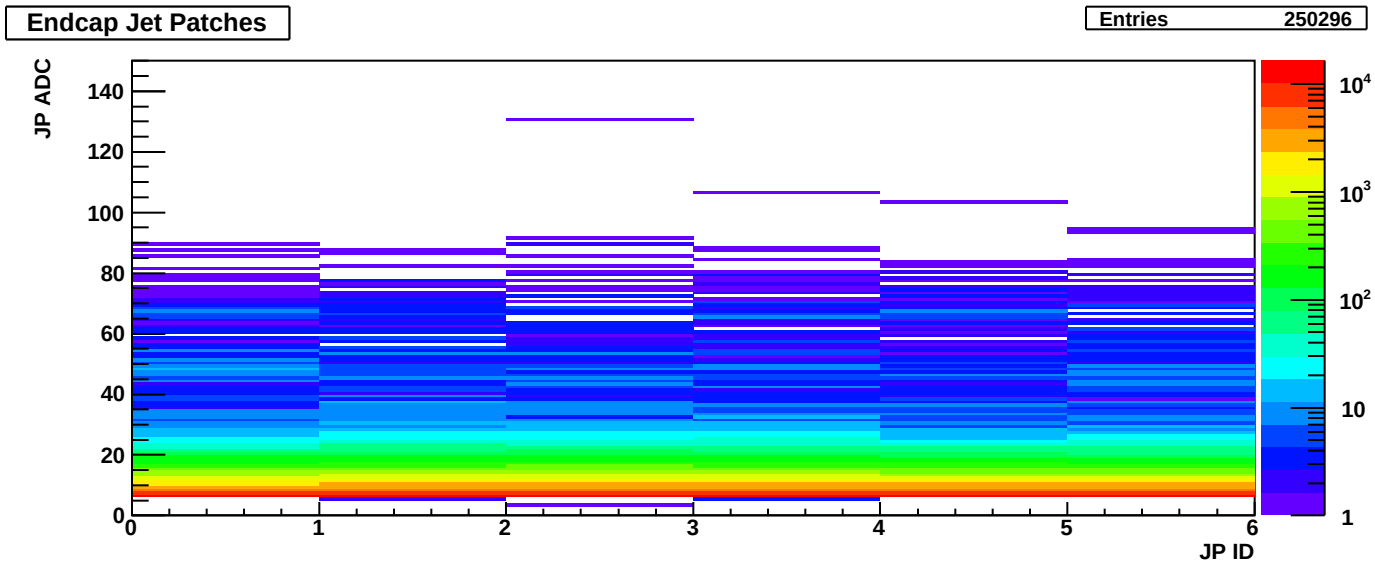

Barrel EMC L0 Input - High Tower
Entries 1.25148e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

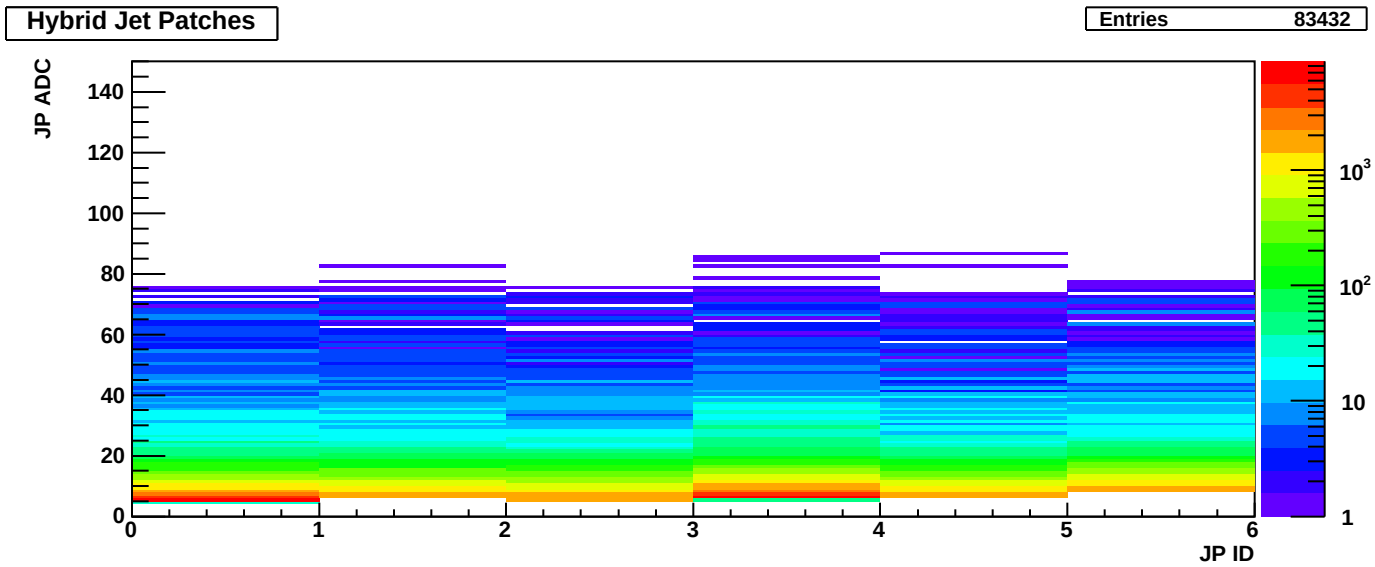

Barrel EMC L0 Input - Patch Sum
Entries 1.25148e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

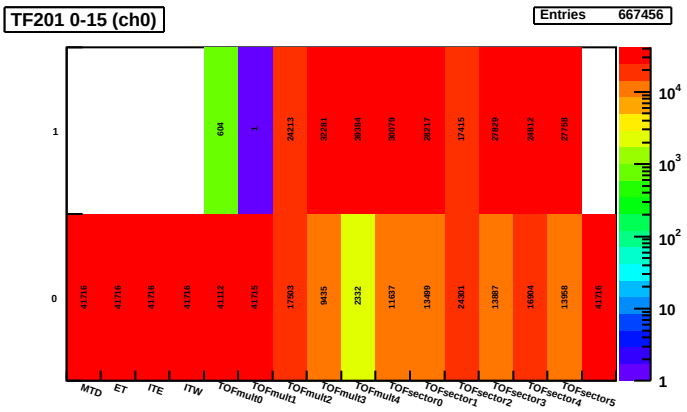

Endcap EMC L0 Input - High Tower
Entries 3754440
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

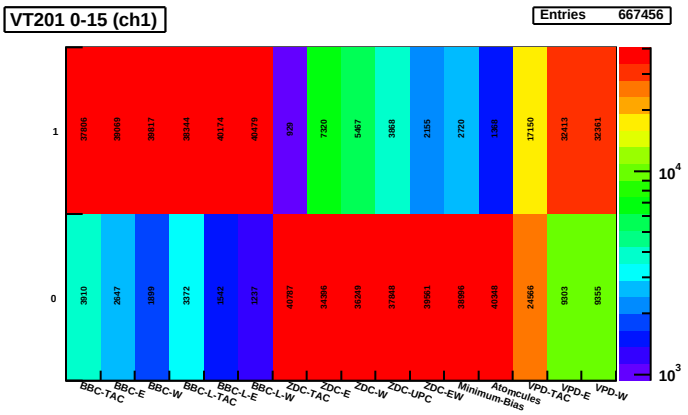

Endcap EMC L0 Input - Patch Sum
Entries 3754440
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

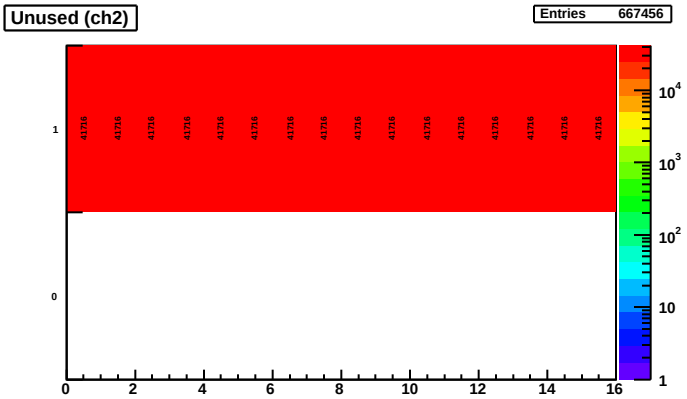

Barrel Jet Patches
Entries 750888
JP ADC
JP ID

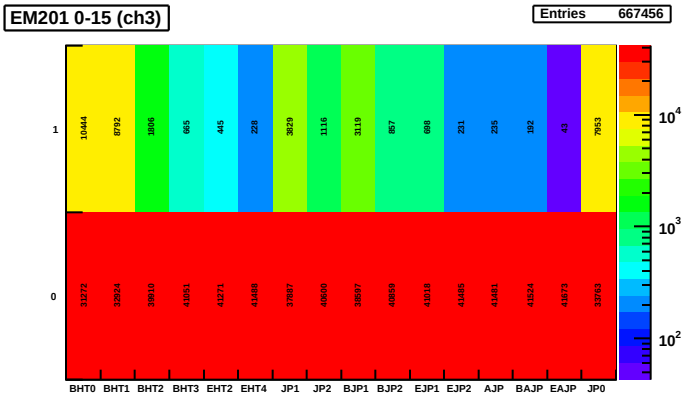

Endcap Jet Patches
Entries 250296
JP ADC
JP ID

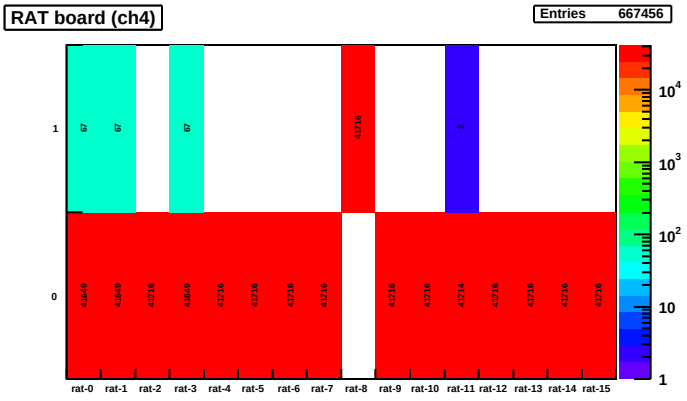

Hybrid Jet Patches
Entries 83432
JP ADC
JP ID

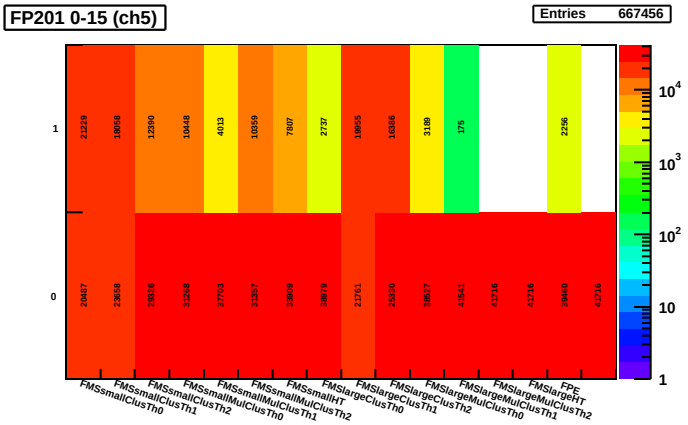

TF201 0-15 (ch0)
Entries 667456
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

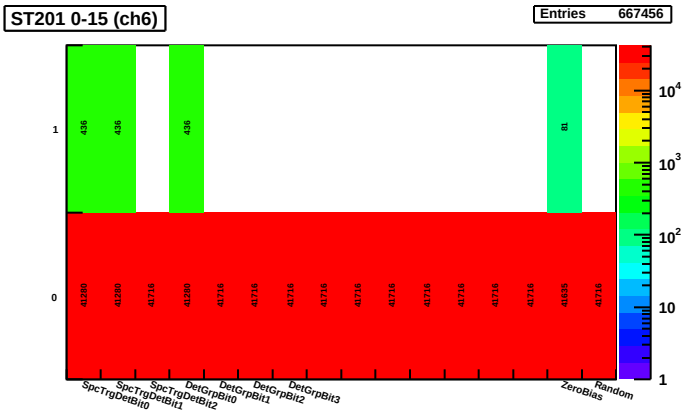

VT201 0-15 (ch1)
Entries 667456
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

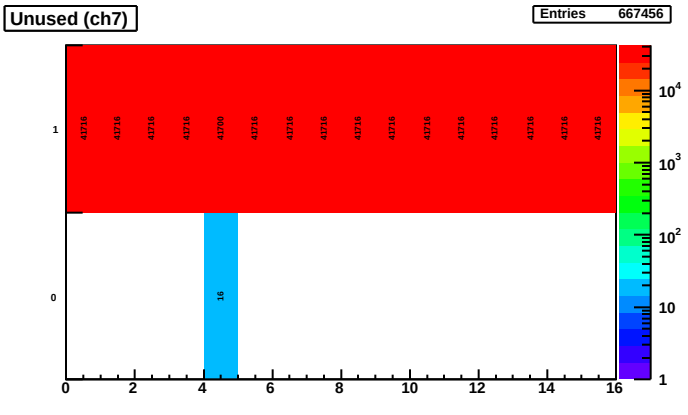

Unused (ch2)
Entries 667456

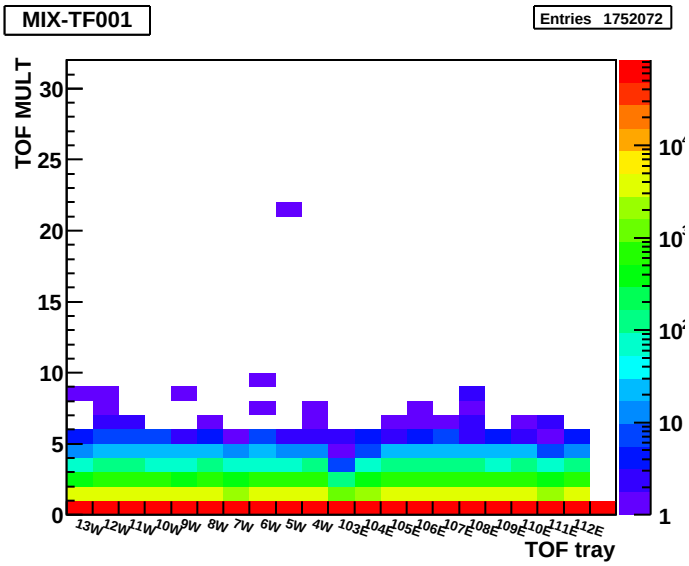

EM201 0-15 (ch3)
Entries 667456
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

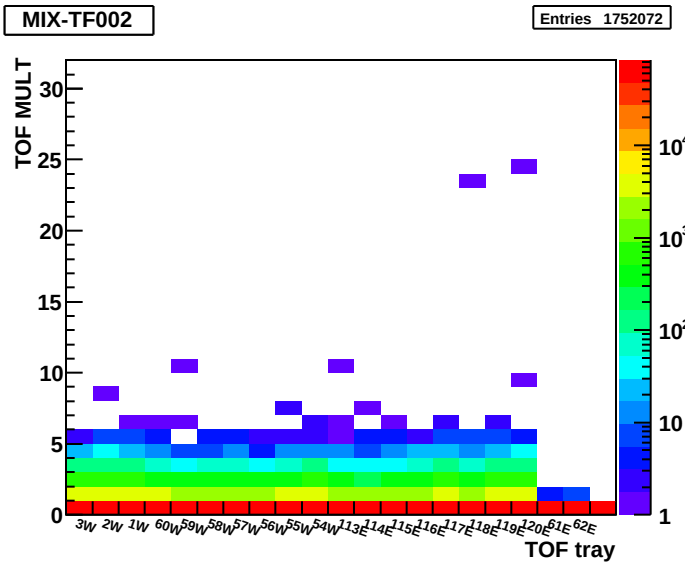

RAT board (ch4)
Entries 667456
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

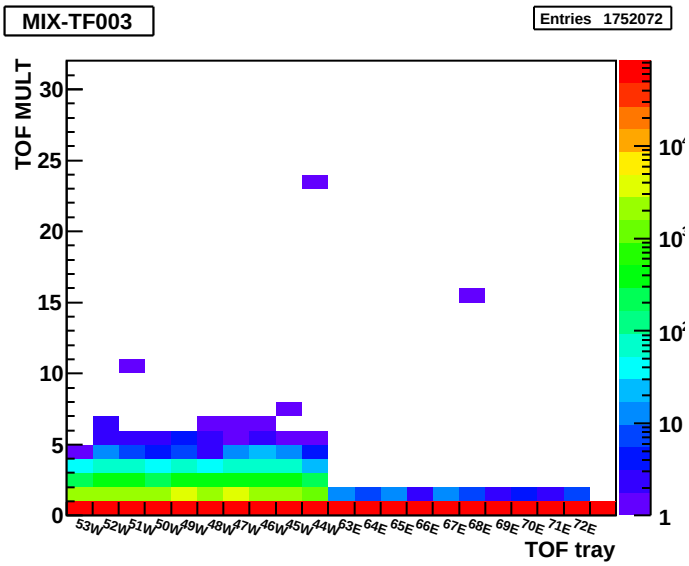

FP201 0-15 (ch5)
Entries 667456
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

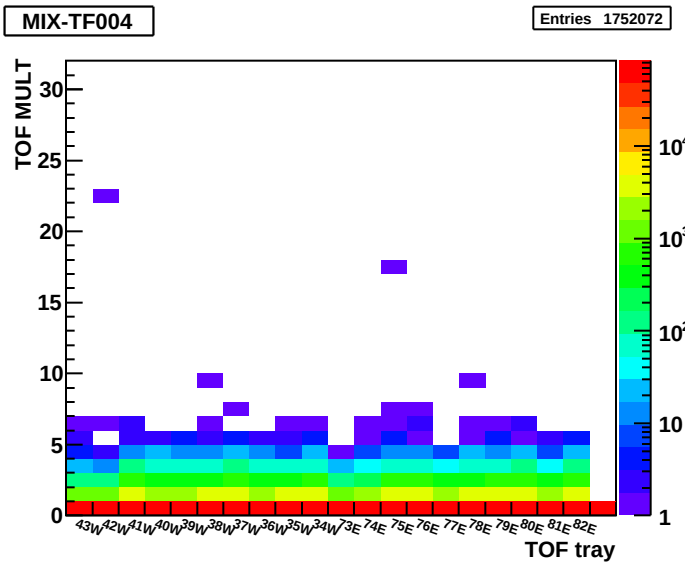

ST201 0-15 (ch6)
Entries 667456
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

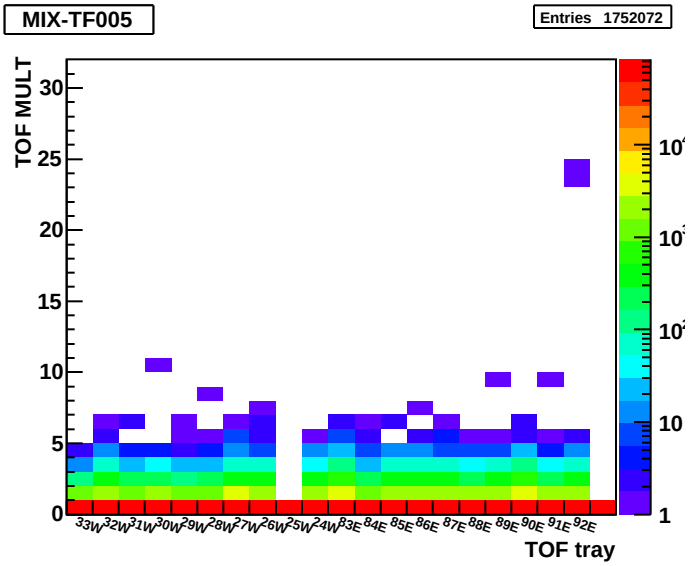

Unused (ch7)
Entries 667456

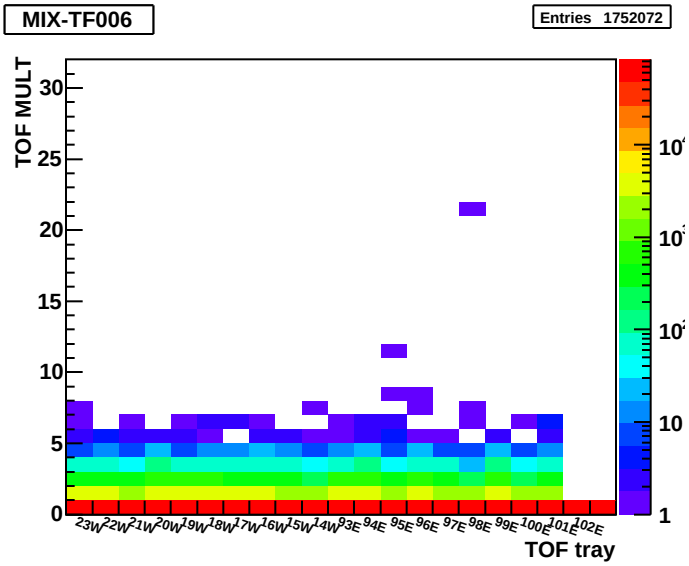

MIX-TF001
Entries 1752072
TOF MULT
TOF tray

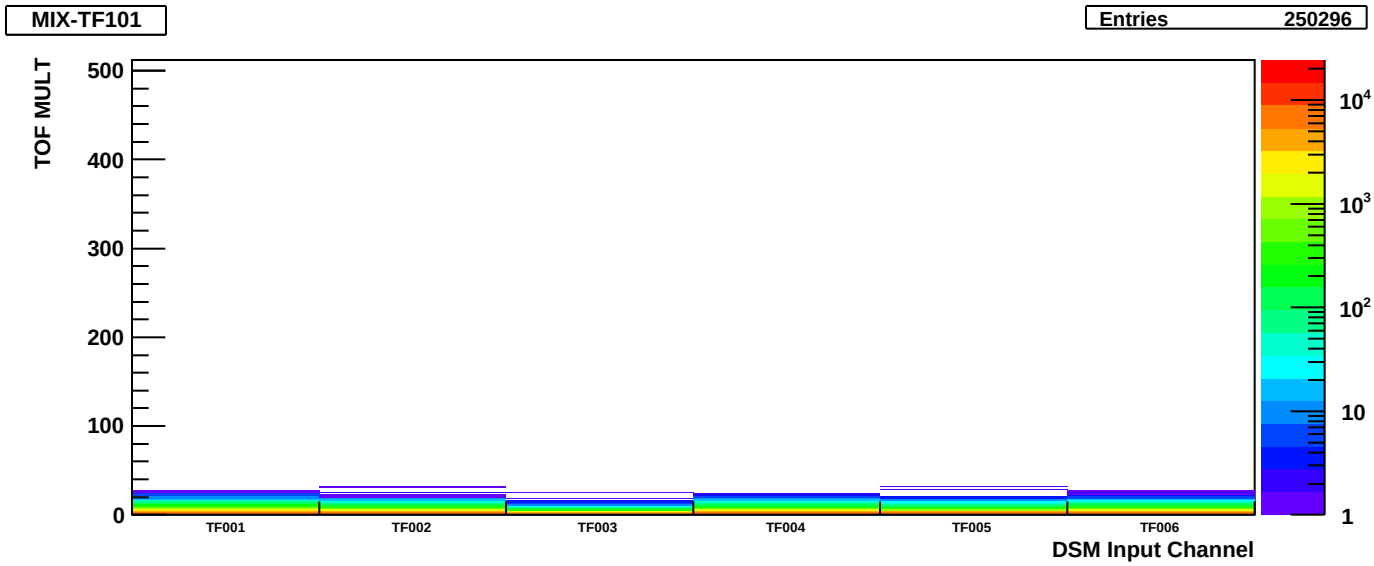

MIX-TF002
Entries 1752072
TOF MULT
TOF tray

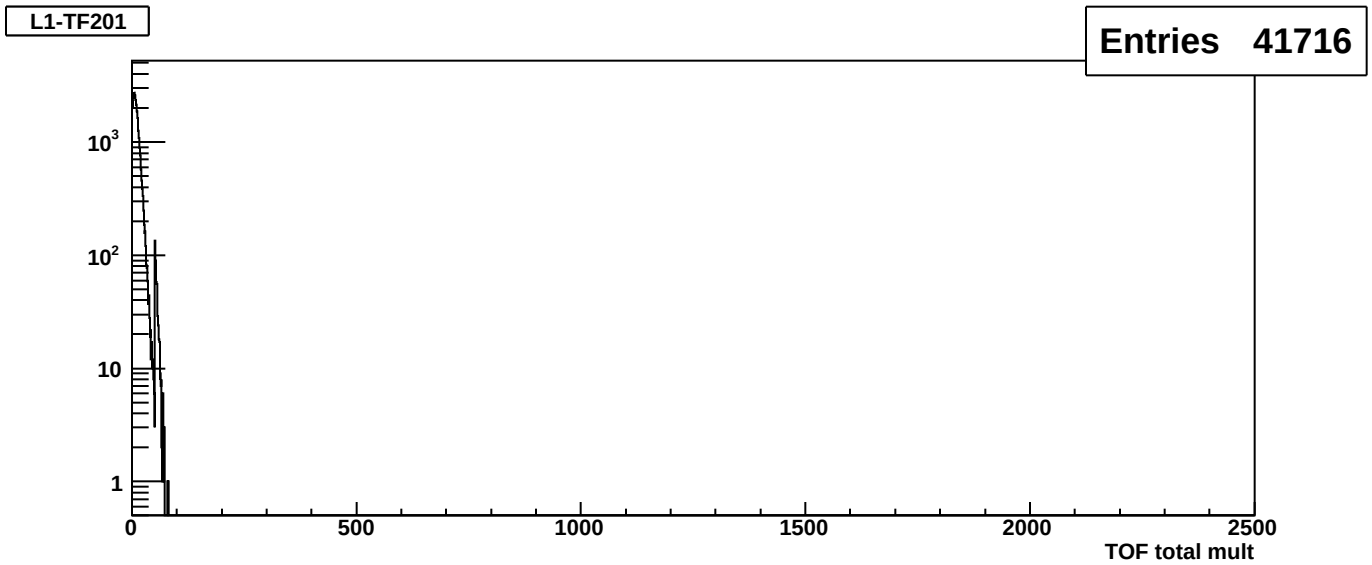

MIX-TF003
Entries 1752072
TOF MULT
TOF tray

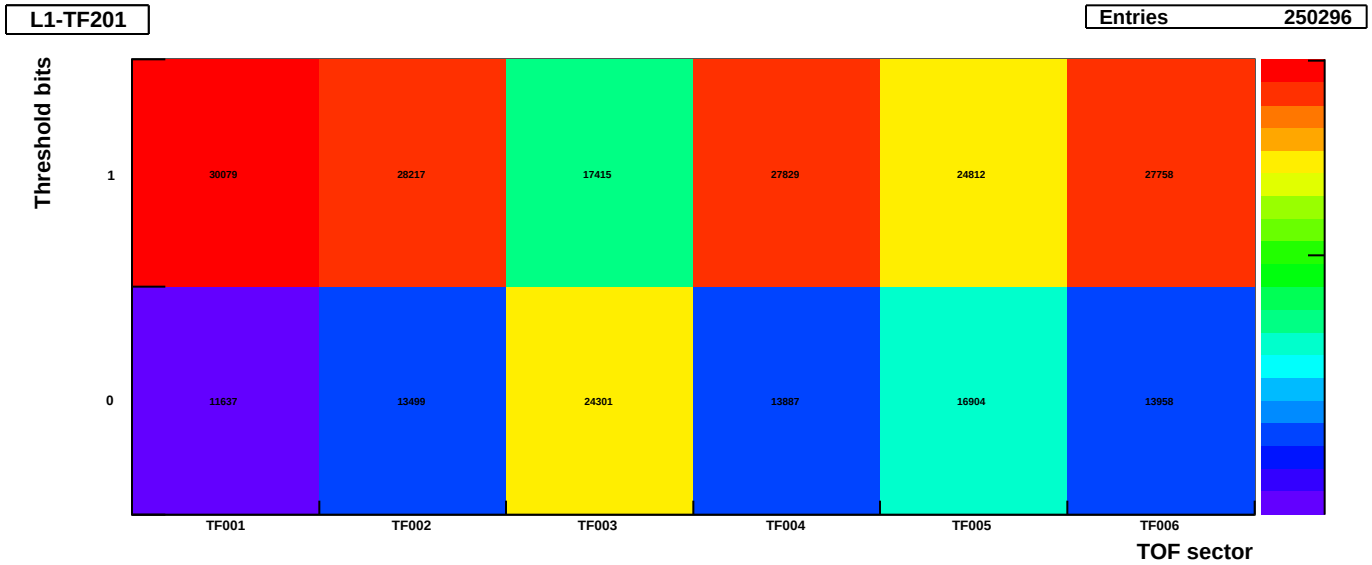

MIX-TF004
Entries 1752072
TOF MULT
TOF tray

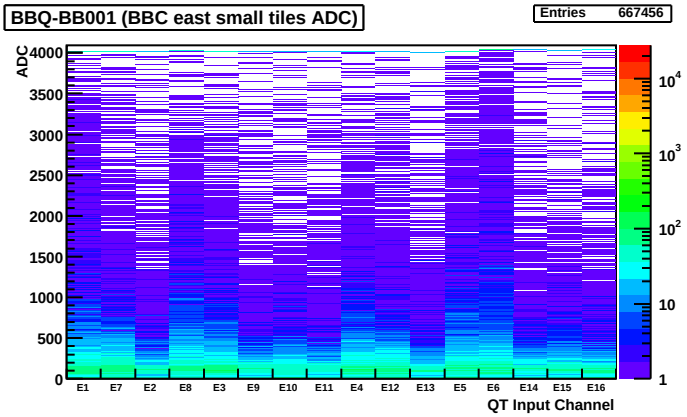

MIX-TF005
Entries 1752072
TOF MULT
TOF tray

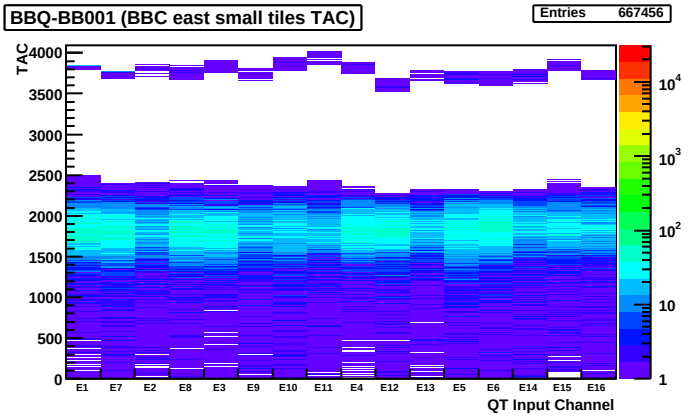

MIX-TF006
Entries 1752072
TOF MULT
TOF tray

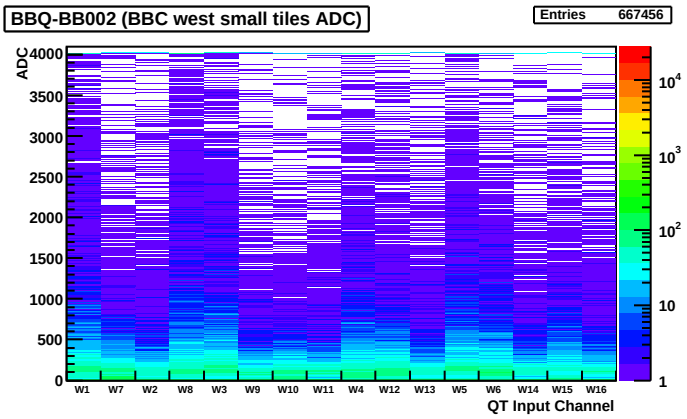

MIX-TF101
Entries 250296
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

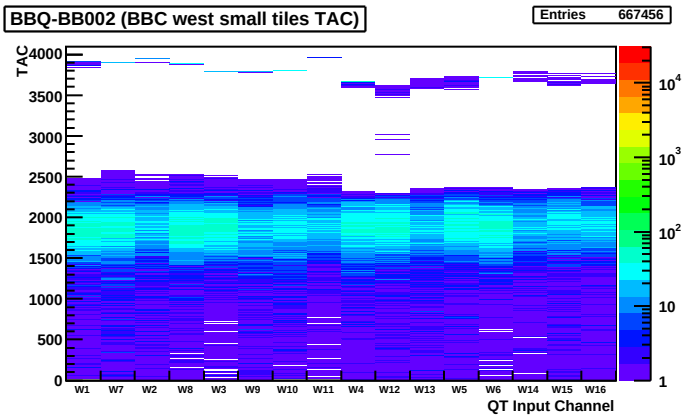

L1-TF201
Entries 41716
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

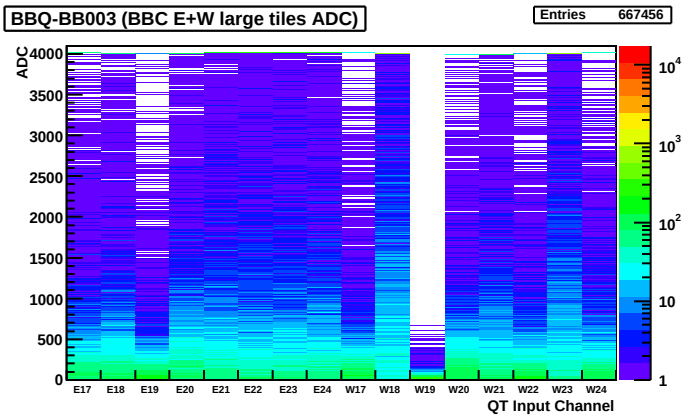

L1-TF201
Entries 250296
Threshold bits
1
0
30079
28217
17415
27829
24812
27758
11637
13499
24301
13887
16904
13958
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

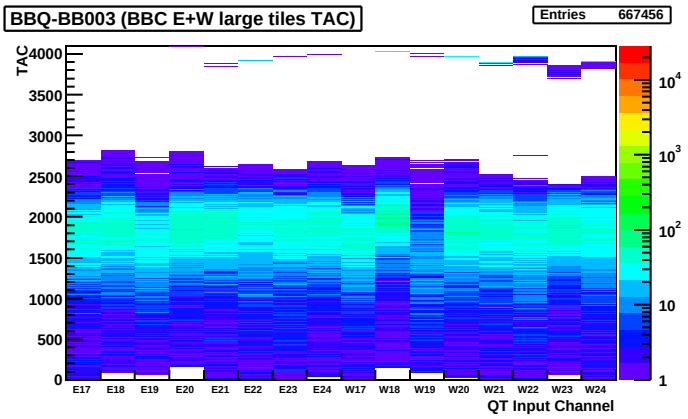

BBQ-BB001 (BBC east small tiles ADC)
Entries 667456
ADC
QT Input Channel

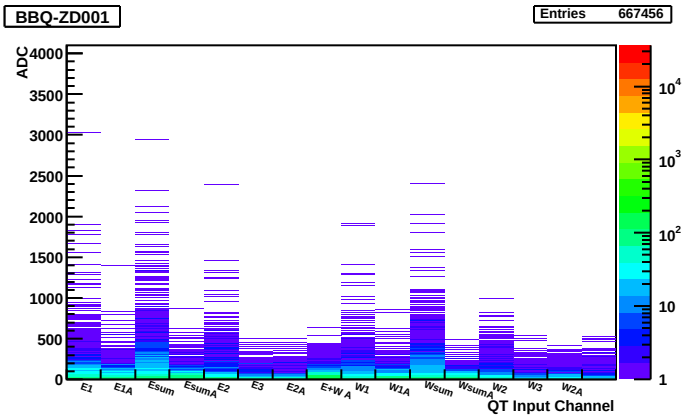

BBQ-BB001 (BBC east small tiles TAC)
Entries 667456
TAC
QT Input Channel

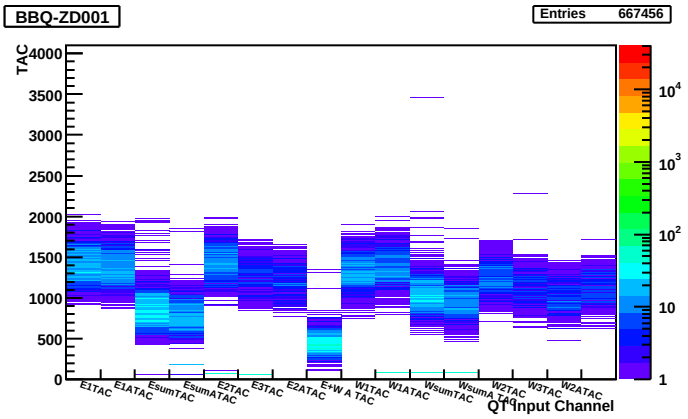

BBQ-BB002 (BBC west small tiles ADC)
Entries 667456
ADC
QT Input Channel

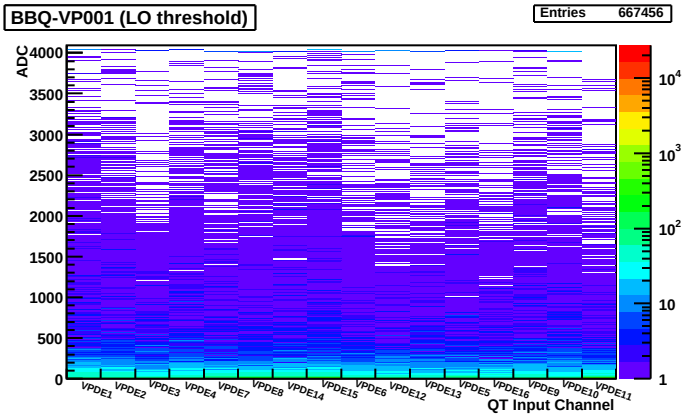

BBQ-BB002 (BBC west small tiles TAC)
Entries 667456
TAC
QT Input Channel

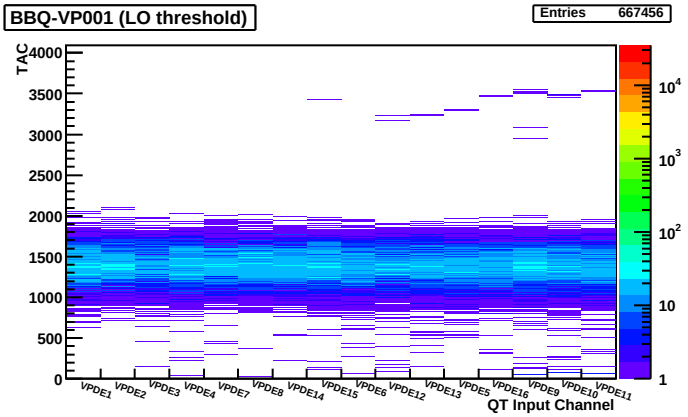

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 667456
ADC
QT Input Channel

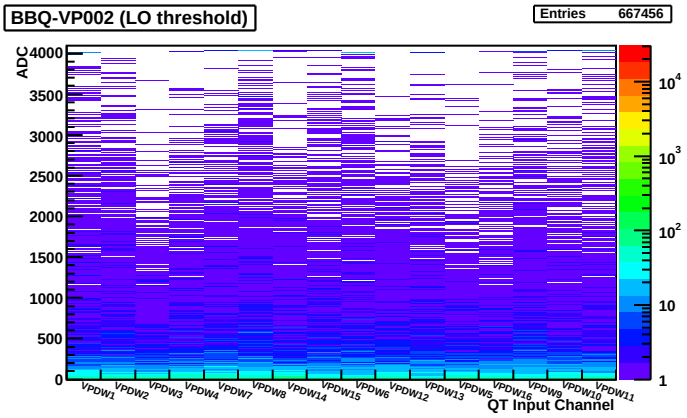

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 667456
TAC
QT Input Channel

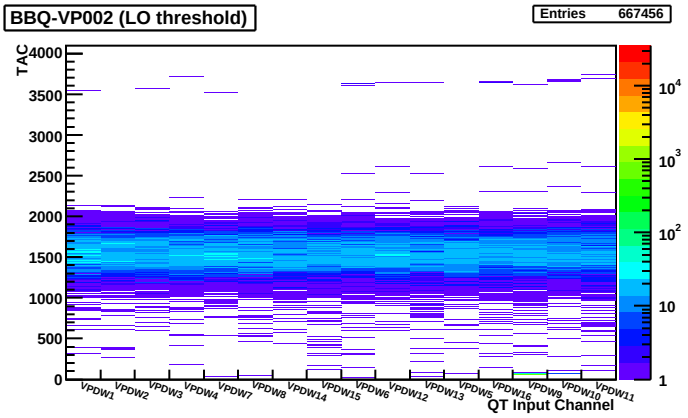

BBQ-ZD001
Entries 667456
ADC
QT Input Channel

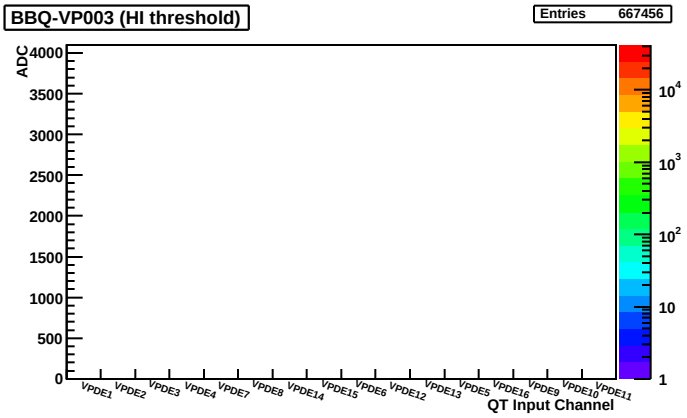

BBQ-ZD001
Entries 667456
TAC
QT Input Channel

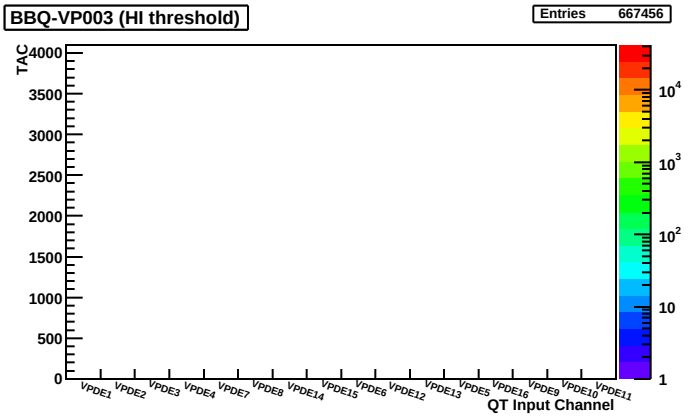

BBQ-VP001 (LO threshold)
Entries 667456
ADC
QT Input Channel

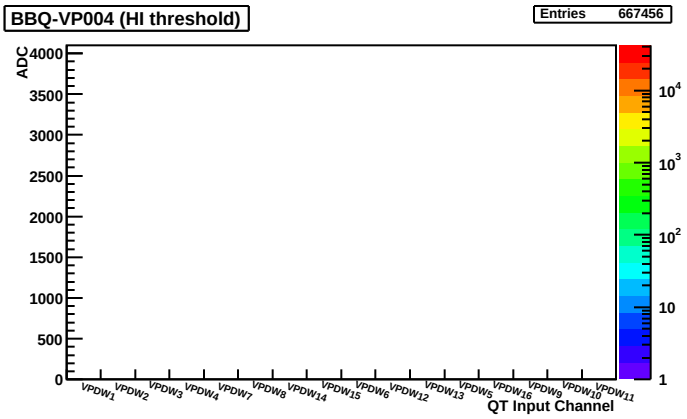

BBQ-VP001 (LO threshold)
Entries 667456
TAC
QT Input Channel

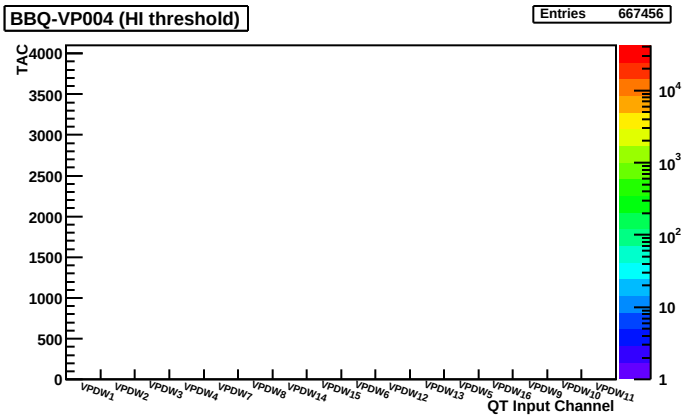

BBQ-VP002 (LO threshold)
Entries 667456
ADC
QT Input Channel

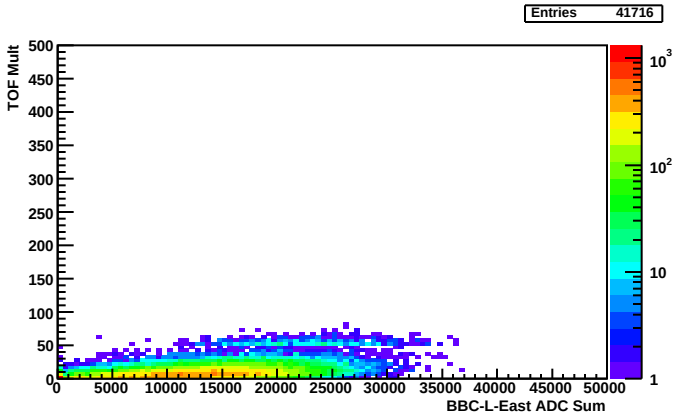

BBQ-VP002 (LO threshold)
Entries 667456
TAC
QT Input Channel

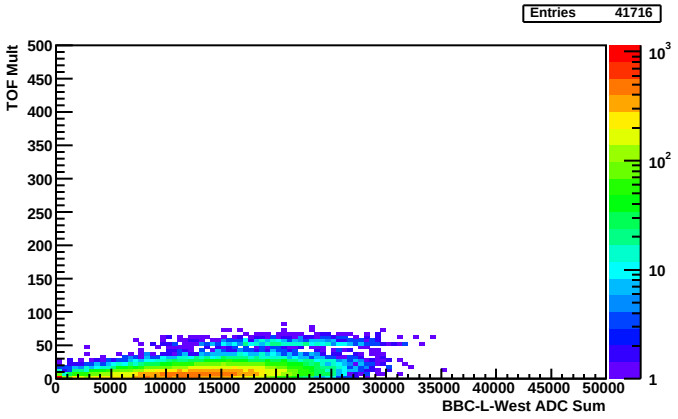

BBQ-VP003 (HI threshold)
Entries 667456
ADC
QT Input Channel

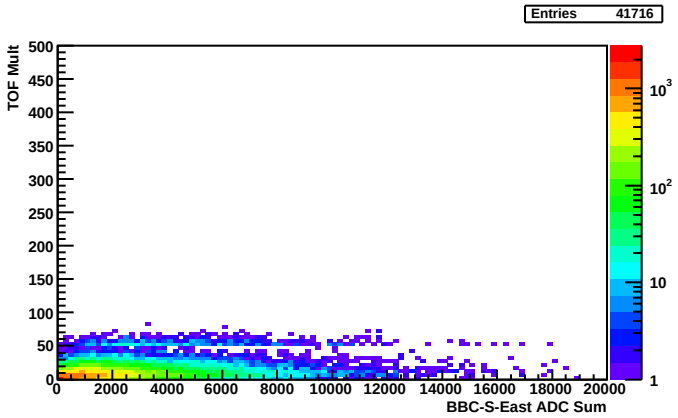

BBQ-VP003 (HI threshold)
Entries 667456
TAC
QT Input Channel

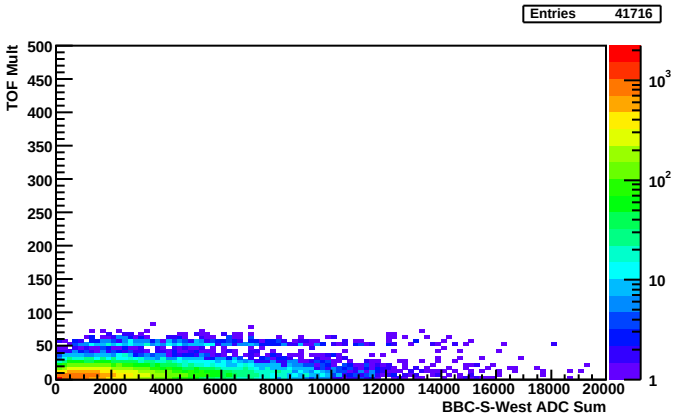

BBQ-VP004 (HI threshold)
Entries 667456
ADC
QT Input Channel

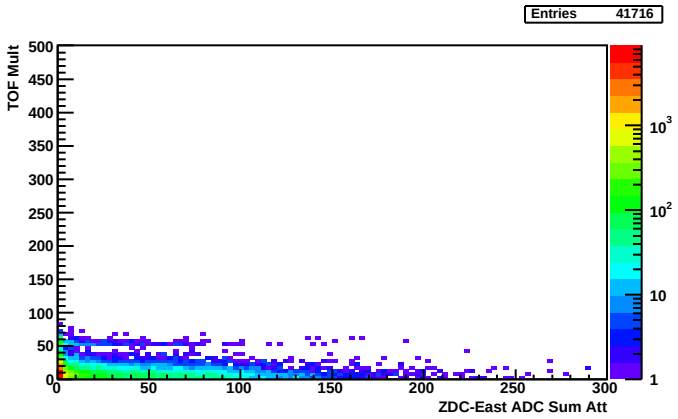

BBQ-VP004 (HI threshold)
Entries 667456
TAC
QT Input Channel

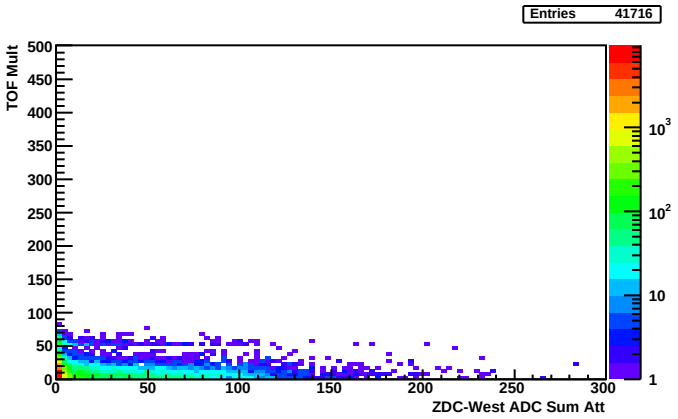

Entries 41716
TOF Mult
BBC-L-East ADC Sum

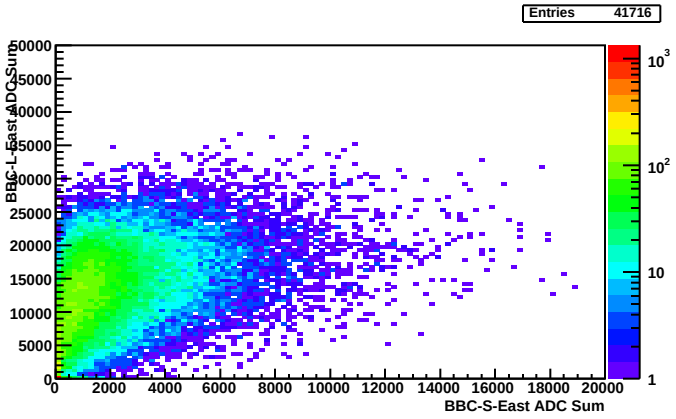

Entries 41716
TOF Mult
BBC-L-West ADC Sum

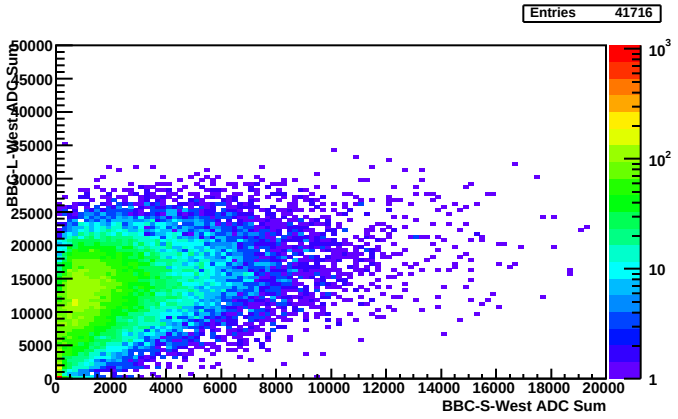

Entries 41716
TOF Mult
BBC-S-East ADC Sum

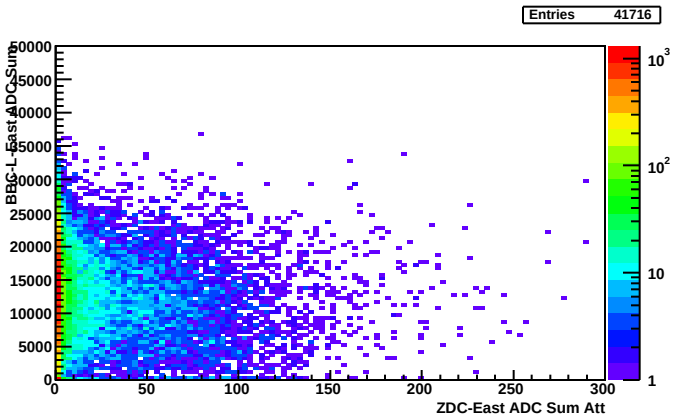

Entries 41716
TOF Mult
BBC-S-West ADC Sum

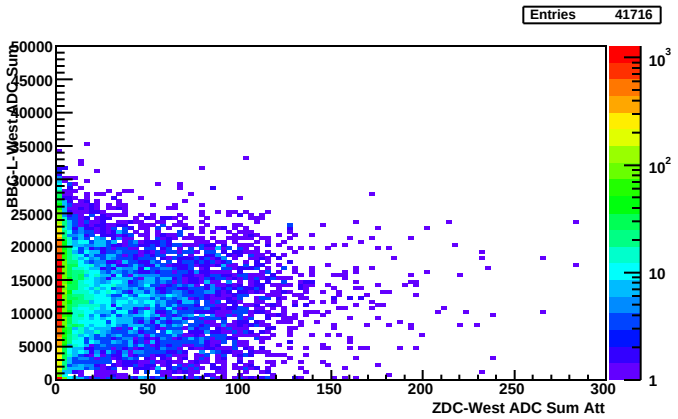

Entries 41716
TOF Mult
ZDC-East ADC Sum Att

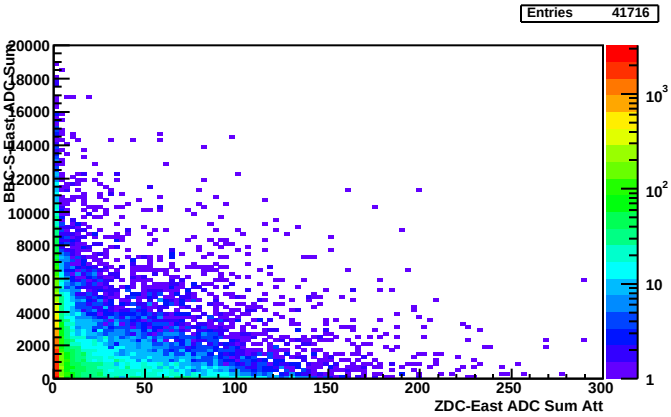

Entries 41716
TOF Mult
ZDC-West ADC Sum Att

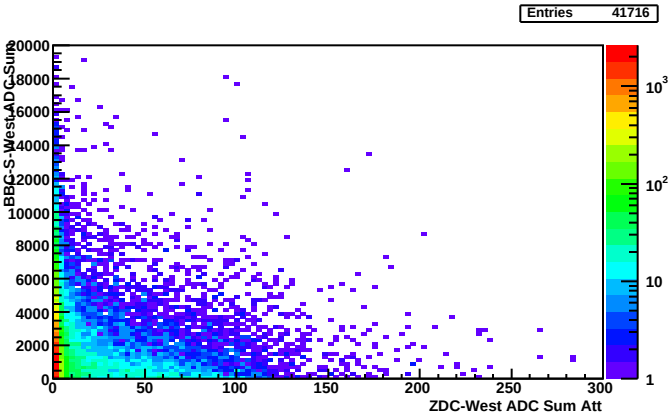

Entries 41716
BBC-L-East ADC Sum
BBC-S-East ADC Sum

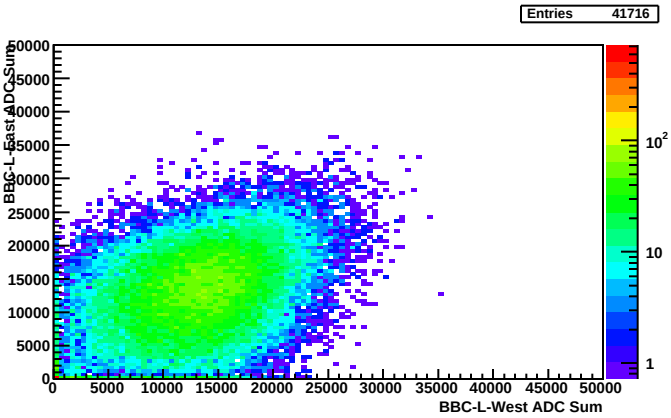

Entries 41716
BBC-L-West ADC Sum
BBC-S-West ADC Sum

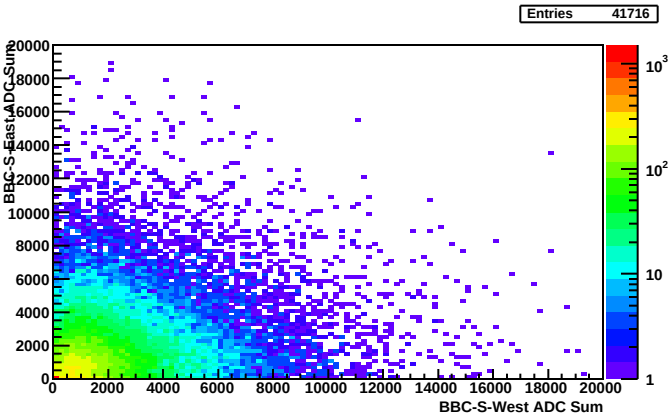

Entries 41716
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

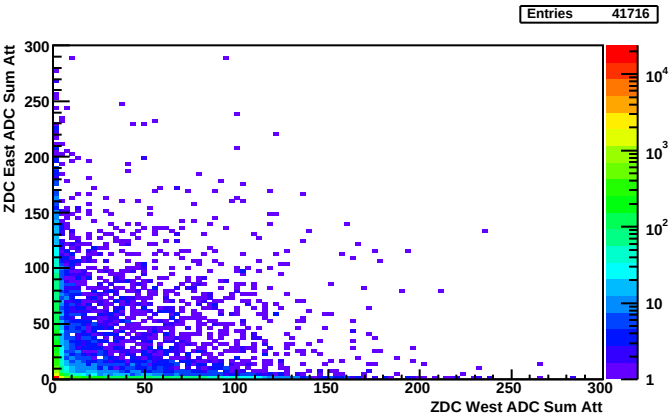

Entries 41716
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

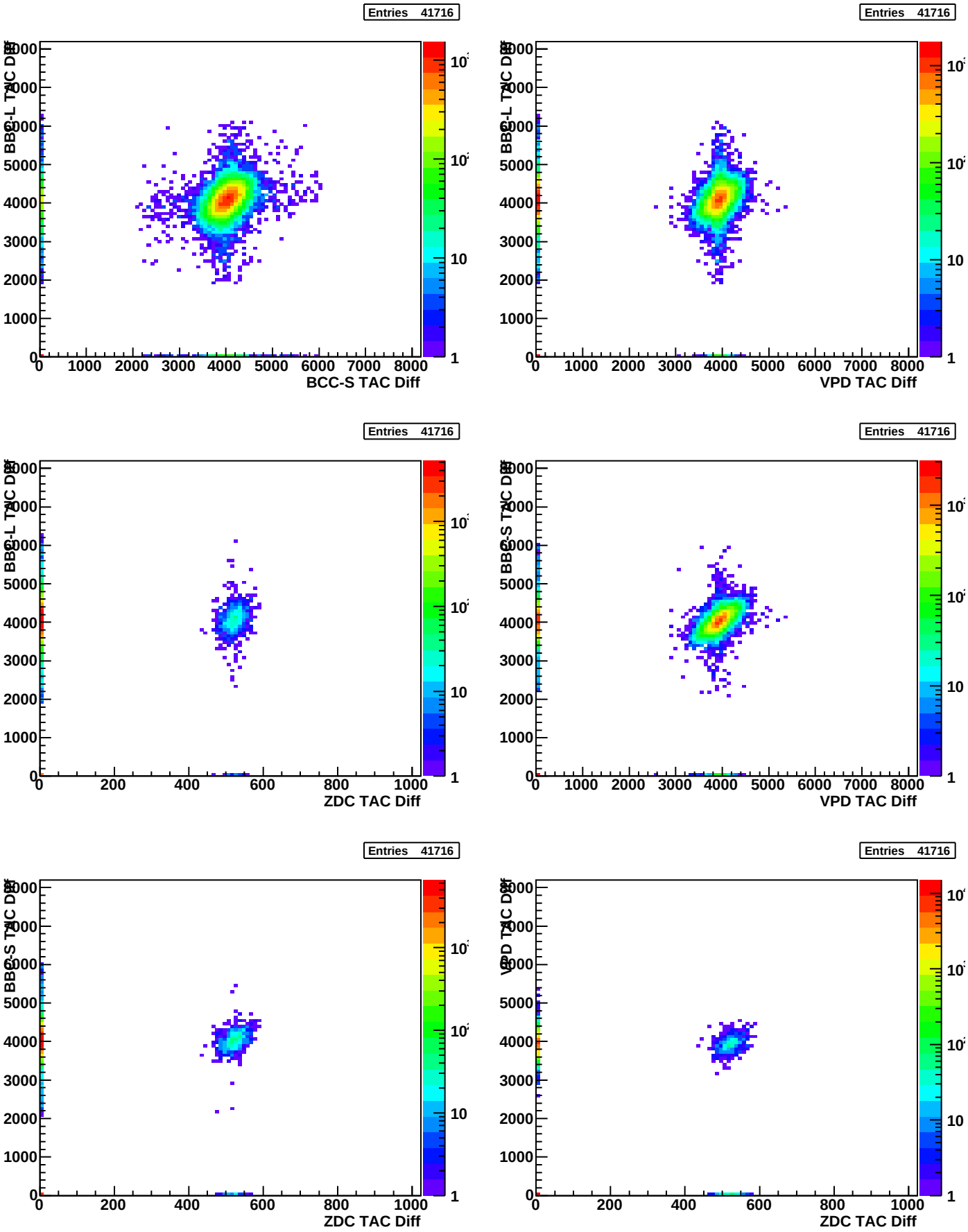

Entries 41716
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

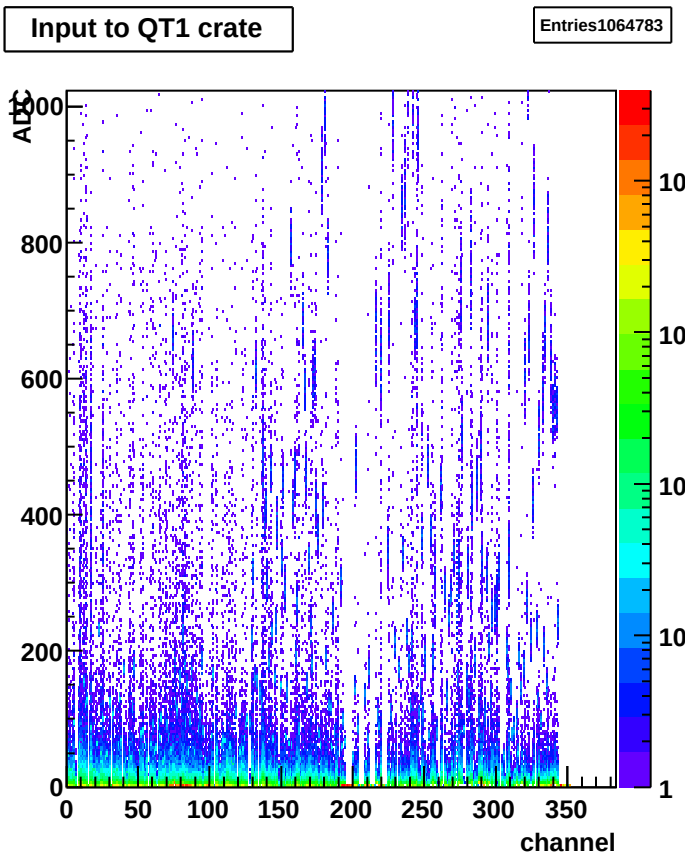

Entries 41716
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

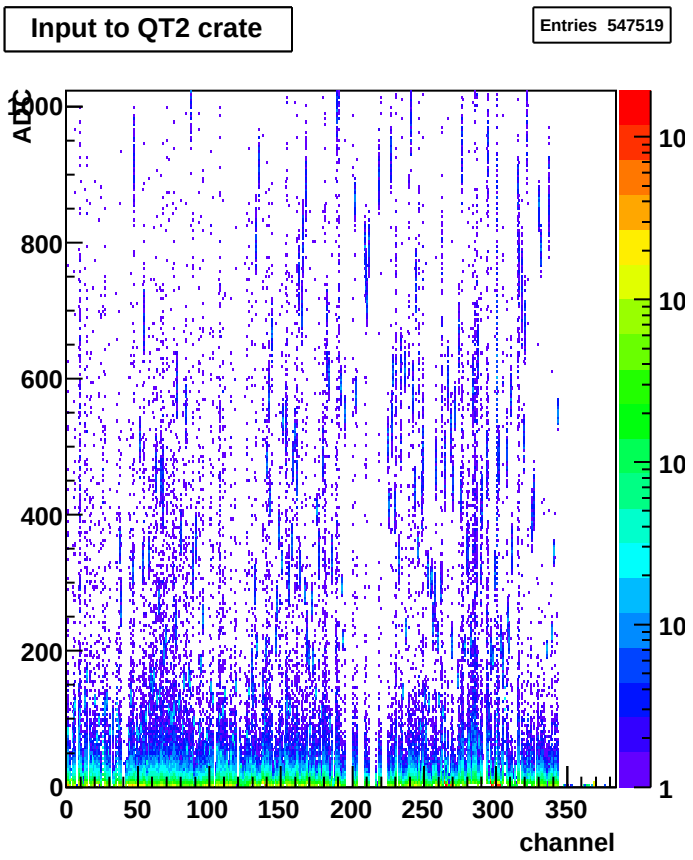

Entries 41716
BBC-L-East ADC Sum
BBC-L-West ADC Sum

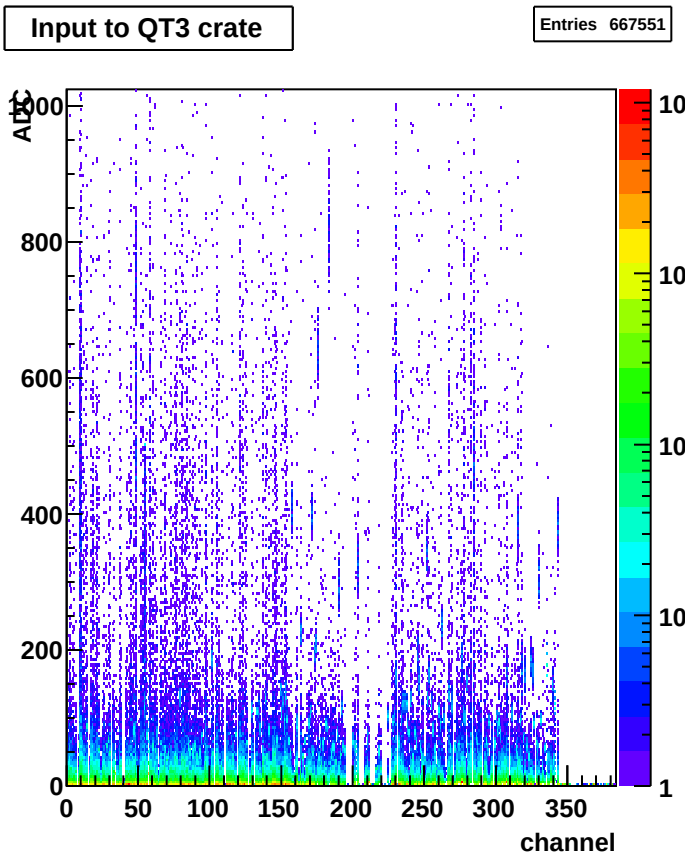

Entries 41716
BBC-S-East ADC Sum
BBC-S-West ADC Sum

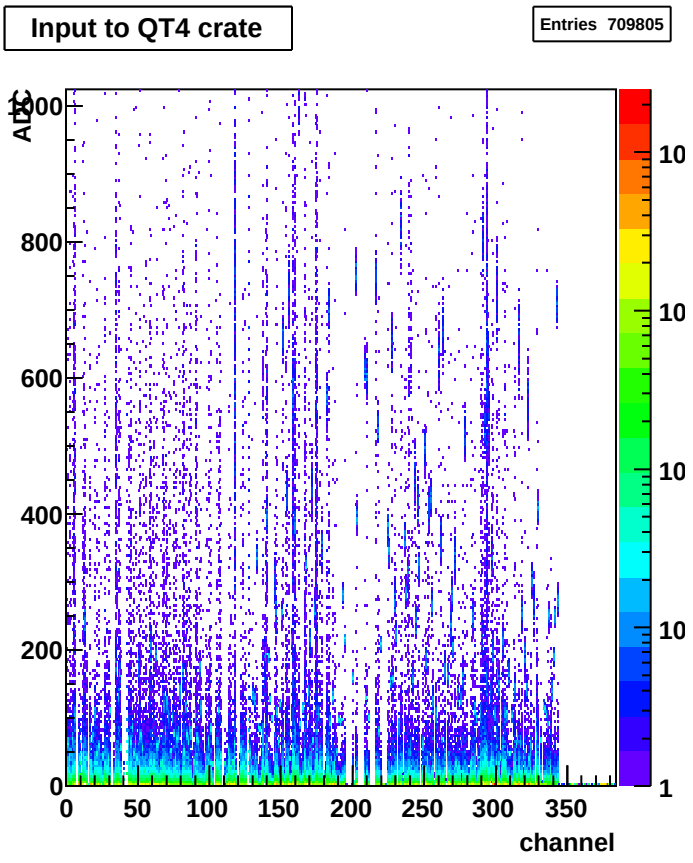

Entries 41716
ZDC East ADC Sum Att
ZDC West ADC Sum Att

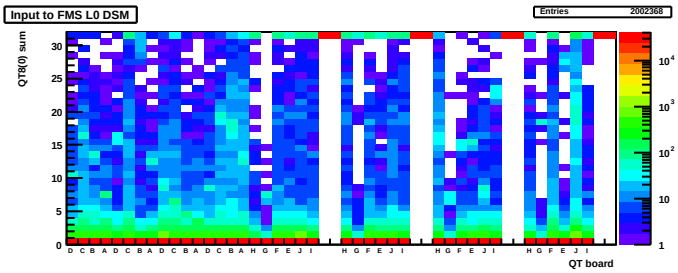

Entries 41716
BBC-L TAC Diff
BCC-S TAC Diff
Entries 41716
BBC-L TAC Diff
VPD TAC Diff
Entries 41716
BBC-L TAC Diff
ZDC TAC Diff
Entries 41716
BBC-S TAC Diff
VPD TAC Diff
Entries 41716
BBC-S TAC Diff
ZDC TAC Diff
Entries 41716
VPD TAC Diff
ZDC TAC Diff

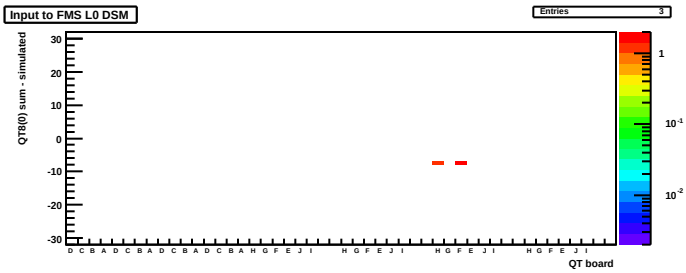

Input to QT1 crate
Entries1064783
ADC
channel

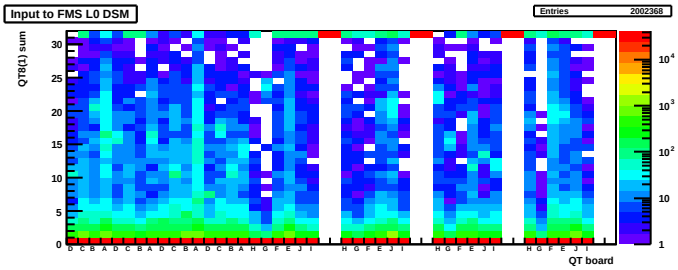

Input to QT2 crate
Entries 547519
ADC
channel

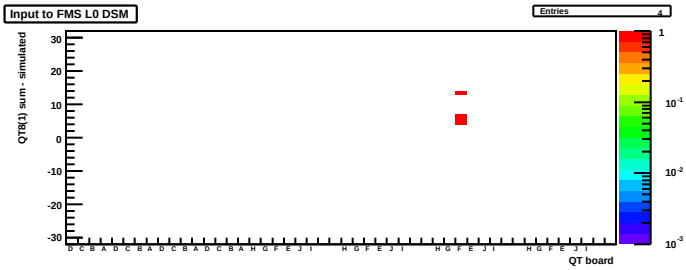

Input to QT3 crate
Entries 667551
ADC
channel

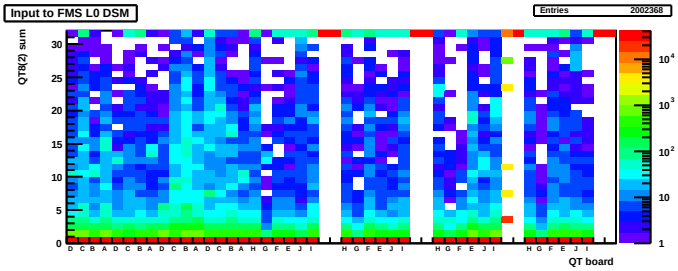

Input to QT4 crate
Entries 709805
ADC
channel

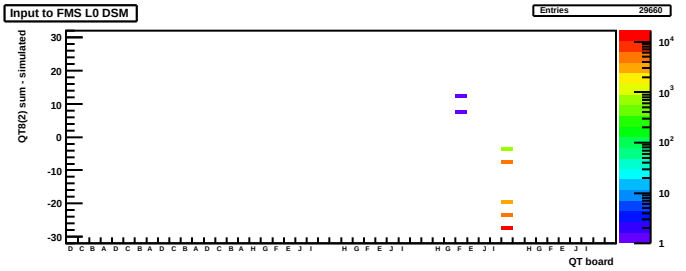

Input to FMS L0 DSM
Entries 2002368
QT8(0) sum
QT board

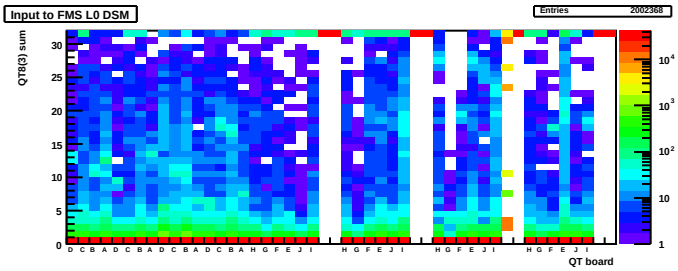

Input to FMS L0 DSM
Entries 3
QT8(0) sum - simulated
QT board

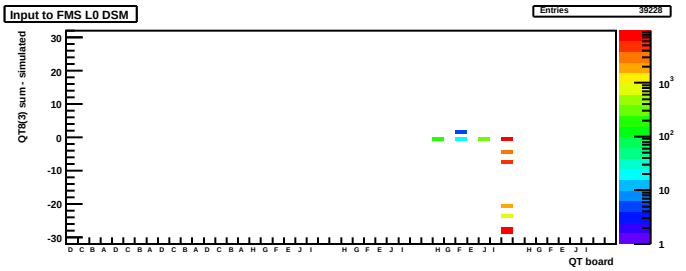

Input to FMS L0 DSM
Entries 2002368
QT8(1) sum
QT board

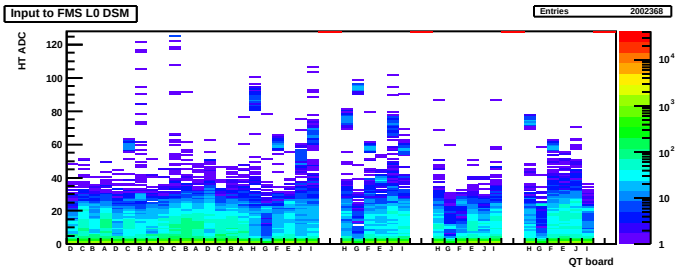

Input to FMS L0 DSM
Entries 4
QT8(1) sum - simulated
QT board

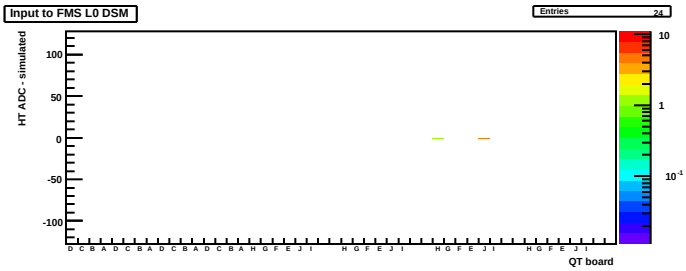

Input to FMS L0 DSM
Entries 2002368
QT8(2) sum
QT board

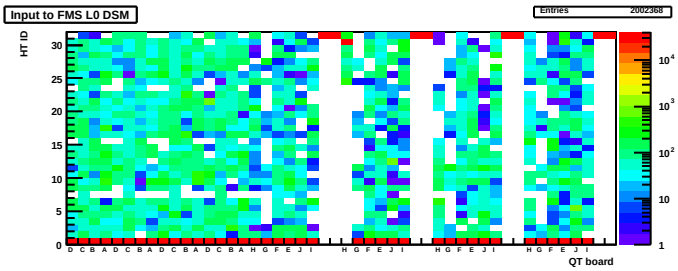

Input to FMS L0 DSM
Entries 29660
QT8(2) sum - simulated
QT board

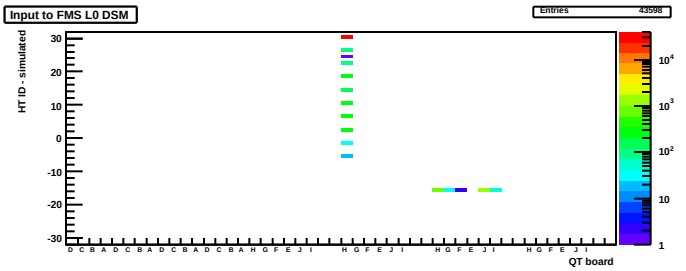

Input to FMS L0 DSM
Entries 2002368
QT8(3) sum
QT board

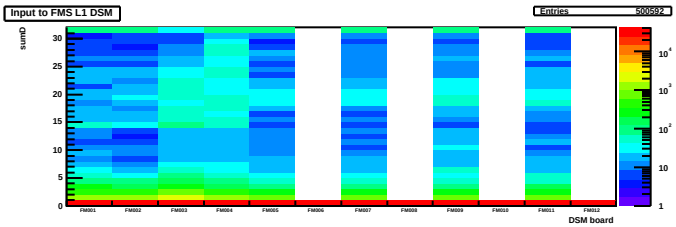

Input to FMS L0 DSM
Entries 39228
QT8(3) sum - simulated
QT board

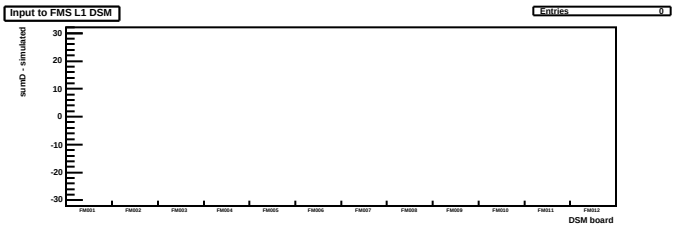

Input to FMS L0 DSM
Entries 2002368
HT ADC
QT board

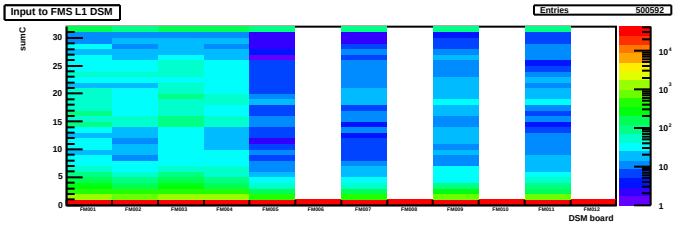

Input to FMS L0 DSM
Entries 24
HT ADC - simulated
QT board

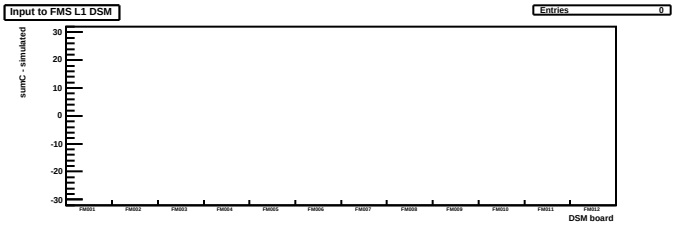

Input to FMS L0 DSM
Entries 2002368
HT ID
QT board

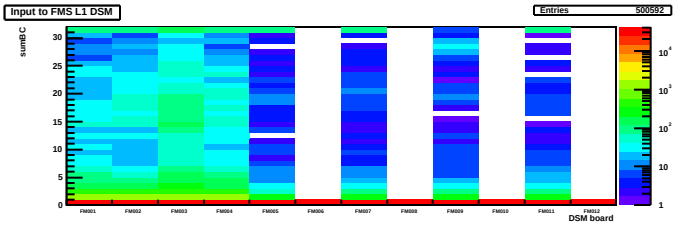

Input to FMS L0 DSM
Entries 43598
HT ID - simulated
QT board

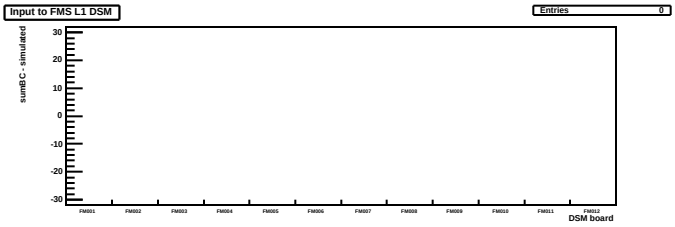

Input to FMS L1 DSM
Entries 500592
sumD
DSM board

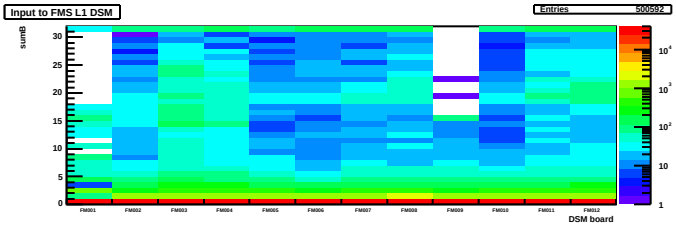

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

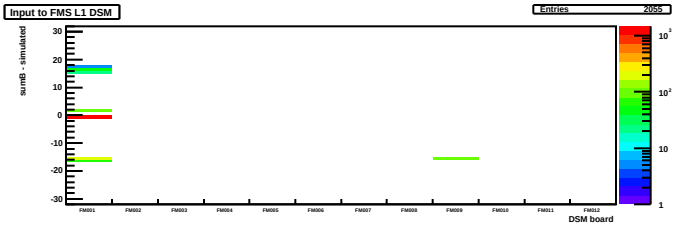

Input to FMS L1 DSM
Entries 500592
sumC
DSM board

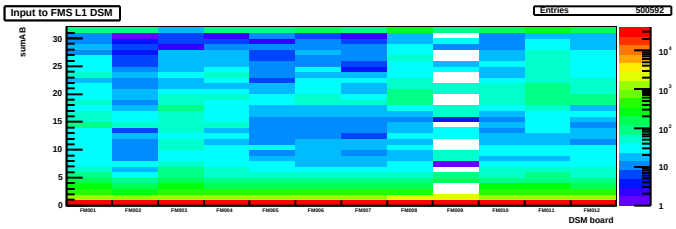

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

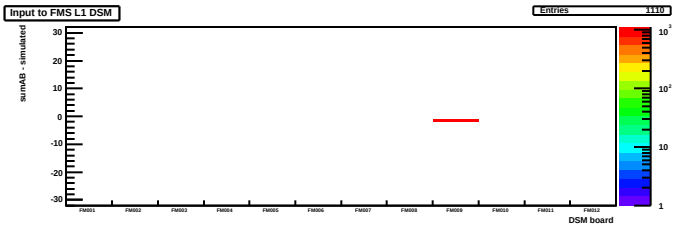

Input to FMS L1 DSM
Entries 500592
sumBC
DSM board

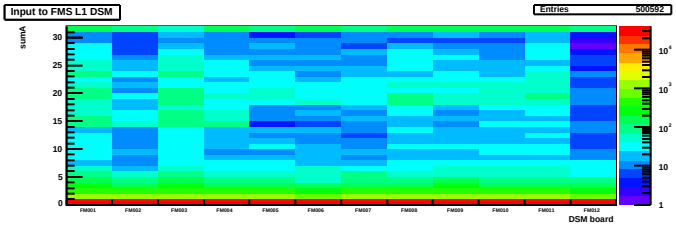

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

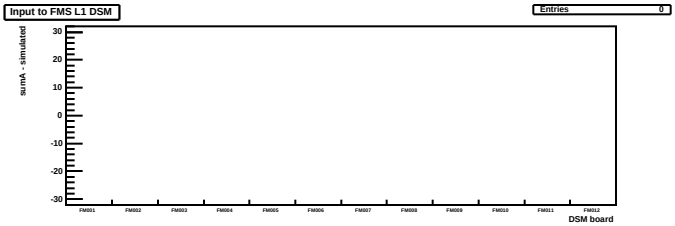

Input to FMS L1 DSM
Entries 500592
sumB
DSM board

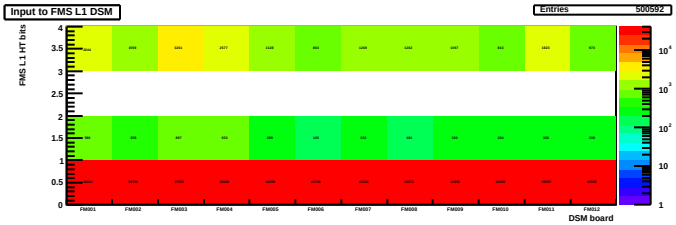

Input to FMS L1 DSM
Entries 2055
sumB - simulated
DSM board

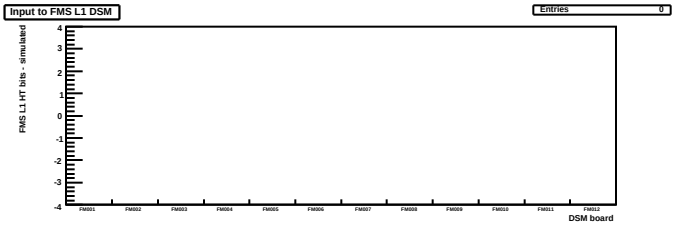

Input to FMS L1 DSM
Entries 500592
sumAB
DSM board

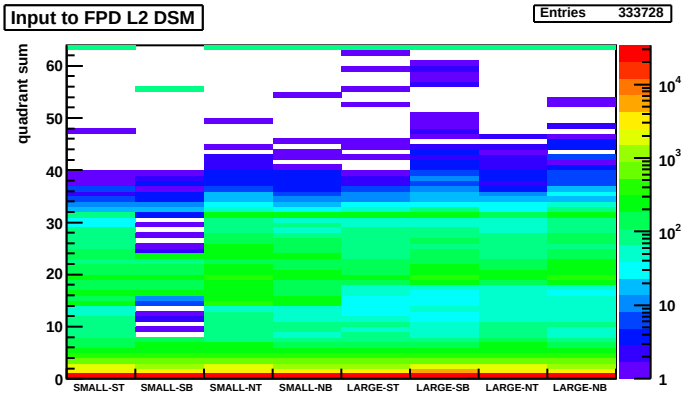

Input to FMS L1 DSM
Entries 1110
sumAB - simulated
DSM board

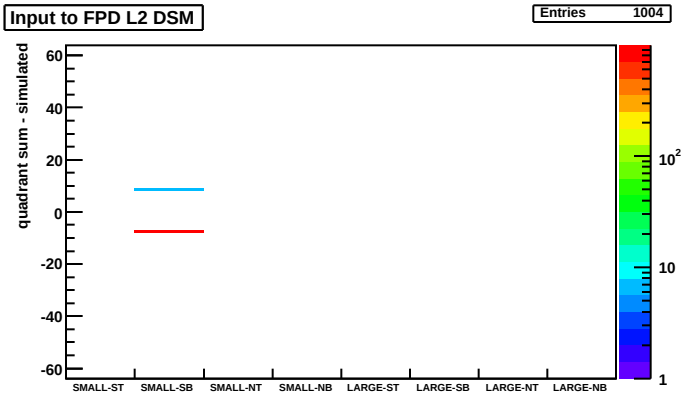

Input to FMS L1 DSM
Entries 500592
sumA
DSM board

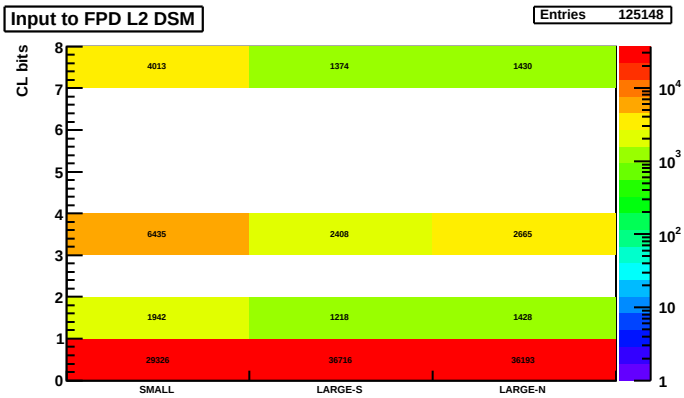

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

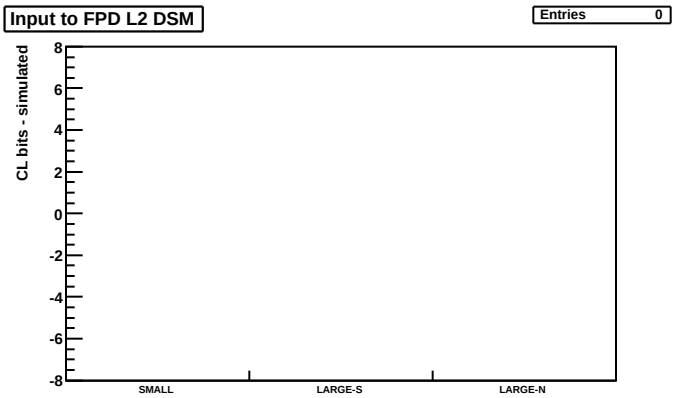

Input to FMS L1 DSM
Entries 500592
FMS L1 HT bits
DSM board

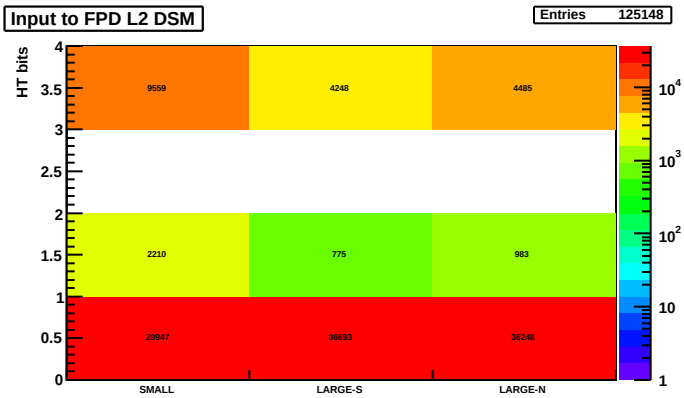

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

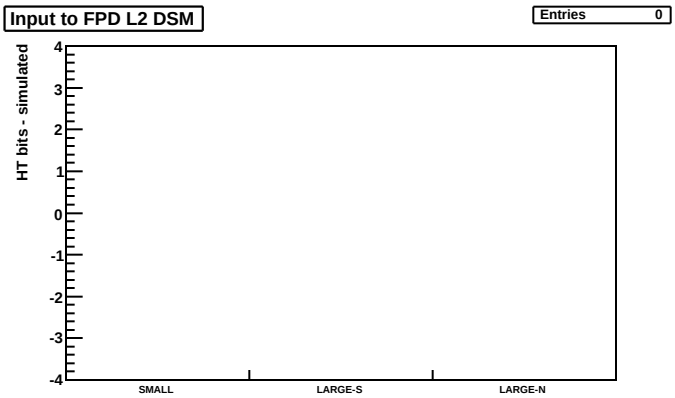

Input to FPD L2 DSM
Entries 333728
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

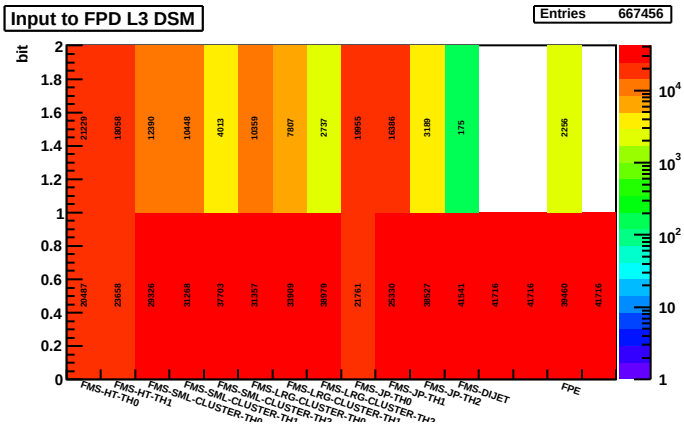

Input to FPD L2 DSM
Entries 1004
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

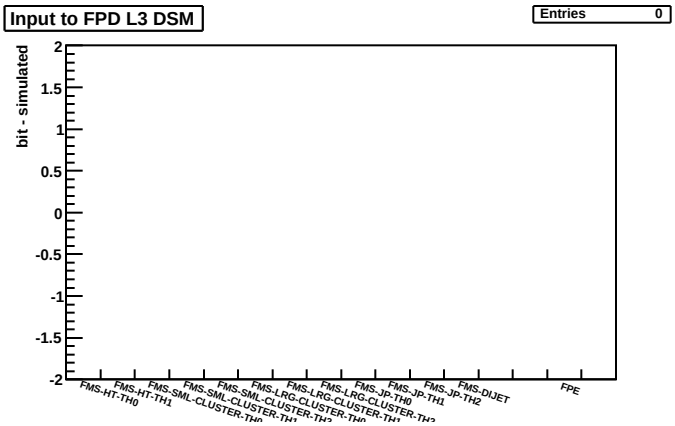

Input to FPD L2 DSM
Entries 125148
CL bits
4013 1374 1430
6435 2408 2665
1942 1218 1428
29326 36716 36193
SMALL LARGE-S LARGE-N

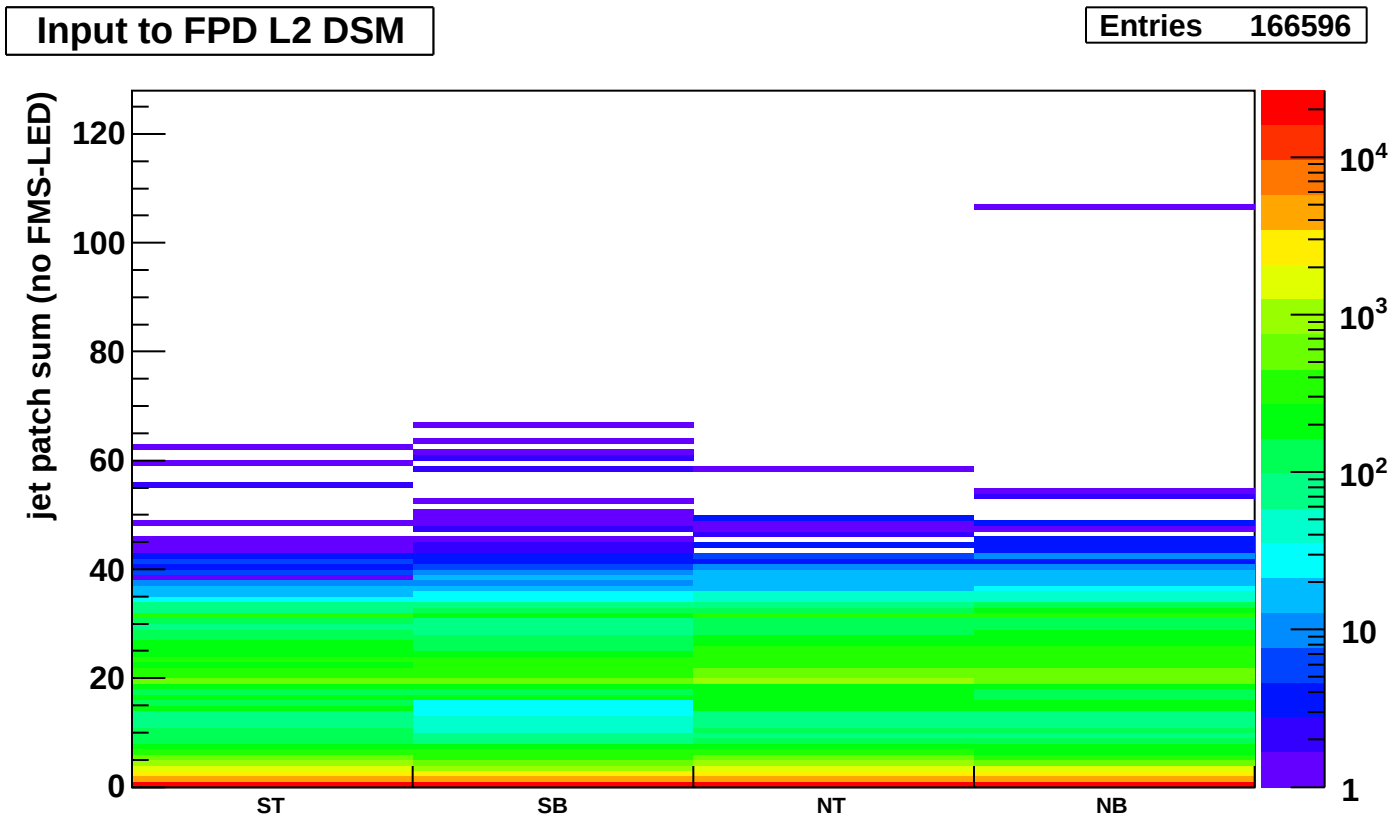

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL LARGE-S LARGE-N

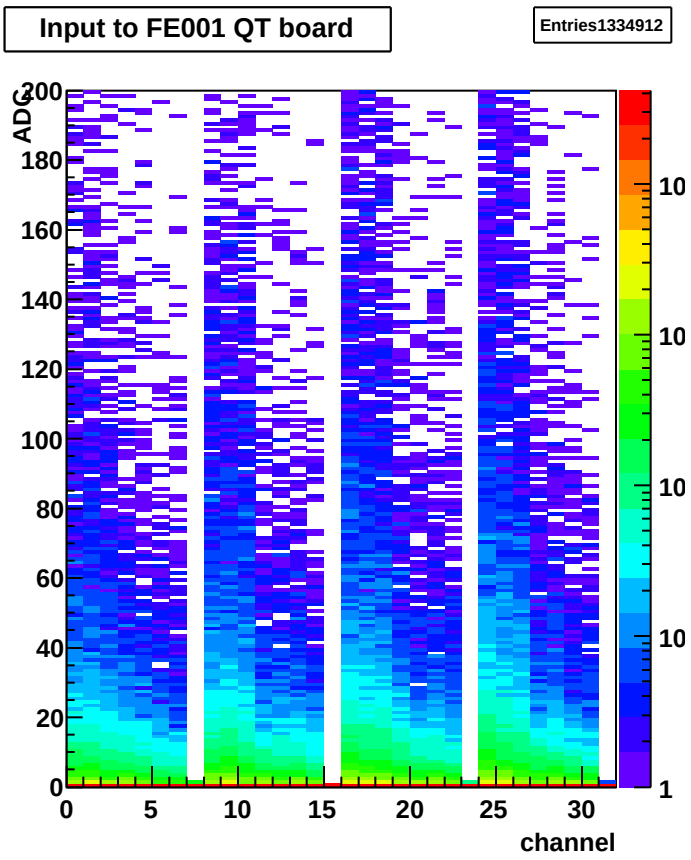

Input to FPD L2 DSM
Entries 125148
HT bits
9559 4248 4485
2210 775 983
29947 36693 36248
SMALL LARGE-S LARGE-N

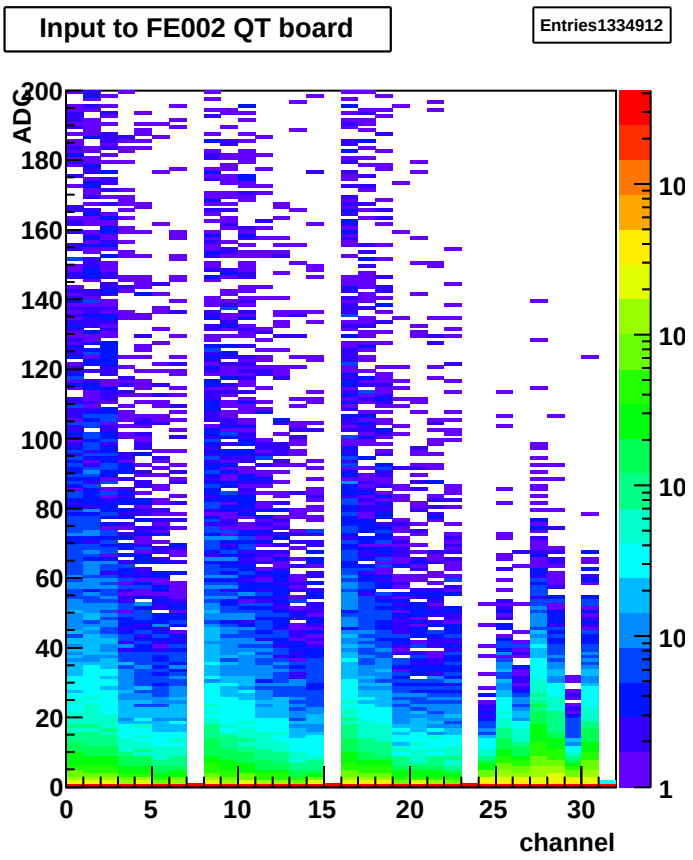

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

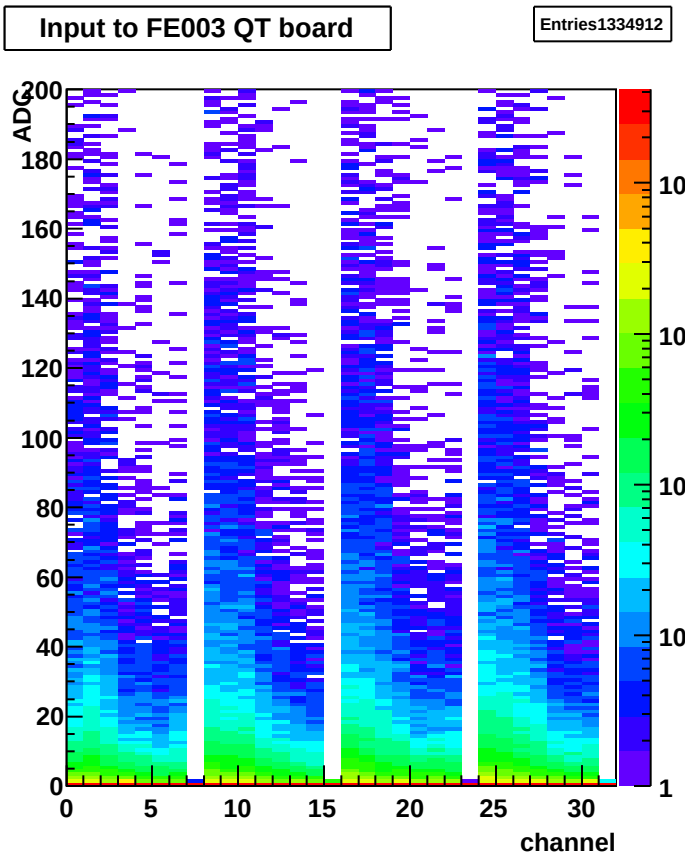

Input to FPD L3 DSM
Entries 667456
bit
22229 18958 12590 10448 4013 10359 7807 2737 10955 16386 3189 175 2256
29487 22658 29326 31258 37703 31357 39909 38979 21761 25330 38527 41541 41716 41716 41716 39450 41716
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

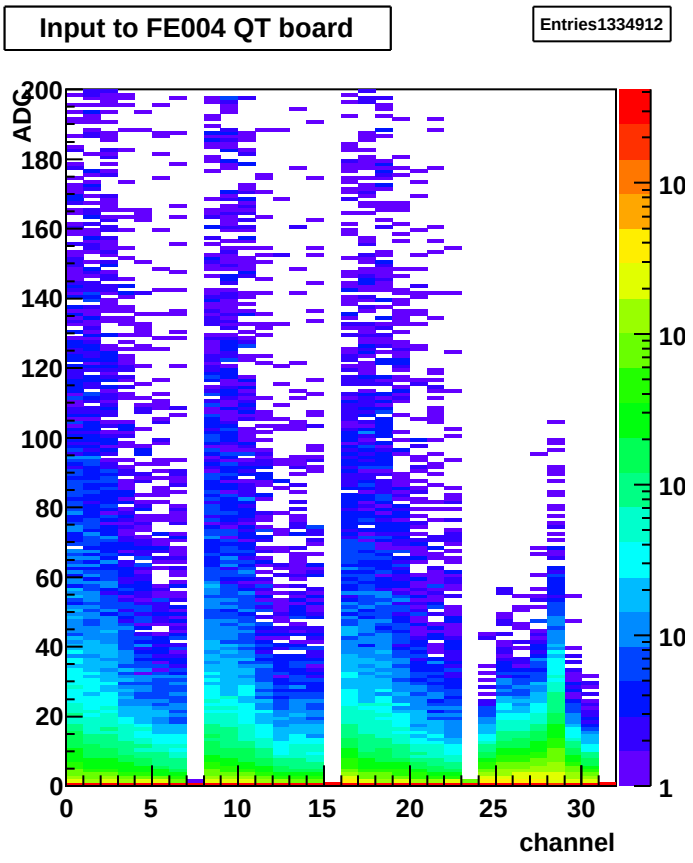

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

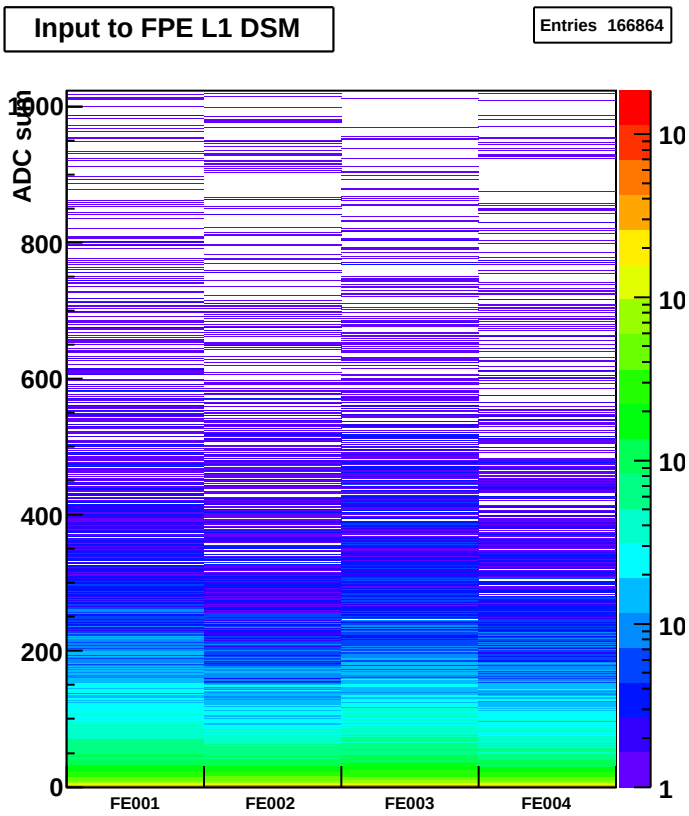

Input to FPD L2 DSM
Entries 166596
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

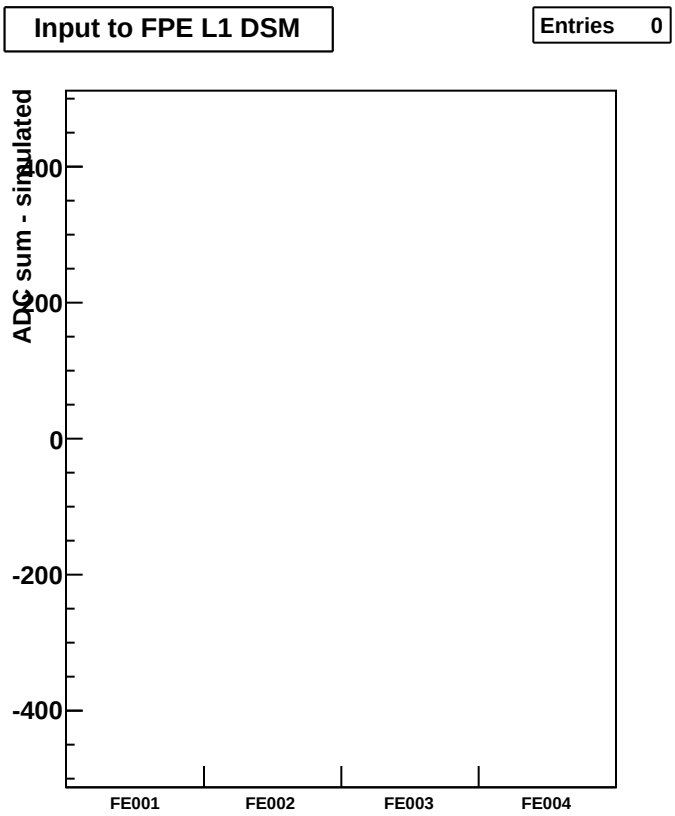

Input to FE001 QT board
Entries1334912
ADC
channel

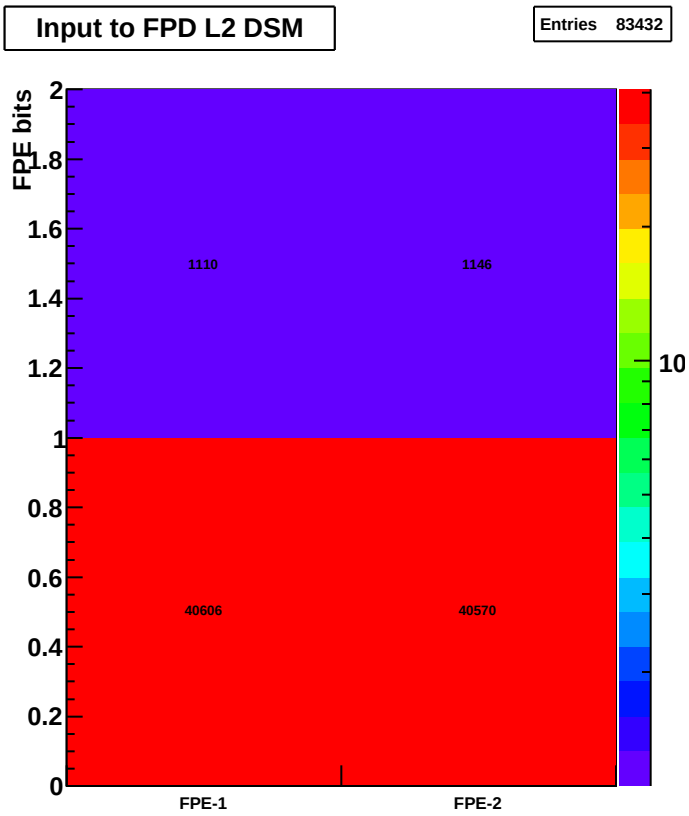

Input to FE002 QT board
Entries1334912
ADC
channel

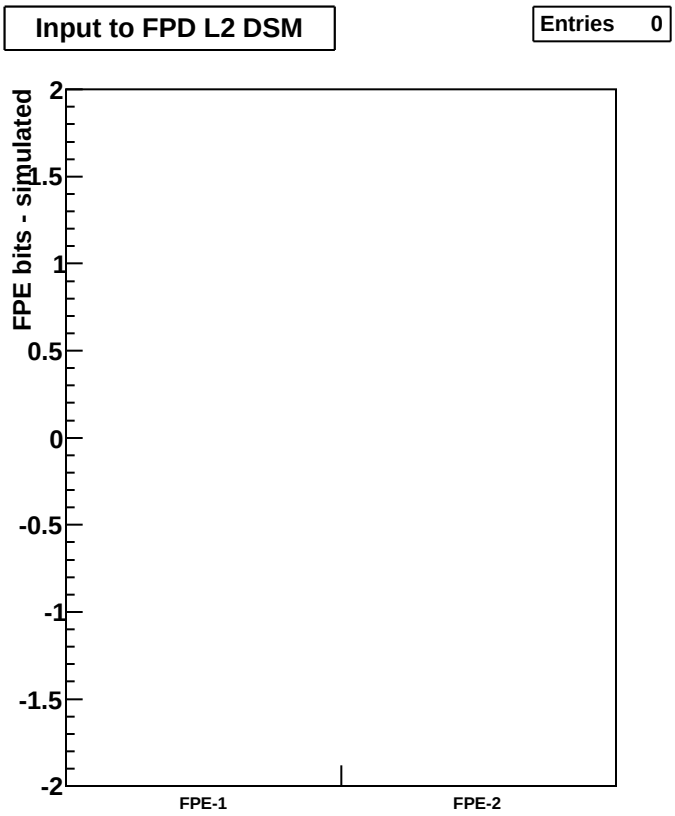

Input to FE003 QT board
Entries1334912
ADC
channel


Input to FE004 QT board
Entries1334912
ADC
channel


Input to FPE L1 DSM
Entries 166864
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 83432
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
1110
1146
40606
40570
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2