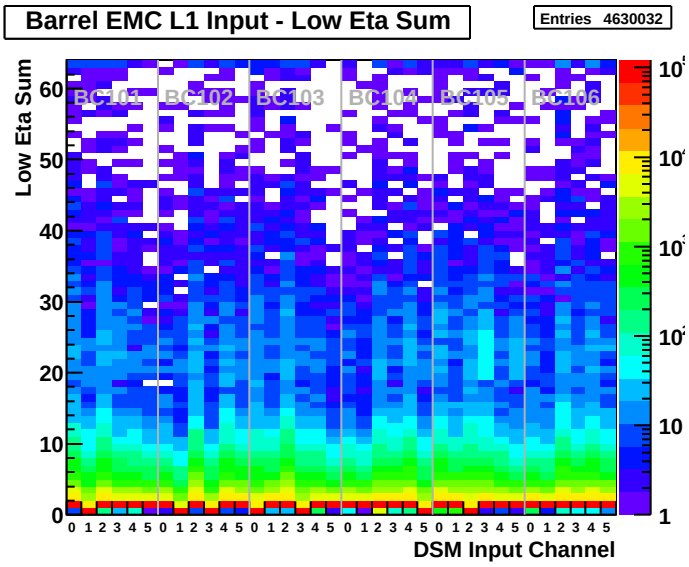
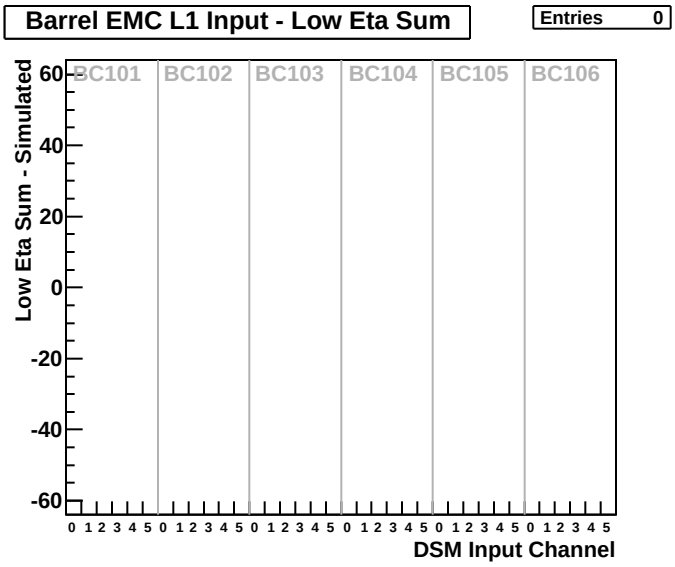
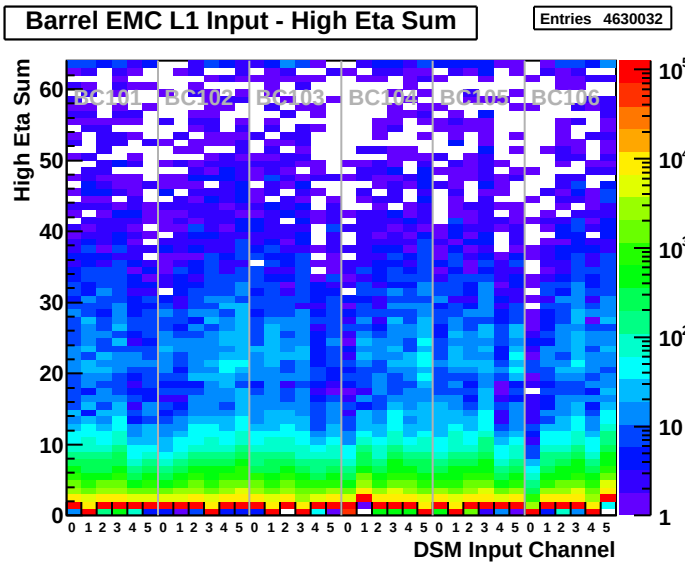
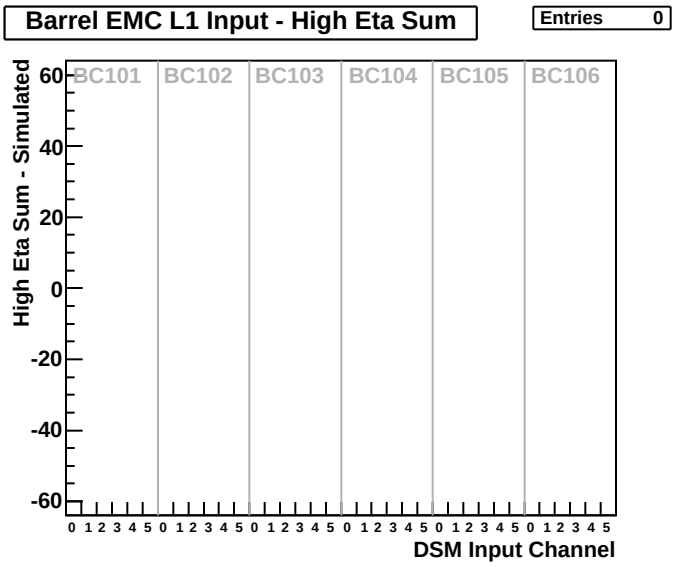
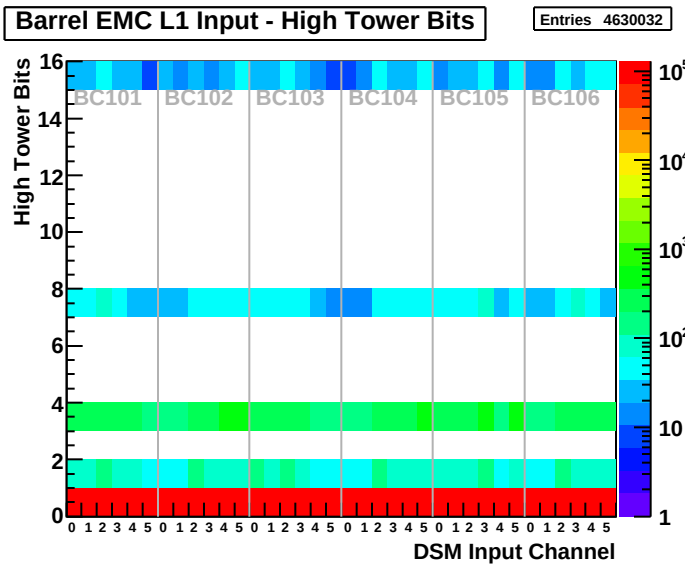
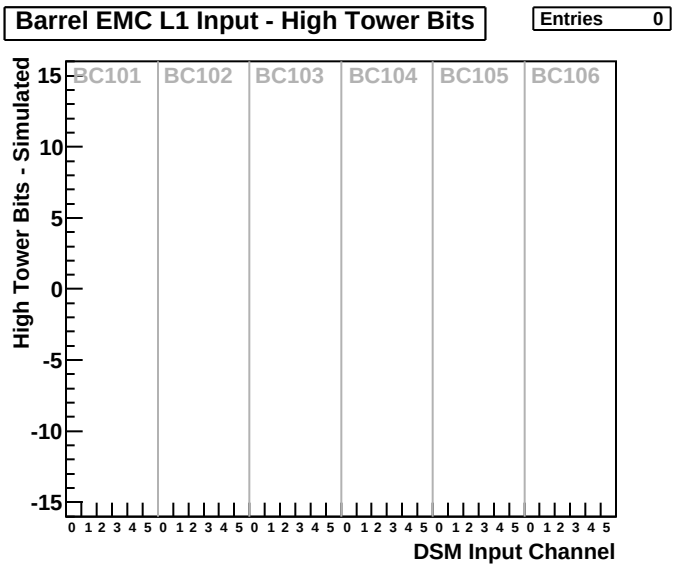

Barrel EMC L1 Input - Low Eta Sum
Entries 4630032
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

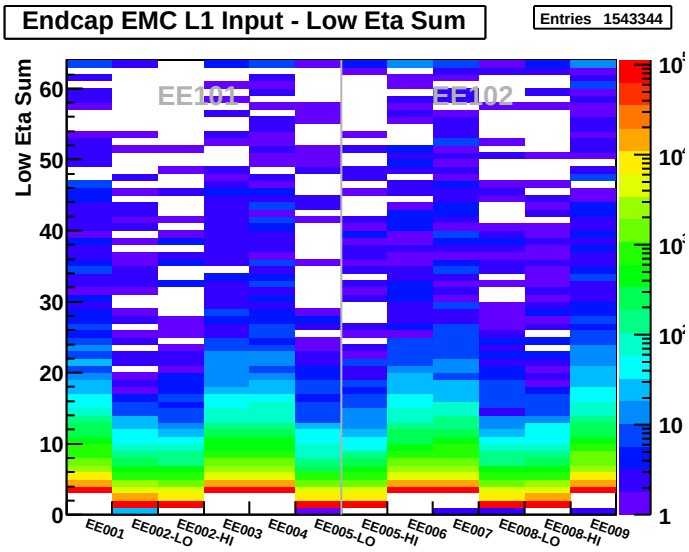

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

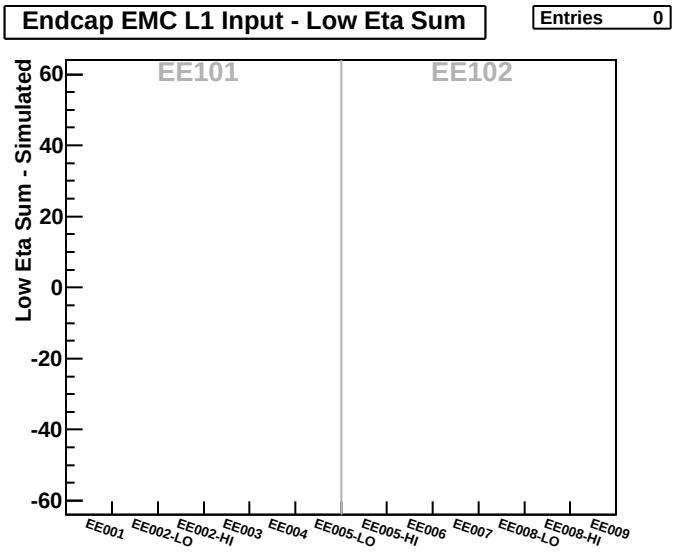

Barrel EMC L1 Input - High Eta Sum
Entries 4630032
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

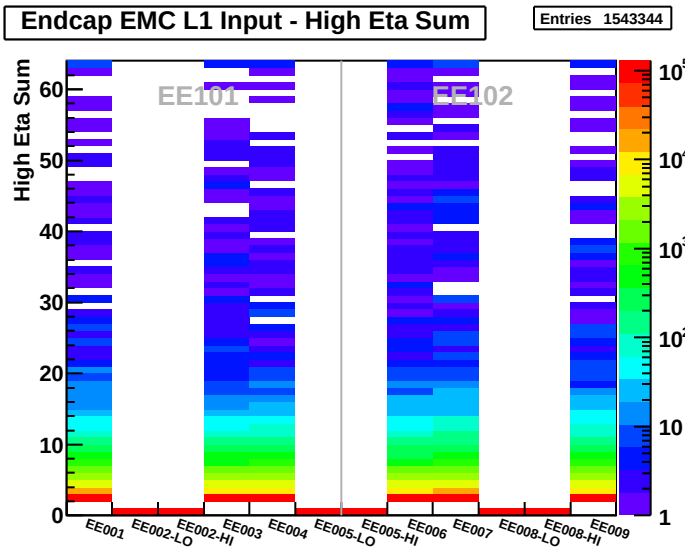

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

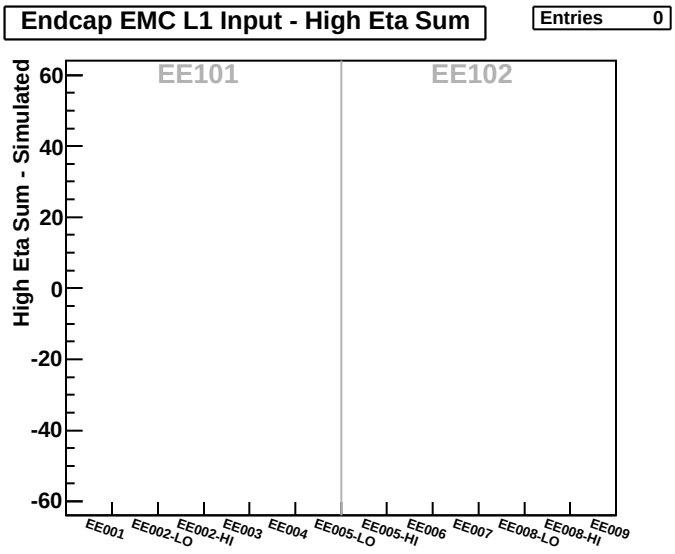

Barrel EMC L1 Input - High Tower Bits
Entries 4630032
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

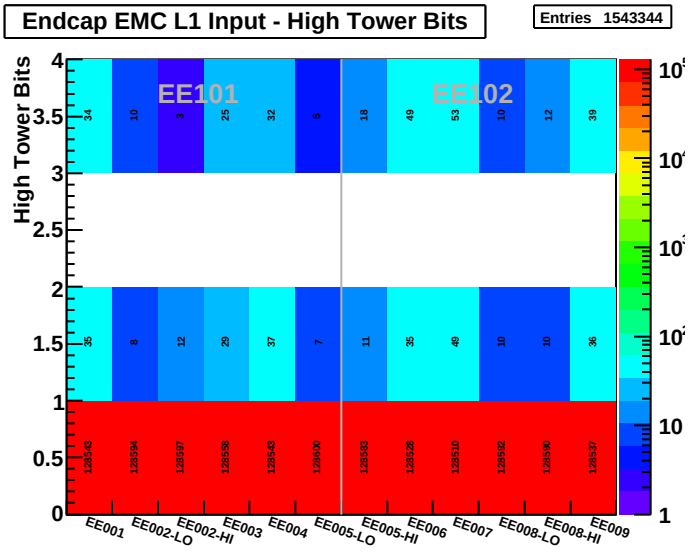

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

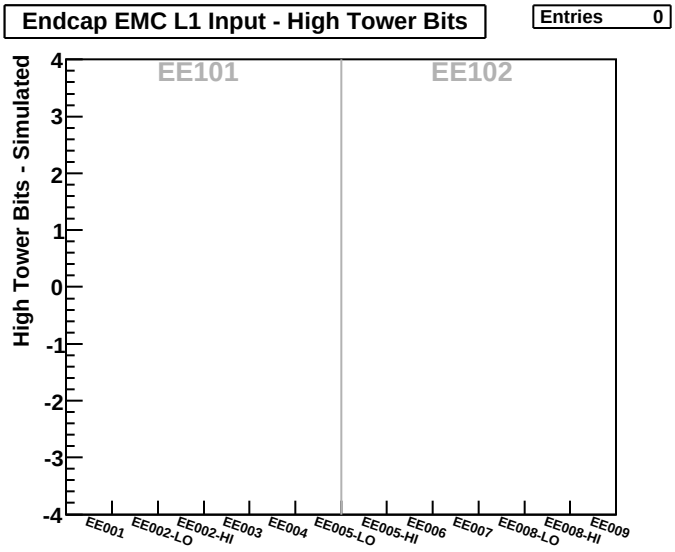

Endcap EMC L1 Input - Low Eta Sum
Entries 1543344
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

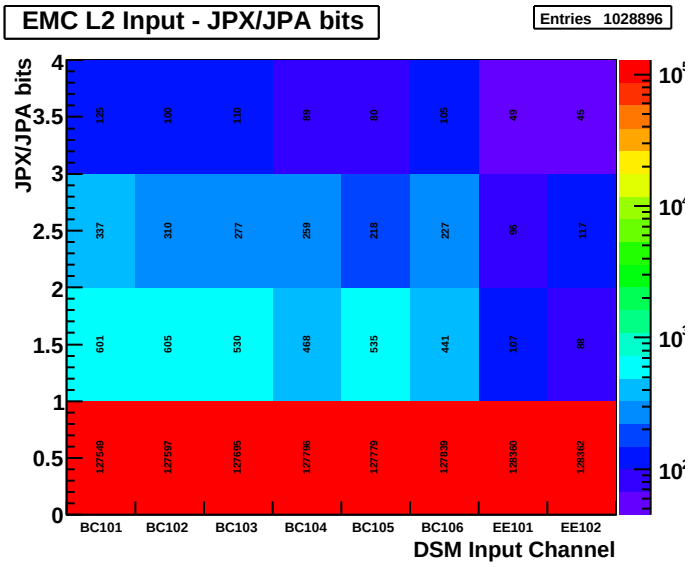

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

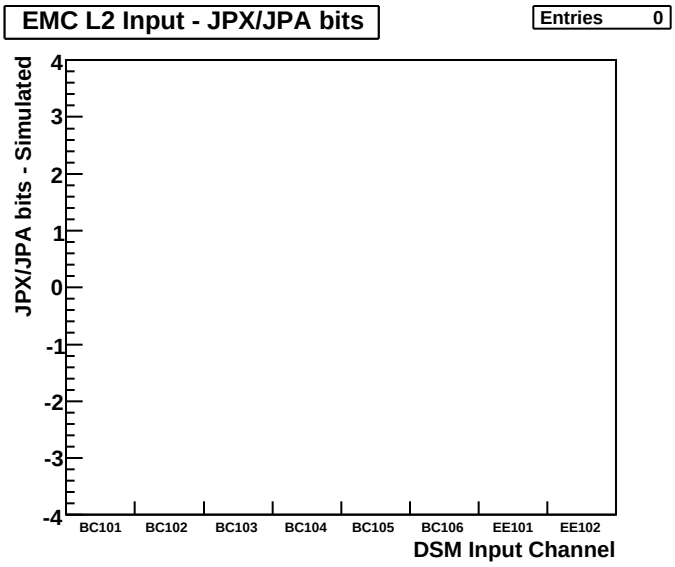

Endcap EMC L1 Input - High Eta Sum
Entries 1543344
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

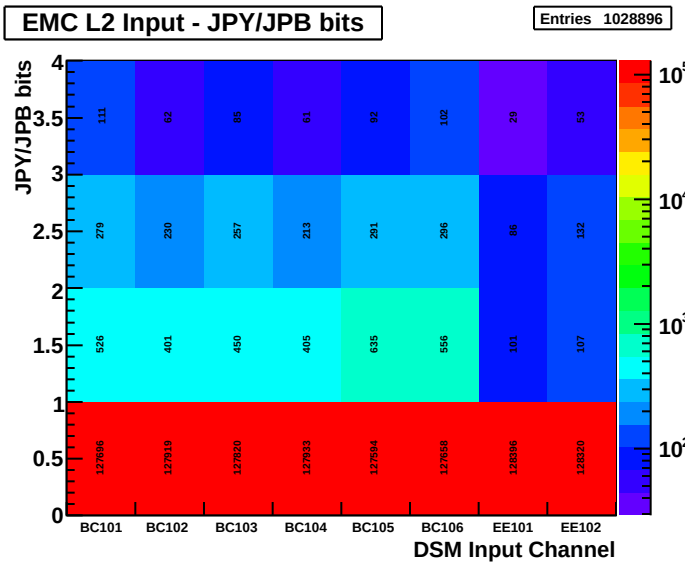

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

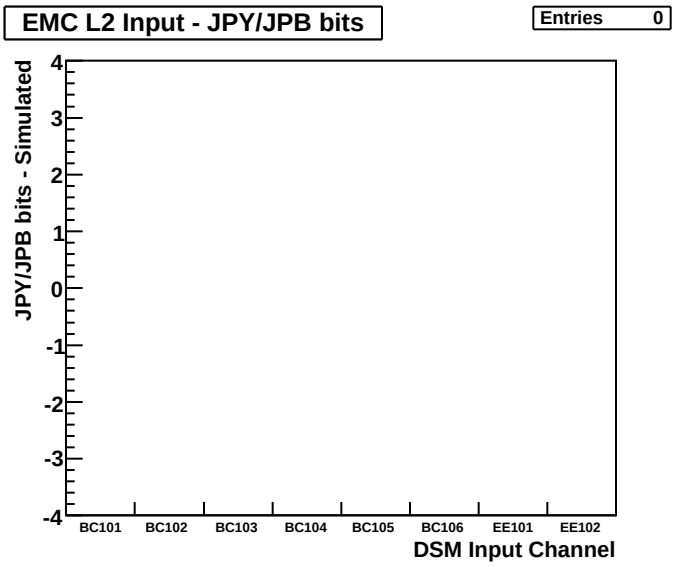

Endcap EMC L1 Input - High Tower Bits
Entries 1543344
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

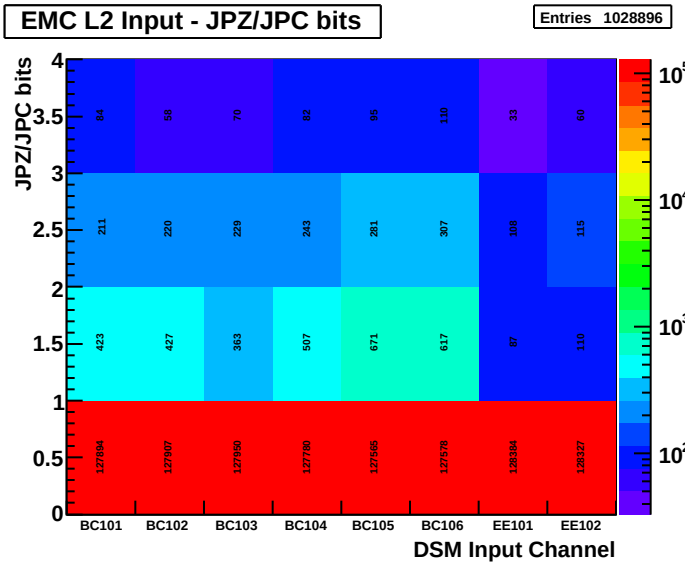

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

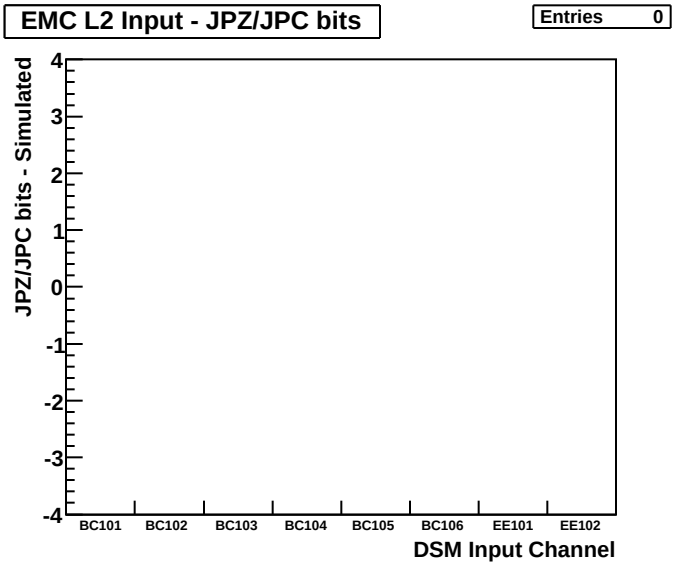

EMC L2 Input - JPX/JPA bits
Entries 1028896
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
125 100 110 89 80 105 49 45
337 310 277 259 218 227 96 117
601 605 530 468 535 441 107 88
127549 127597 127695 127796 127779 127839 128360 128362

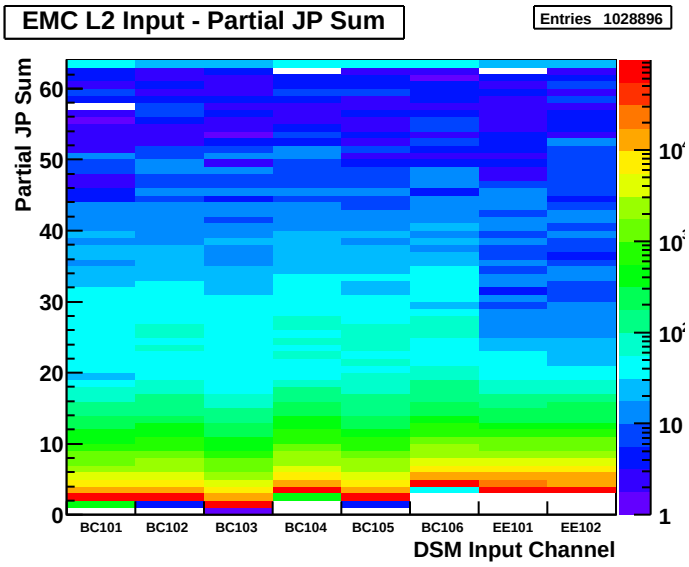

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

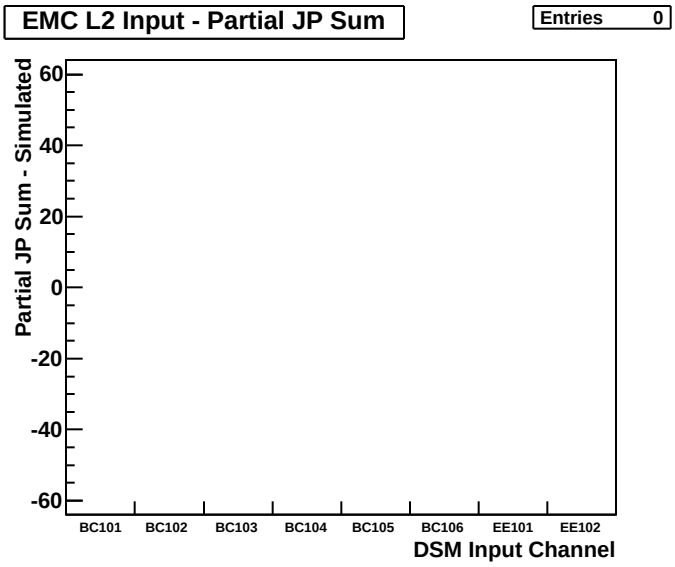

EMC L2 Input - JPY/JPB bits
Entries 1028896
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
111 62 85 61 92 102 29 53
279 230 257 213 291 296 86 132
526 401 450 405 635 556 101 107
127696 127919 127820 127933 127594 127658 128396 128320

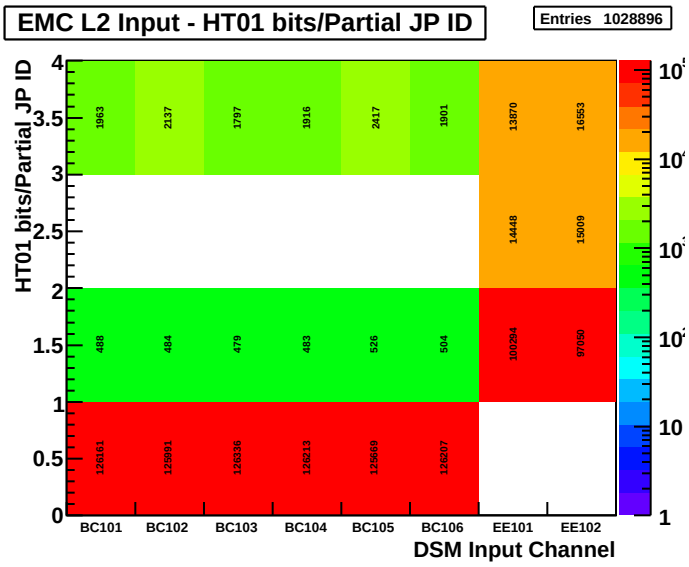

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

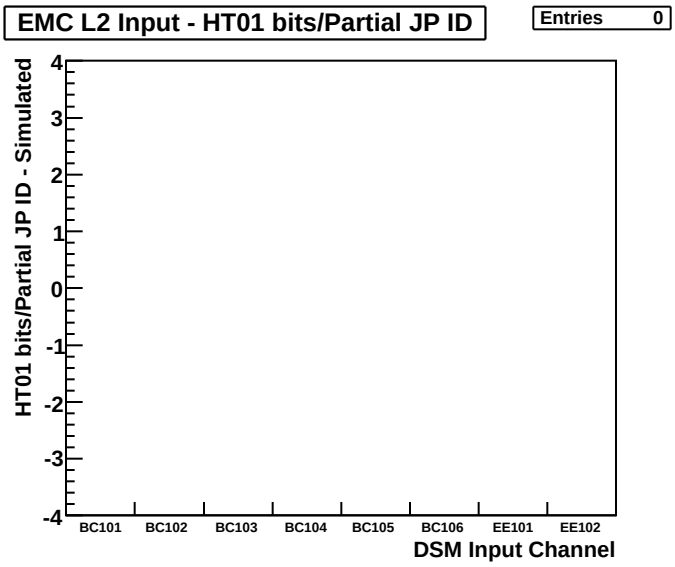

EMC L2 Input - JPZ/JPC bits
Entries 1028896
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
84 58 70 82 95 110 33 60
211 220 229 243 281 307 108 115
423 427 363 507 671 617 87 110
127894 127907 127950 127780 127565 127578 128384 128327

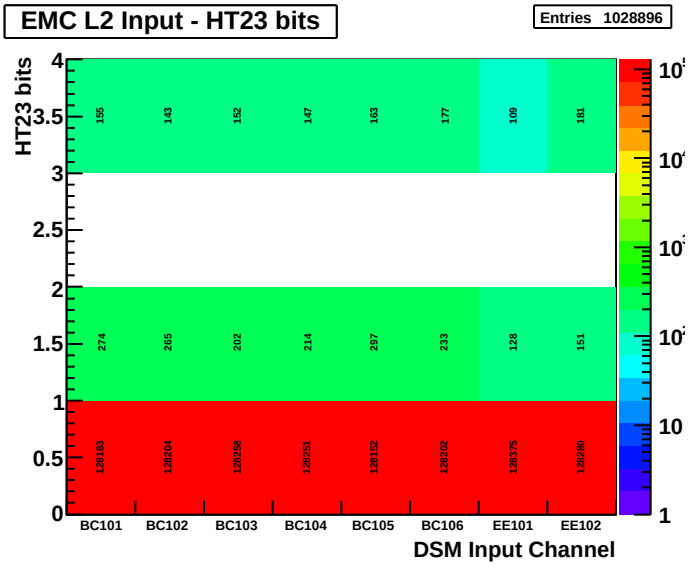

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

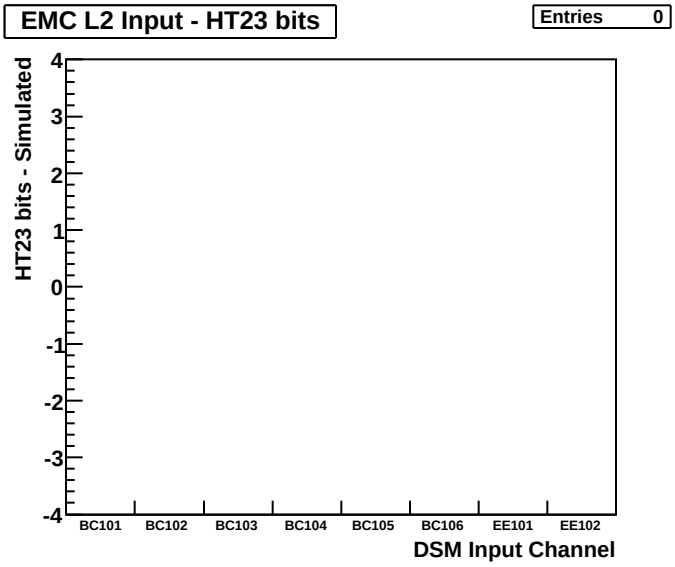

EMC L2 Input - Partial JP Sum
Entries 1028896
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

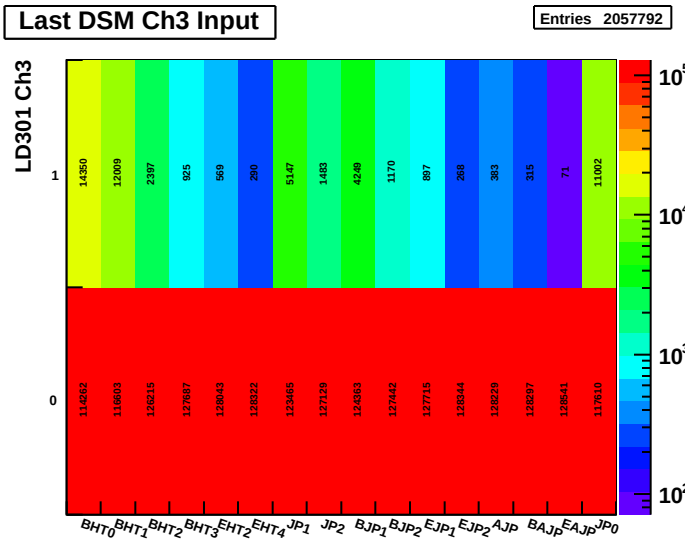

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

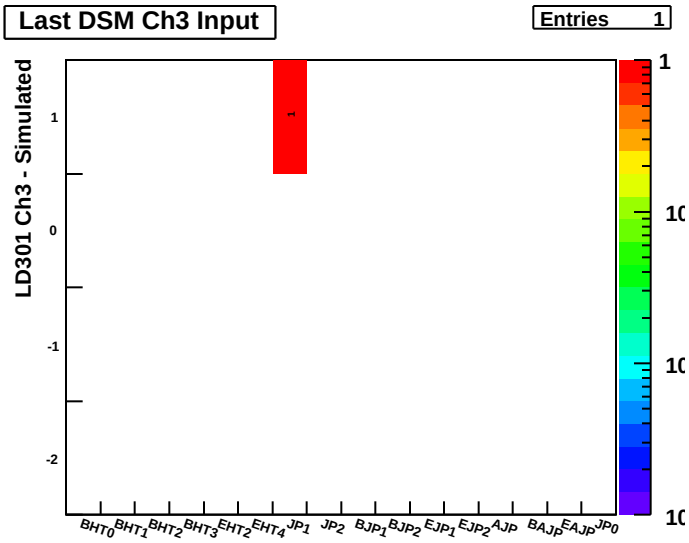

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1028896
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

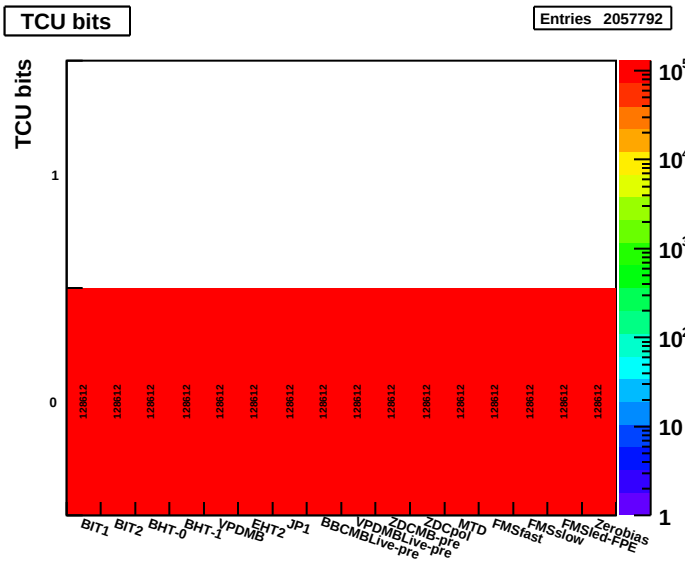

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

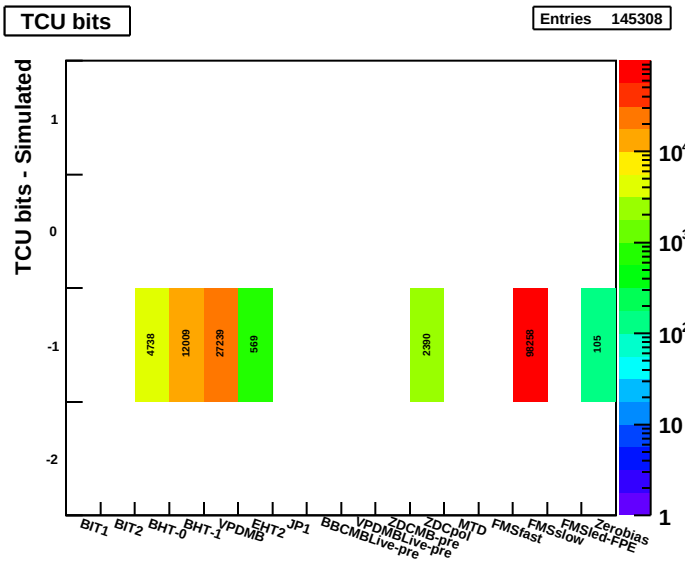

EMC L2 Input - HT23 bits
Entries 1028896
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

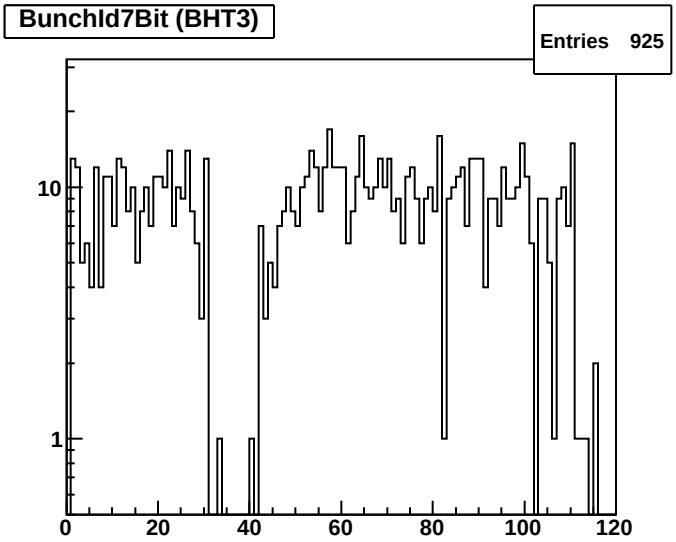

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

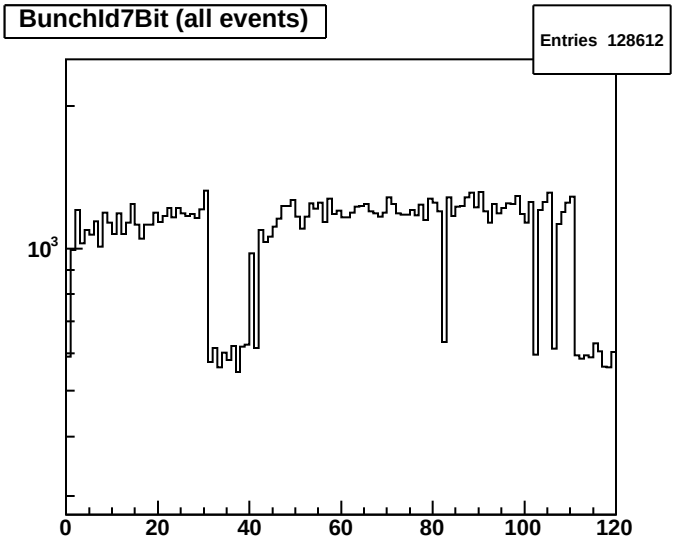

Last DSM Ch3 Input
Entries 2057792
LD301 Ch3
14350 12009 2397 925 569 290 5147 1483 4240 1170 897 268 383 315 71 11002
114262 116603 126215 127687 128043 128322 123465 127129 124363 127442 127715 128344 128229 128297 128541 117610
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

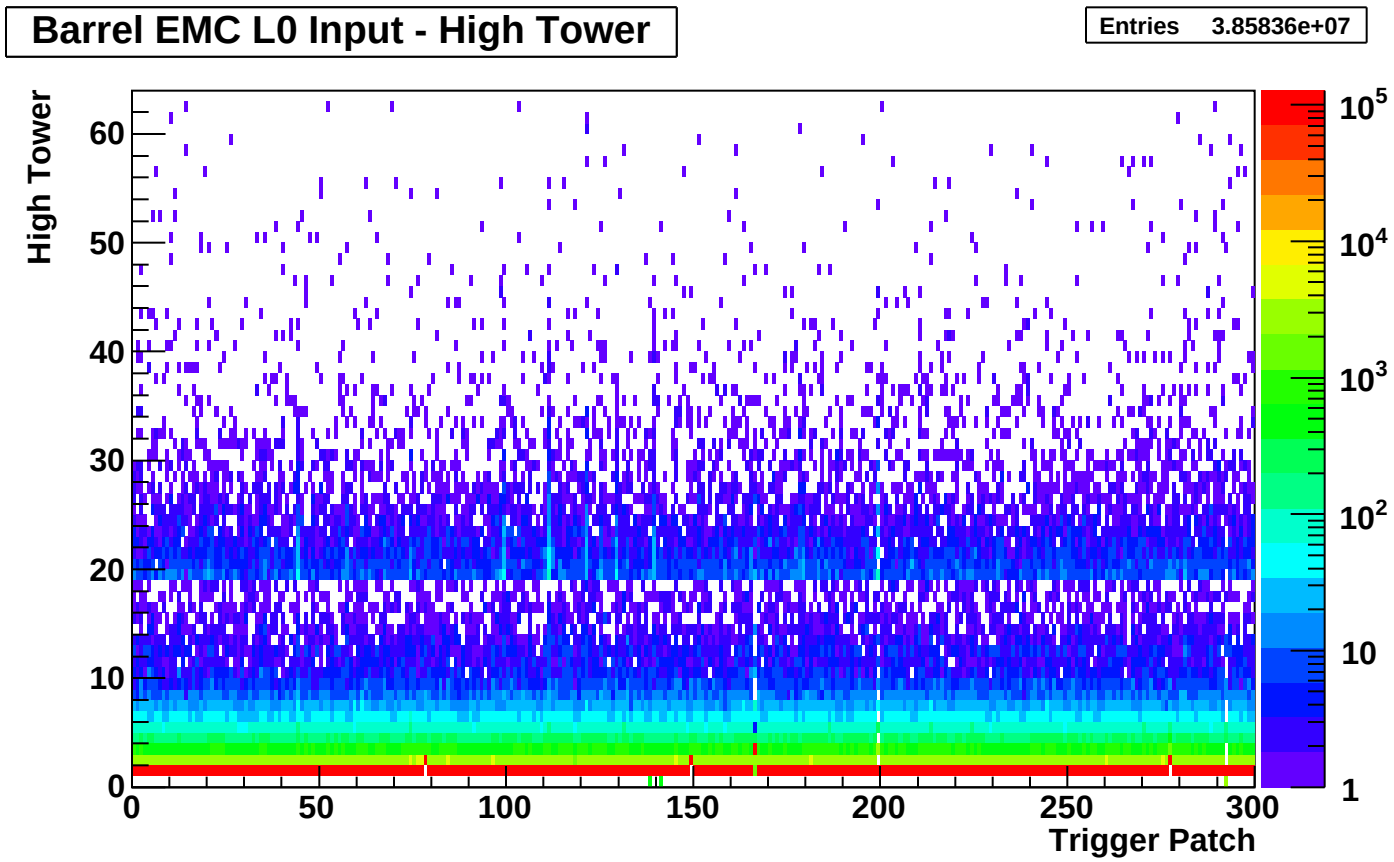

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

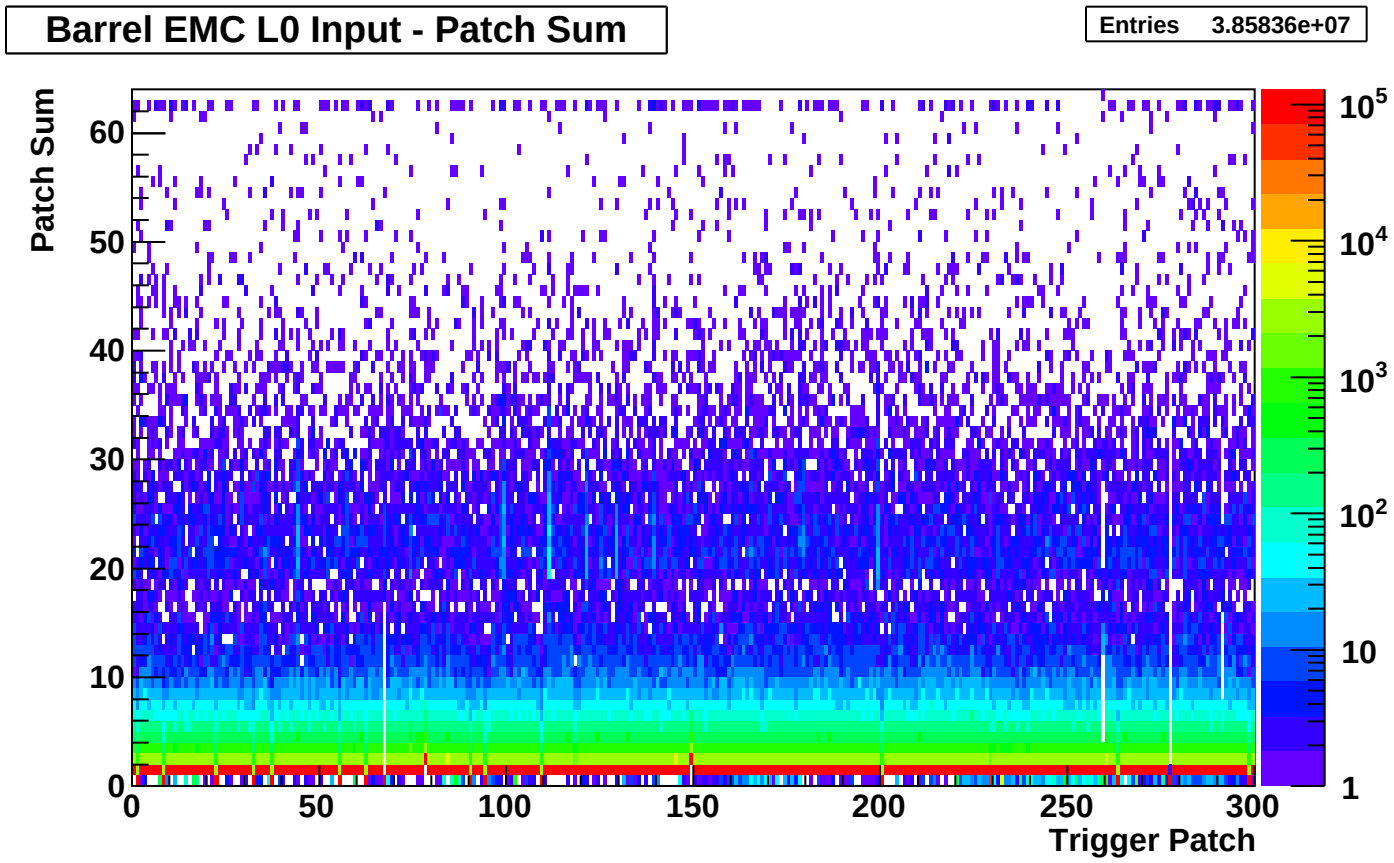

TCU bits
Entries 2057792
TCU bits
128612 128612 128612 128612 128612 128612 128612 128612 128612 128612 128612 128612 128612 128612 128612
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

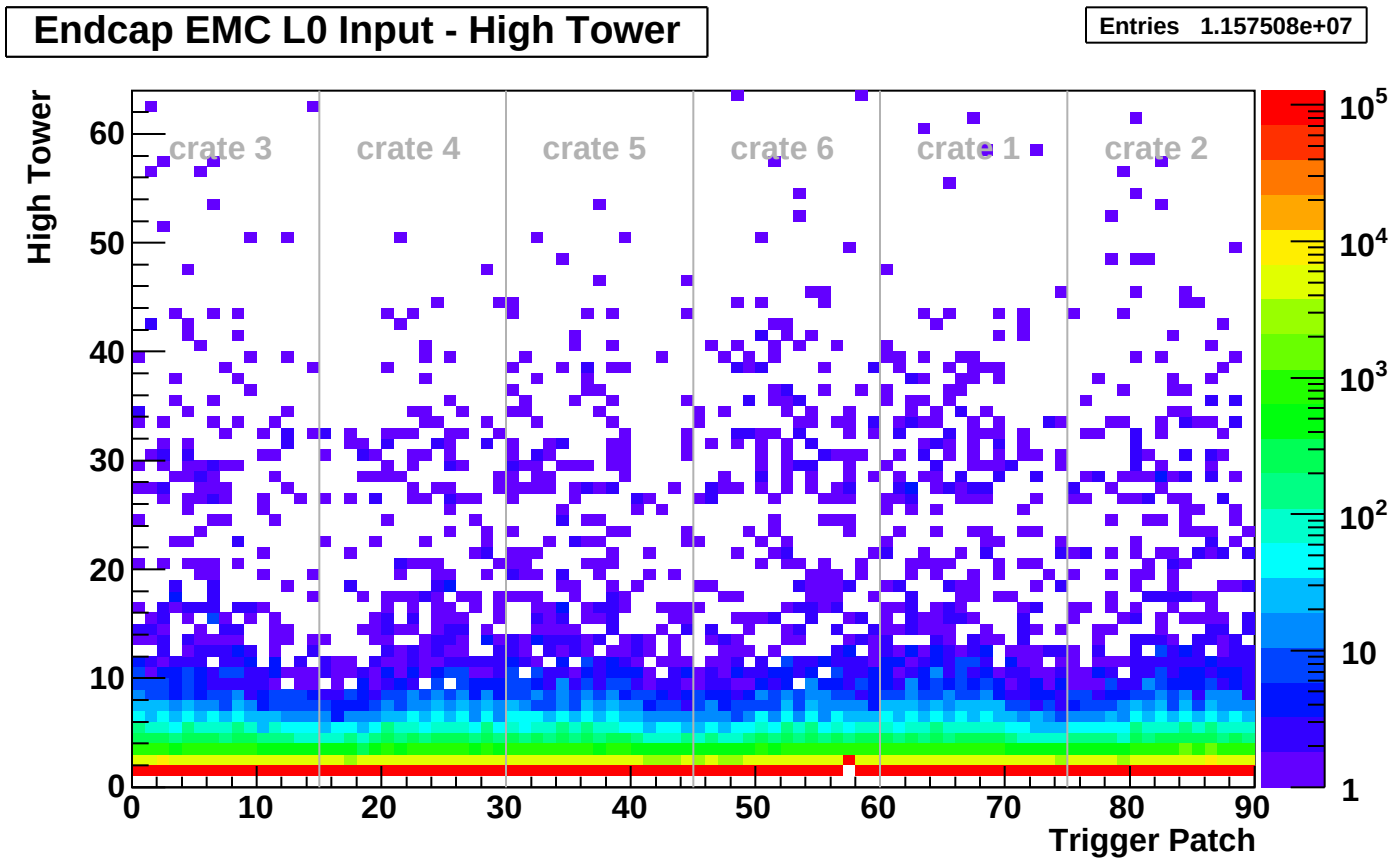

TCU bits
Entries 145308
TCU bits - Simulated
4738 12009 27239 569 2390 98258 105
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

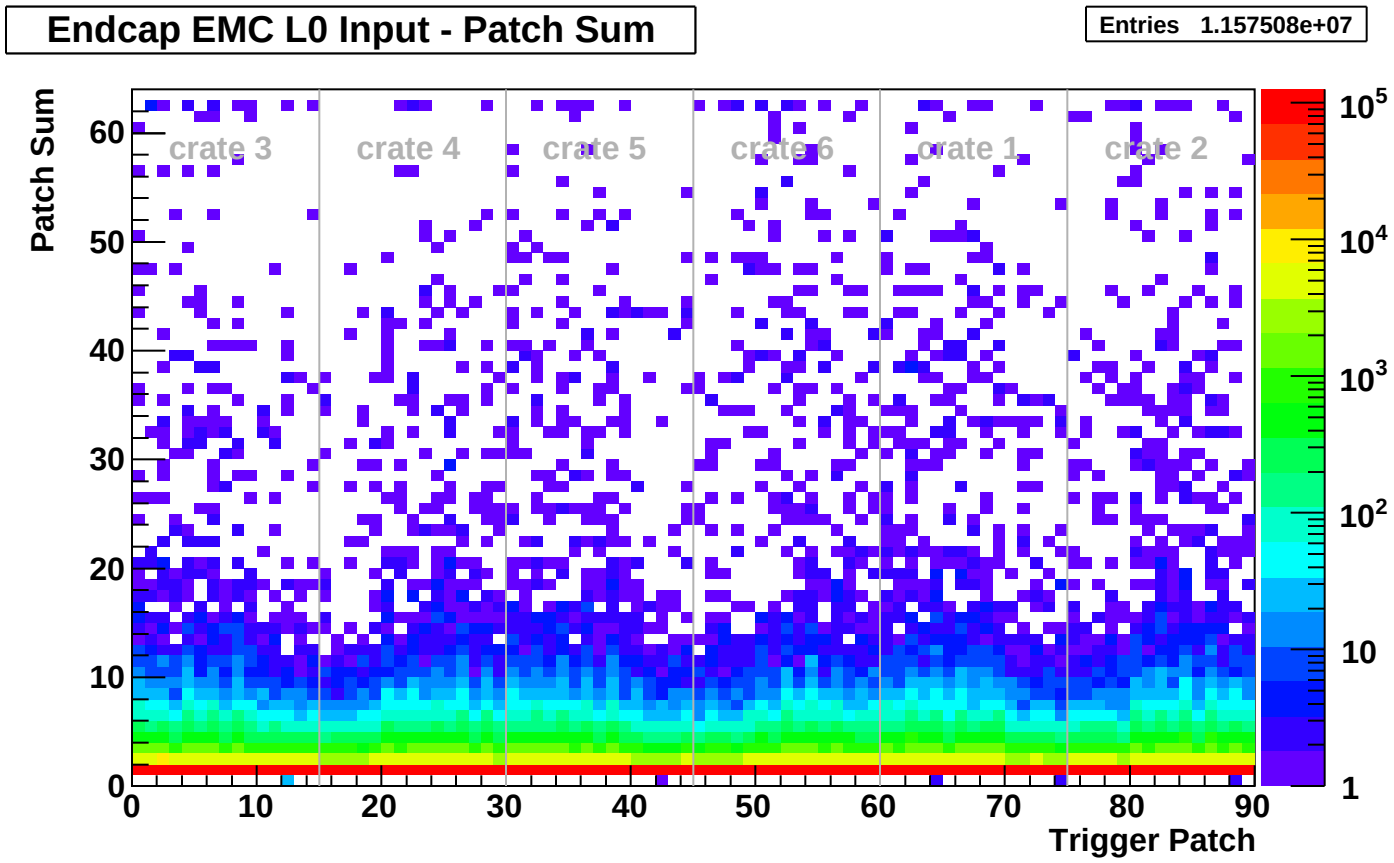

BunchId7Bit (BHT3)
Entries 925

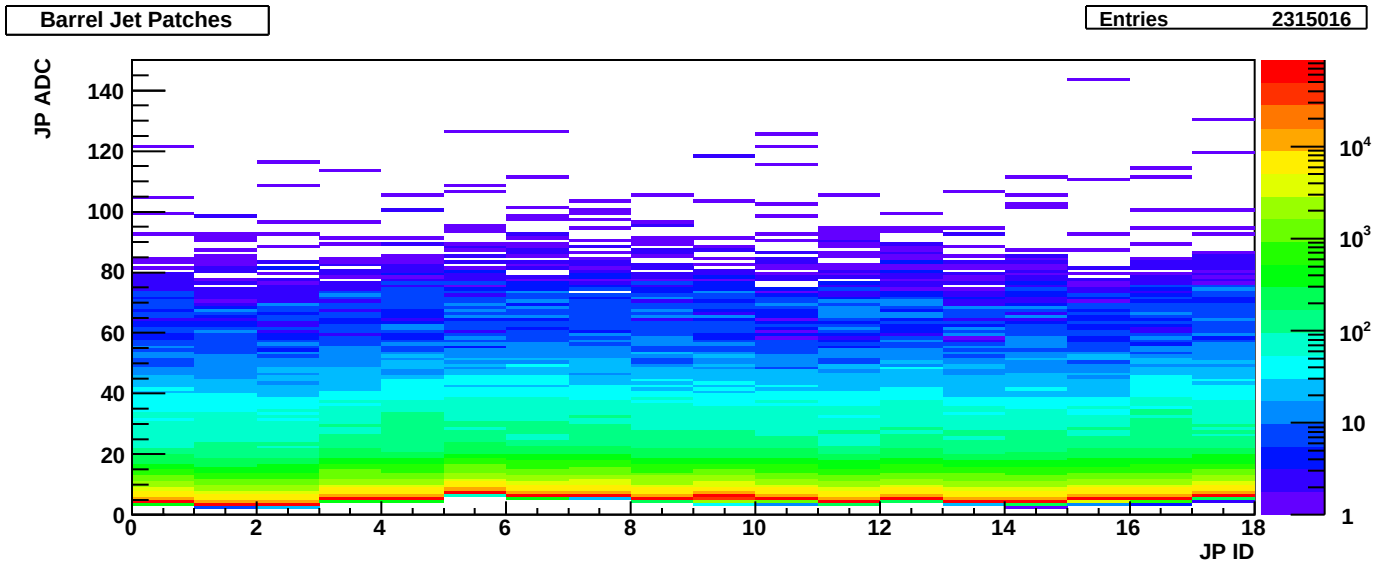

BunchId7Bit (all events)
Entries 128612

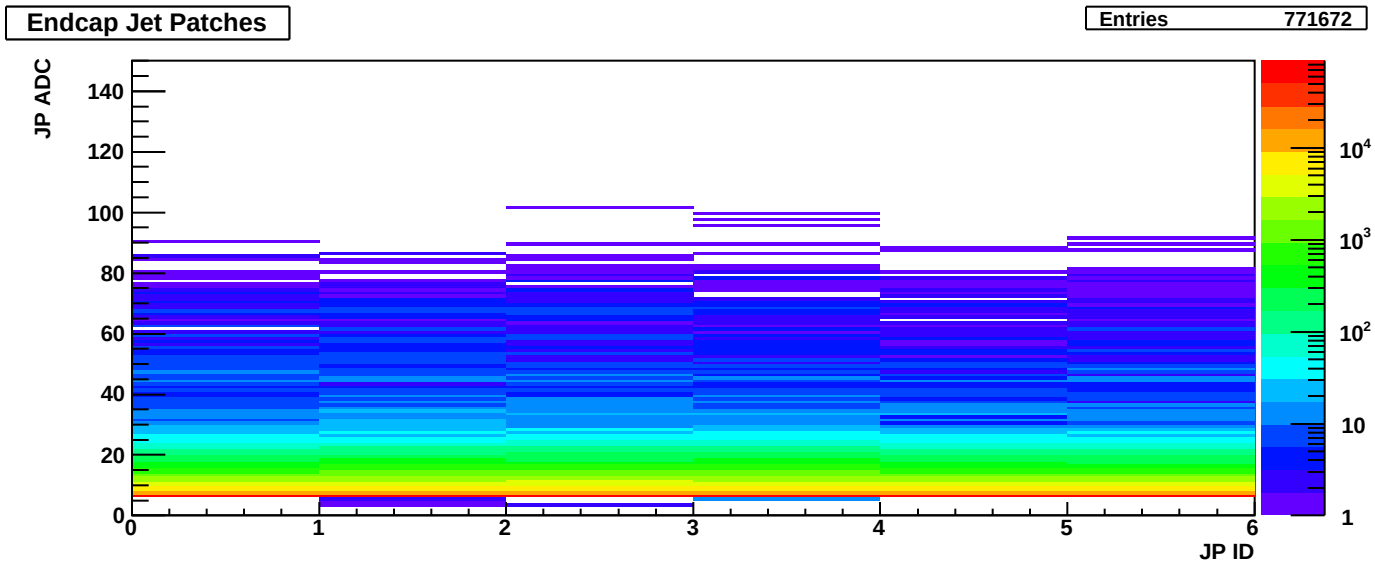

Barrel EMC L0 Input - High Tower
Entries 3.85836e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

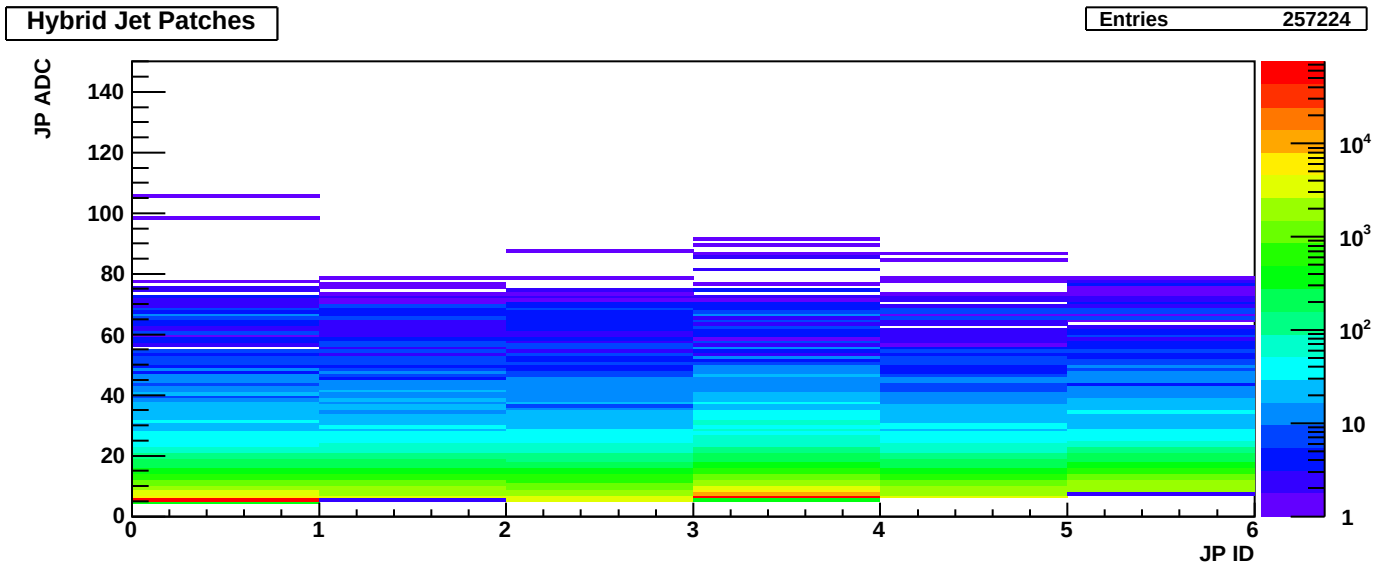

Barrel EMC L0 Input - Patch Sum
Entries 3.85836e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

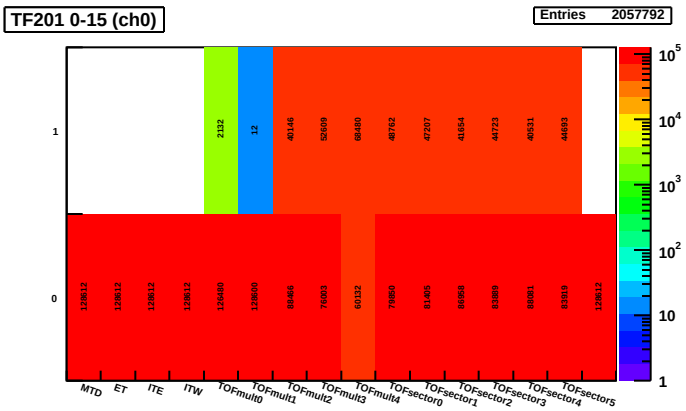

Endcap EMC L0 Input - High Tower
Entries 1.157508e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

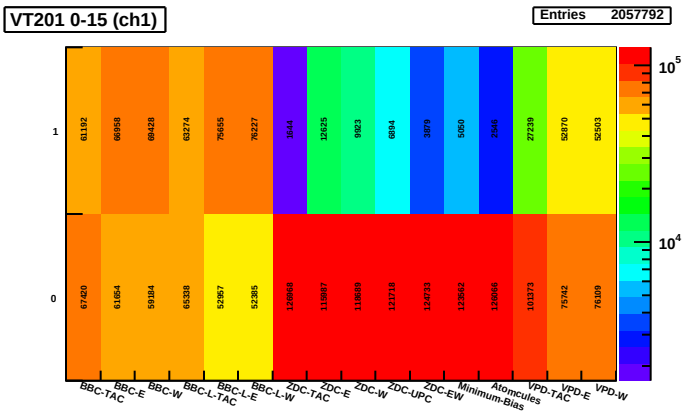

Endcap EMC L0 Input - Patch Sum
Entries 1.157508e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

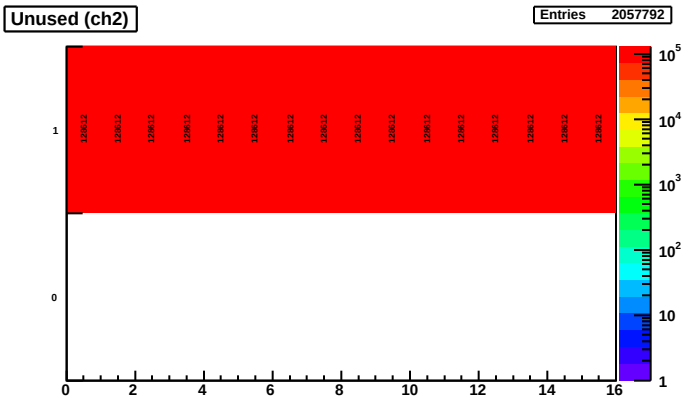

Barrel Jet Patches
Entries 2315016
JP ADC
JP ID

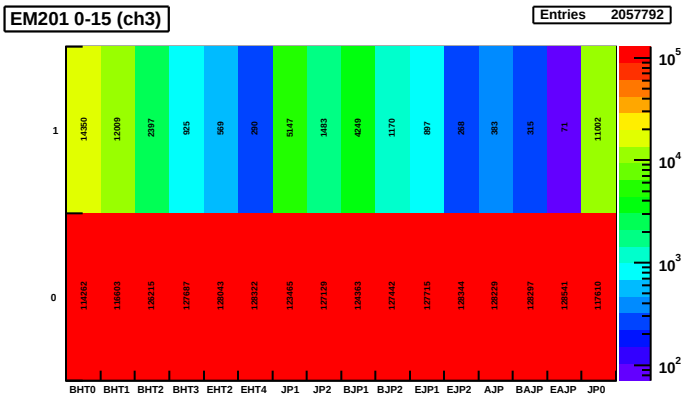

Endcap Jet Patches
Entries 771672
JP ADC
JP ID

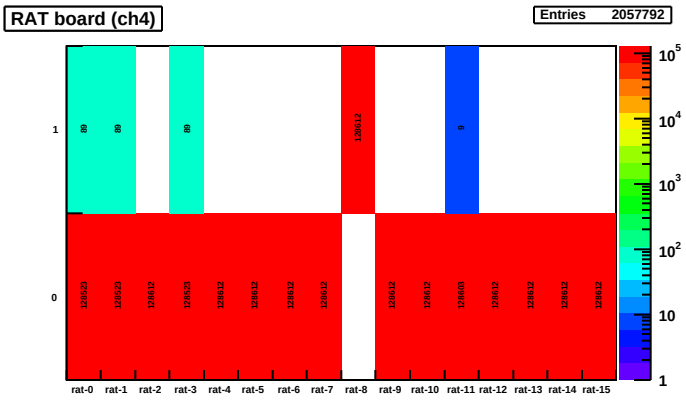

Hybrid Jet Patches
Entries 257224
JP ADC
JP ID

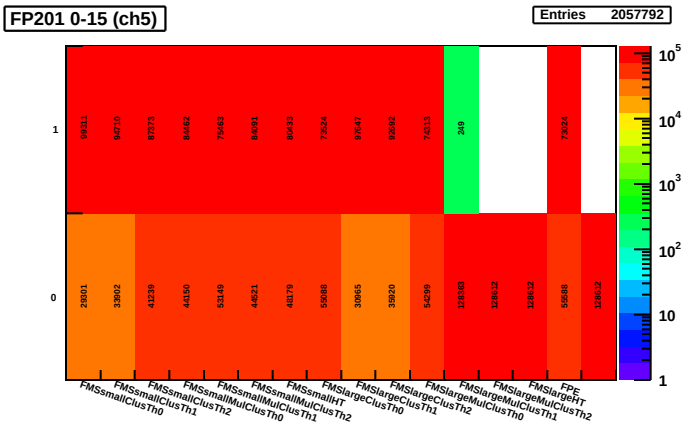

TF201 0-15 (ch0)
Entries 2057792
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

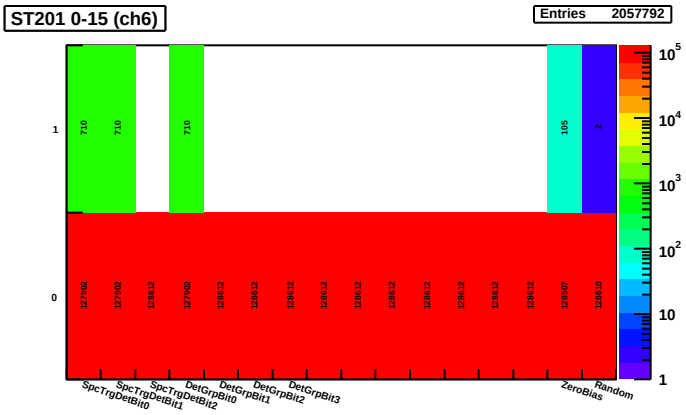

VT201 0-15 (ch1)
Entries 2057792
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

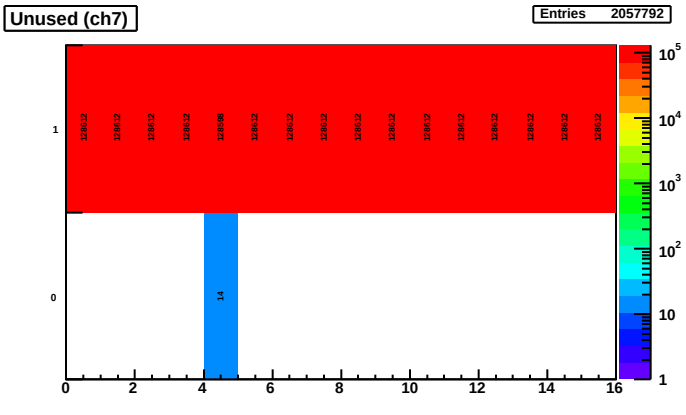

Unused (ch2)
Entries 2057792

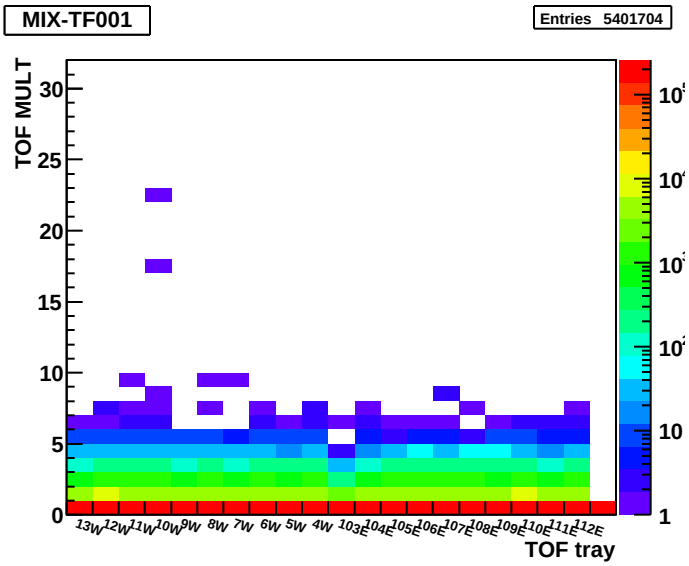

EM201 0-15 (ch3)
Entries 2057792
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

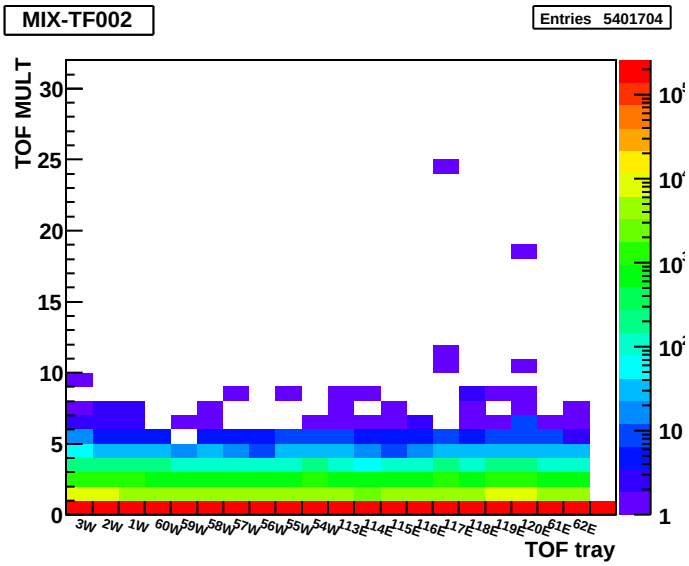

RAT board (ch4)
Entries 2057792
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

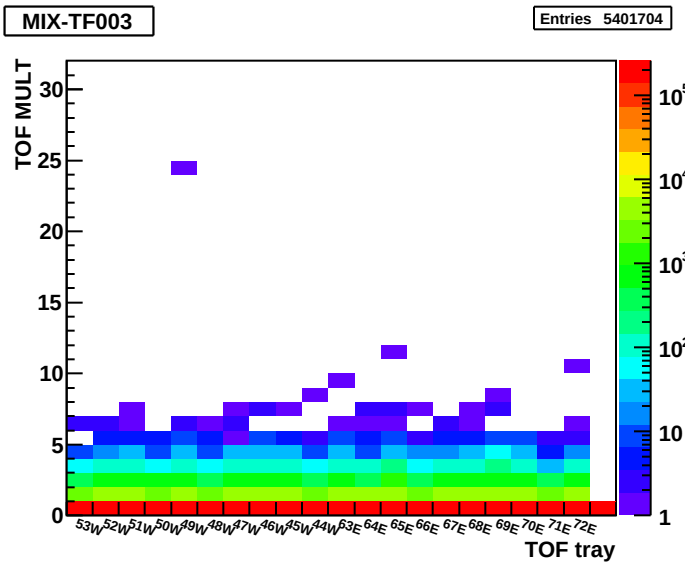

FP201 0-15 (ch5)
Entries 2057792
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

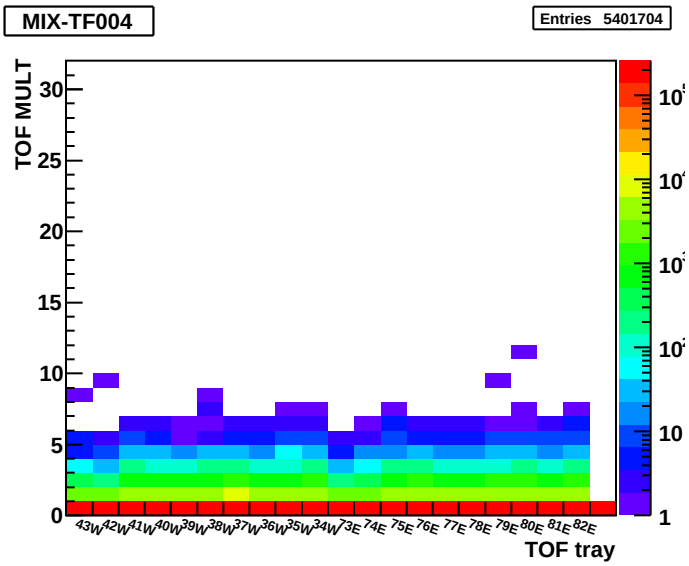

ST201 0-15 (ch6)
Entries 2057792
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

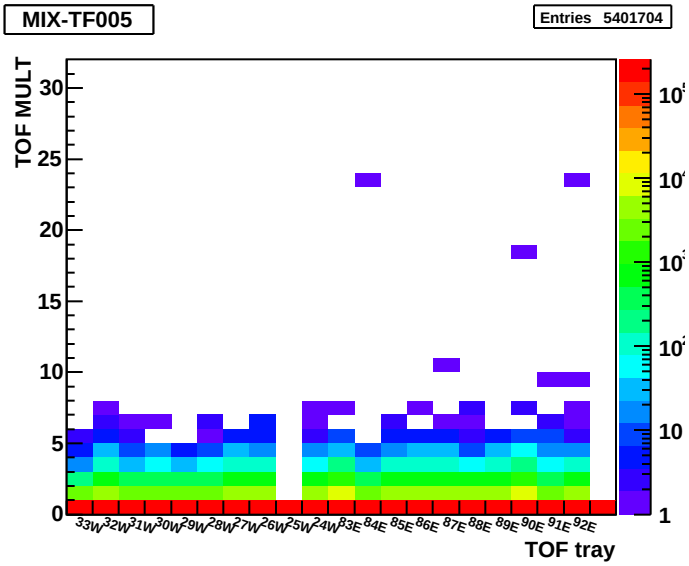

Unused (ch7)
Entries 2057792

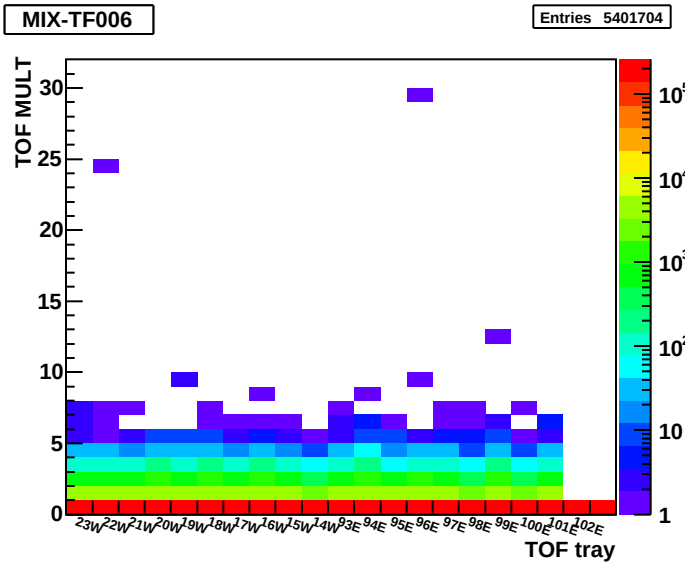

MIX-TF001
Entries 5401704
TOF MULT
TOF tray

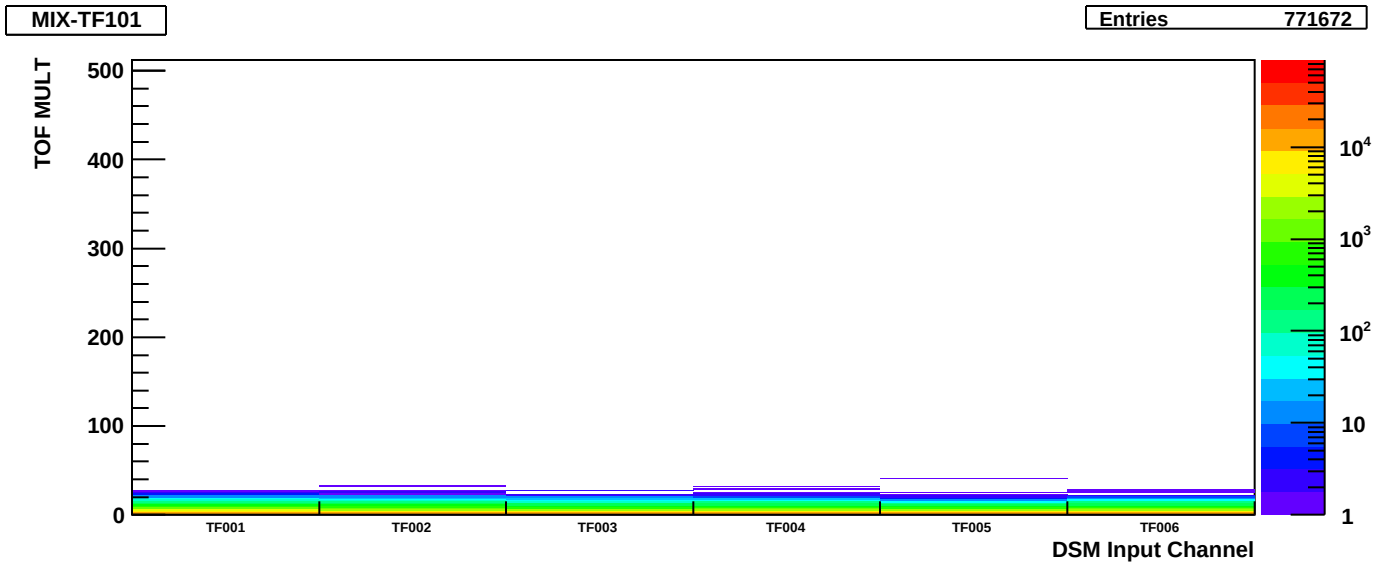

MIX-TF002
Entries 5401704
TOF MULT
TOF tray

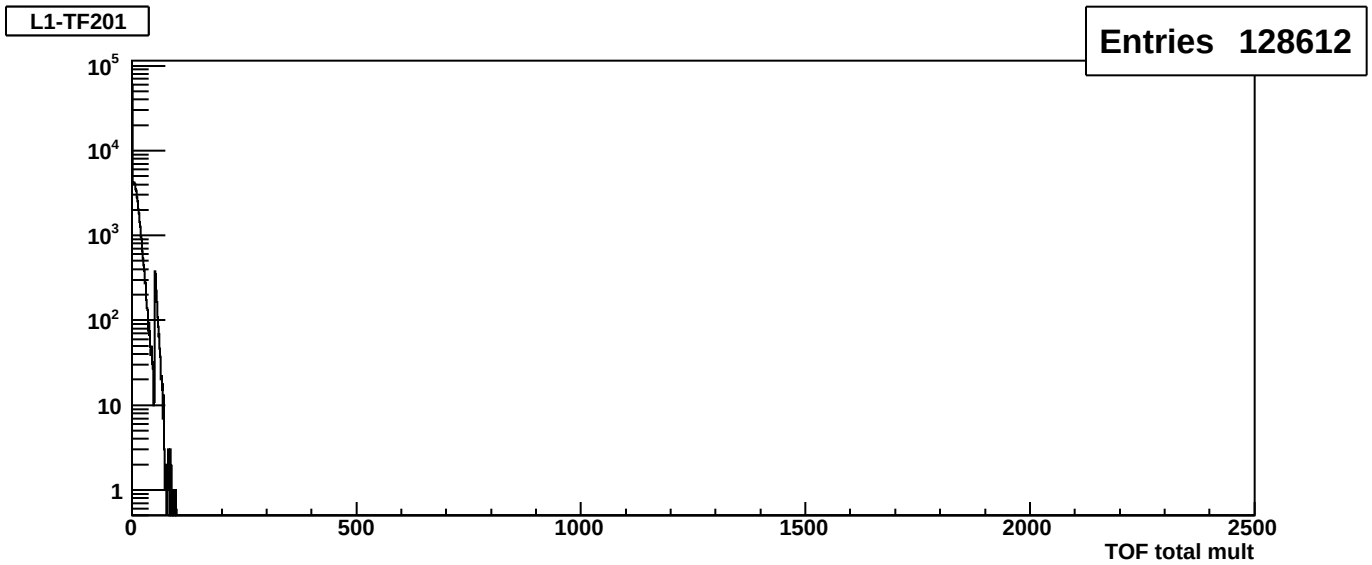

MIX-TF003
Entries 5401704
TOF MULT
TOF tray

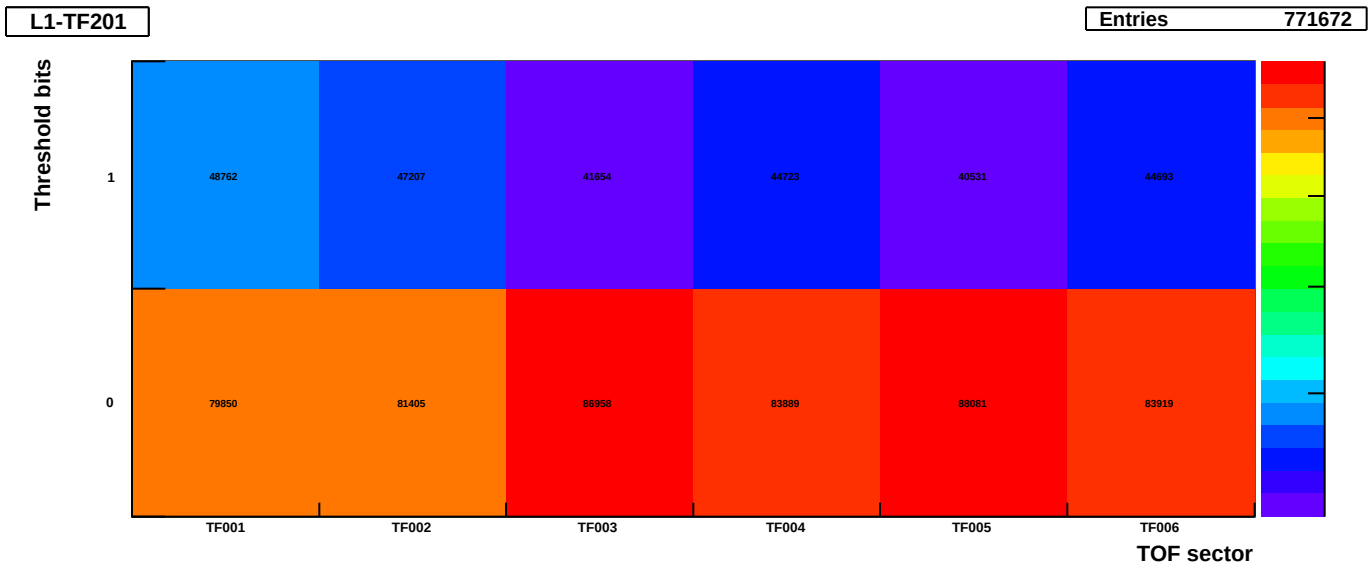

MIX-TF004
Entries 5401704
TOF MULT
TOF tray

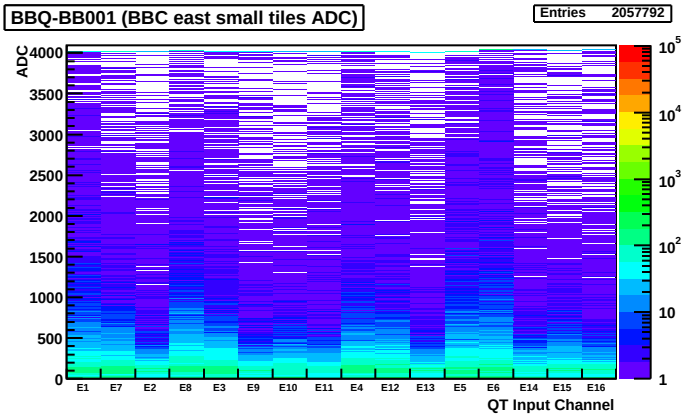

MIX-TF005
Entries 5401704
TOF MULT
TOF tray

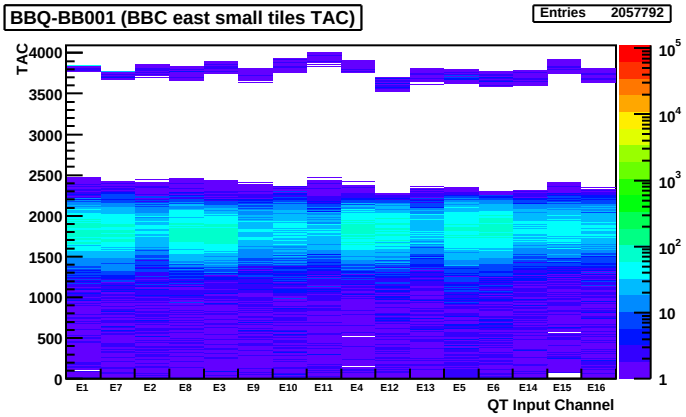

MIX-TF006
Entries 5401704
TOF MULT
TOF tray

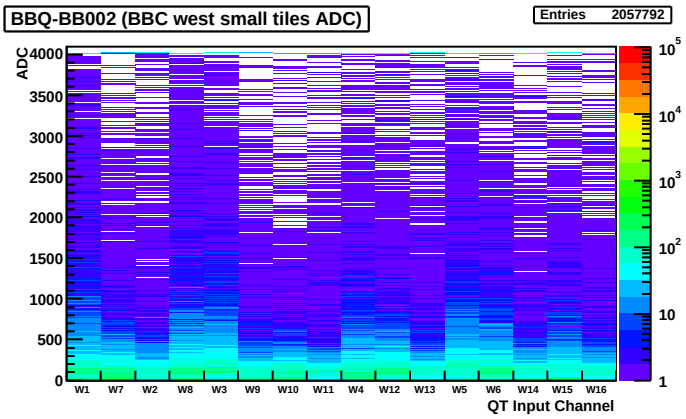

MIX-TF101
Entries 771672
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

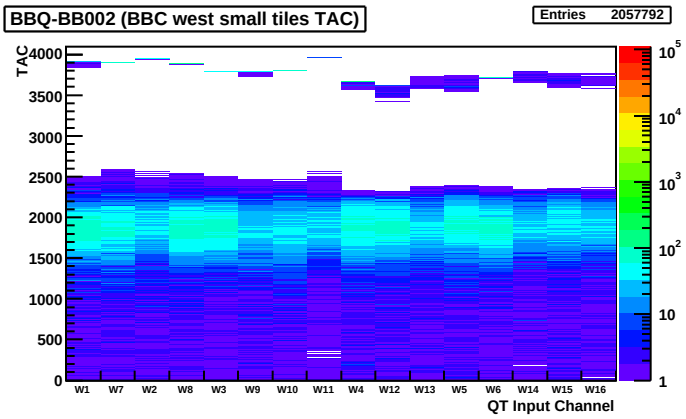

L1-TF201
Entries 128612
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

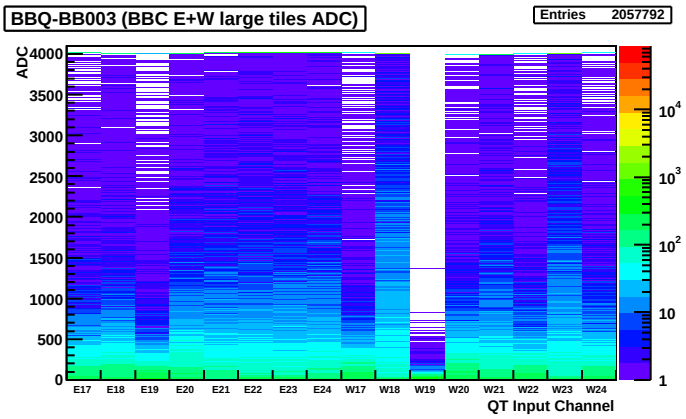

L1-TF201
Entries 771672
Threshold bits
1
0
48762
47207
41654
44723
40531
44693
79850
81405
86958
83889
88081
83919
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

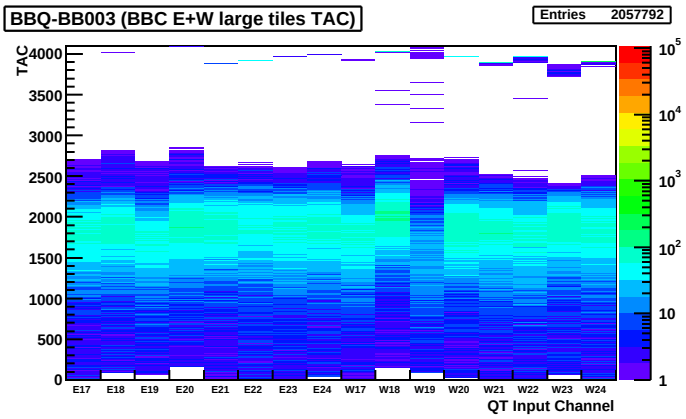

BBQ-BB001 (BBC east small tiles ADC)
Entries 2057792
ADC
QT Input Channel

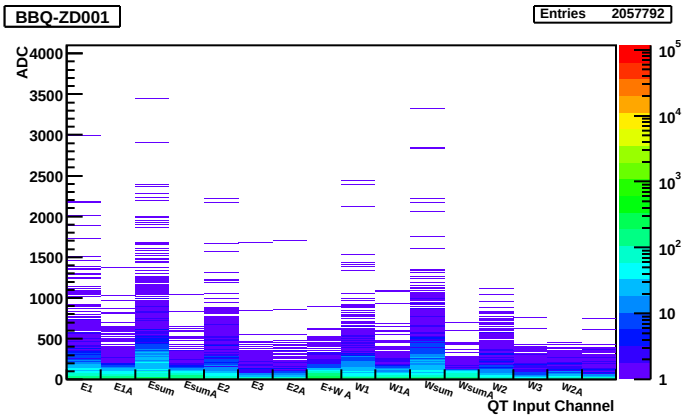

BBQ-BB001 (BBC east small tiles TAC)
Entries 2057792
TAC
QT Input Channel

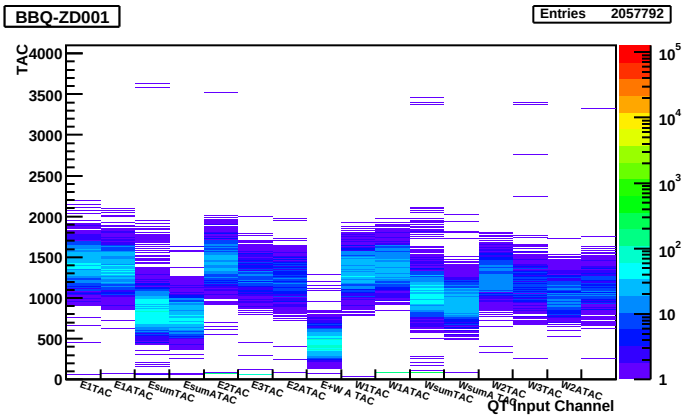

BBQ-BB002 (BBC west small tiles ADC)
Entries 2057792
ADC
QT Input Channel

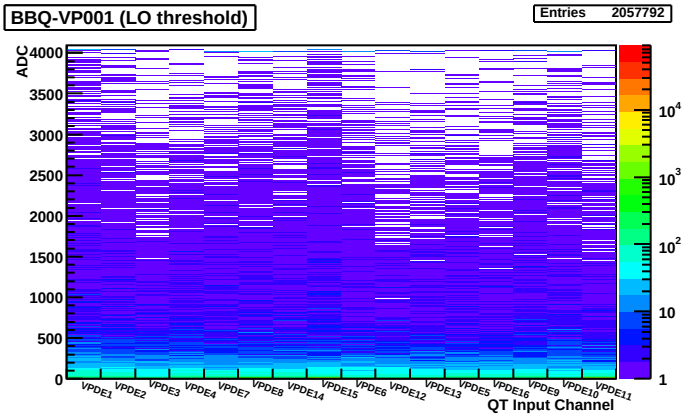

BBQ-BB002 (BBC west small tiles TAC)
Entries 2057792
TAC
QT Input Channel

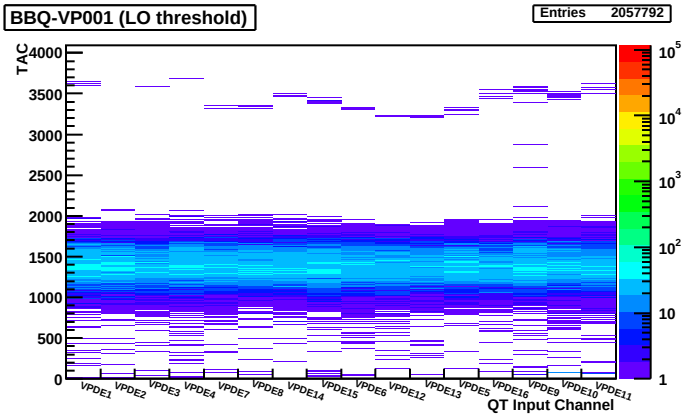

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2057792
ADC
QT Input Channel

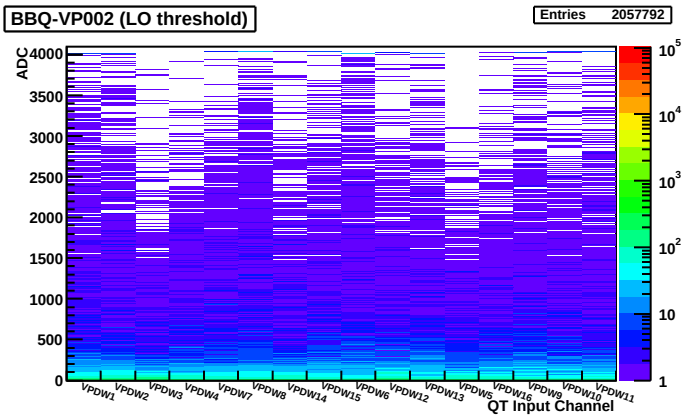

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2057792
TAC
QT Input Channel

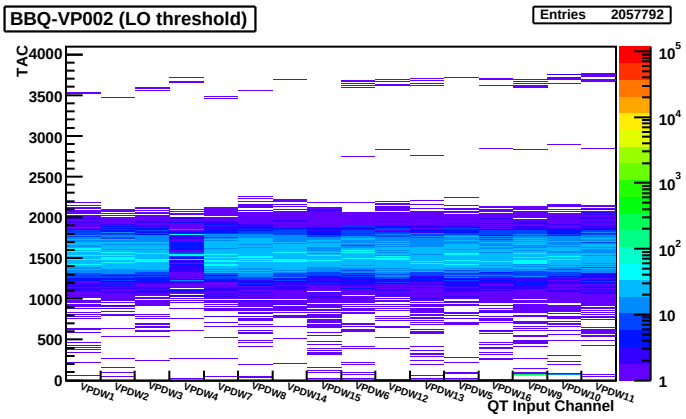

BBQ-ZD001
Entries 2057792
ADC
QT Input Channel

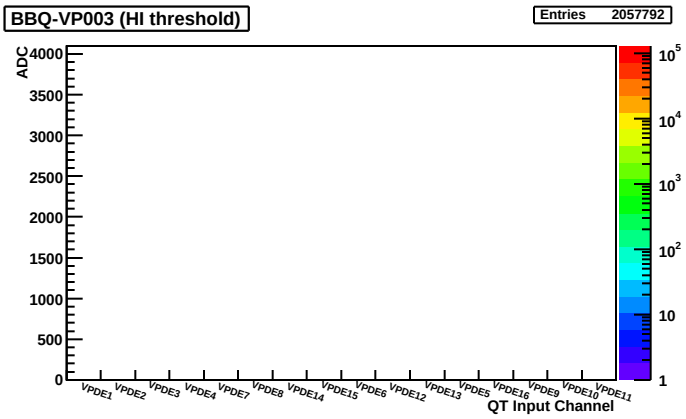

BBQ-ZD001
Entries 2057792
TAC
QT Input Channel

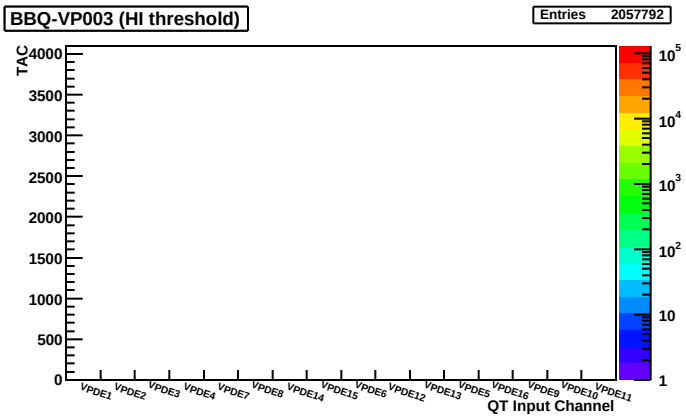

BBQ-VP001 (LO threshold)
Entries 2057792
ADC
QT Input Channel

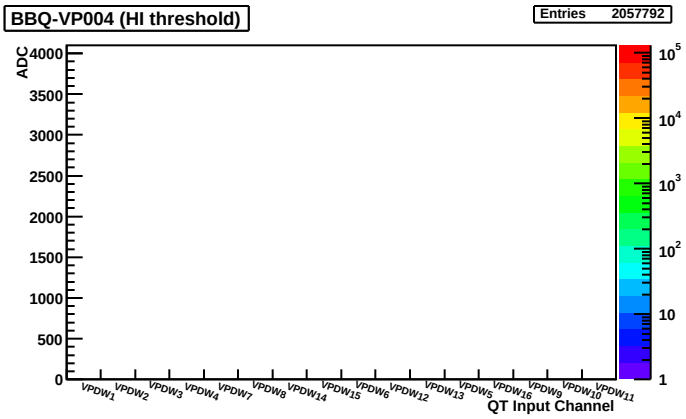

BBQ-VP001 (LO threshold)
Entries 2057792
TAC
QT Input Channel

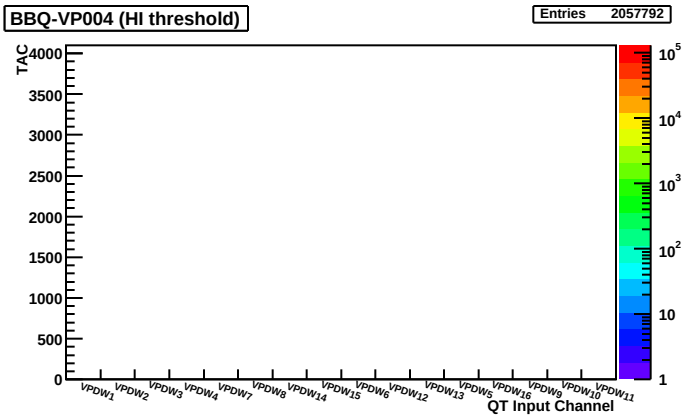

BBQ-VP002 (LO threshold)
Entries 2057792
ADC
QT Input Channel

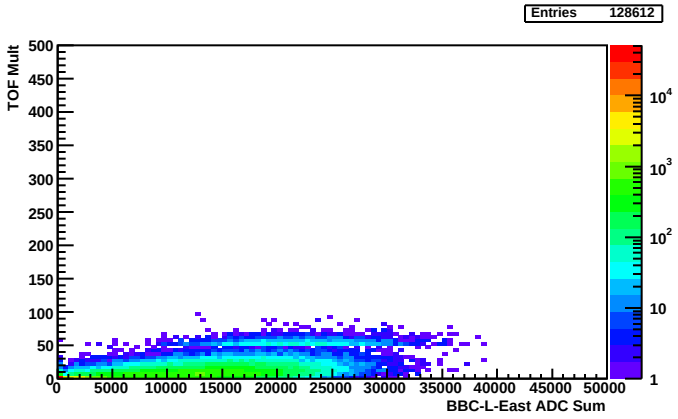

BBQ-VP002 (LO threshold)
Entries 2057792
TAC
QT Input Channel

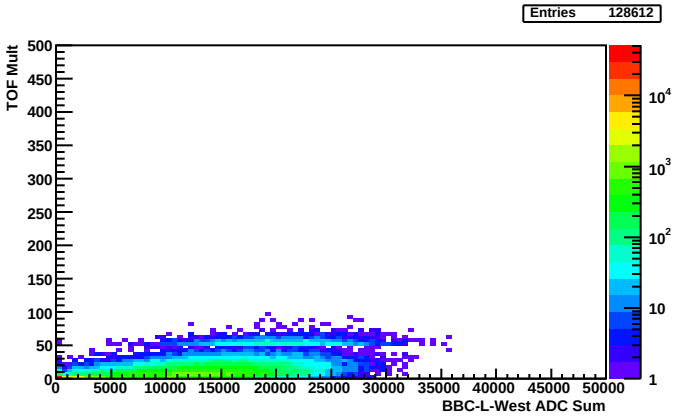

BBQ-VP003 (HI threshold)
Entries 2057792
ADC
QT Input Channel

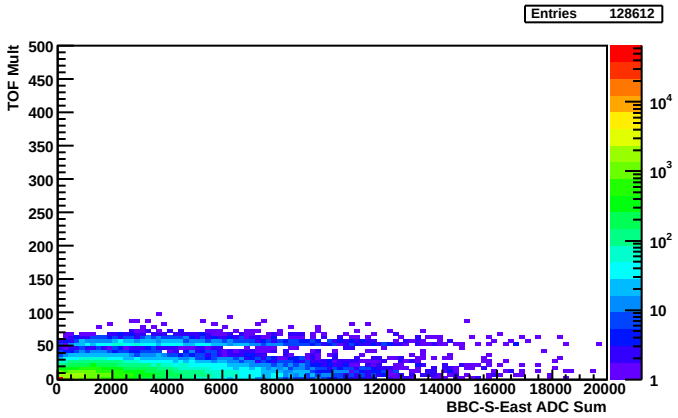

BBQ-VP003 (HI threshold)
Entries 2057792
TAC
QT Input Channel

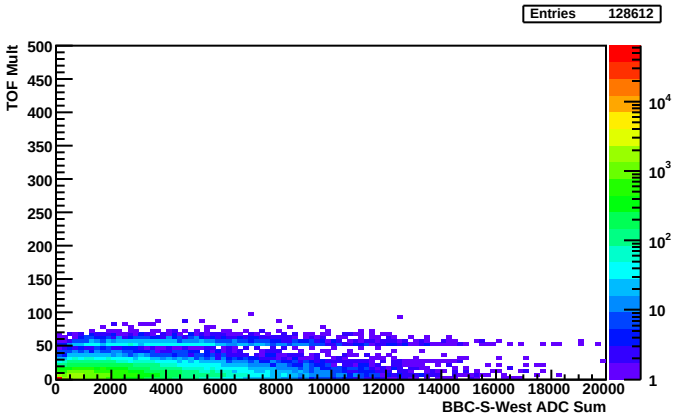

BBQ-VP004 (HI threshold)
Entries 2057792
ADC
QT Input Channel

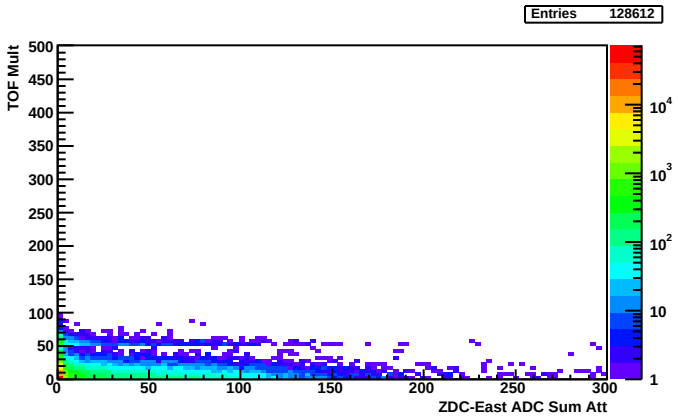

BBQ-VP004 (HI threshold)
Entries 2057792
TAC
QT Input Channel

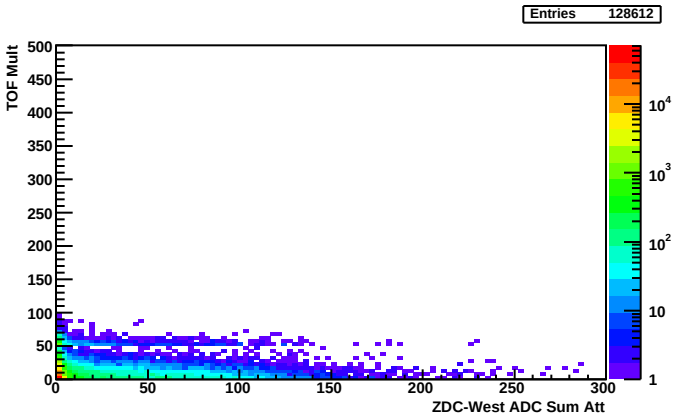

Entries 128612
TOF Mult
BBC-L-East ADC Sum

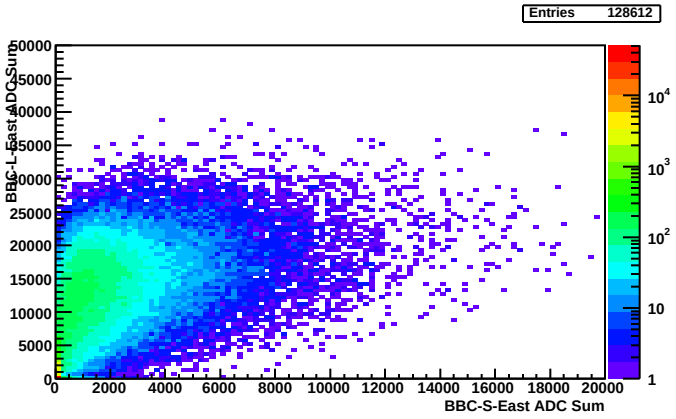

Entries 128612
TOF Mult
BBC-L-West ADC Sum

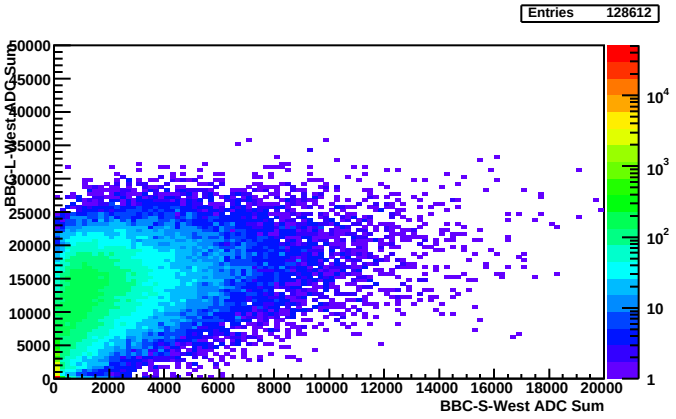

Entries 128612
TOF Mult
BBC-S-East ADC Sum

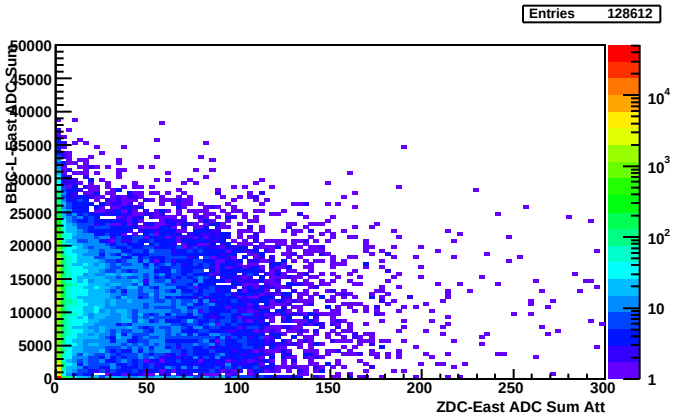

Entries 128612
TOF Mult
BBC-S-West ADC Sum

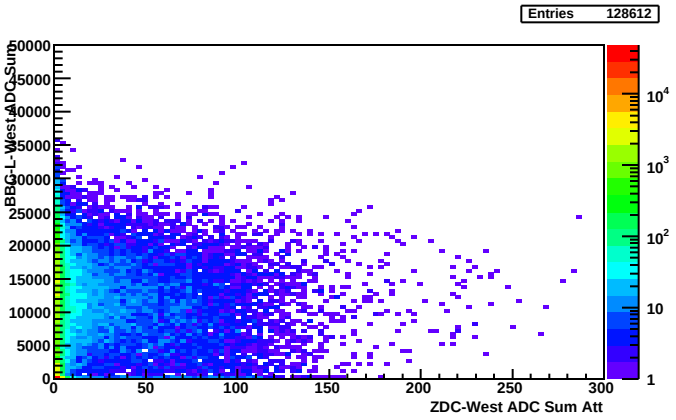

Entries 128612
TOF Mult
ZDC-East ADC Sum Att

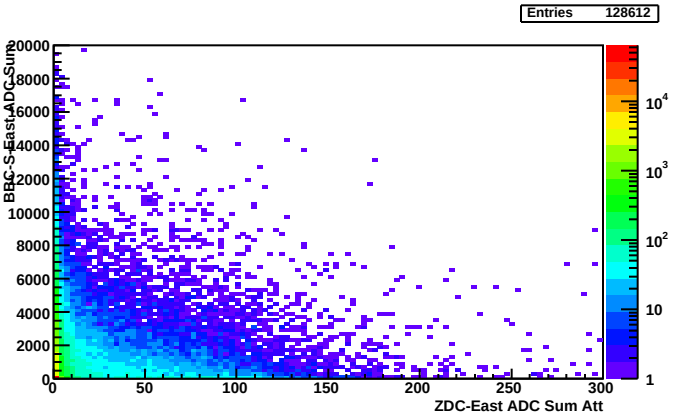

Entries 128612
TOF Mult
ZDC-West ADC Sum Att

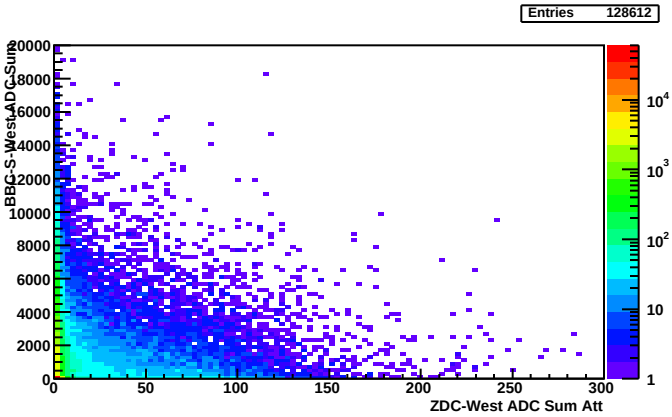

Entries 128612
BBC-L-East ADC Sum
BBC-S-East ADC Sum

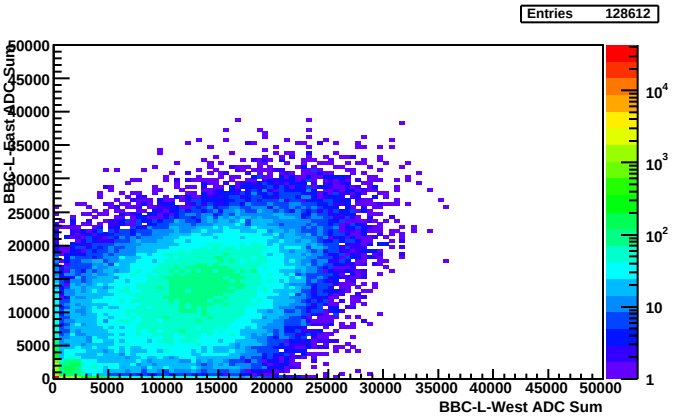

Entries 128612
BBC-L-West ADC Sum
BBC-S-West ADC Sum

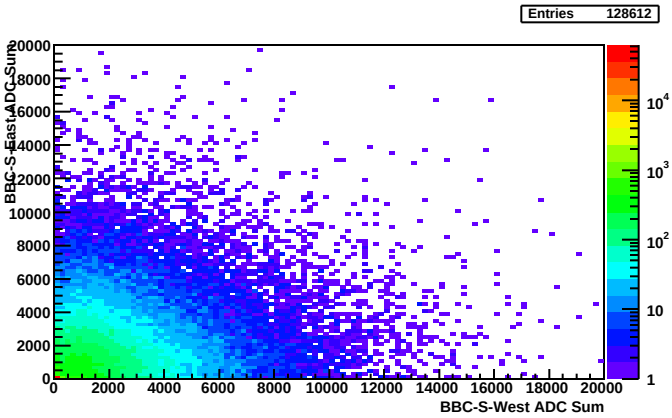

Entries 128612
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

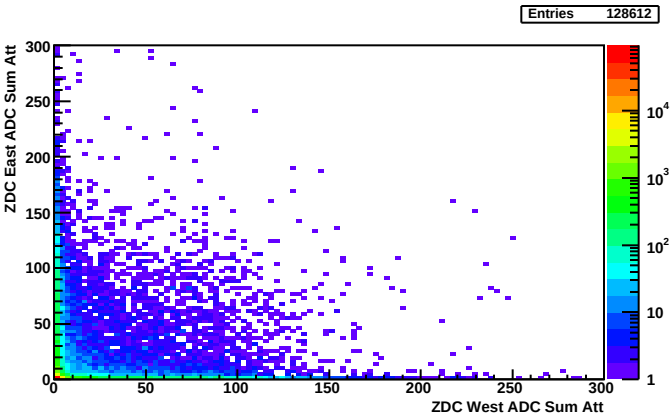

Entries 128612
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

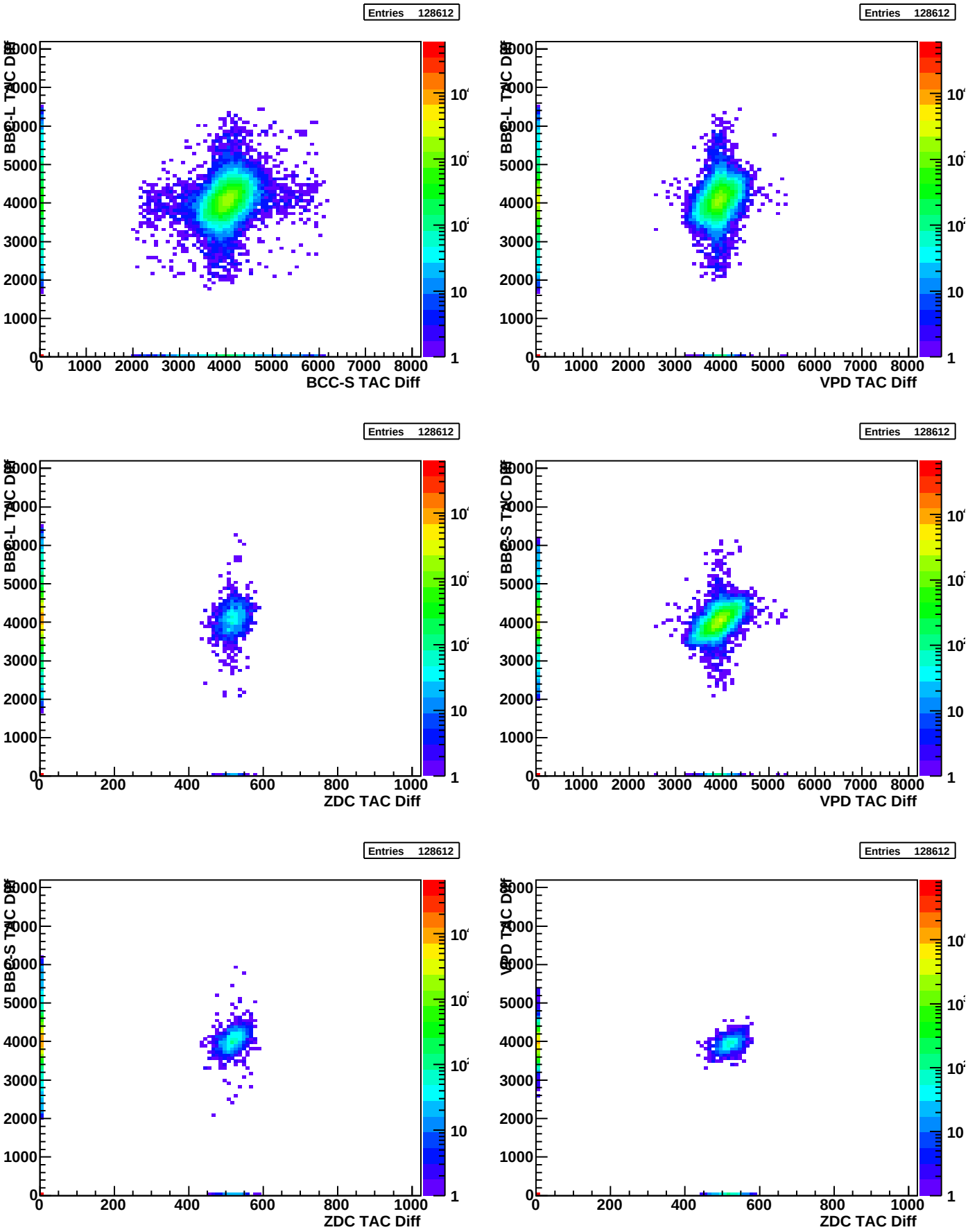

Entries 128612
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

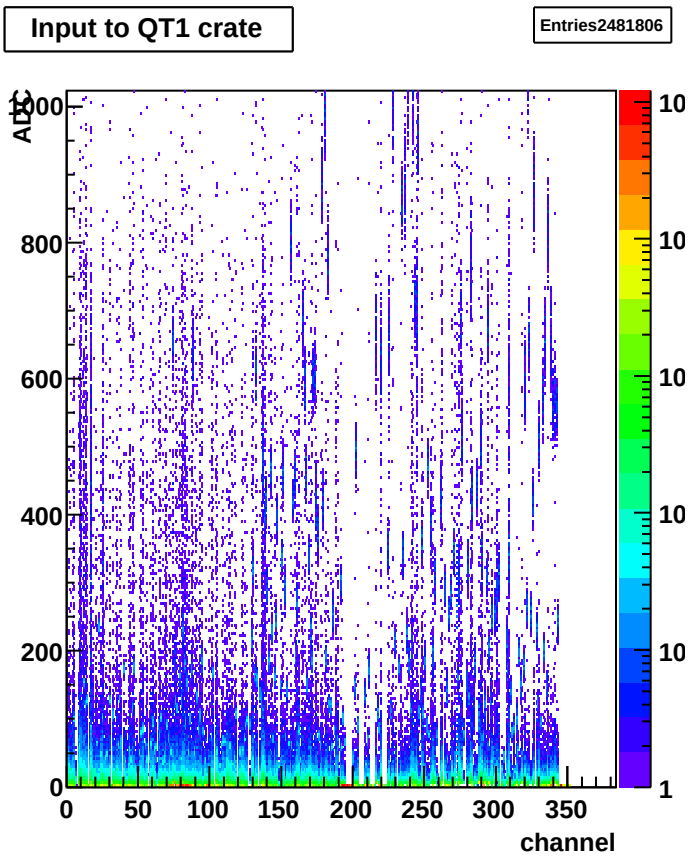

Entries 128612
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

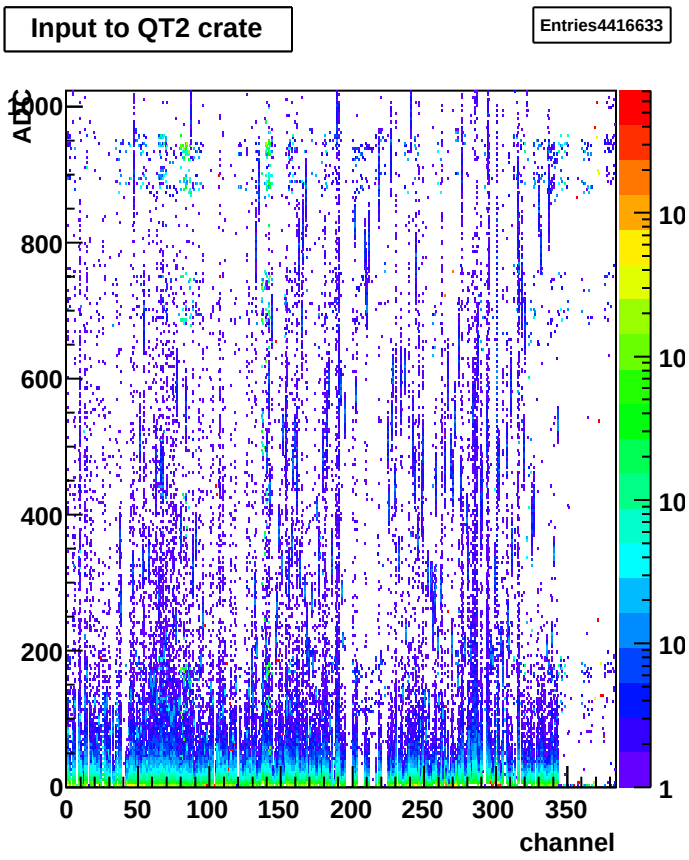

Entries 128612
BBC-L-East ADC Sum
BBC-L-West ADC Sum

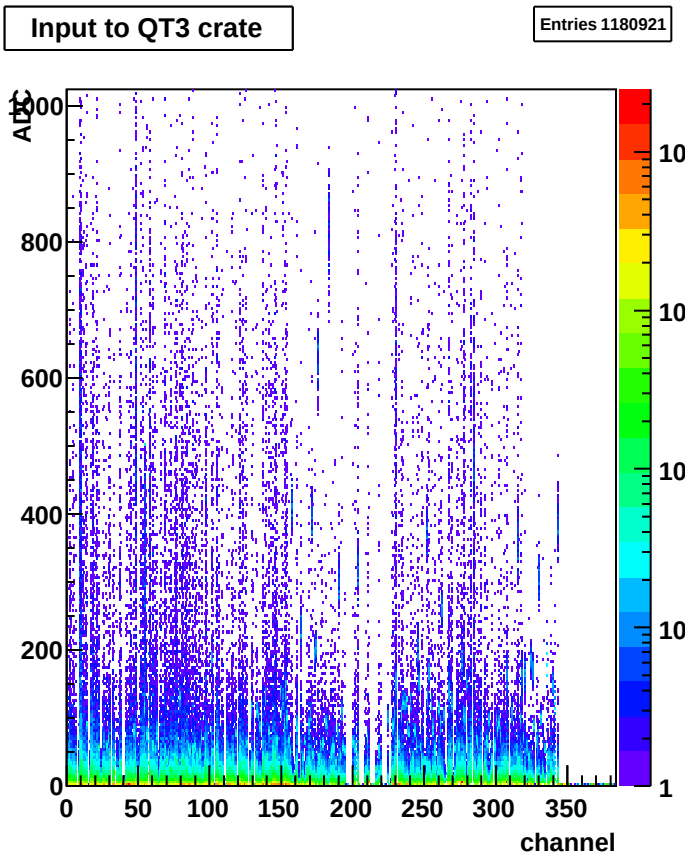

Entries 128612
BBC-S-East ADC Sum
BBC-S-West ADC Sum

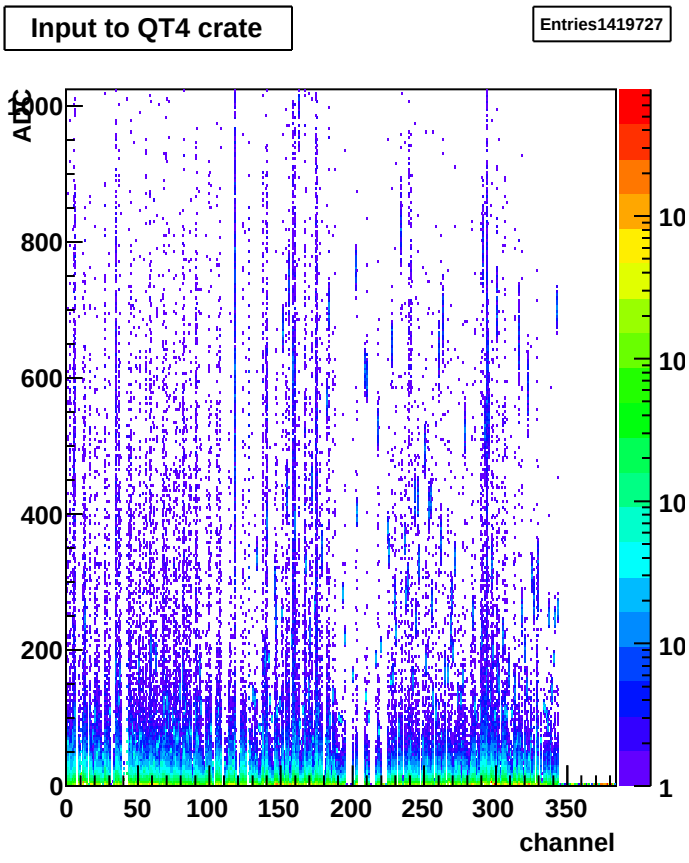

Entries 128612
ZDC East ADC Sum Att
ZDC West ADC Sum Att

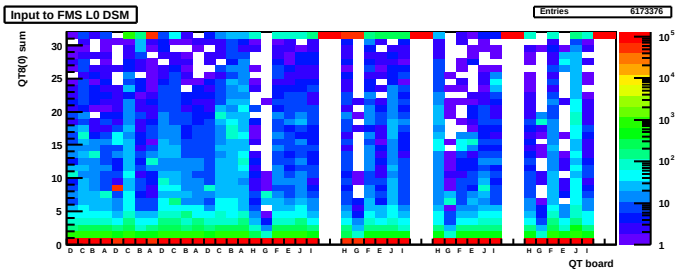

Entries 128612
BBC-L TAC Diff
BCC-S TAC Diff
Entries 128612
BBC-L TAC Diff
VPD TAC Diff
Entries 128612
BBC-L TAC Diff
ZDC TAC Diff
Entries 128612
BBC-S TAC Diff
VPD TAC Diff
Entries 128612
BBC-S TAC Diff
ZDC TAC Diff
Entries 128612
VPD TAC Diff
ZDC TAC Diff

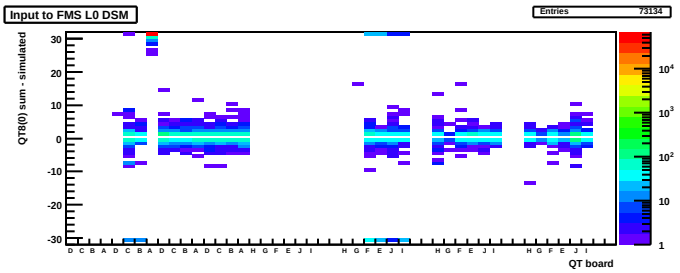

Input to QT1 crate
Entries2481806
ADC
channel

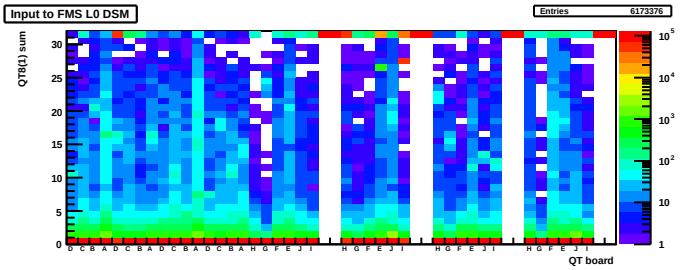

Input to QT2 crate
Entries4416633
ADC
channel

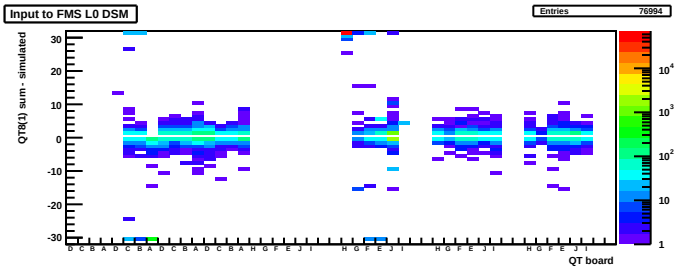

Input to QT3 crate
Entries 1180921
ADC
channel

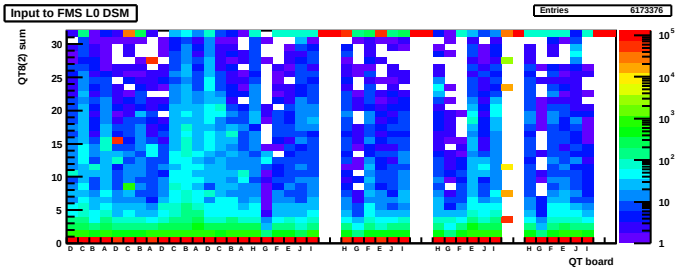

Input to QT4 crate
Entries1419727
ADC
channel

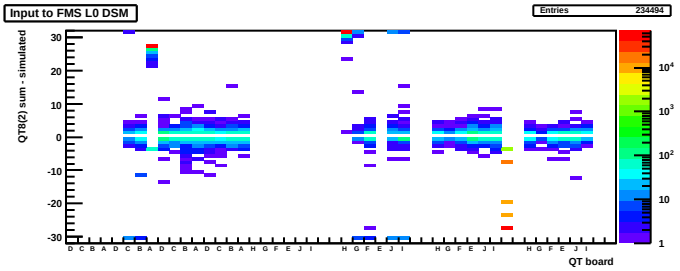

Input to FMS L0 DSM
Entries 6173376
QT8(0) sum
QT board

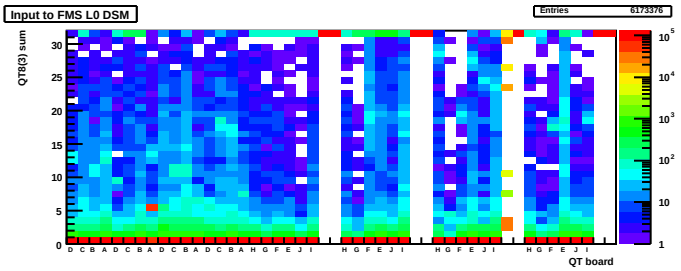

Input to FMS L0 DSM
Entries 73134
QT8(0) sum - simulated
QT board

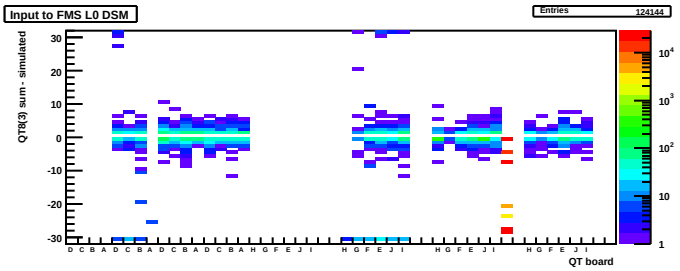

Input to FMS L0 DSM
Entries 6173376
QT8(1) sum
QT board

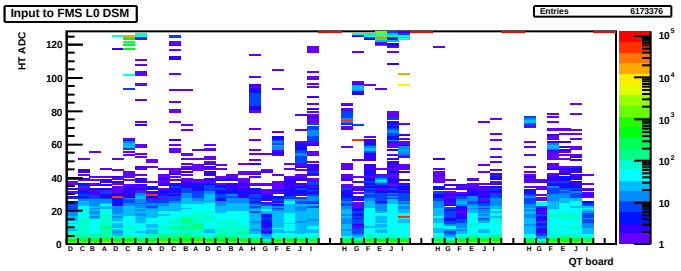

Input to FMS L0 DSM
Entries 76994
QT8(1) sum - simulated
QT board

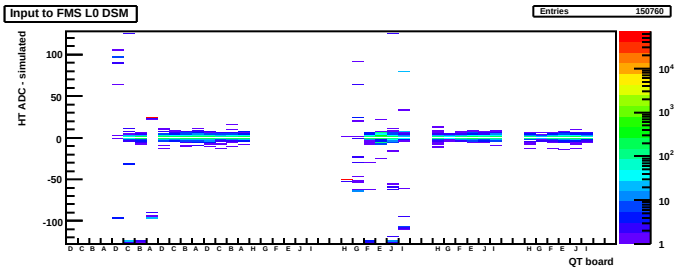

Input to FMS L0 DSM
Entries 6173376
QT8(2) sum
QT board

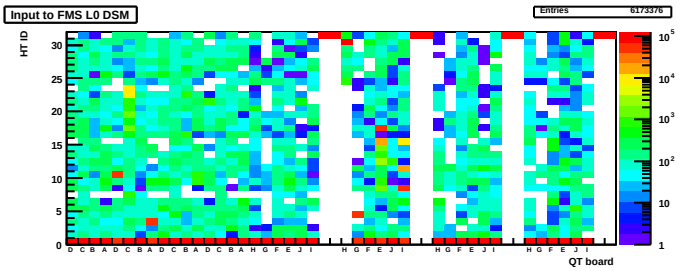

Input to FMS L0 DSM
Entries 234494
QT8(2) sum - simulated
QT board

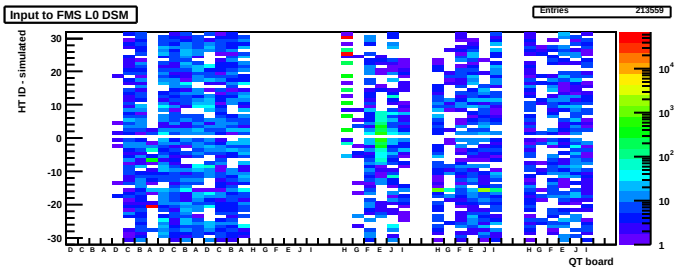

Input to FMS L0 DSM
Entries 6173376
QT8(3) sum
QT board

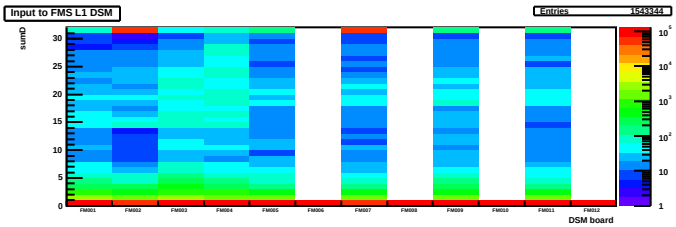

Input to FMS L0 DSM
Entries 124144
QT8(3) sum - simulated
QT board

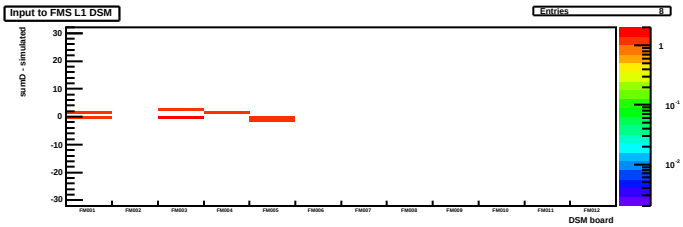

Input to FMS L0 DSM
Entries 6173376
HT ADC
QT board

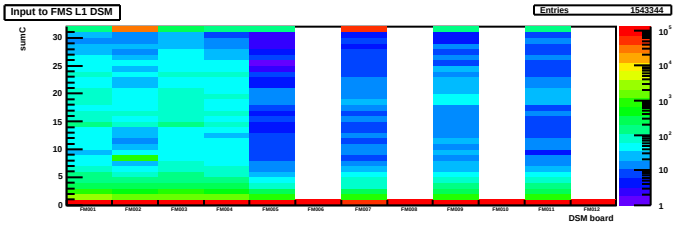

Input to FMS L0 DSM
Entries 150760
HT ADC - simulated
QT board

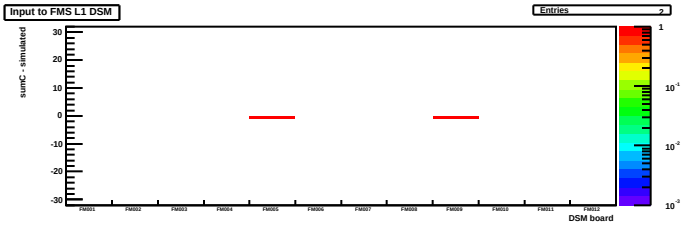

Input to FMS L0 DSM
Entries 6173376
HT ID
QT board

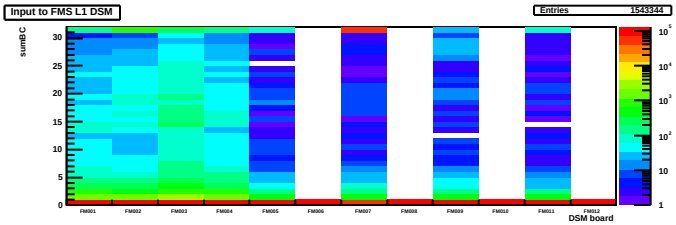

Input to FMS L0 DSM
Entries 213559
HT ID - simulated
QT board

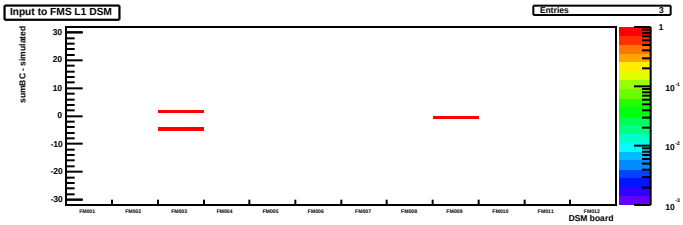

Input to FMS L1 DSM
Entries 1543344
sumD
DSM board

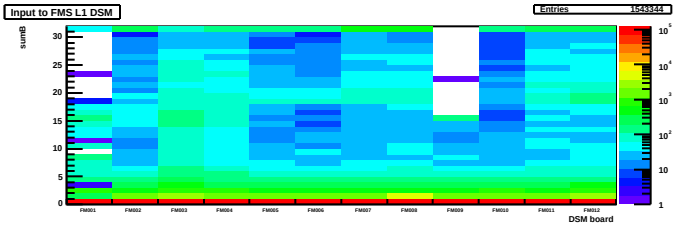

Input to FMS L1 DSM
Entries 8
sumD - simulated
DSM board

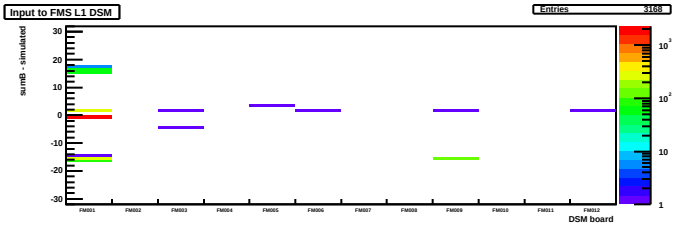

Input to FMS L1 DSM
Entries 1543344
sumC
DSM board

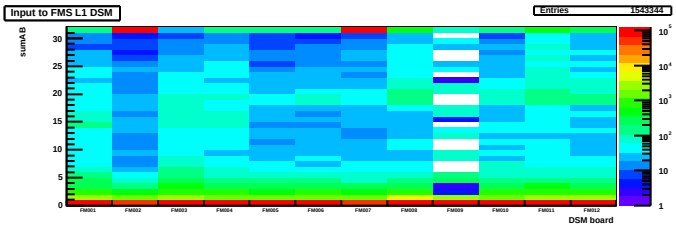

Input to FMS L1 DSM
Entries 2
sumC - simulated
DSM board

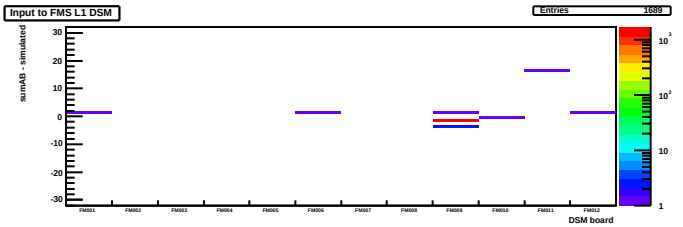

Input to FMS L1 DSM
Entries 1543344
sumBC
DSM board

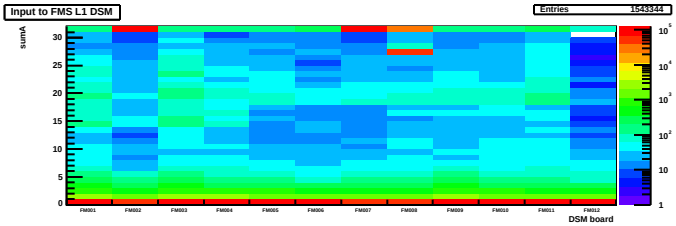

Input to FMS L1 DSM
Entries 3
sumBC - simulated
DSM board

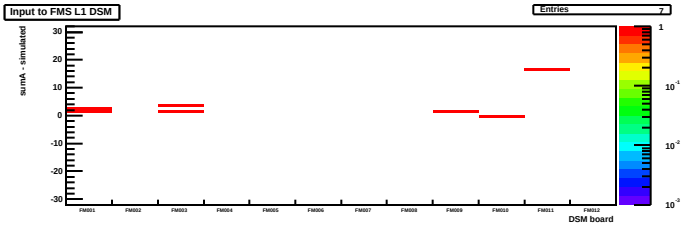

Input to FMS L1 DSM
Entries 1543344
sumB
DSM board

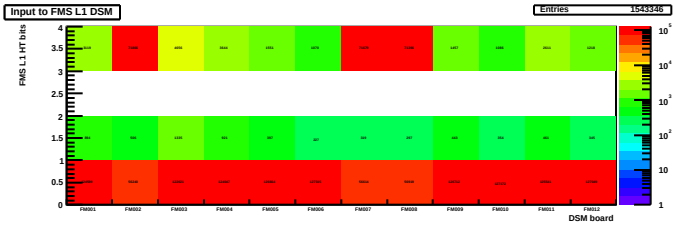

Input to FMS L1 DSM
Entries 3168
sumB - simulated
DSM board

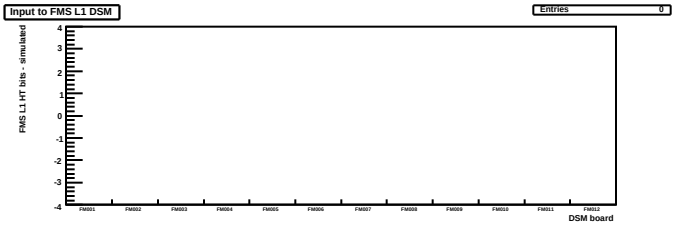

Input to FMS L1 DSM
Entries 1543344
sumAB
DSM board

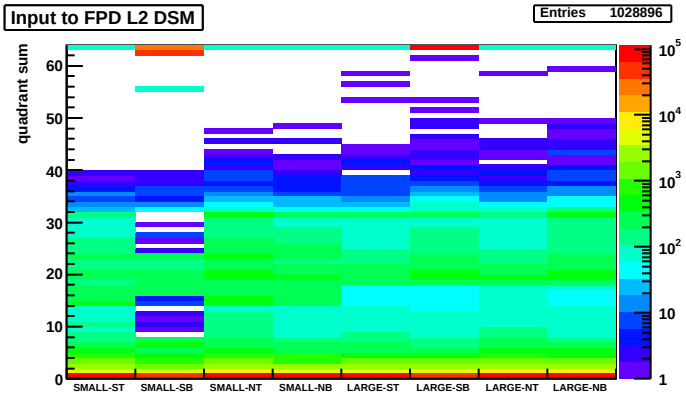

Input to FMS L1 DSM
Entries 1689
sumAB - simulated
DSM board

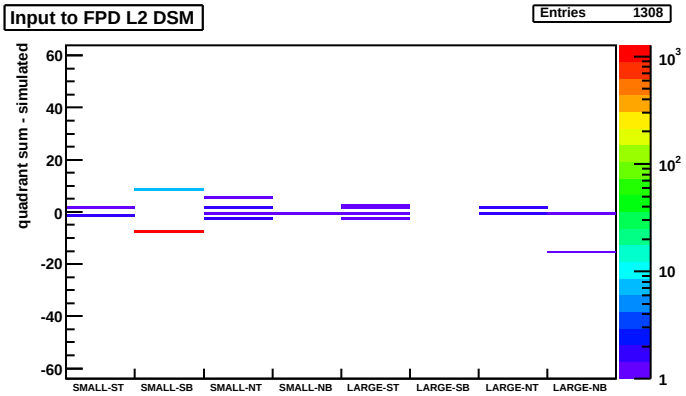

Input to FMS L1 DSM
Entries 1543344
sumA
DSM board

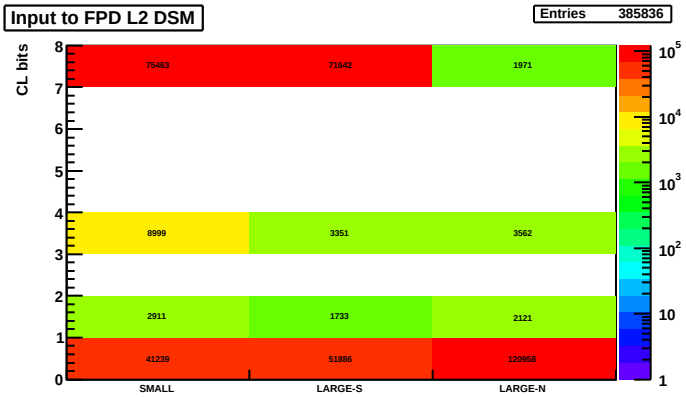

Input to FMS L1 DSM
Entries 7
sumA - simulated
DSM board

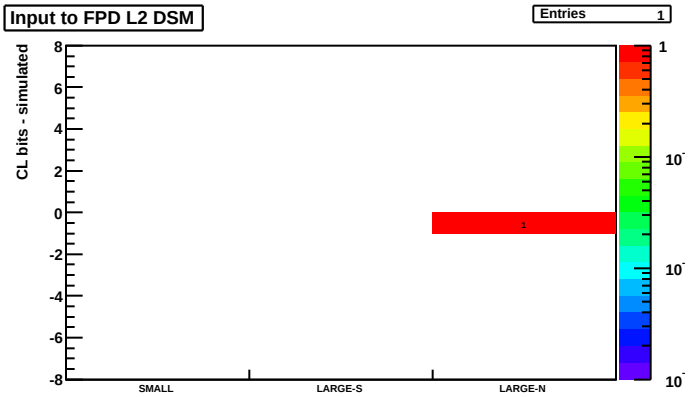

Input to FMS L1 DSM
Entries 1543346
FMS L1 HT bits
DSM board

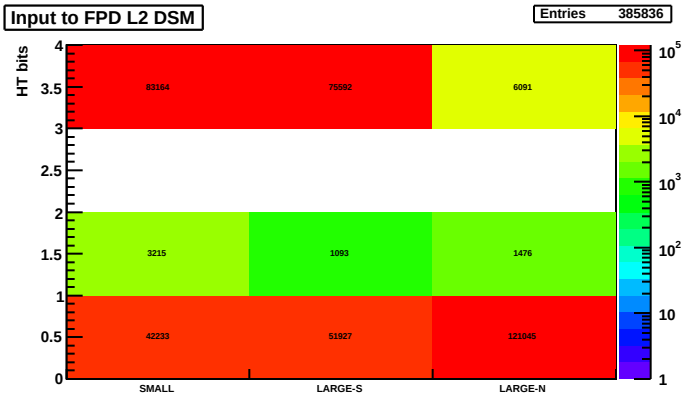

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

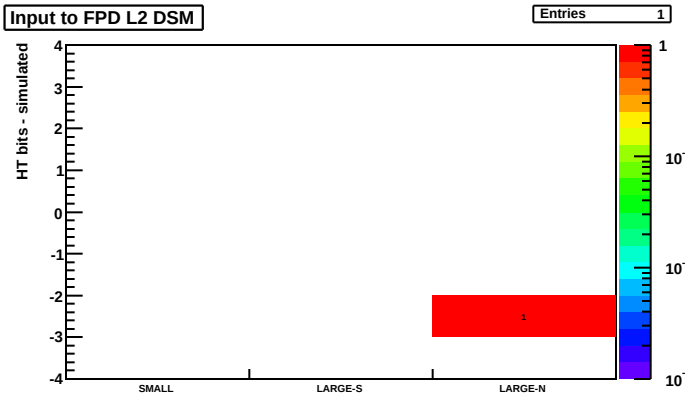

Input to FPD L2 DSM
Entries 1028896
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

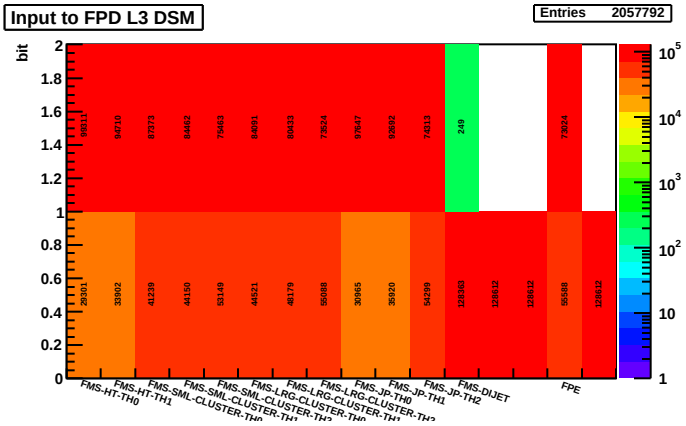

Input to FPD L2 DSM
Entries 1308
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

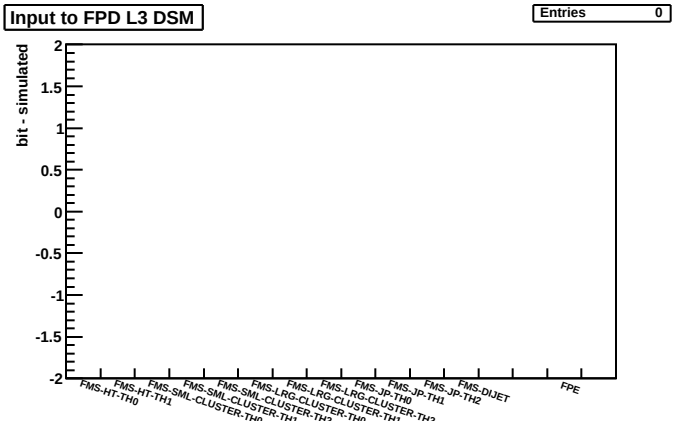

Input to FPD L2 DSM
Entries 385836
CL bits
75463
71642
1971
8999
3351
3562
2911
1733
2121
41239
51886
120958
SMALL
LARGE-S
LARGE-N

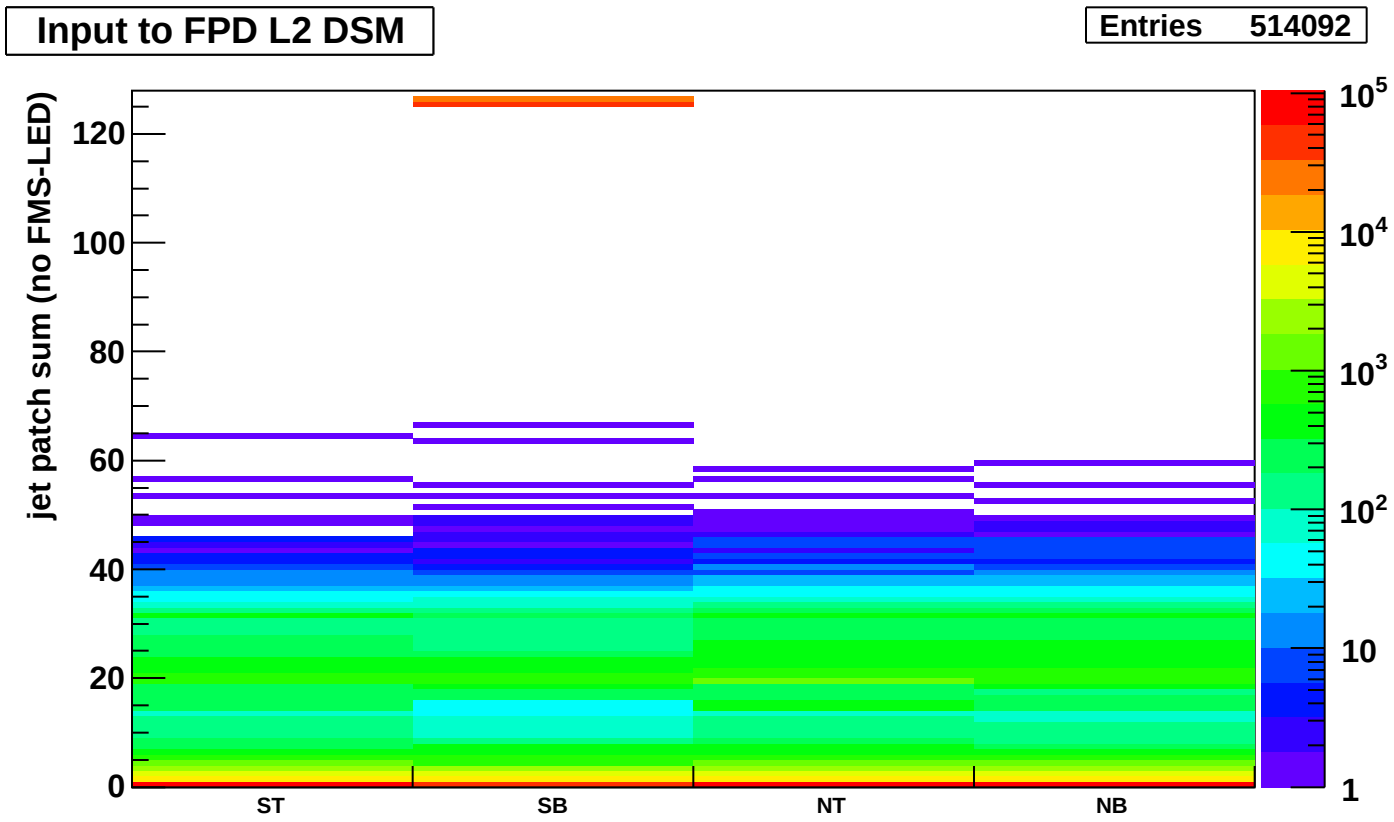

Input to FPD L2 DSM
Entries 1
CL bits - simulated
1
SMALL
LARGE-S
LARGE-N

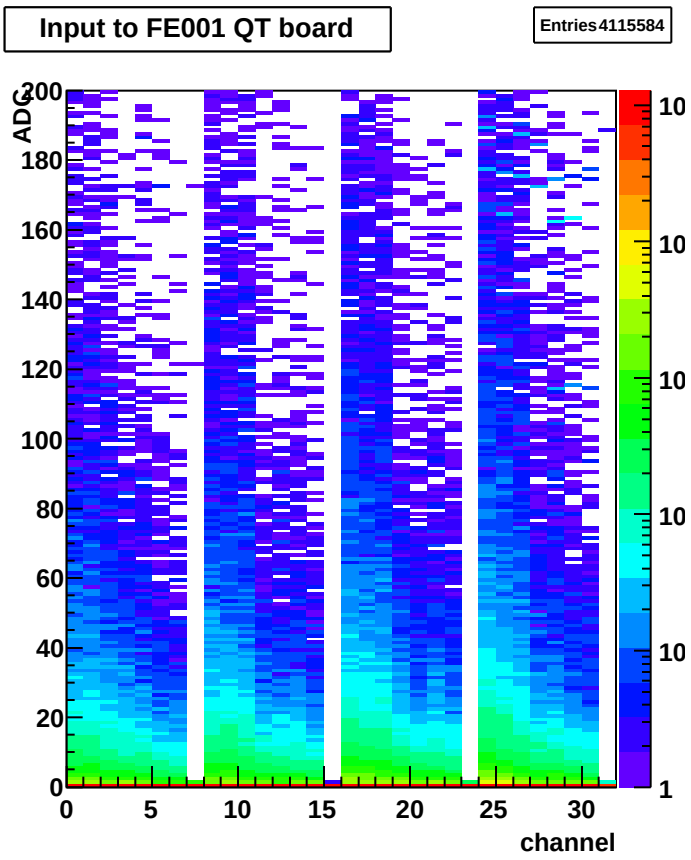

Input to FPD L2 DSM
Entries 385836
HT bits
83164
75592
6091
3215
1093
1476
42233
51927
121045
SMALL
LARGE-S
LARGE-N

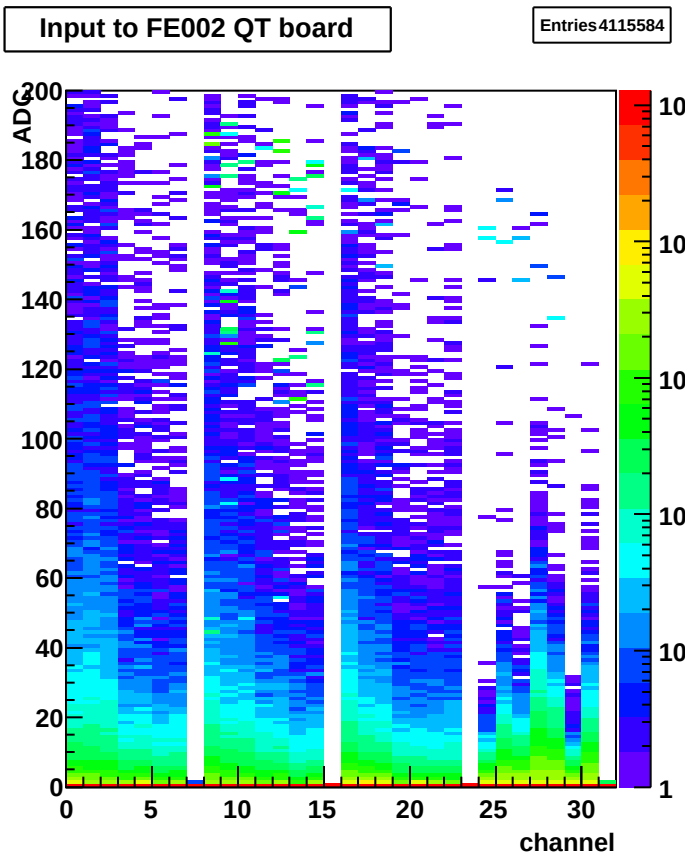

Input to FPD L2 DSM
Entries 1
HT bits - simulated
1
SMALL
LARGE-S
LARGE-N

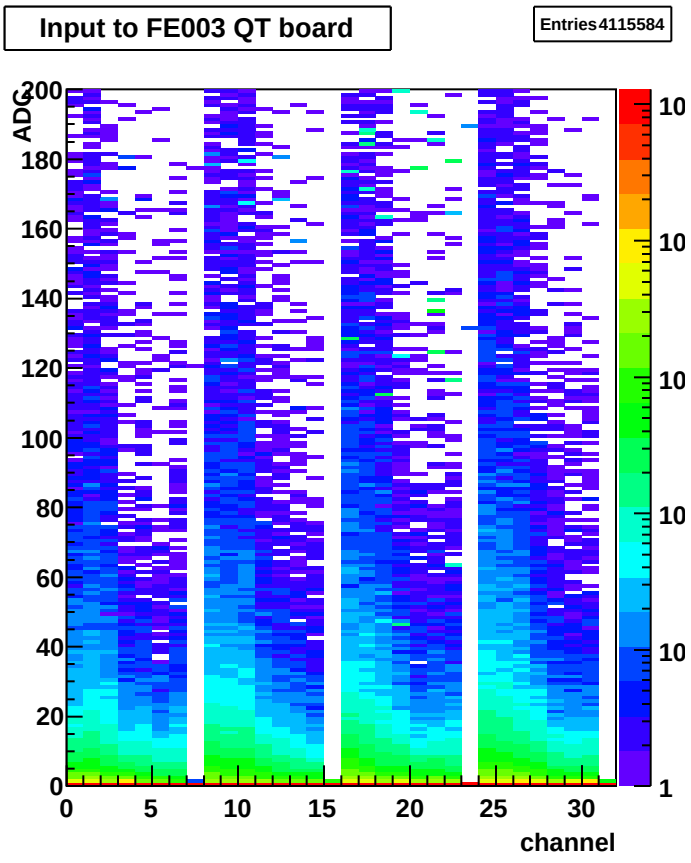

Input to FPD L3 DSM
Entries 2057792
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

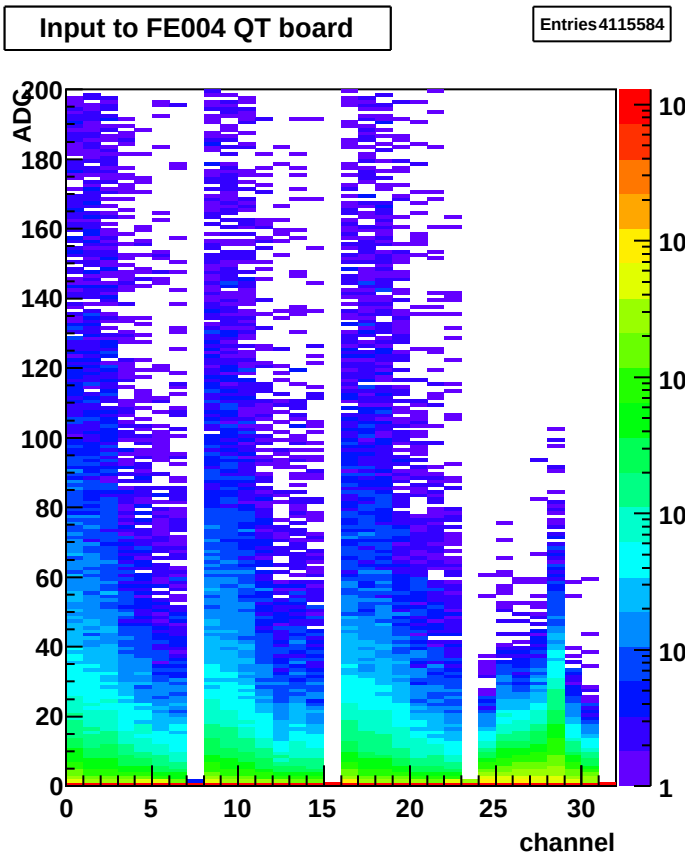

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

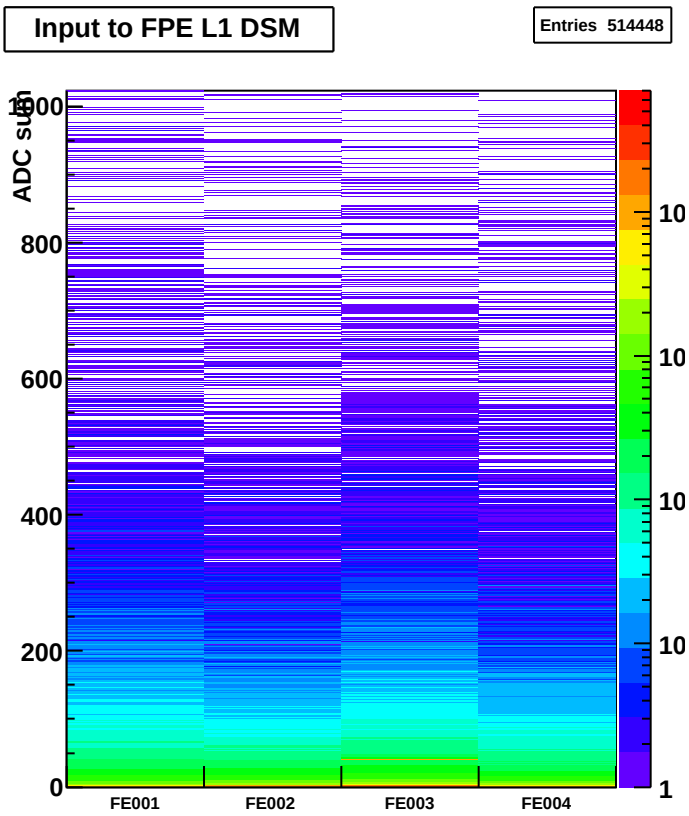

Input to FPD L2 DSM
Entries 514092
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

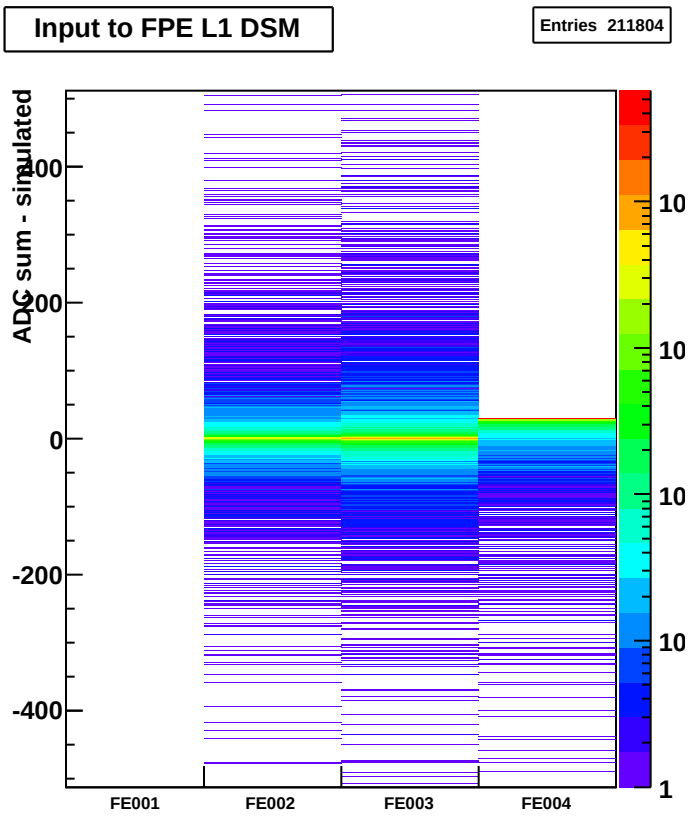

Input to FE001 QT board
Entries 4115584
ADC
channel

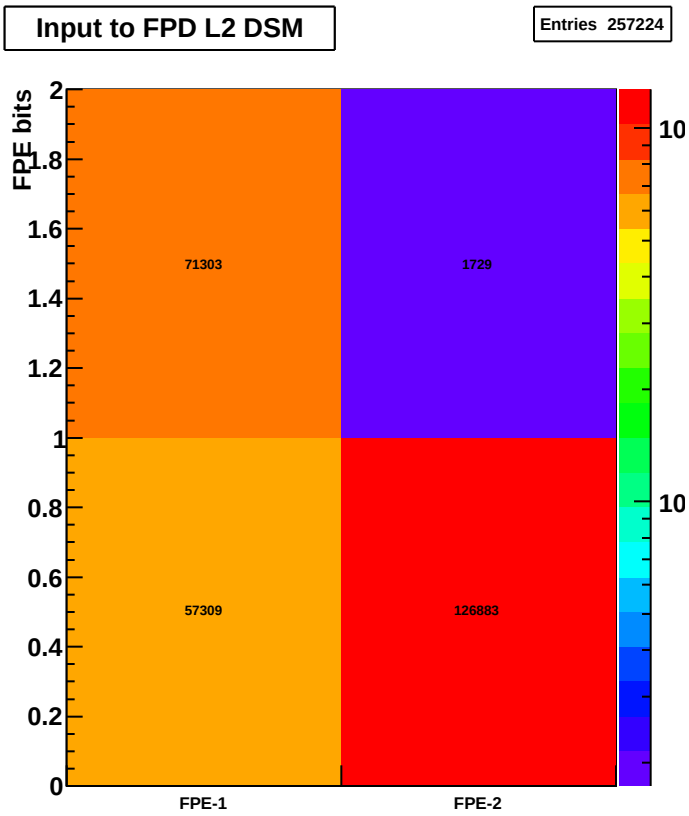

Input to FE002 QT board
Entries 4115584
ADC
channel

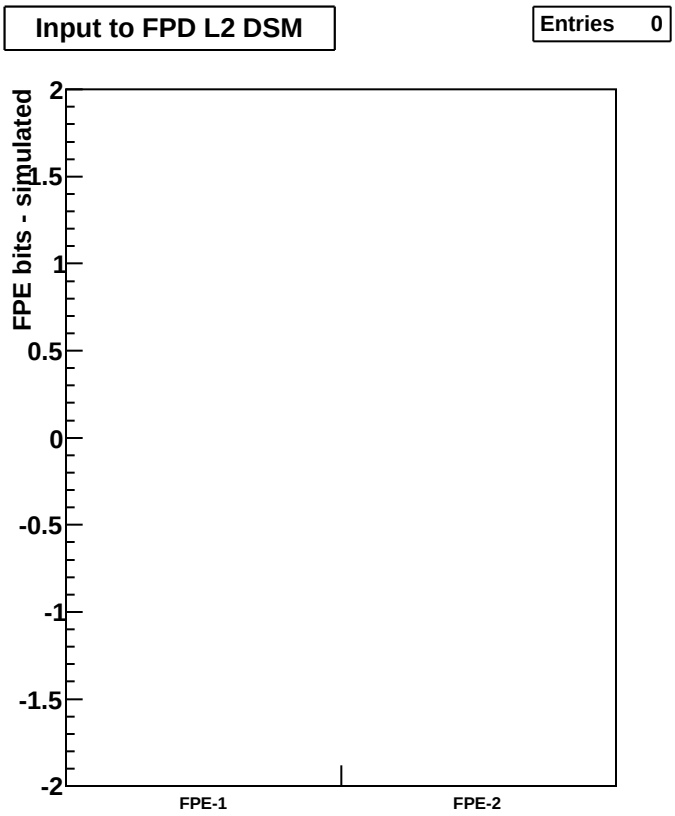

Input to FE003 QT board
Entries 4115584
ADC
channel


Input to FE004 QT board
Entries 4115584
ADC
channel


Input to FPE L1 DSM
Entries 514448
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 211804
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPD L2 DSM
Entries 257224
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
71303
1729
57309
126883
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2