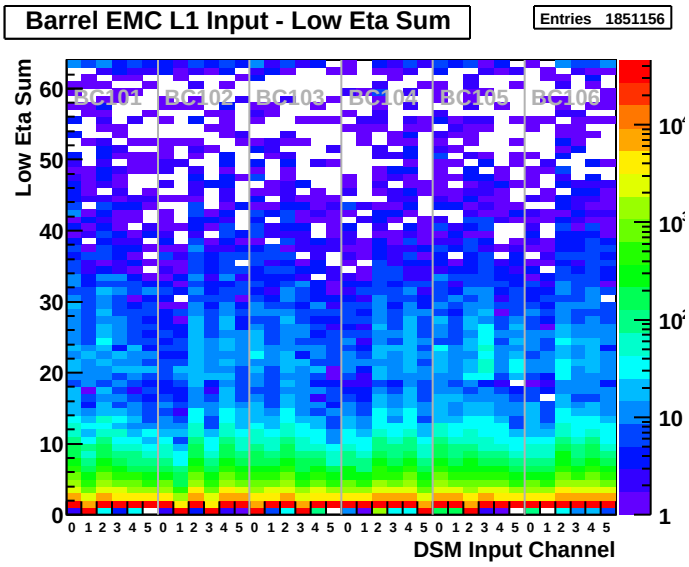
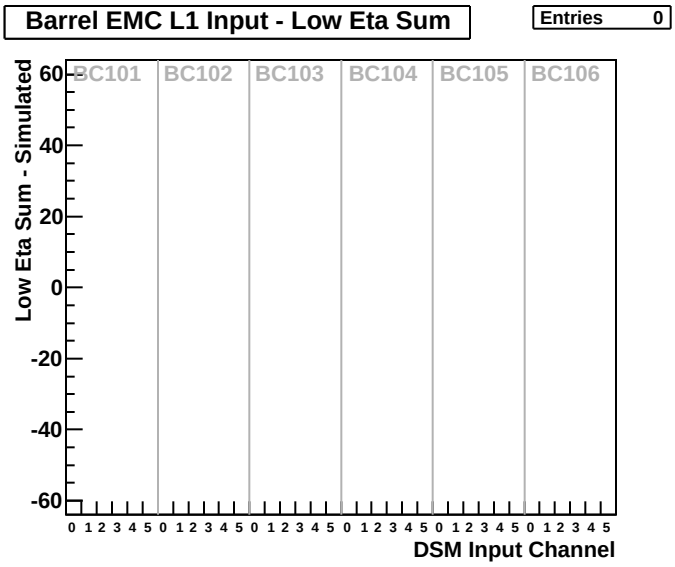
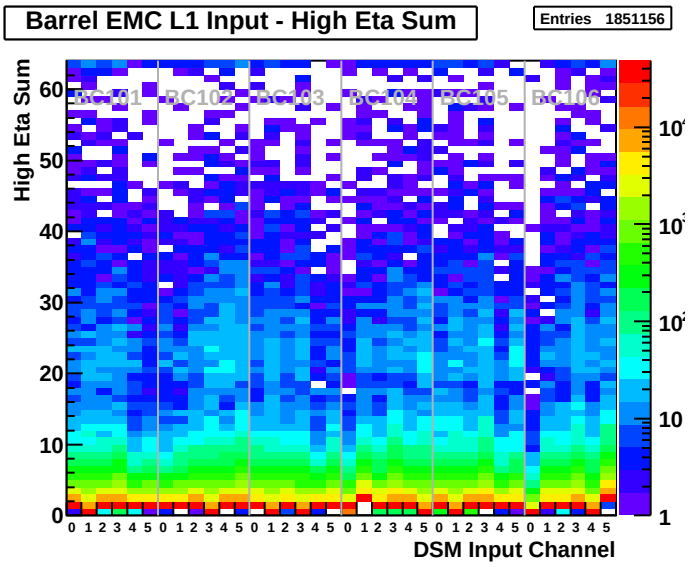
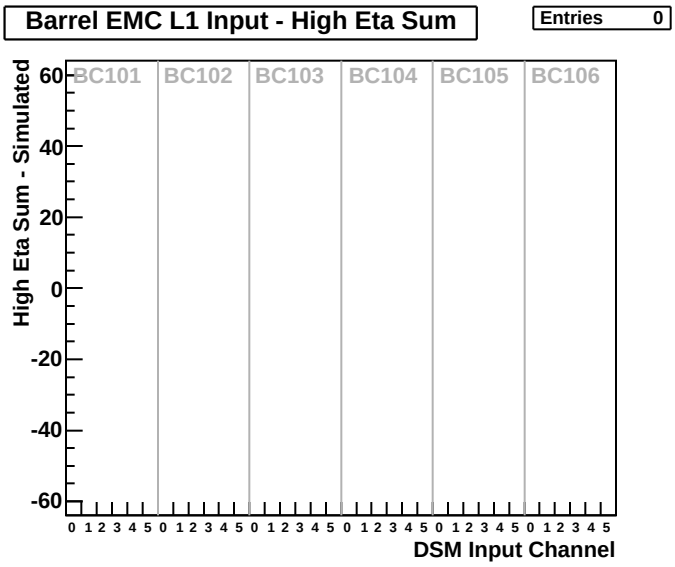
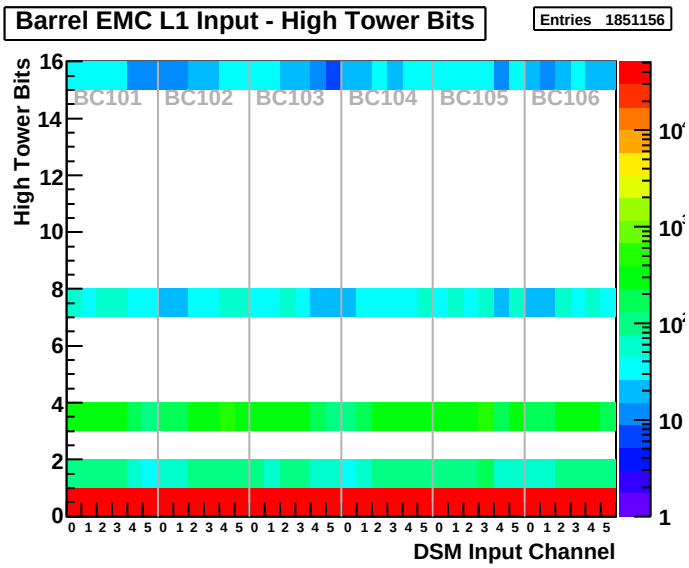
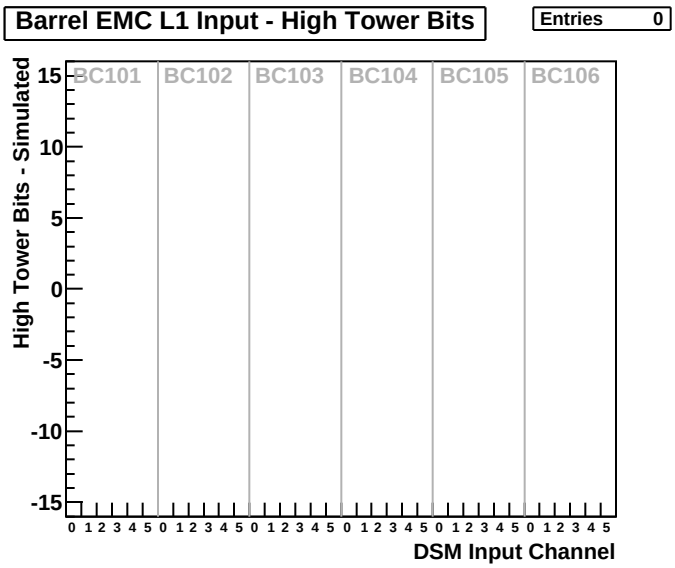

Barrel EMC L1 Input - Low Eta Sum
Entries 1851156
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

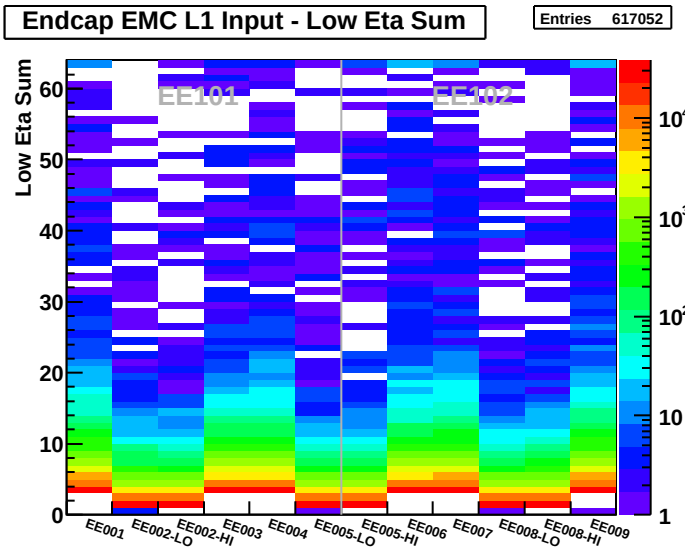

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

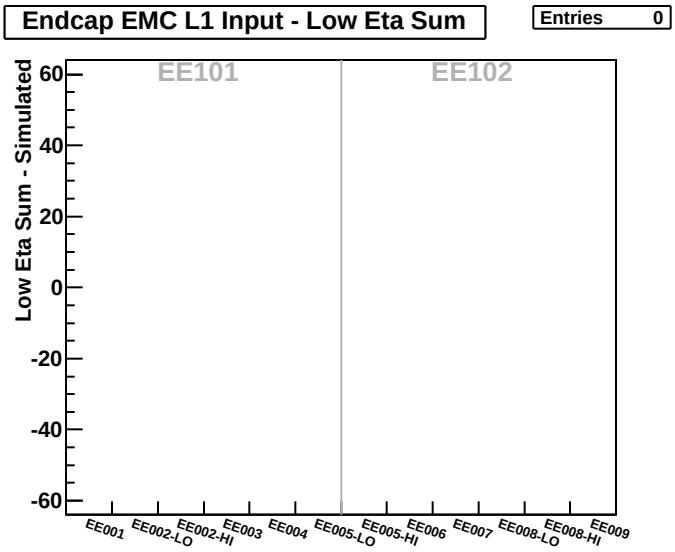

Barrel EMC L1 Input - High Eta Sum
Entries 1851156
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

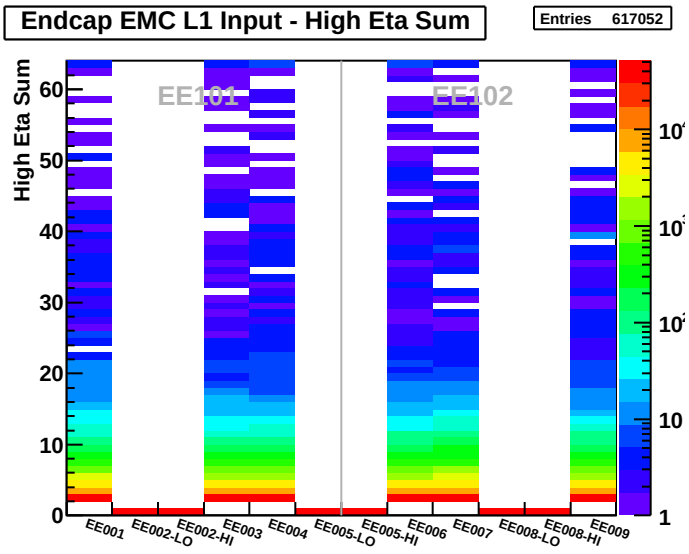

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

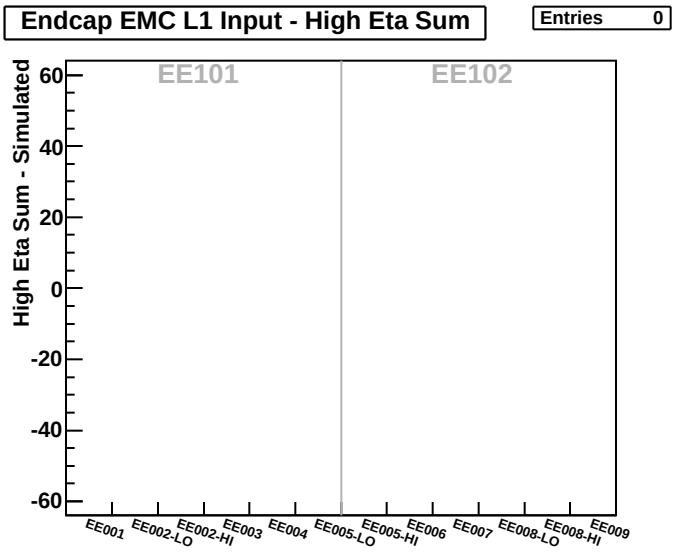

Barrel EMC L1 Input - High Tower Bits
Entries 1851156
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

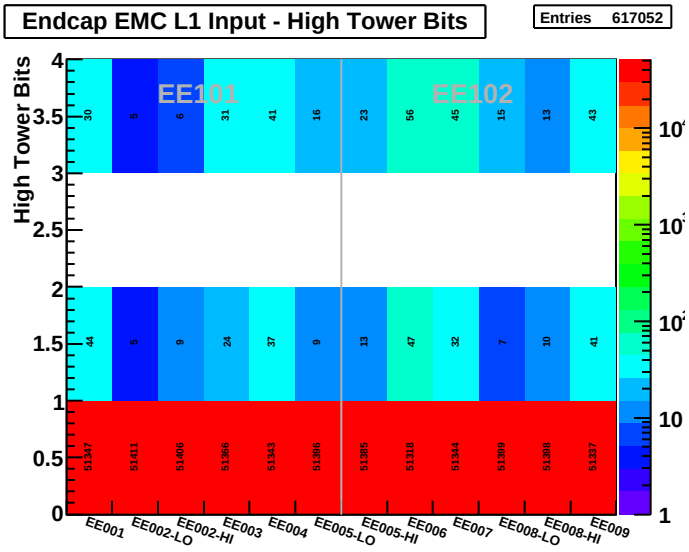

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

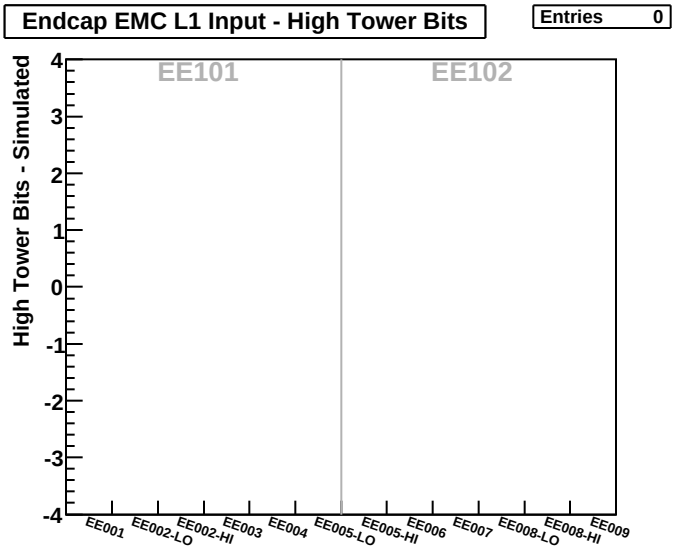

Endcap EMC L1 Input - Low Eta Sum
Entries 617052
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

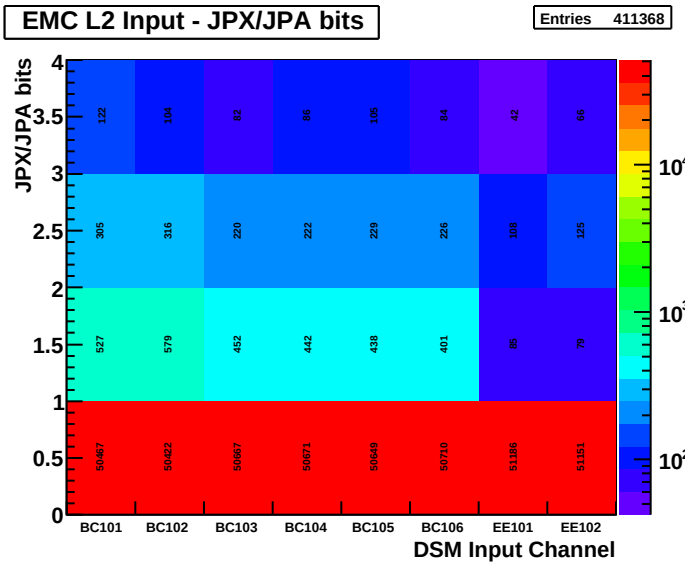

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

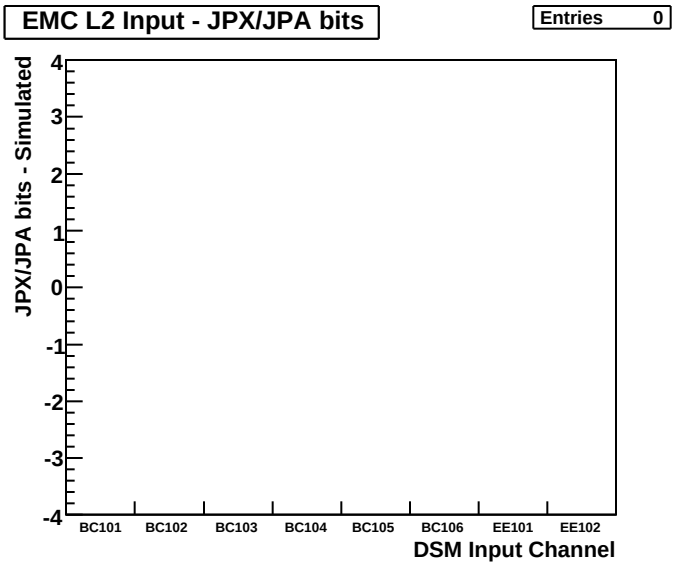

Endcap EMC L1 Input - High Eta Sum
Entries 617052
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

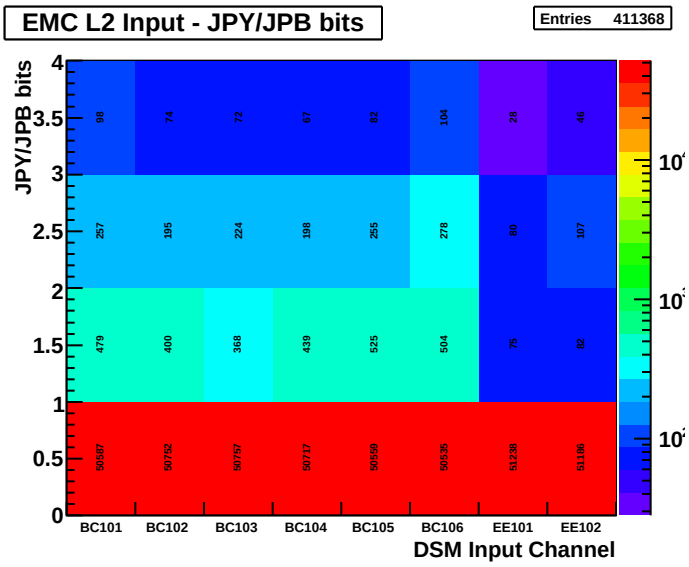

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

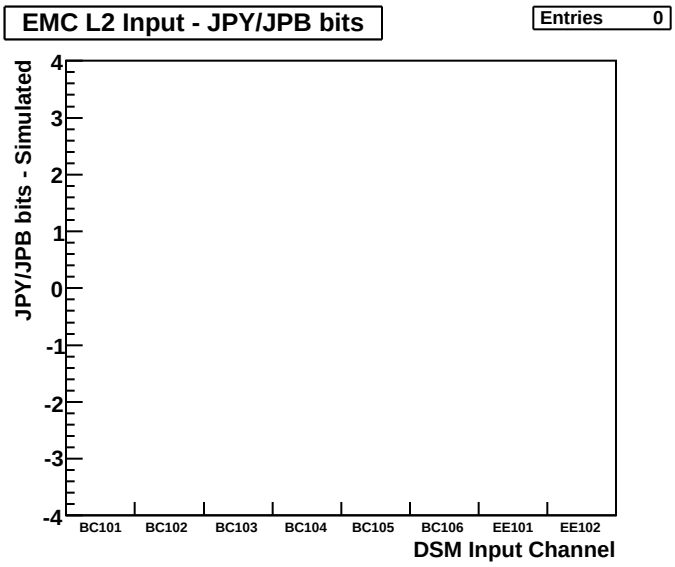

Endcap EMC L1 Input - High Tower Bits
Entries 617052
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

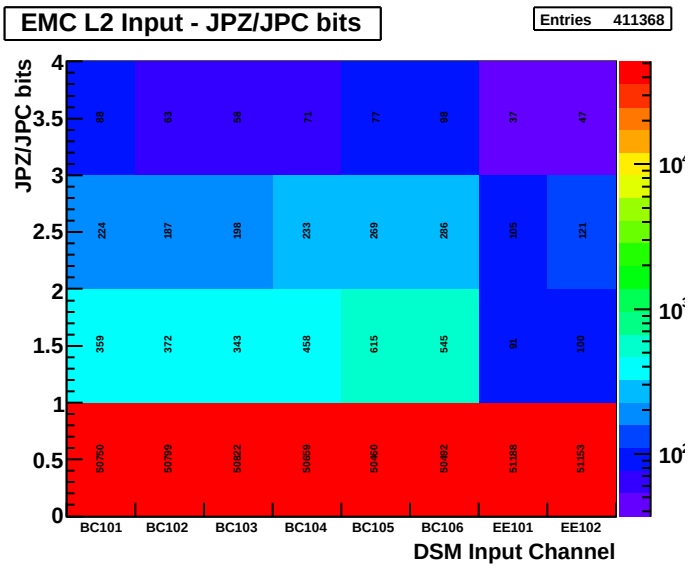

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

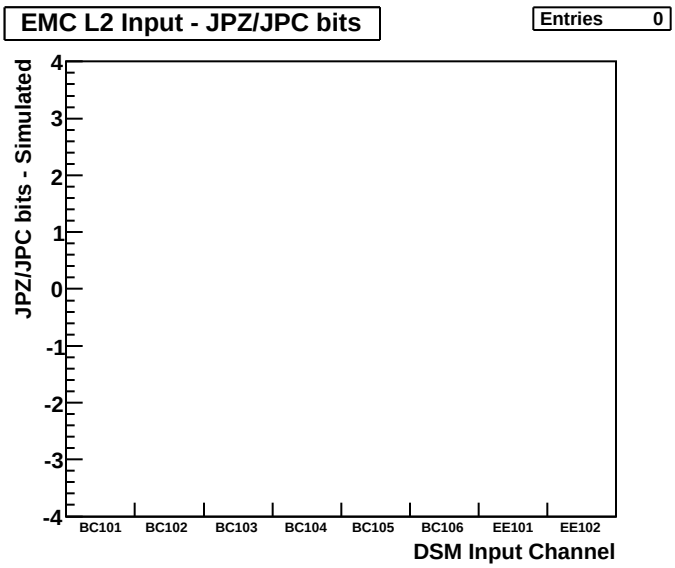

EMC L2 Input - JPX/JPA bits
Entries 411368
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
122 104 82 86 105 84 42 66
305 316 220 222 229 226 108 125
527 579 452 442 438 401 85 79
50467 50422 50667 50671 50649 50710 51186 51151

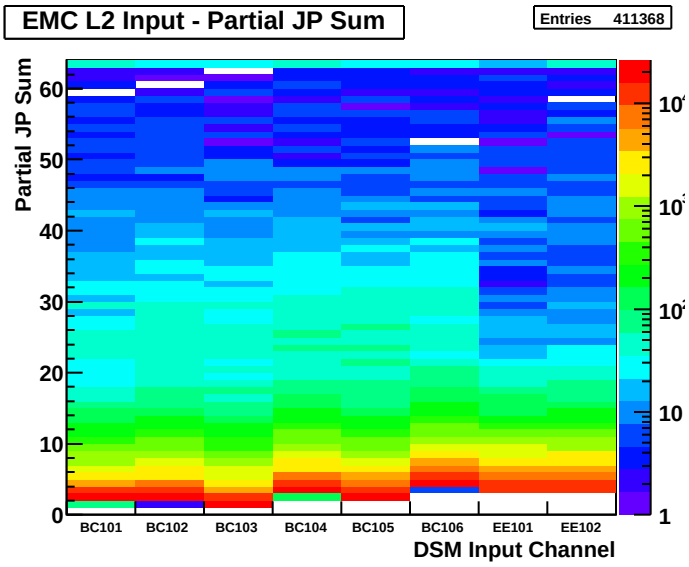

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

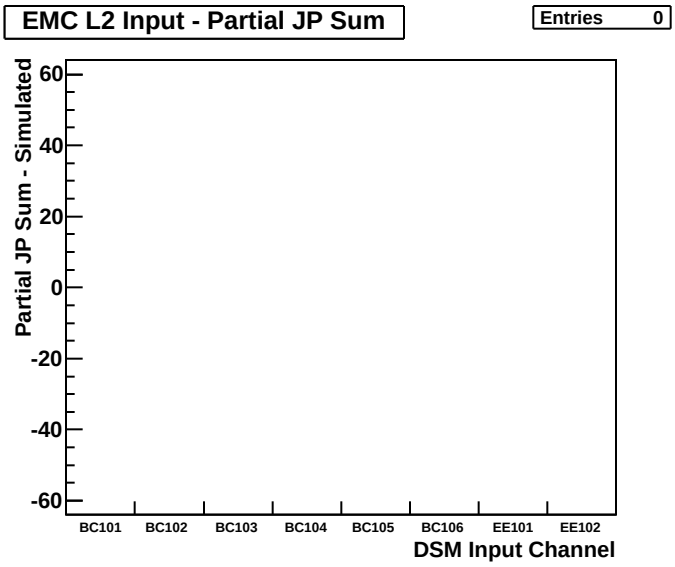

EMC L2 Input - JPY/JPB bits
Entries 411368
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
98 74 72 67 82 104 28 46
257 195 224 198 255 278 80 107
479 400 368 439 525 504 75 82
50587 50752 50757 50717 50559 50535 51238 51186

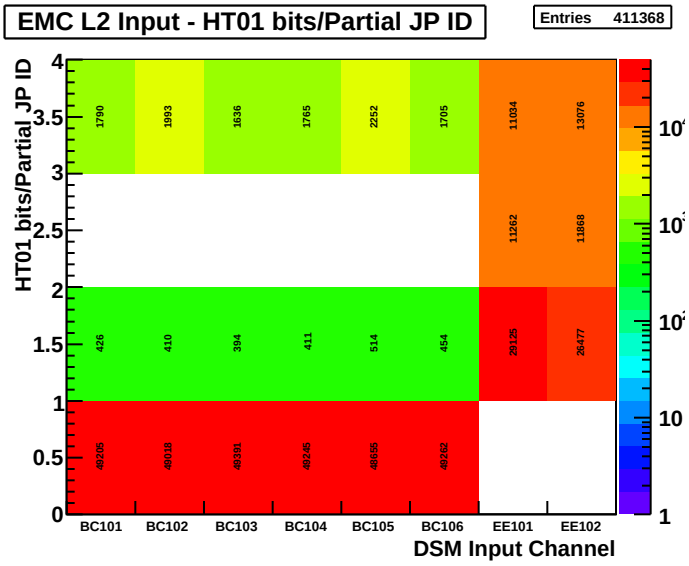

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

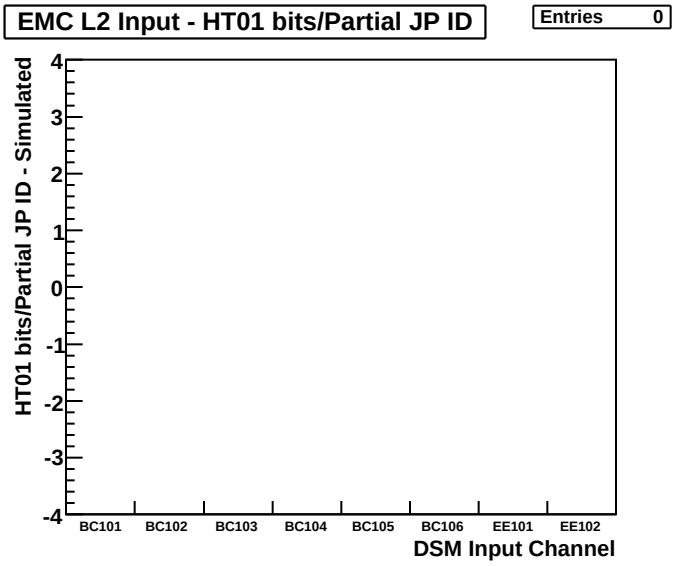

EMC L2 Input - JPZ/JPC bits
Entries 411368
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
88 63 59 71 77 98 37 47
224 187 198 233 269 286 105 121
359 372 343 458 615 545 91 100
50750 50799 50822 50659 50460 50492 51188 51153

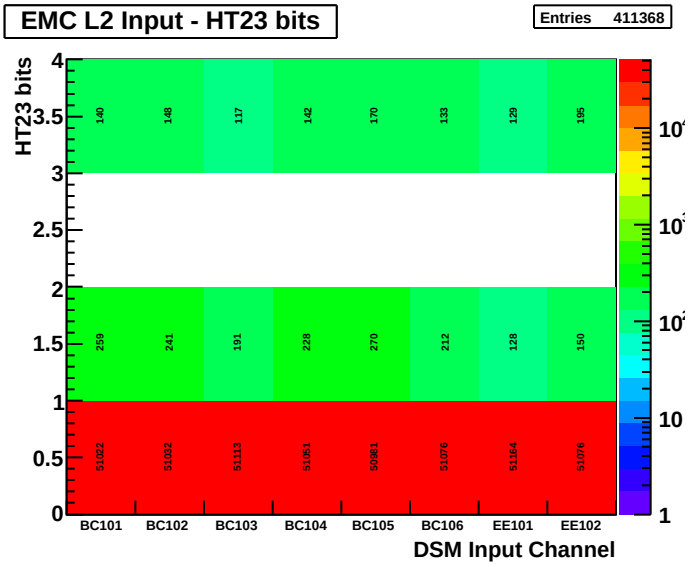

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

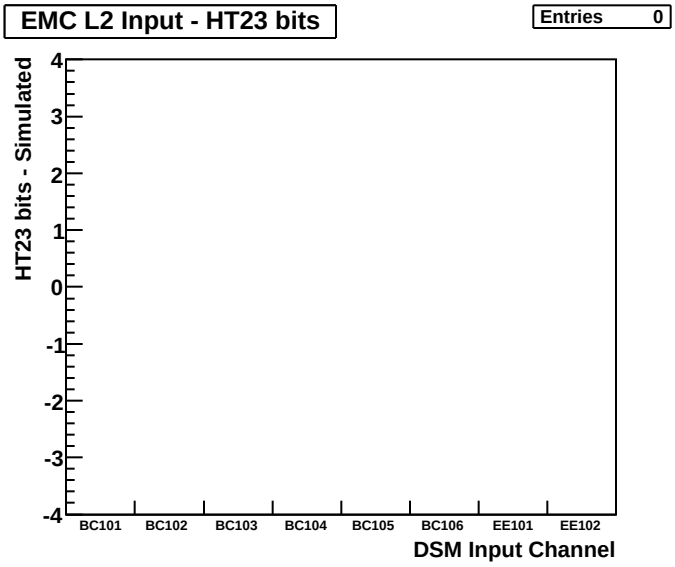

EMC L2 Input - Partial JP Sum
Entries 411368
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

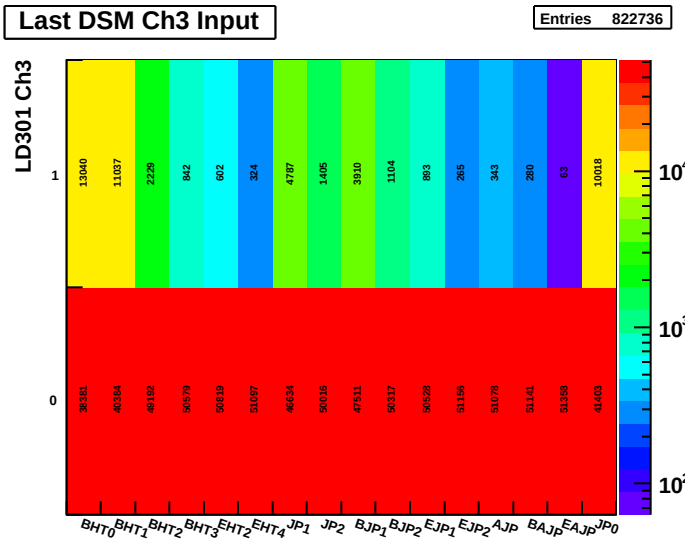

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

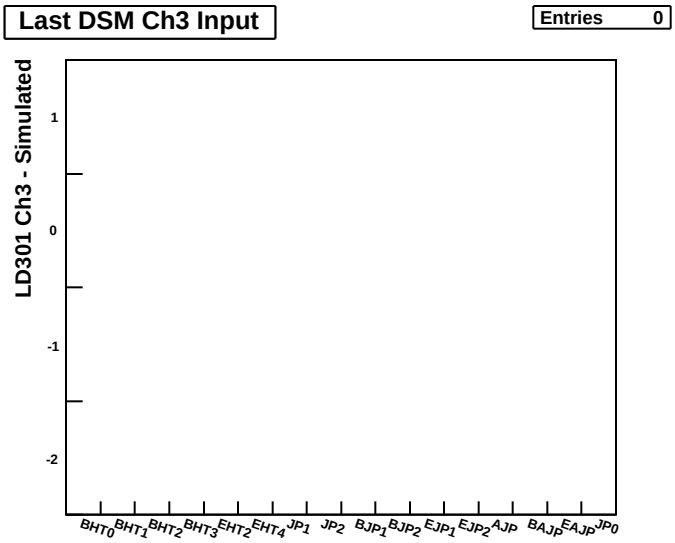

EMC L2 Input - HT01 bits/Partial JP ID
Entries 411368
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

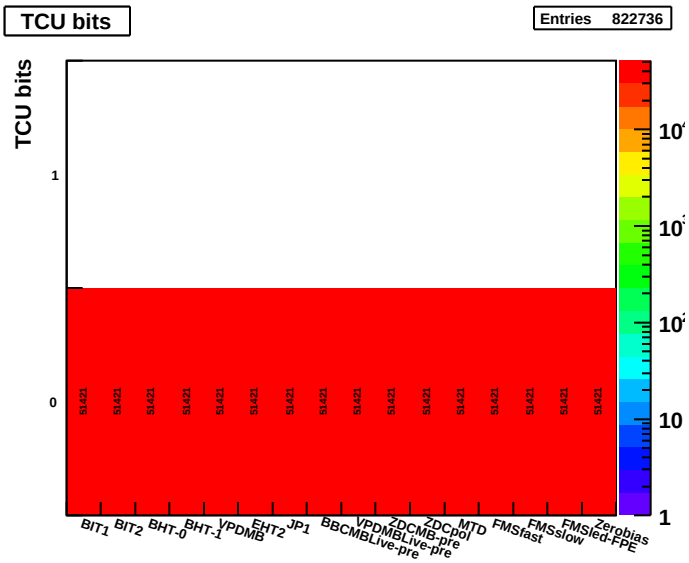

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

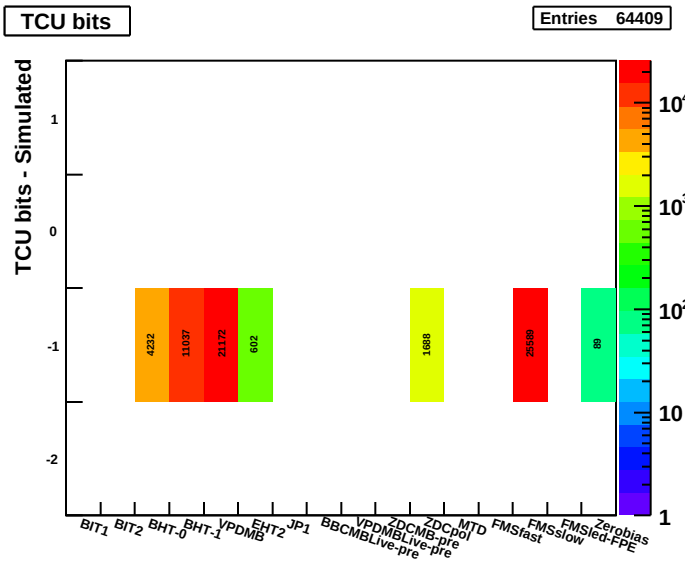

EMC L2 Input - HT23 bits
Entries 411368
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

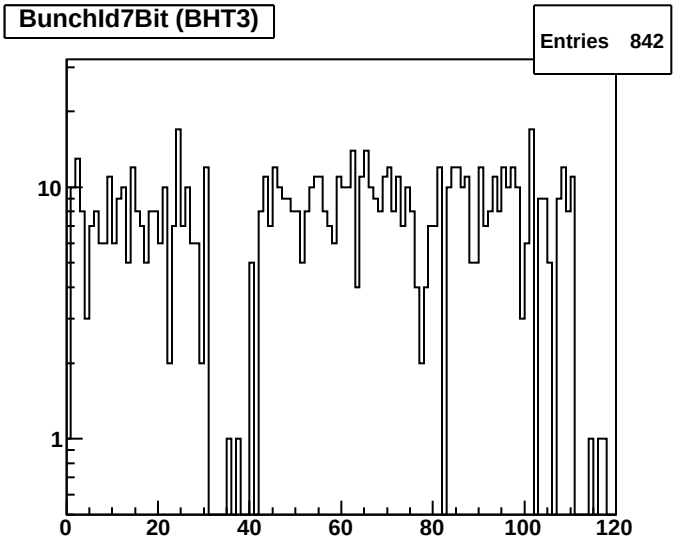

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

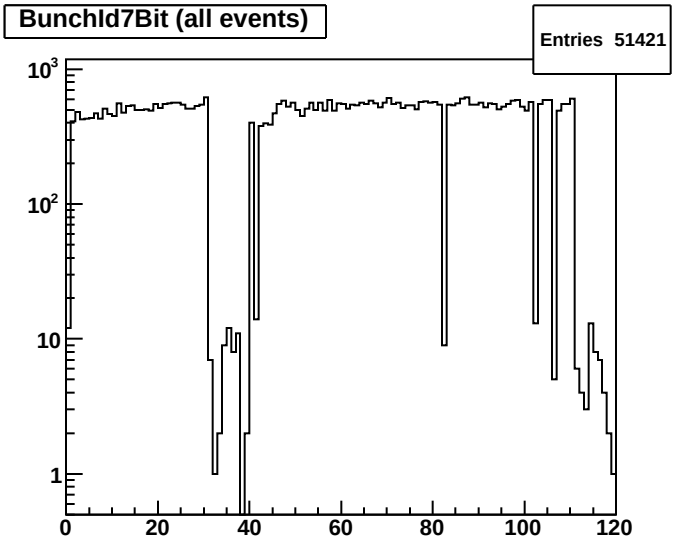

Last DSM Ch3 Input
Entries 822736
LD301 Ch3
13040 11037 2229 842 602 324 4787 1405 3910 1104 893 265 343 280 63 10018
38381 40384 49192 50579 50819 51097 46634 50016 47511 50317 50528 51156 51078 51141 51358 41403
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

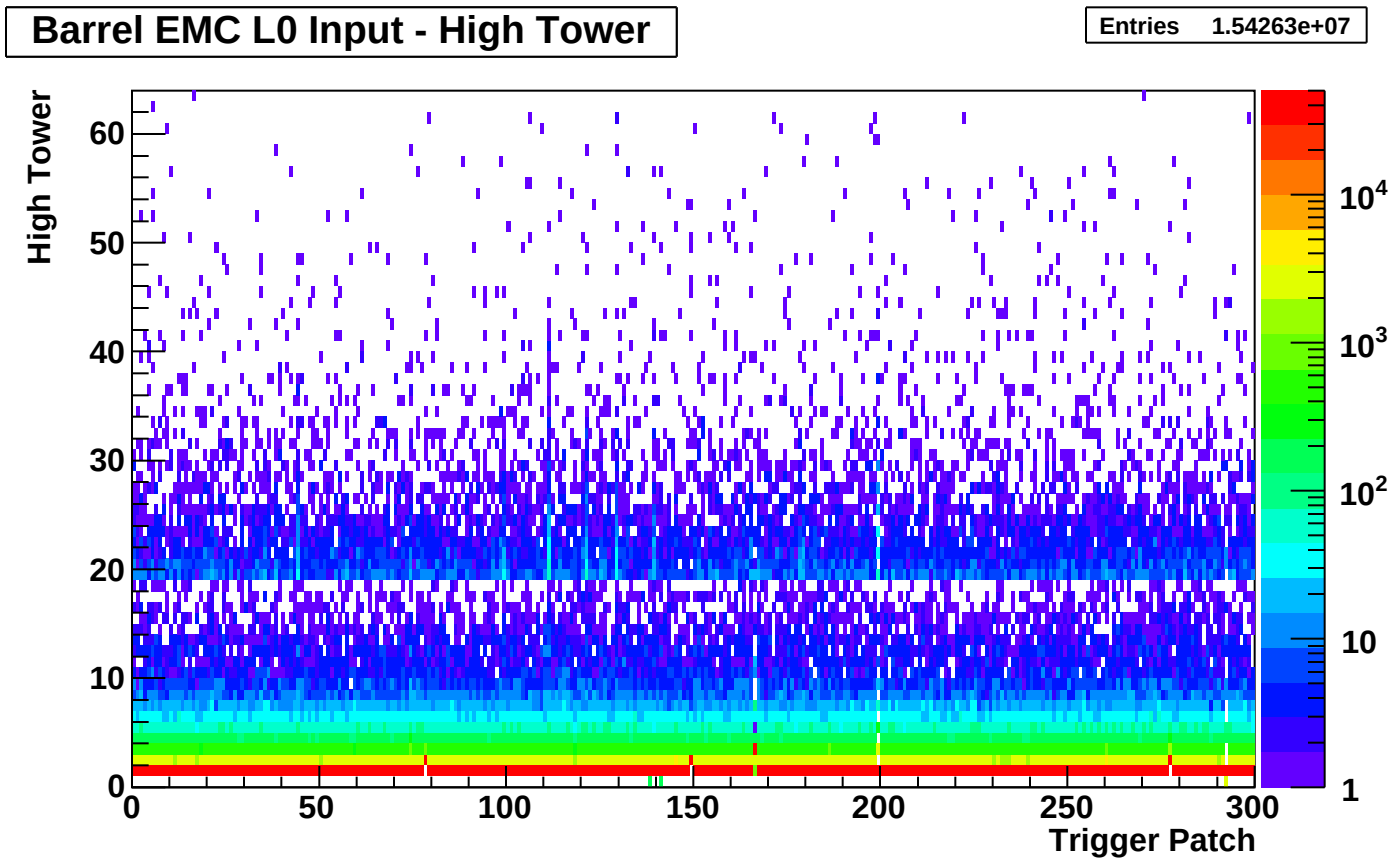

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

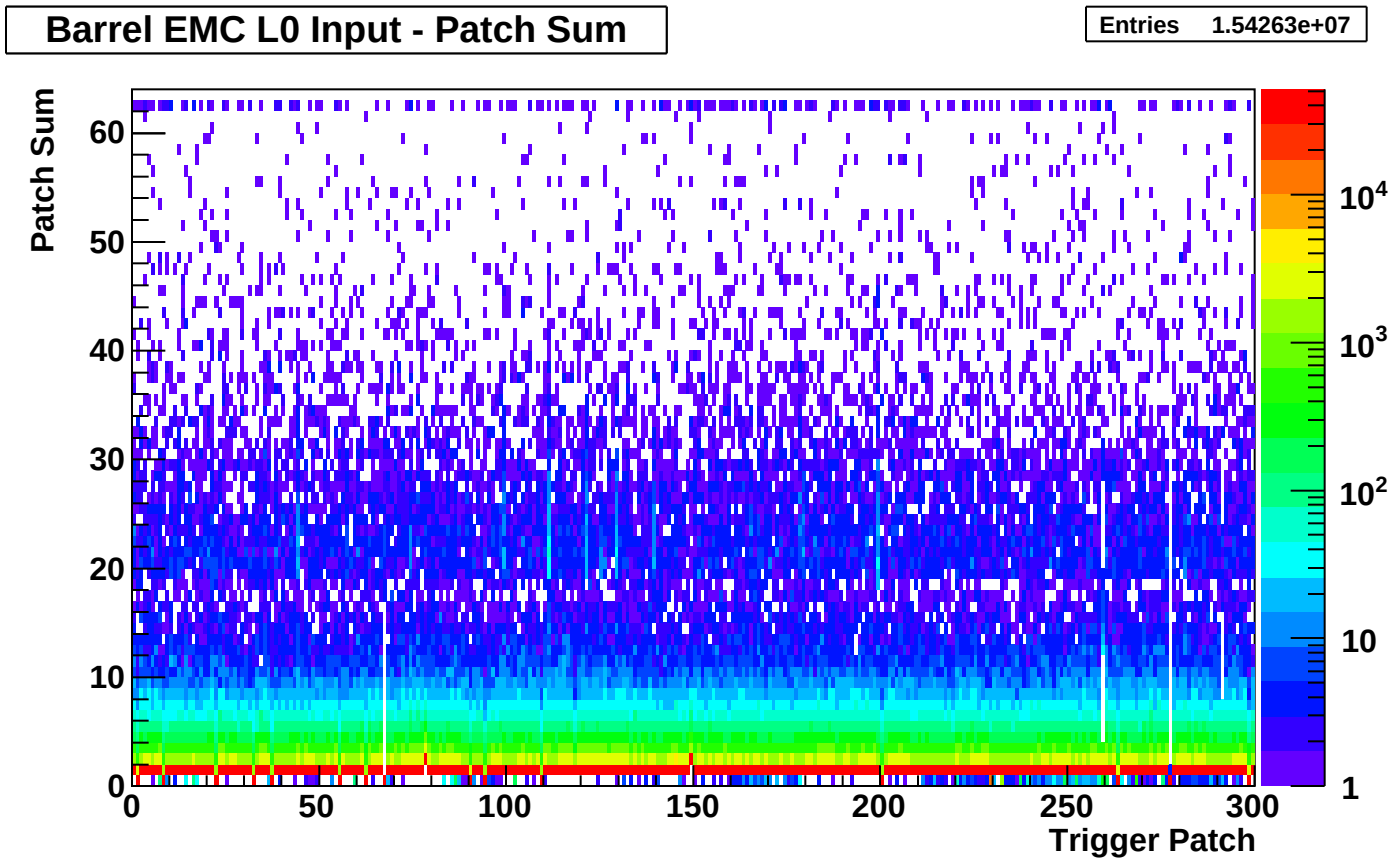

TCU bits
Entries 822736
TCU bits
51421 51421 51421 51421 51421 51421 51421 51421 51421 51421 51421 51421 51421 51421 51421 51421
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

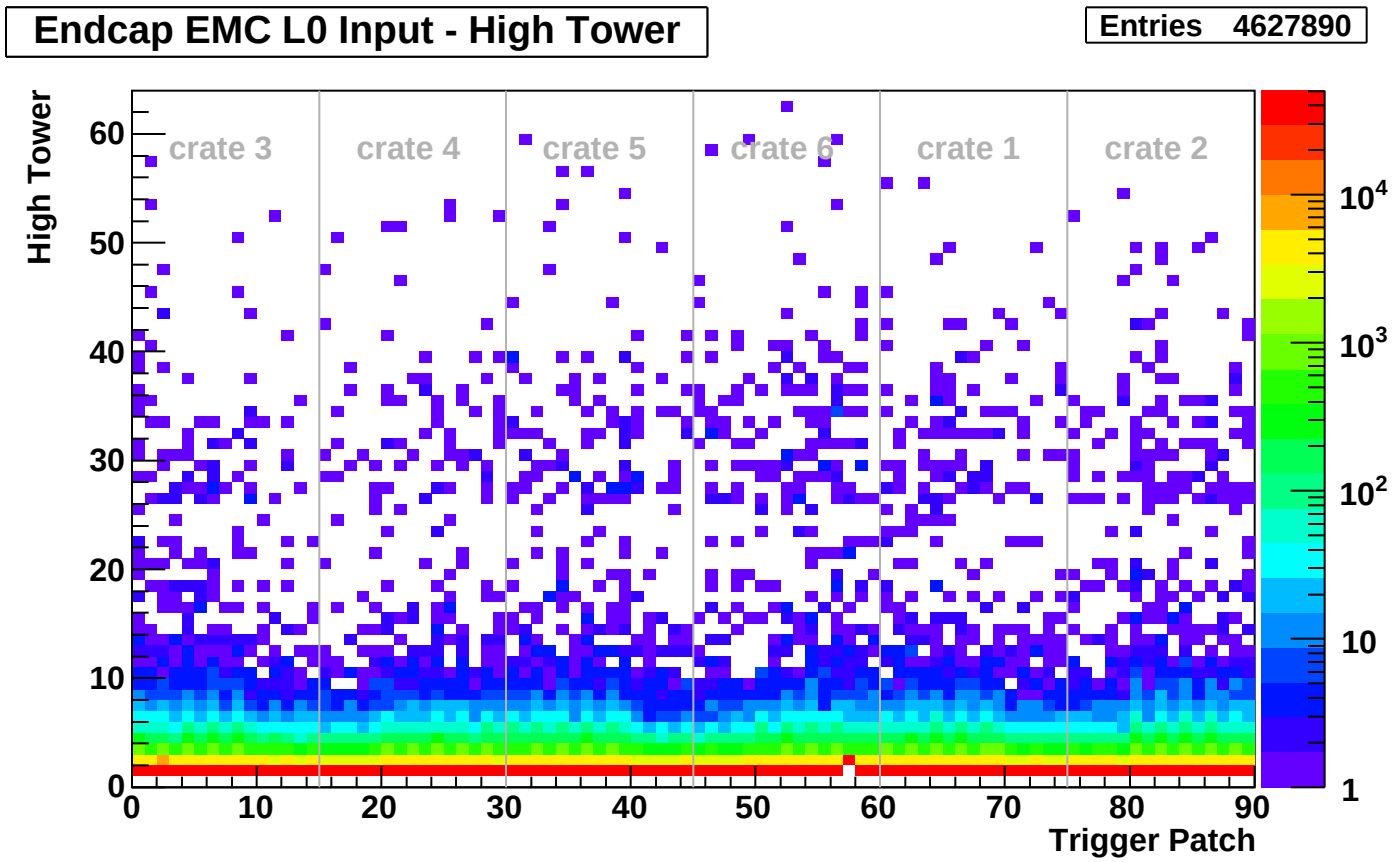

TCU bits
Entries 64409
TCU bits - Simulated
4232 11037 21172 602 1888 25589 89
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

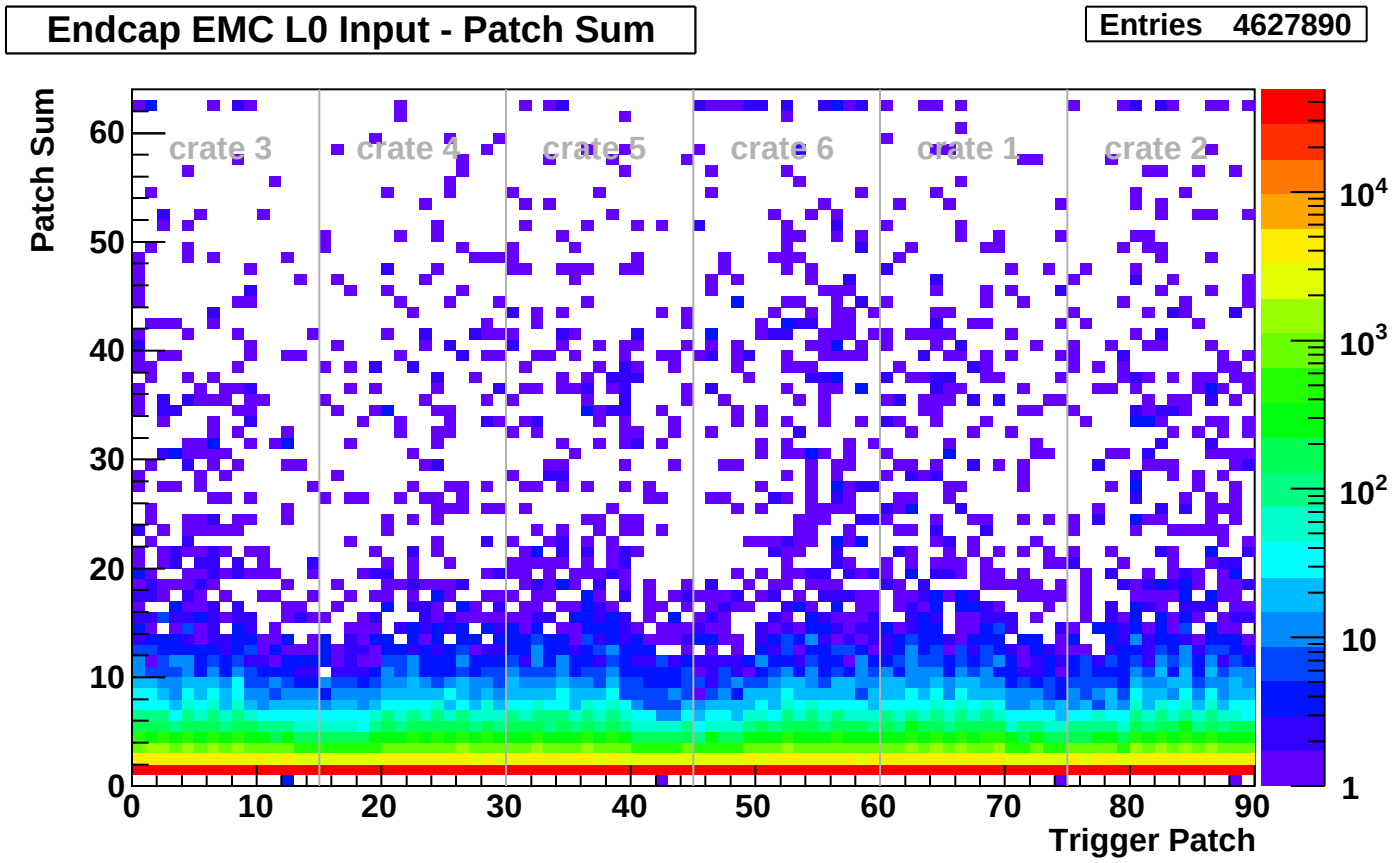

BunchId7Bit (BHT3)
Entries 842

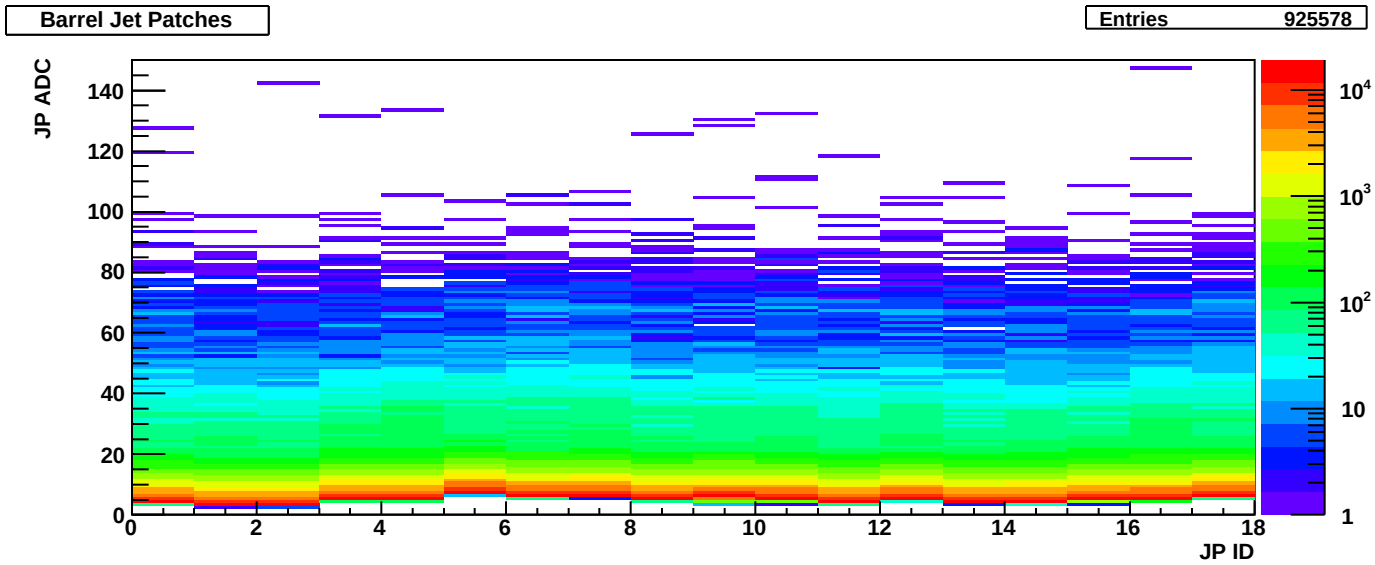

BunchId7Bit (all events)
Entries 51421

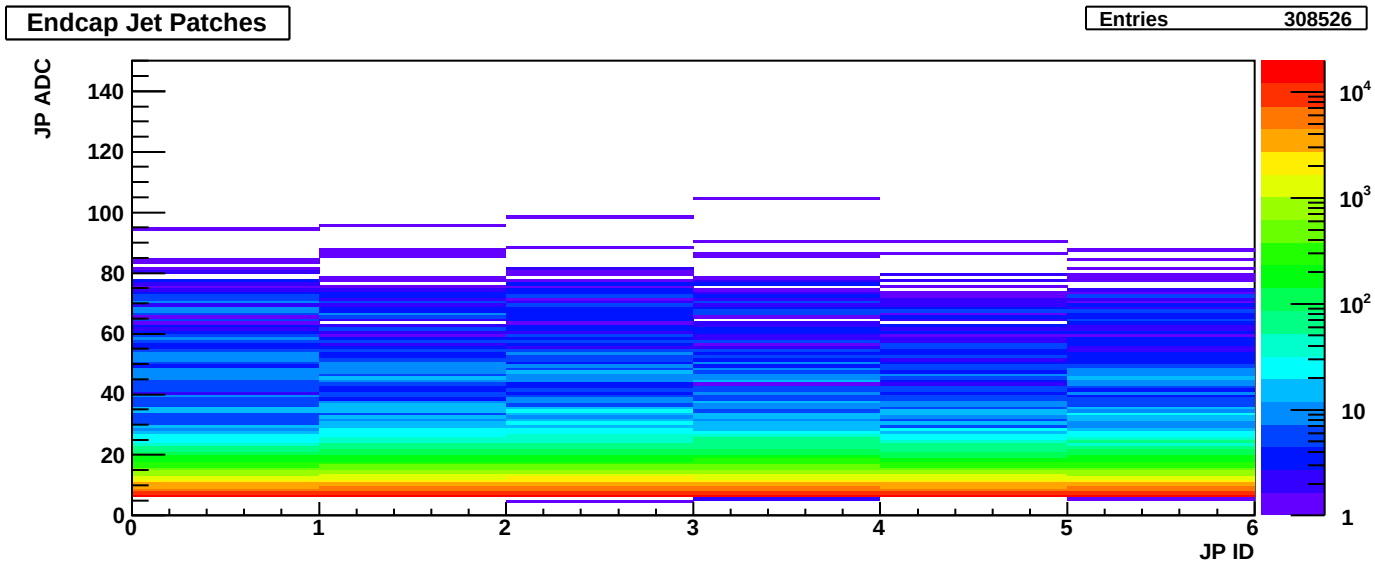

Barrel EMC L0 Input - High Tower
Entries 1.54263e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

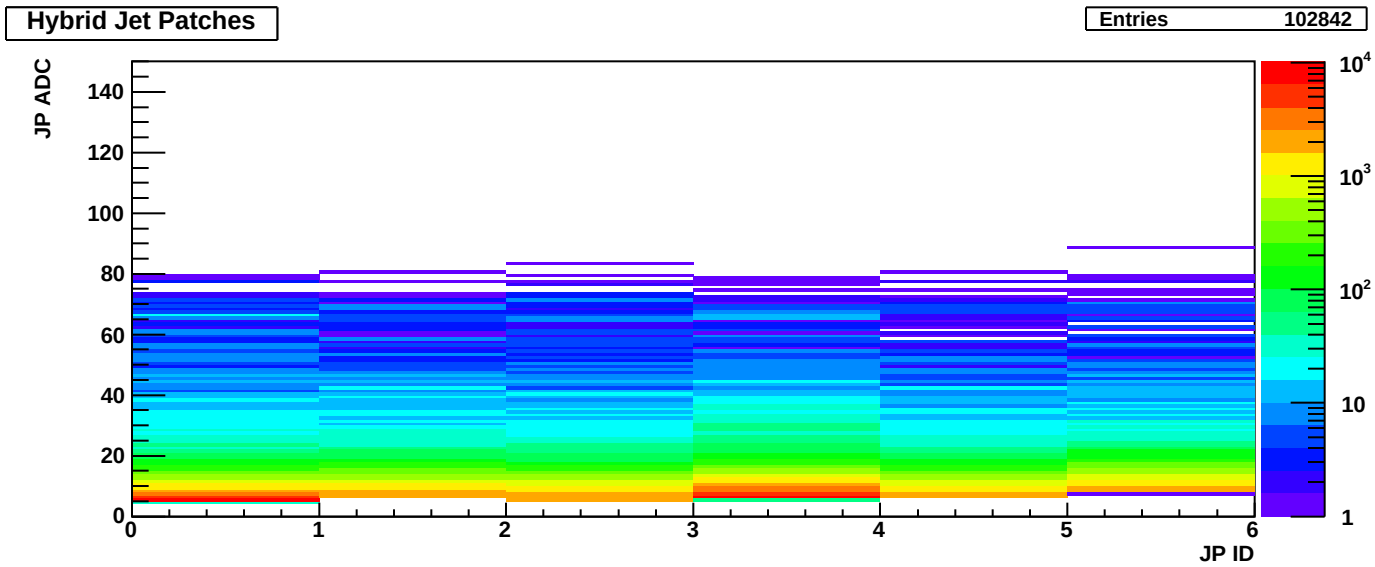

Barrel EMC L0 Input - Patch Sum
Entries 1.54263e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

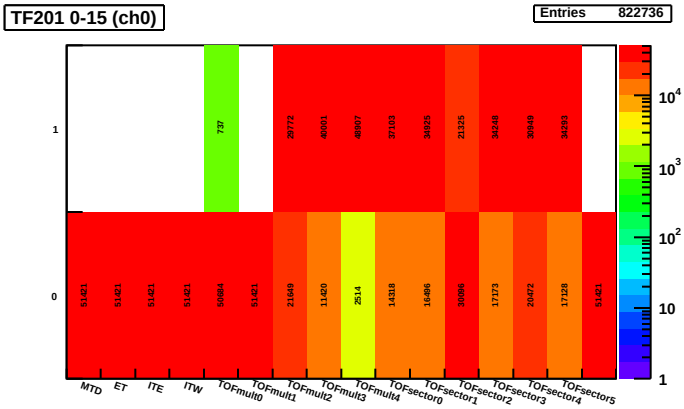

Endcap EMC L0 Input - High Tower
Entries 4627890
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

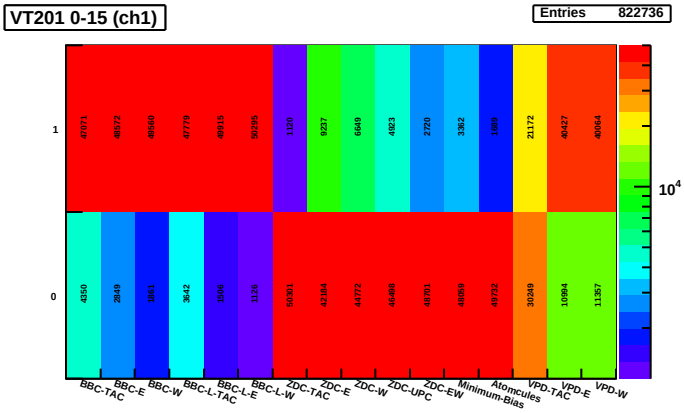

Endcap EMC L0 Input - Patch Sum
Entries 4627890
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

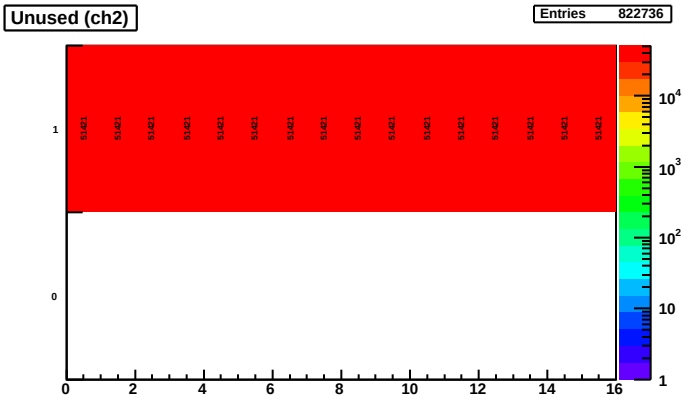

Barrel Jet Patches
Entries 925578
JP ADC
JP ID

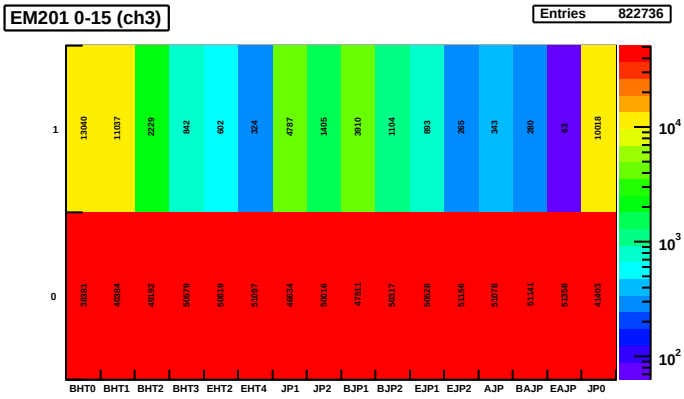

Endcap Jet Patches
Entries 308526
JP ADC
JP ID

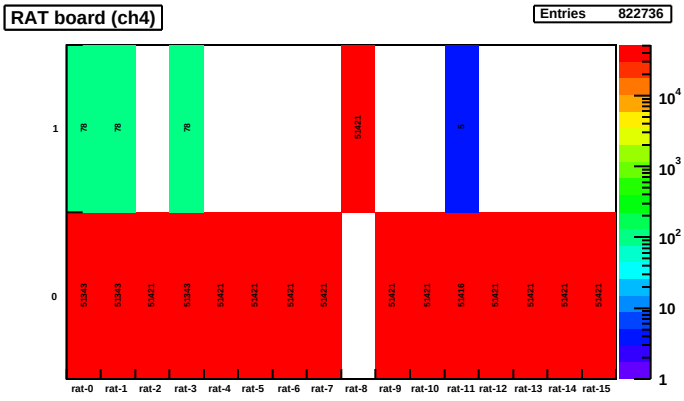

Hybrid Jet Patches
Entries 102842
JP ADC
JP ID

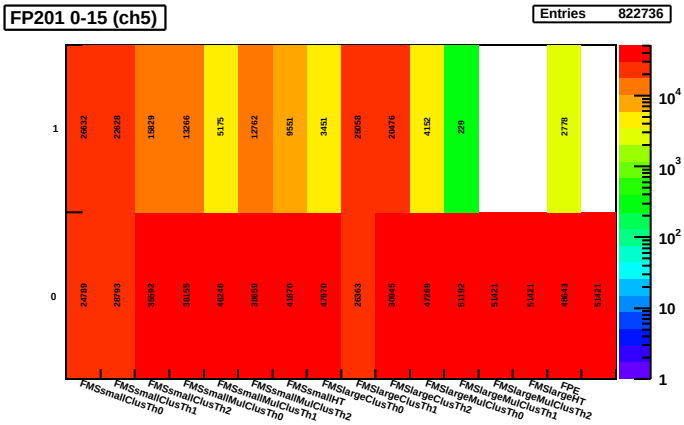

TF201 0-15 (ch0)
Entries 822736
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

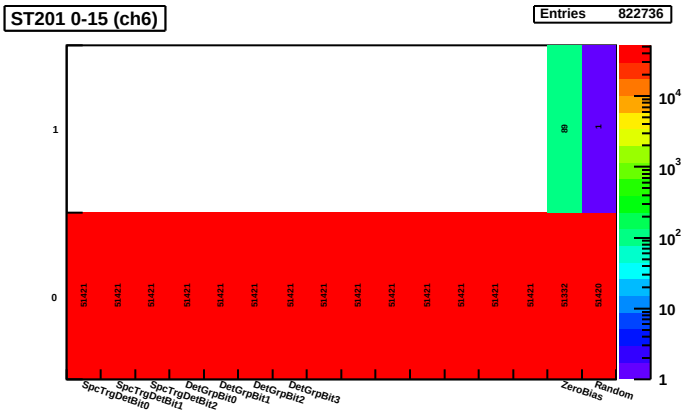

VT201 0-15 (ch1)
Entries 822736
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Atomcules Minimum-Bias VPD-TAC VPD-E VPD-W

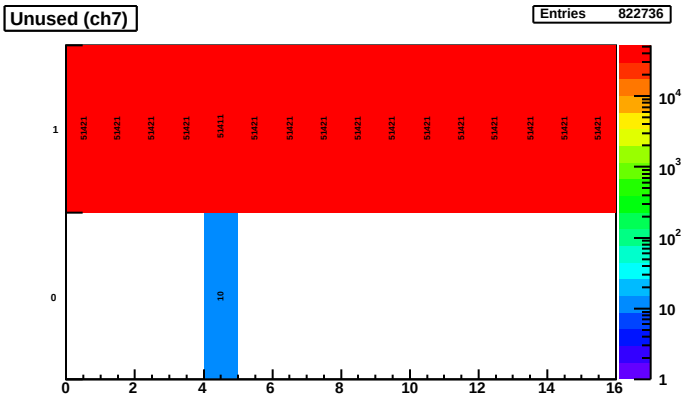

Unused (ch2)
Entries 822736

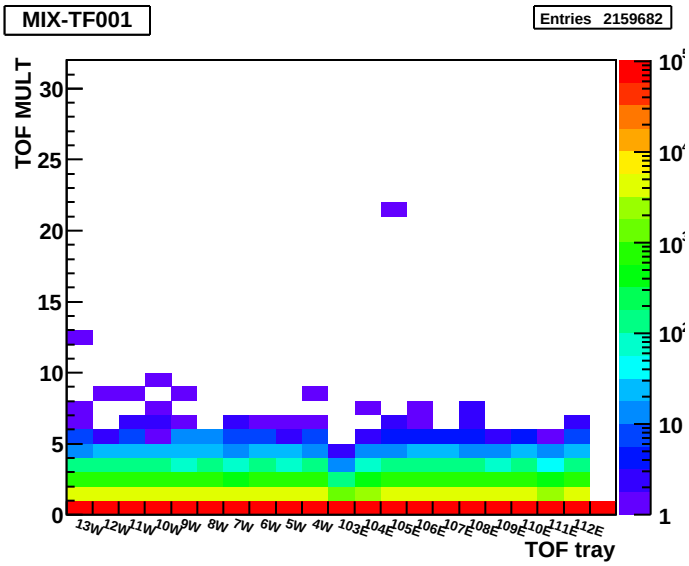

EM201 0-15 (ch3)
Entries 822736
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

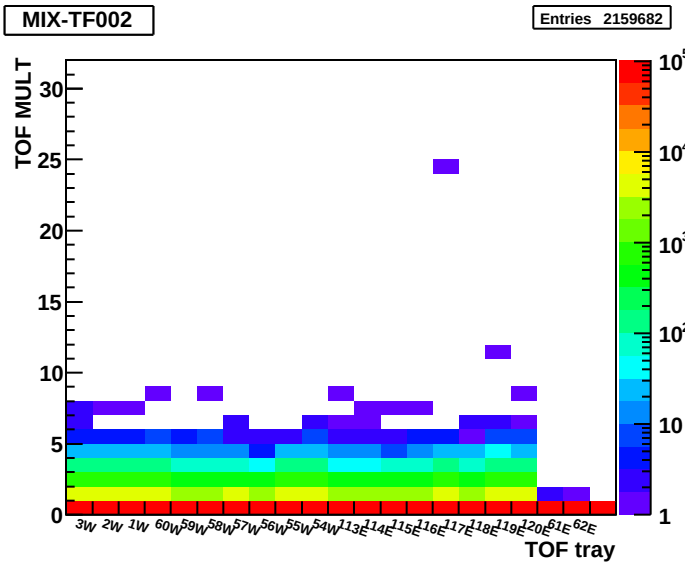

RAT board (ch4)
Entries 822736
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

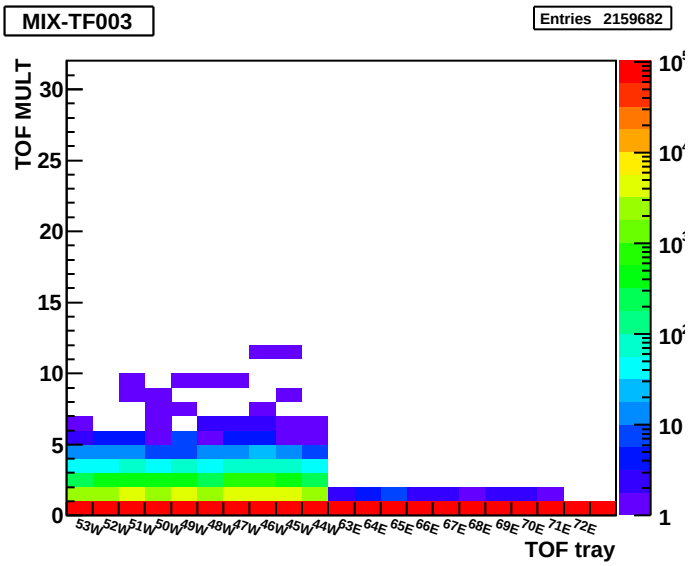

FP201 0-15 (ch5)
Entries 822736
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

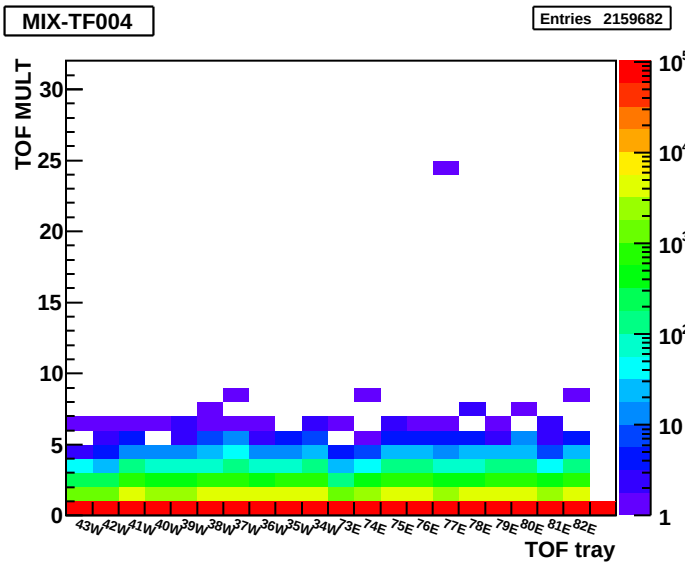

ST201 0-15 (ch6)
Entries 822736
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

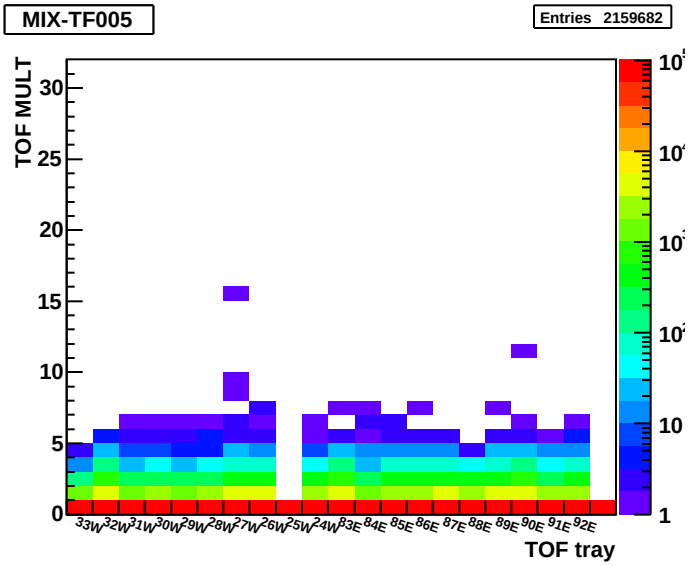

Unused (ch7)
Entries 822736

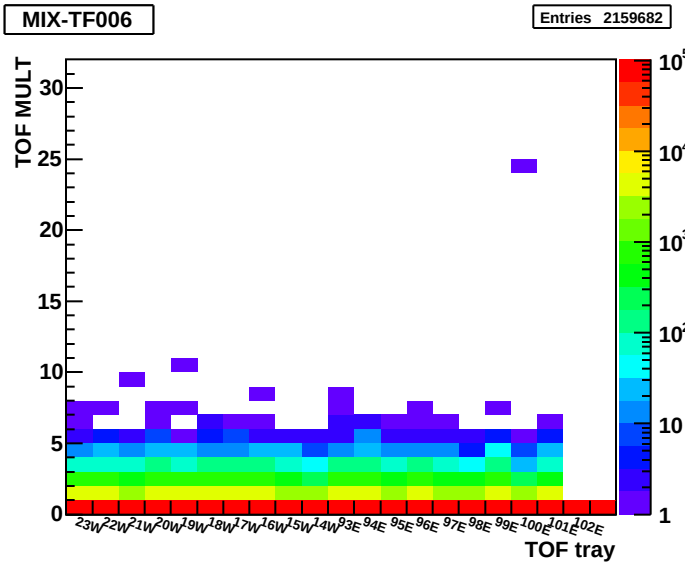

MIX-TF001
Entries 2159682
TOF MULT
TOF tray

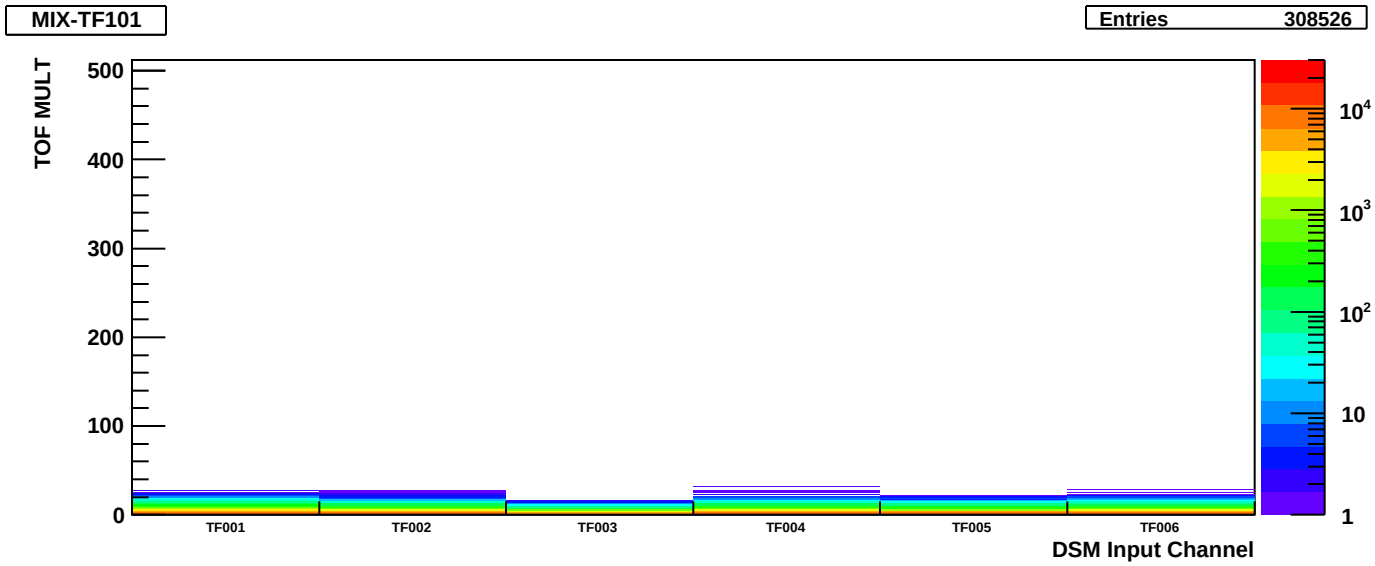

MIX-TF002
Entries 2159682
TOF MULT
TOF tray

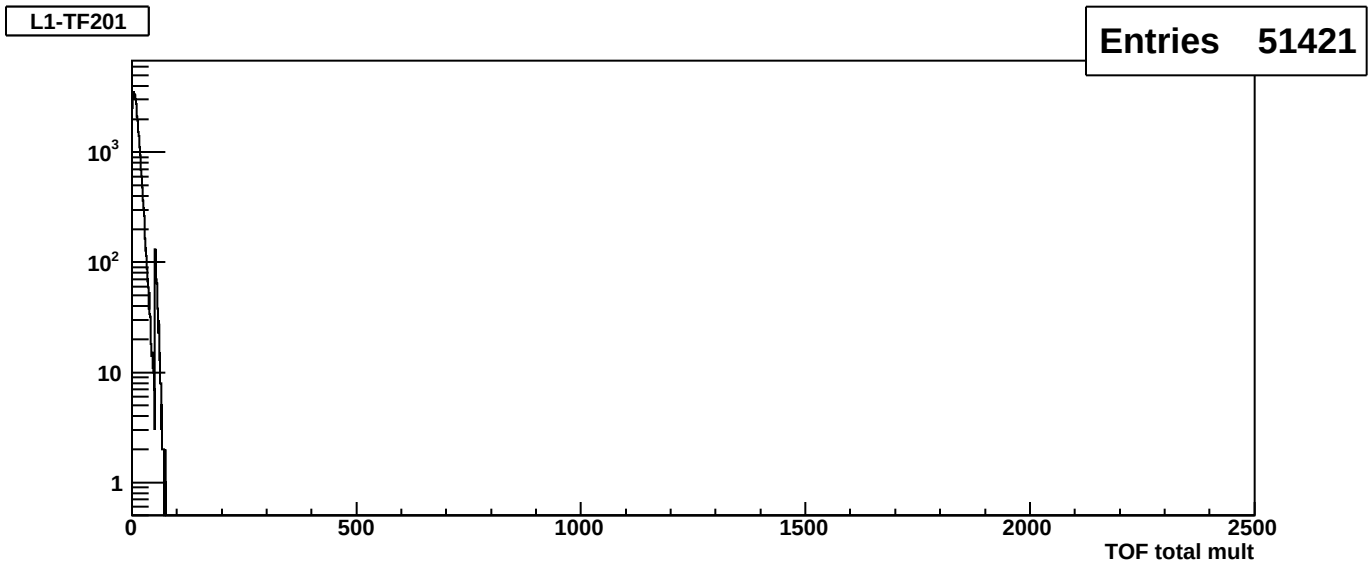

MIX-TF003
Entries 2159682
TOF MULT
TOF tray

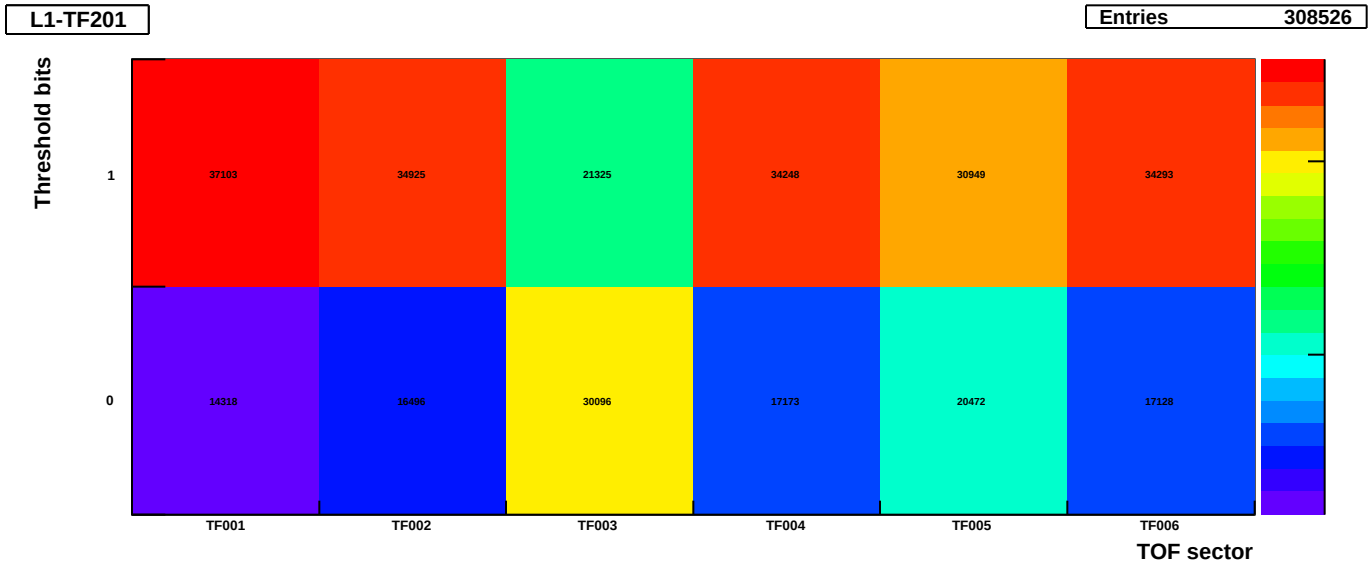

MIX-TF004
Entries 2159682
TOF MULT
TOF tray

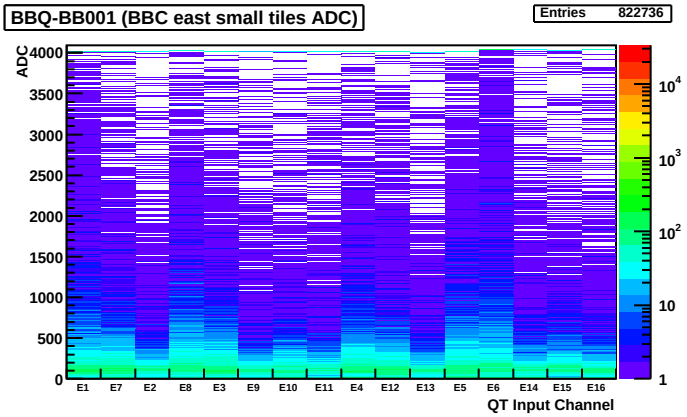

MIX-TF005
Entries 2159682
TOF MULT
TOF tray

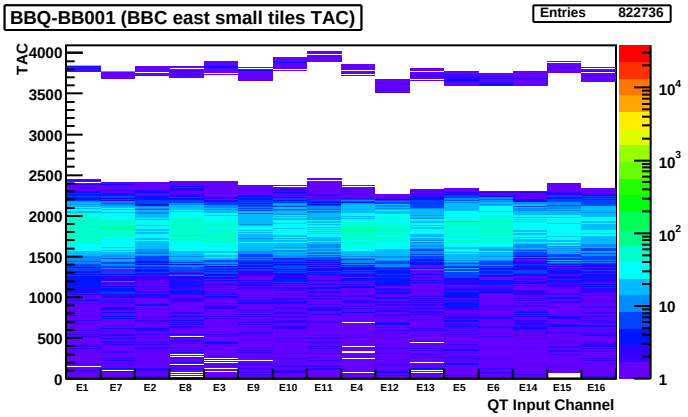

MIX-TF006
Entries 2159682
TOF MULT
TOF tray

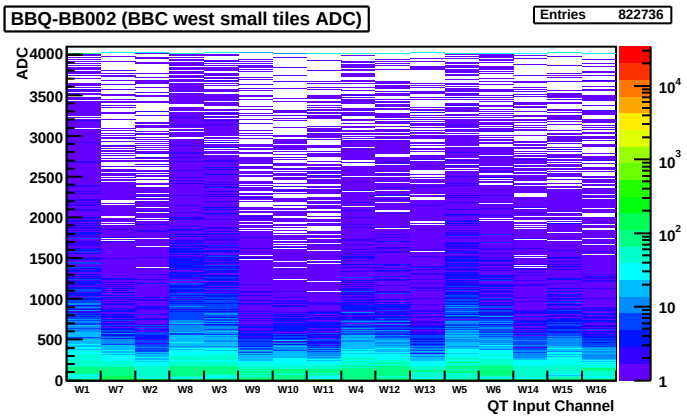

MIX-TF101
Entries 308526
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

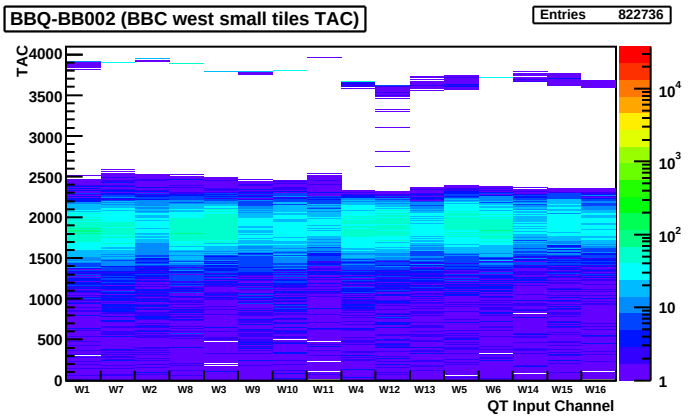

L1-TF201
Entries 51421
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

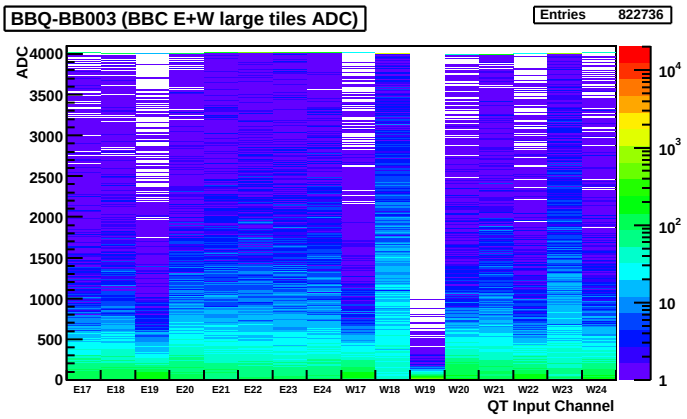

L1-TF201
Entries 308526
Threshold bits
1
0
37103
34925
21325
34248
30949
34293
14318
16496
30096
17173
20472
17128
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

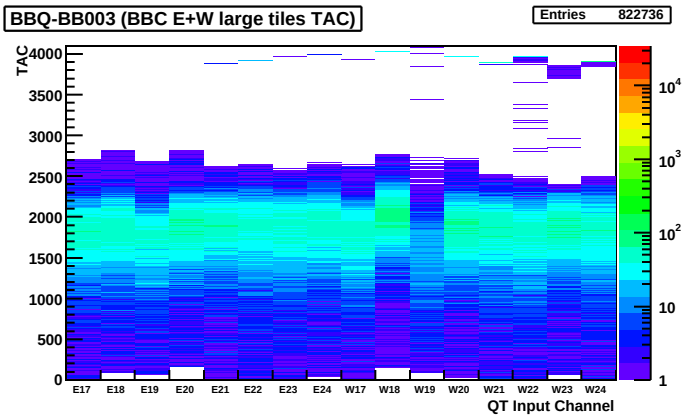

BBQ-BB001 (BBC east small tiles ADC)
Entries 822736
ADC
QT Input Channel

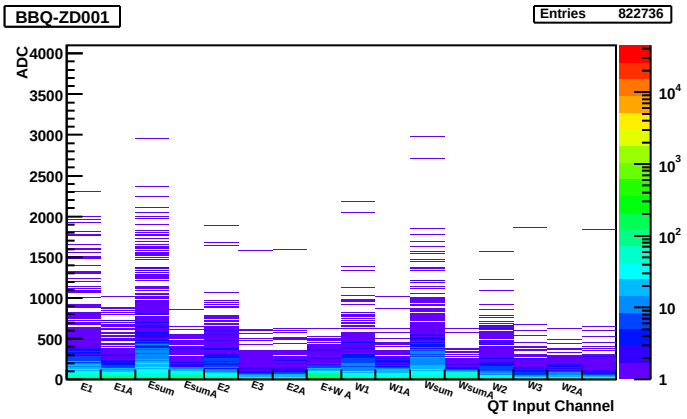

BBQ-BB001 (BBC east small tiles TAC)
Entries 822736
TAC
QT Input Channel

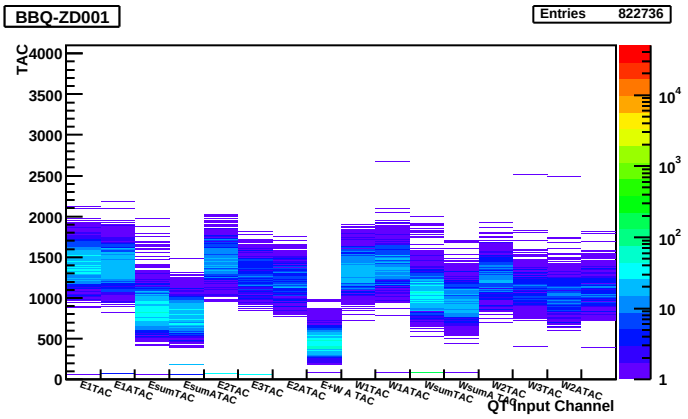

BBQ-BB002 (BBC west small tiles ADC)
Entries 822736
ADC
QT Input Channel

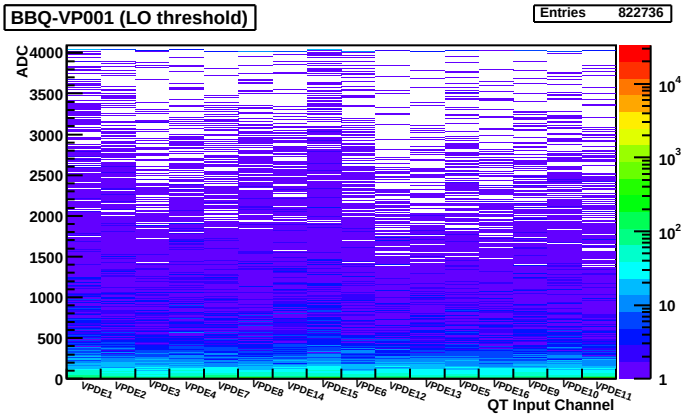

BBQ-BB002 (BBC west small tiles TAC)
Entries 822736
TAC
QT Input Channel

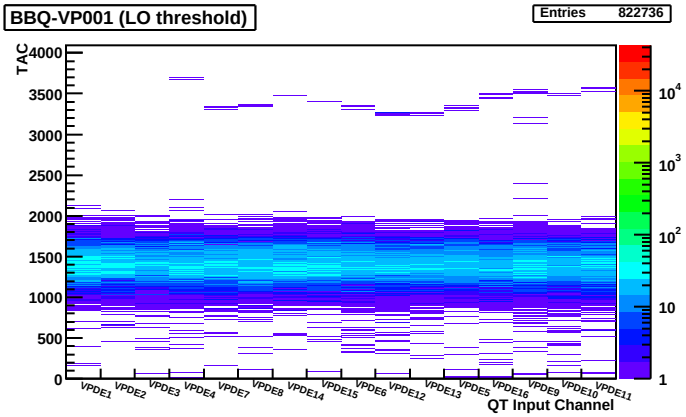

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 822736
ADC
QT Input Channel

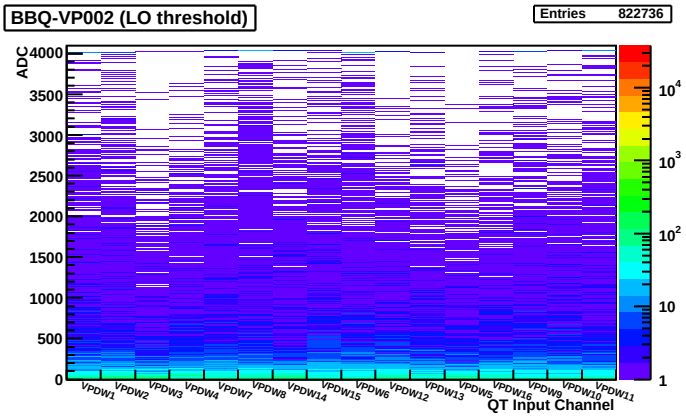

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 822736
TAC
QT Input Channel

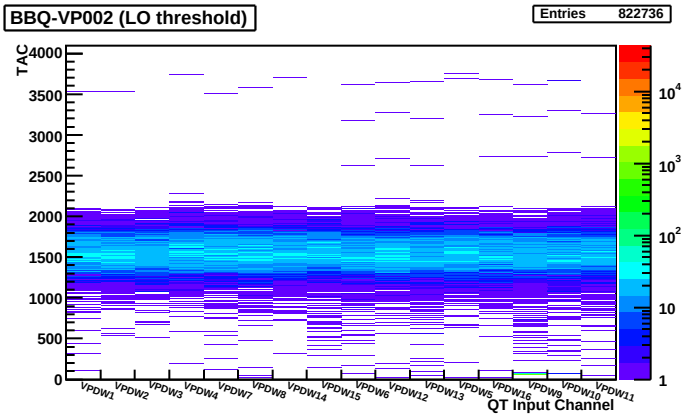

BBQ-ZD001
Entries 822736
ADC
QT Input Channel

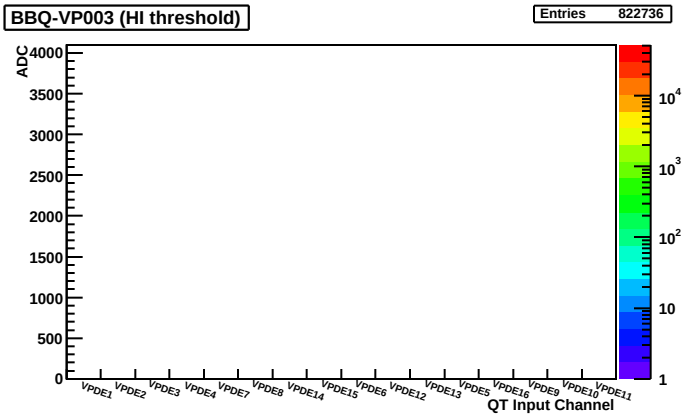

BBQ-ZD001
Entries 822736
TAC
QT Input Channel

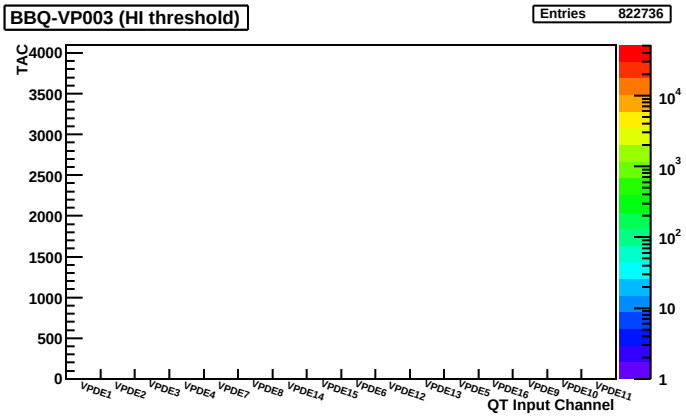

BBQ-VP001 (LO threshold)
Entries 822736
ADC
QT Input Channel

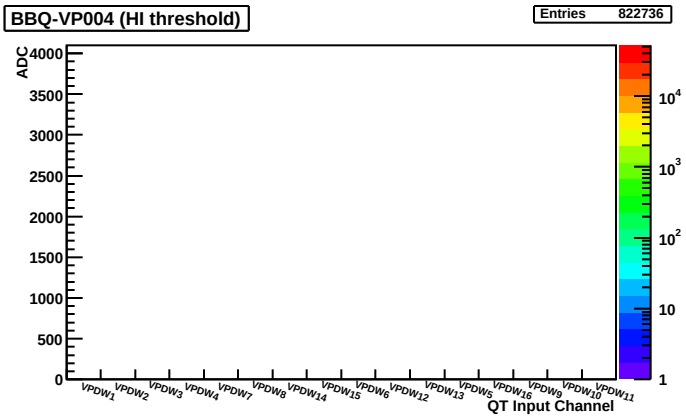

BBQ-VP001 (LO threshold)
Entries 822736
TAC
QT Input Channel

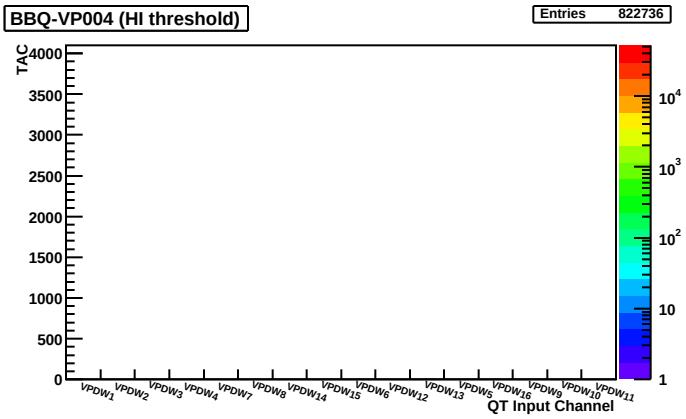

BBQ-VP002 (LO threshold)
Entries 822736
ADC
QT Input Channel

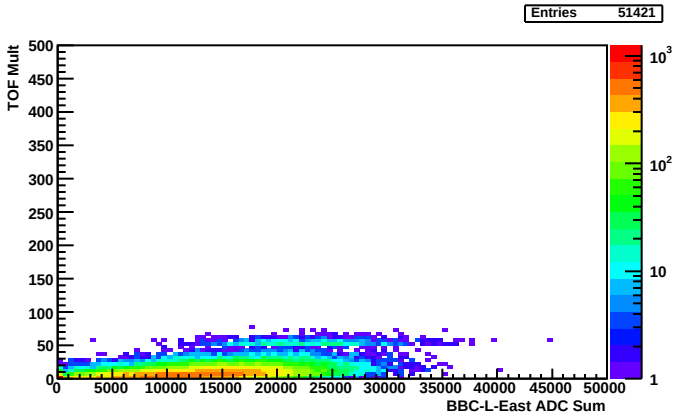

BBQ-VP002 (LO threshold)
Entries 822736
TAC
QT Input Channel

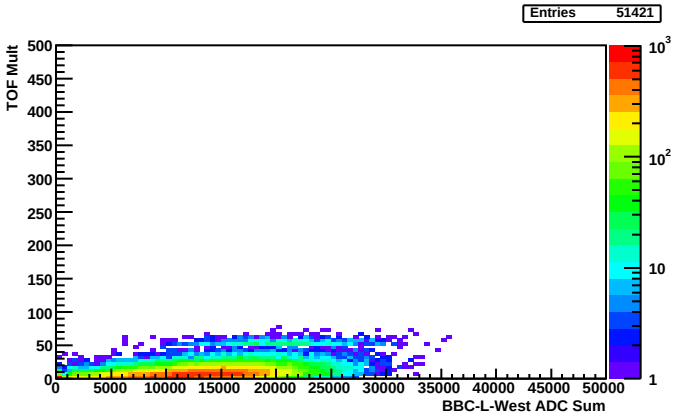

BBQ-VP003 (HI threshold)
Entries 822736
ADC
QT Input Channel

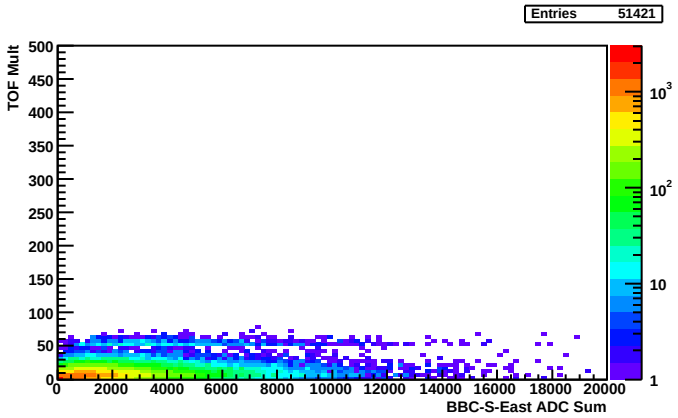

BBQ-VP003 (HI threshold)
Entries 822736
TAC
QT Input Channel

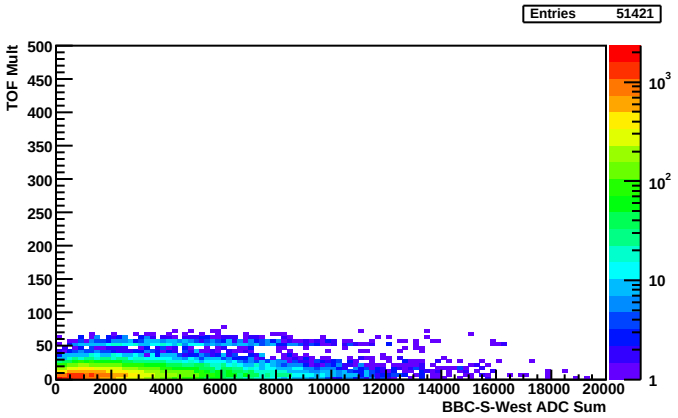

BBQ-VP004 (HI threshold)
Entries 822736
ADC
QT Input Channel

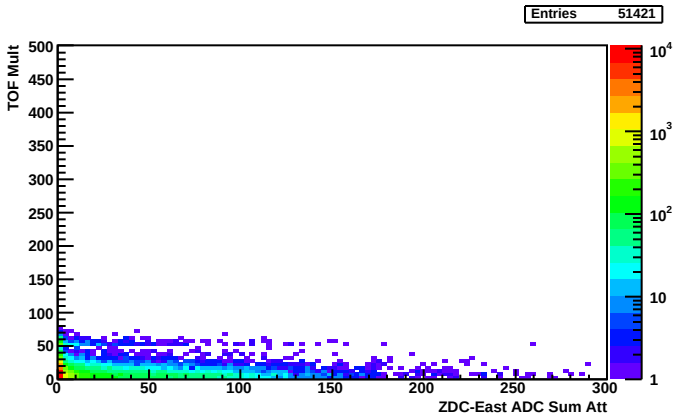

BBQ-VP004 (HI threshold)
Entries 822736
TAC
QT Input Channel

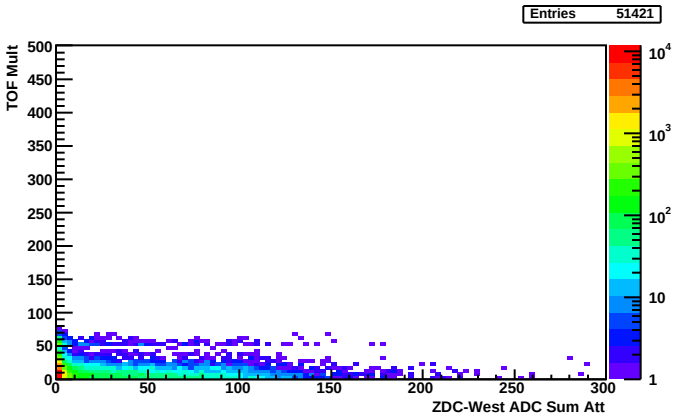

Entries 51421
TOF Mult
BBC-L-East ADC Sum

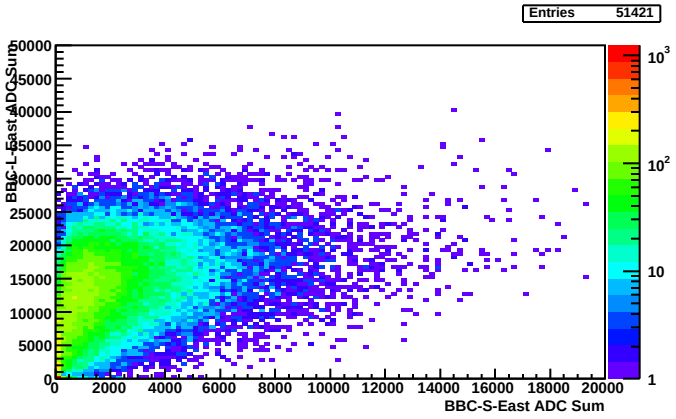

Entries 51421
TOF Mult
BBC-L-West ADC Sum

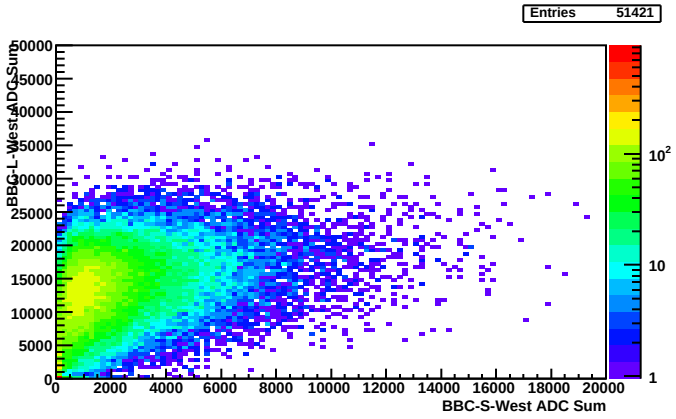

Entries 51421
TOF Mult
BBC-S-East ADC Sum

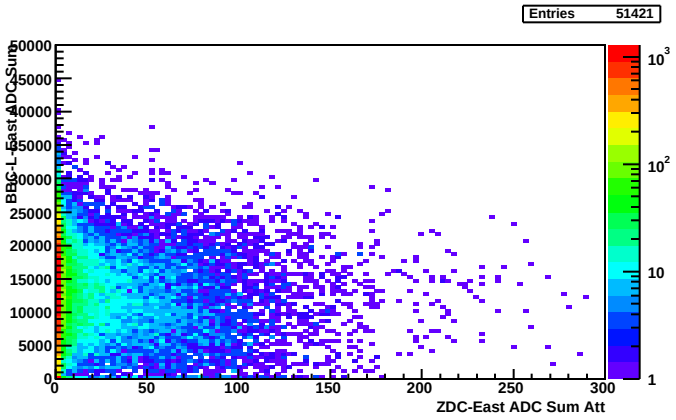

Entries 51421
TOF Mult
BBC-S-West ADC Sum

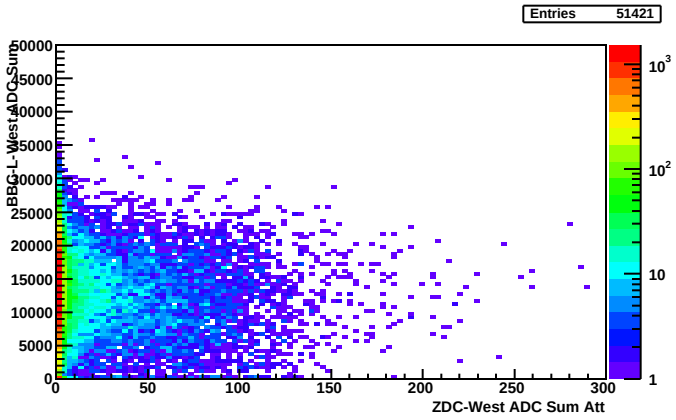

Entries 51421
TOF Mult
ZDC-East ADC Sum Att

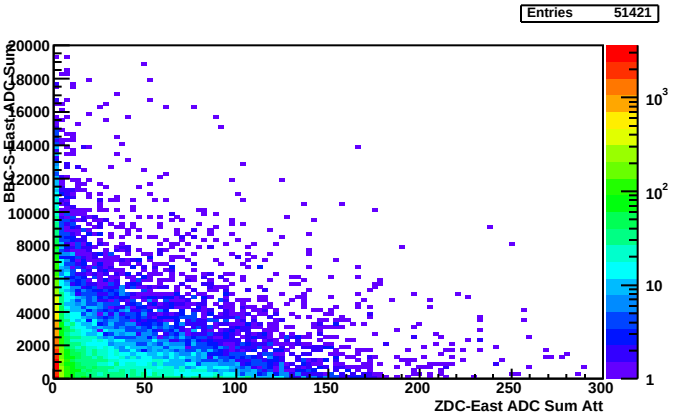

Entries 51421
TOF Mult
ZDC-West ADC Sum Att

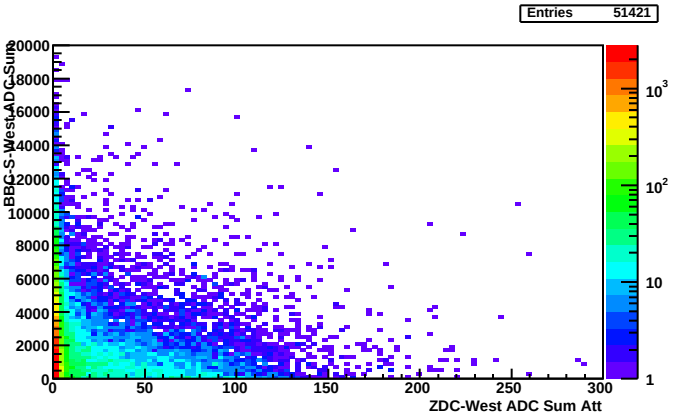

Entries 51421
BBC-L-East ADC Sum
BBC-S-East ADC Sum

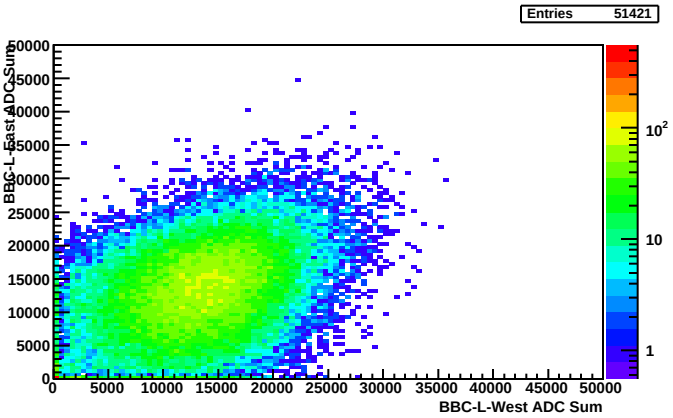

Entries 51421
BBC-L-West ADC Sum
BBC-S-West ADC Sum

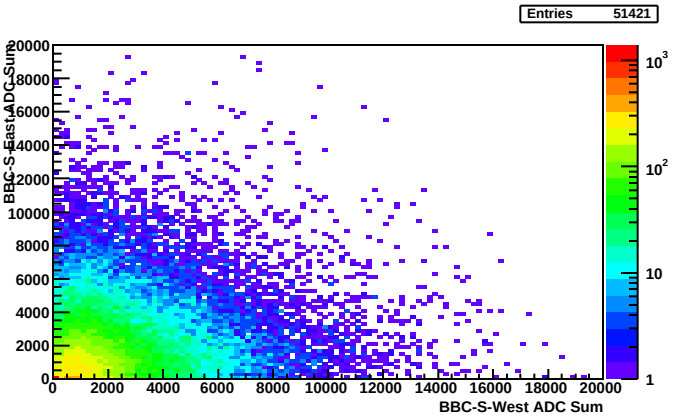

Entries 51421
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

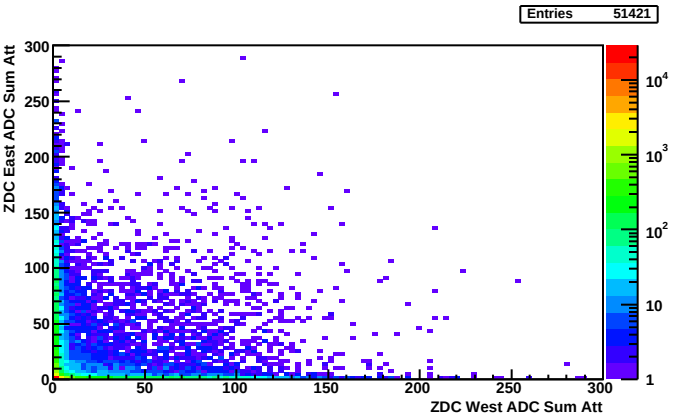

Entries 51421
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

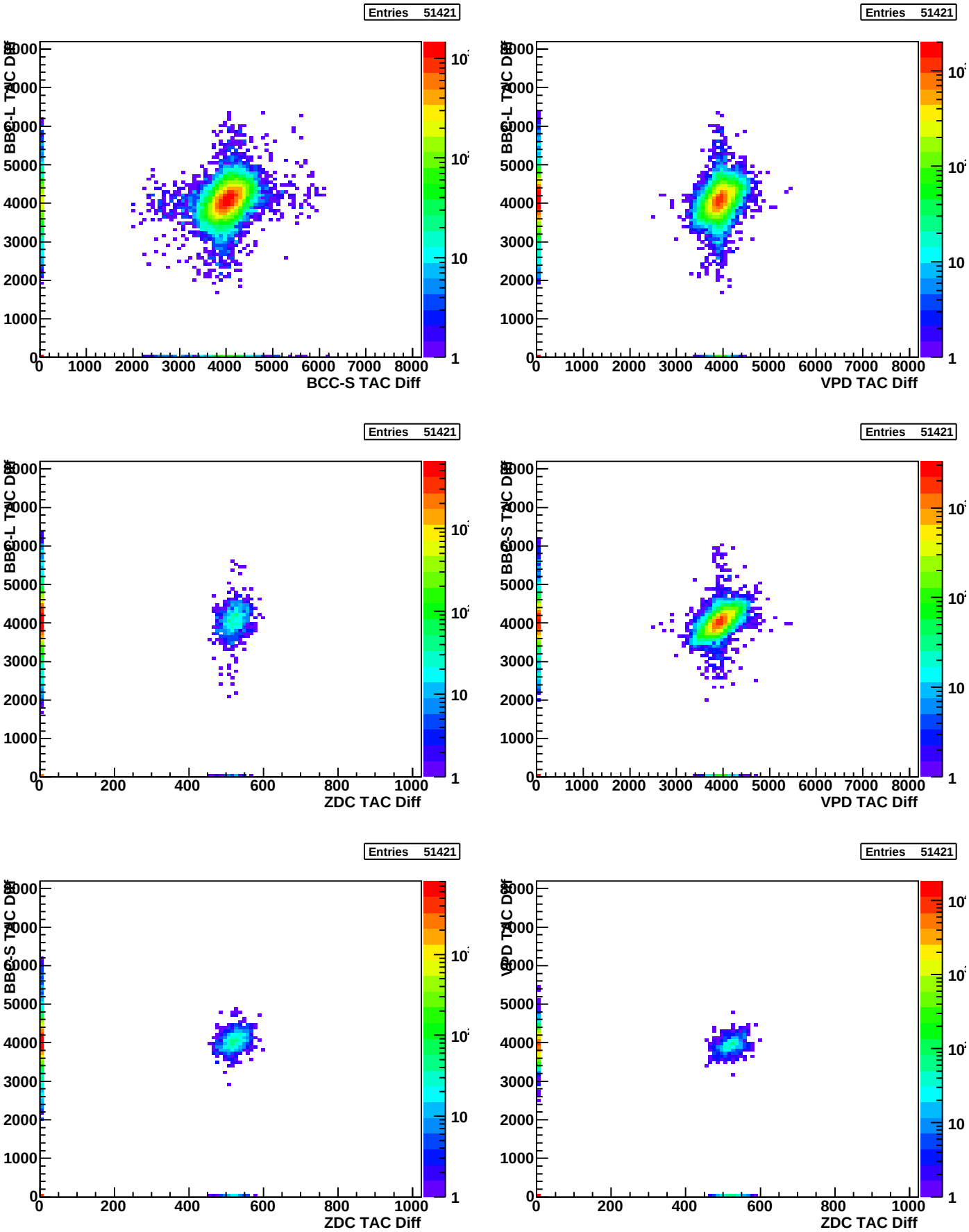

Entries 51421
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

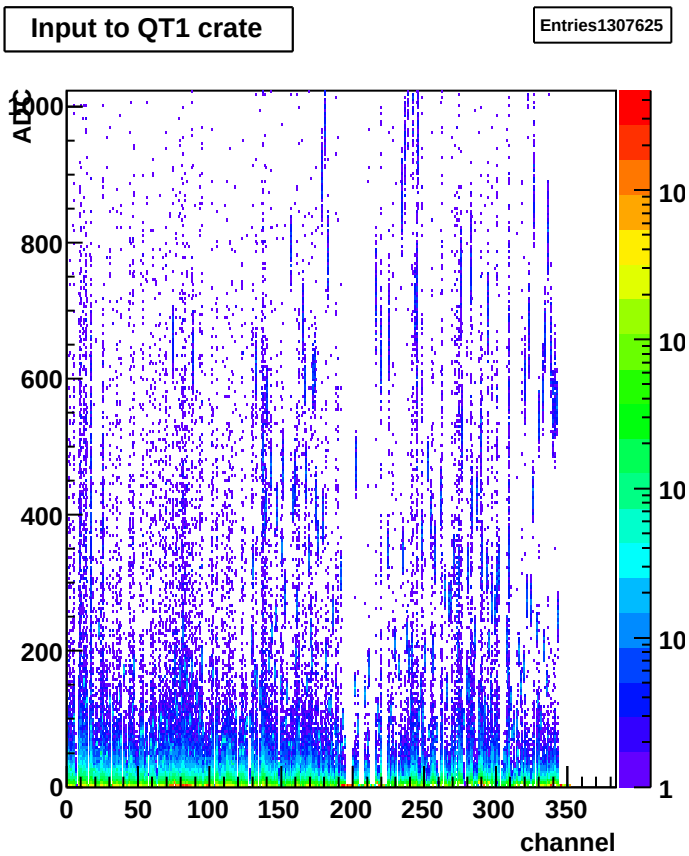

Entries 51421
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

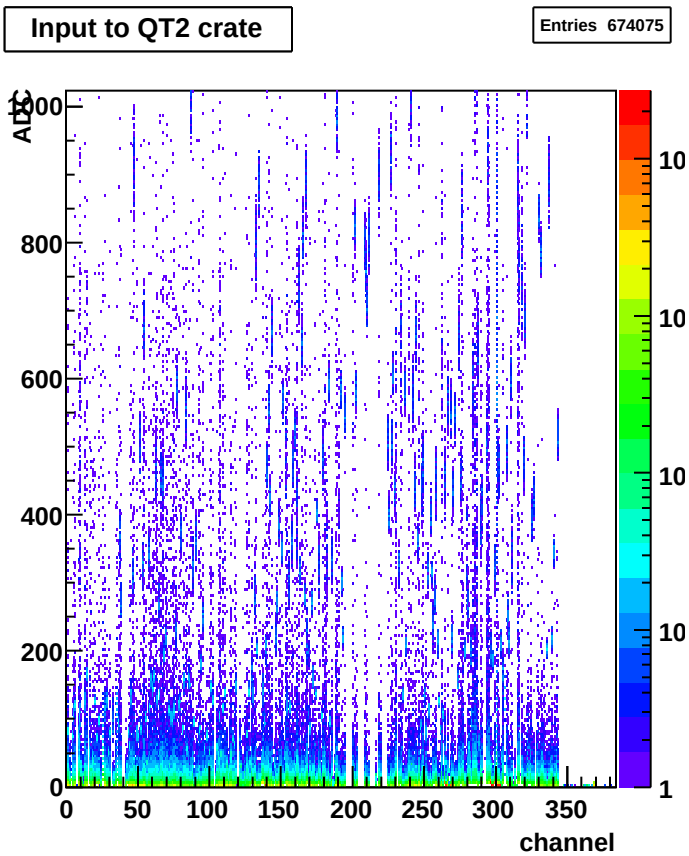

Entries 51421
BBC-L-East ADC Sum
BBC-L-West ADC Sum

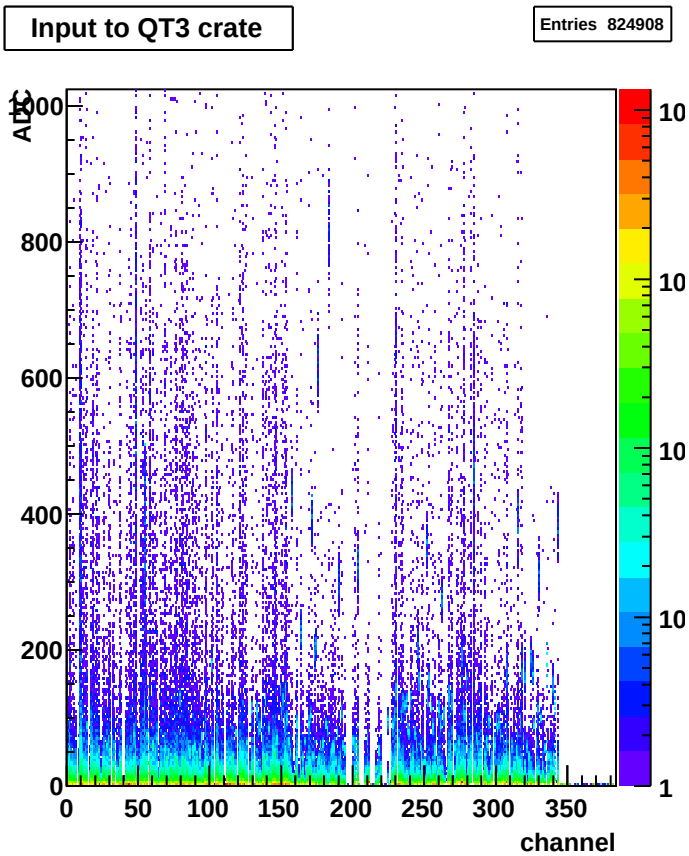

Entries 51421
BBC-S-East ADC Sum
BBC-S-West ADC Sum

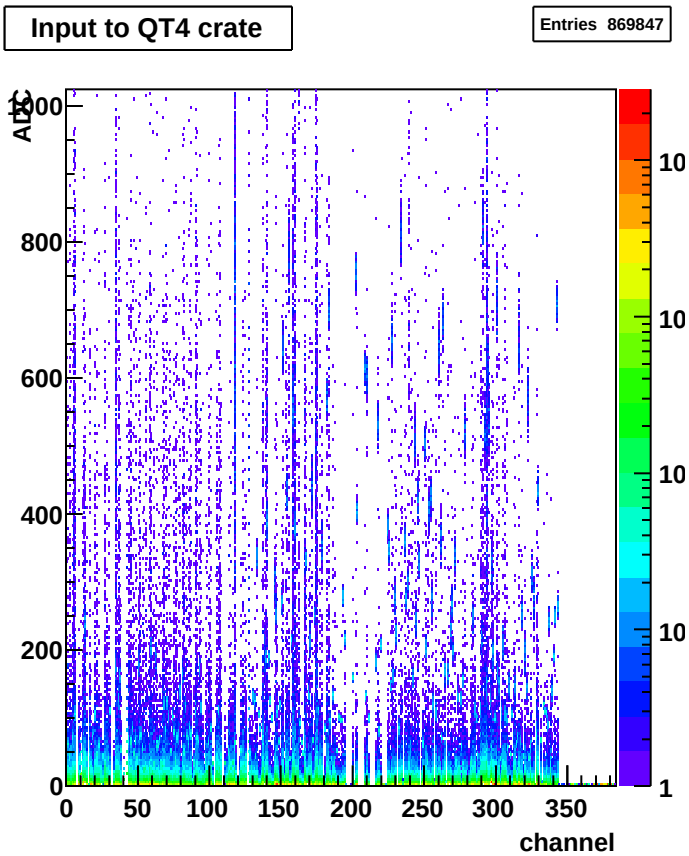

Entries 51421
ZDC East ADC Sum Att
ZDC West ADC Sum Att

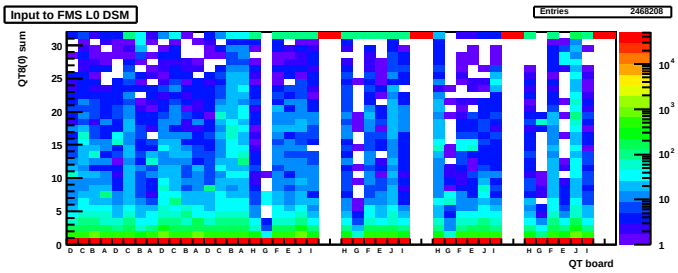

Entries 51421
BBC-L TAC Diff
BCC-S TAC Diff
Entries 51421
BBC-L TAC Diff
VPD TAC Diff
Entries 51421
BBC-L TAC Diff
ZDC TAC Diff
Entries 51421
BBC-S TAC Diff
VPD TAC Diff
Entries 51421
BBC-S TAC Diff
ZDC TAC Diff
Entries 51421
VPD TAC Diff
ZDC TAC Diff

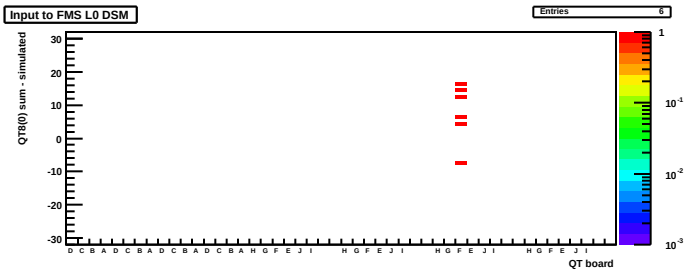

Input to QT1 crate
Entries1307625
ADC
channel

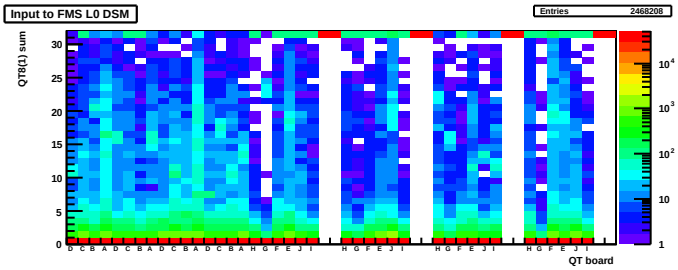

Input to QT2 crate
Entries 674075
ADC
channel

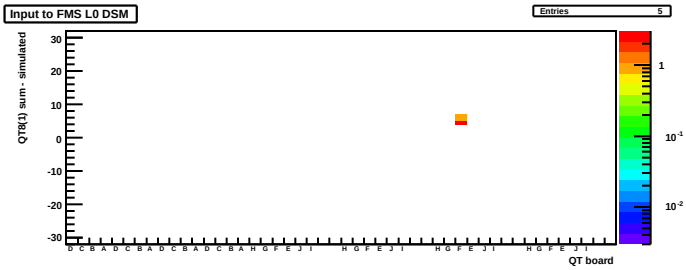

Input to QT3 crate
Entries 824908
ADC
channel

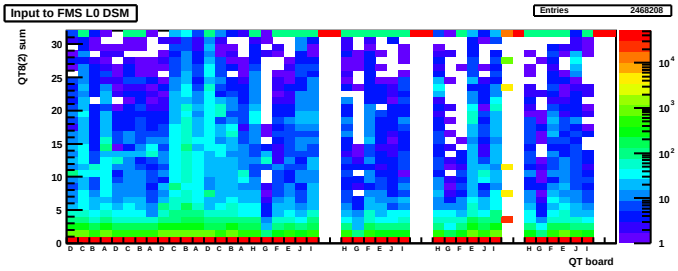

Input to QT4 crate
Entries 869847
ADC
channel

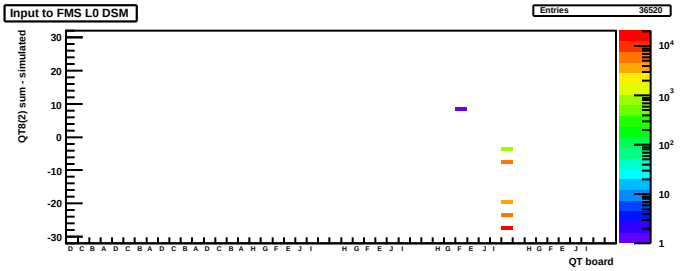

Input to FMS L0 DSM
Entries 2468208
QT8(0) sum
QT board

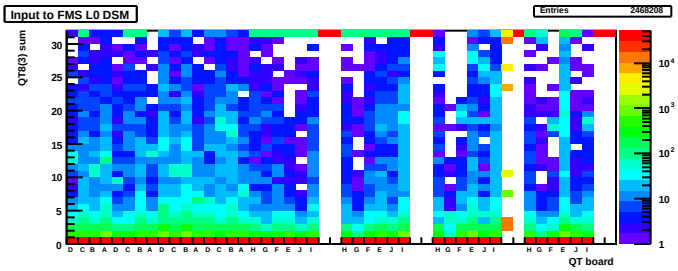

Input to FMS L0 DSM
Entries 6
QT8(0) sum - simulated
QT board

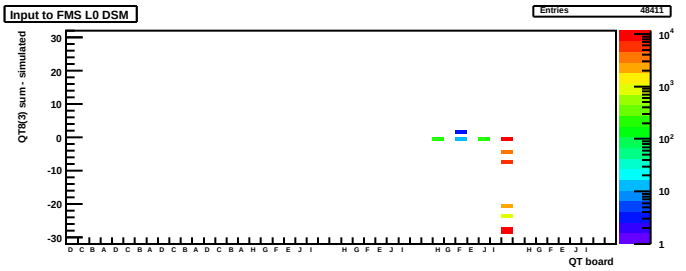

Input to FMS L0 DSM
Entries 2468208
QT8(1) sum
QT board

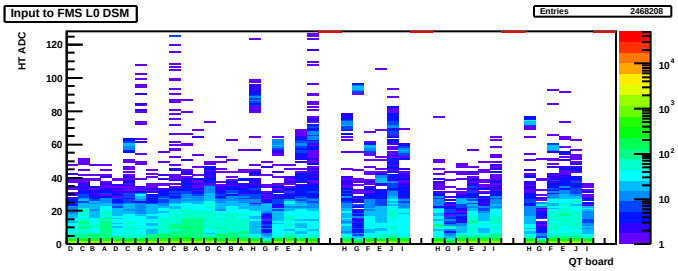

Input to FMS L0 DSM
Entries 5
QT8(1) sum - simulated
QT board

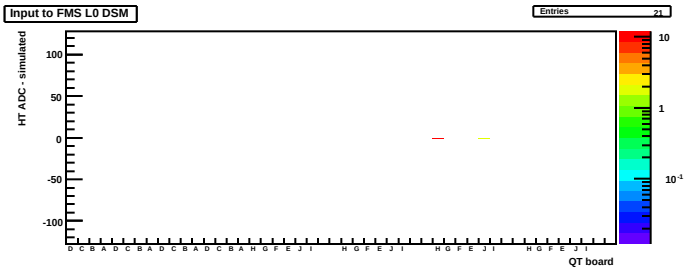

Input to FMS L0 DSM
Entries 2468208
QT8(2) sum
QT board

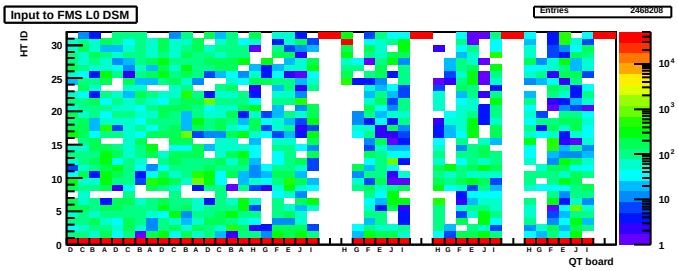

Input to FMS L0 DSM
Entries 36520
QT8(2) sum - simulated
QT board

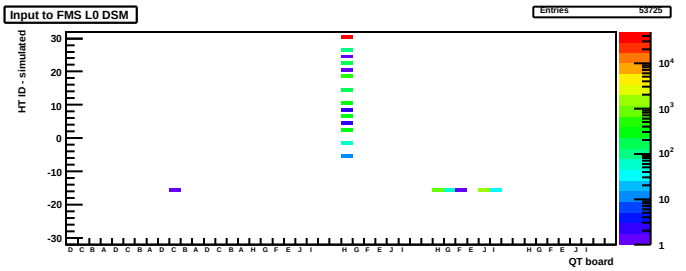

Input to FMS L0 DSM
Entries 2468208
QT8(3) sum
QT board

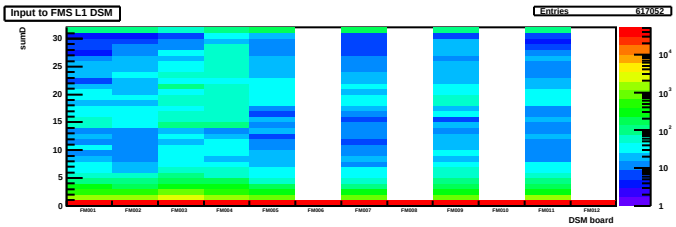

Input to FMS L0 DSM
Entries 48411
QT8(3) sum - simulated
QT board

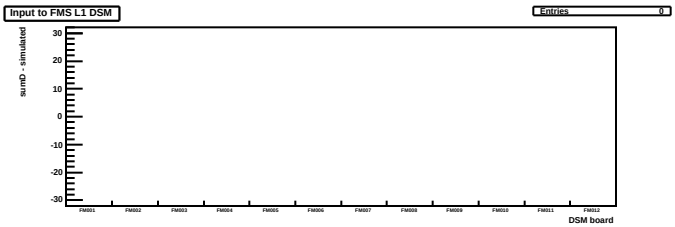

Input to FMS L0 DSM
Entries 2468208
HT ADC
QT board

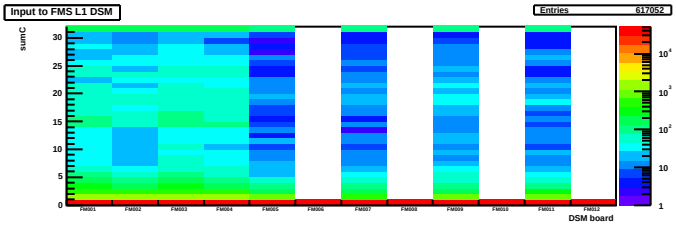

Input to FMS L0 DSM
Entries 21
HT ADC - simulated
QT board

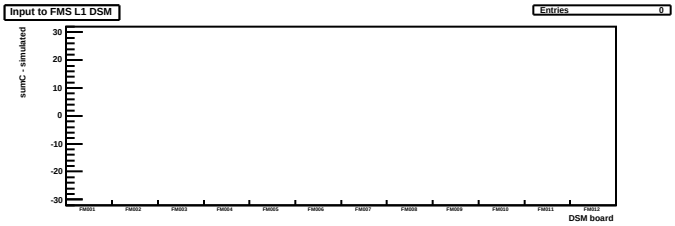

Input to FMS L0 DSM
Entries 2468208
HT ID
QT board

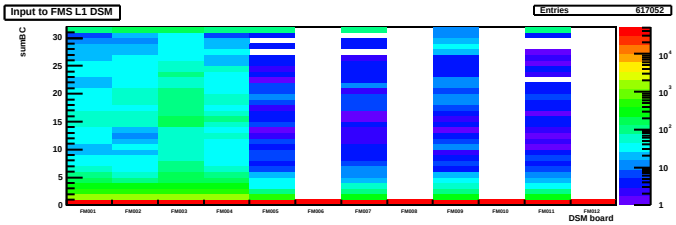

Input to FMS L0 DSM
Entries 53725
HT ID - simulated
QT board

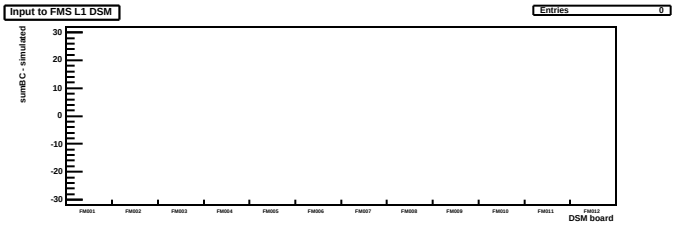

Input to FMS L1 DSM
Entries 617052
sumD
DSM board

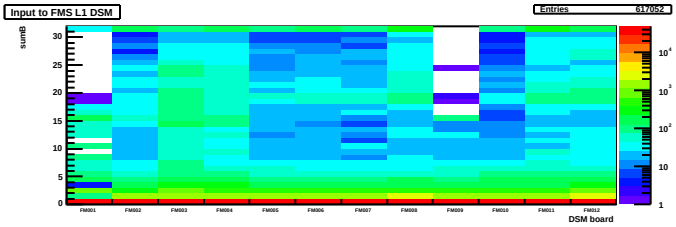

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

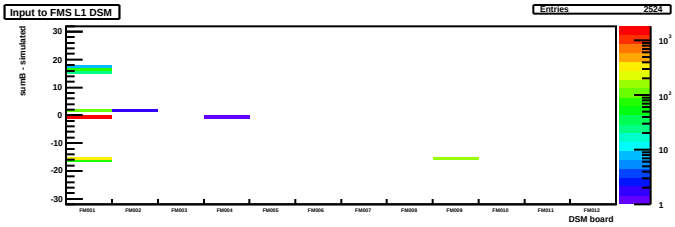

Input to FMS L1 DSM
Entries 617052
sumC
DSM board

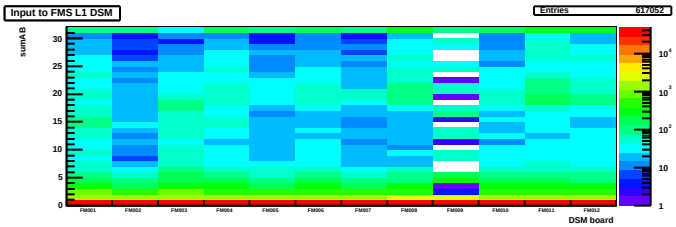

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

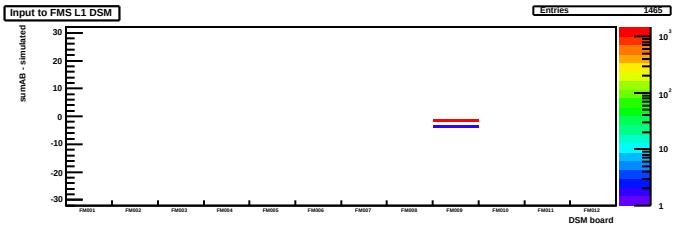

Input to FMS L1 DSM
Entries 617052
sumBC
DSM board

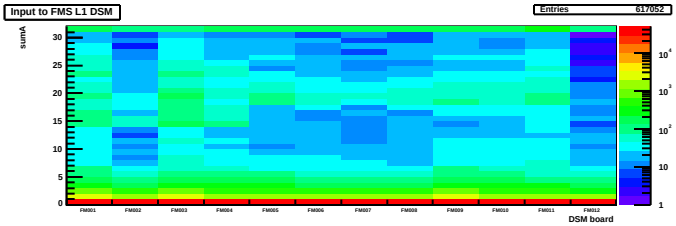

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

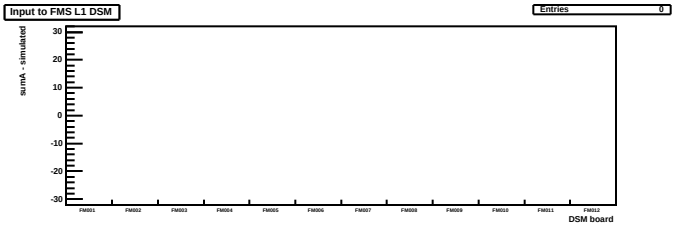

Input to FMS L1 DSM
Entries 617052
sumB
DSM board

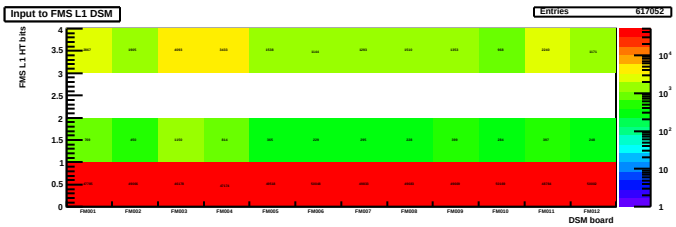

Input to FMS L1 DSM
Entries 2524
sumB - simulated
DSM board

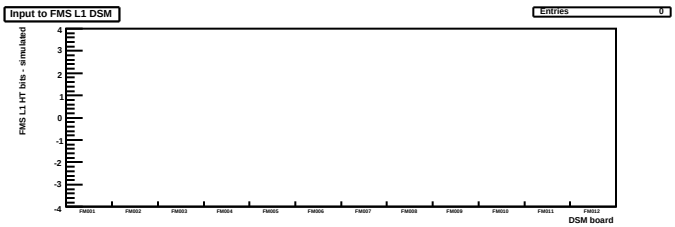

Input to FMS L1 DSM
Entries 617052
sumAB
DSM board

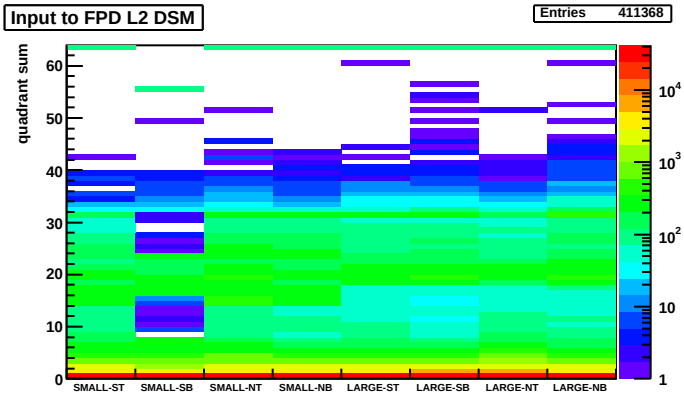

Input to FMS L1 DSM
Entries 1465
sumAB - simulated
DSM board

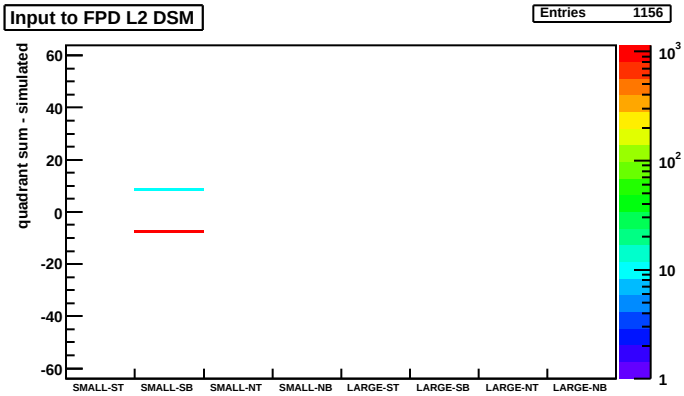

Input to FMS L1 DSM
Entries 617052
sumA
DSM board

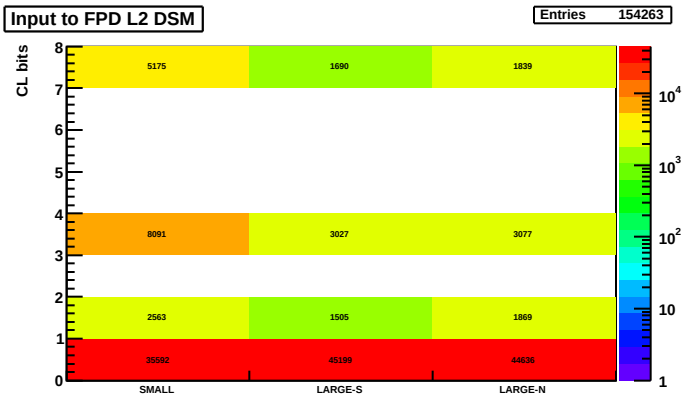

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

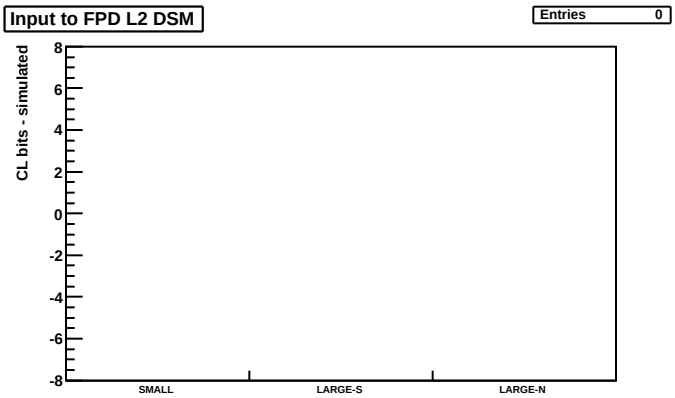

Input to FMS L1 DSM
Entries 617052
FMS L1 HT bits
DSM board

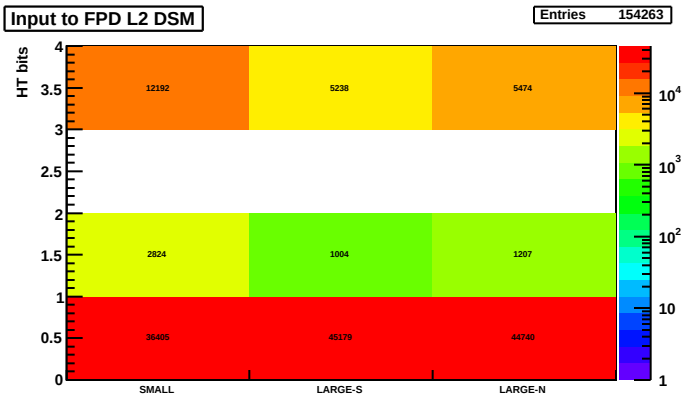

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

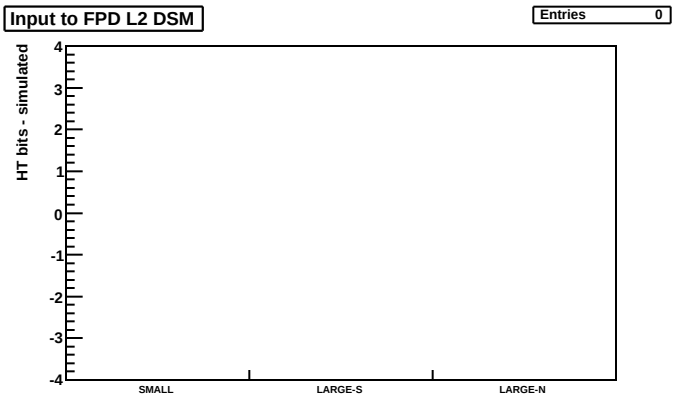

Input to FPD L2 DSM
Entries 411368
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

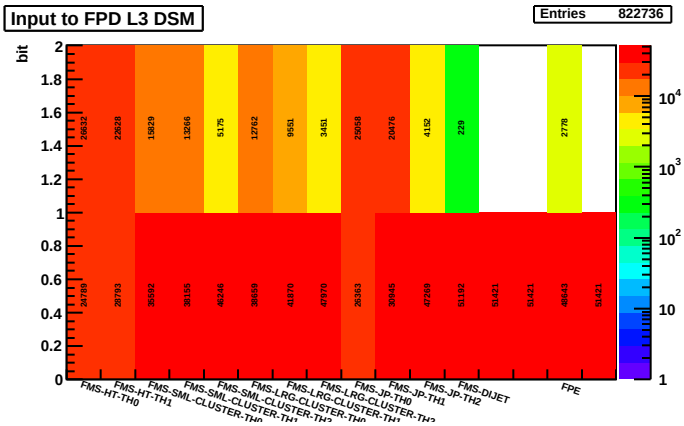

Input to FPD L2 DSM
Entries 1156
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

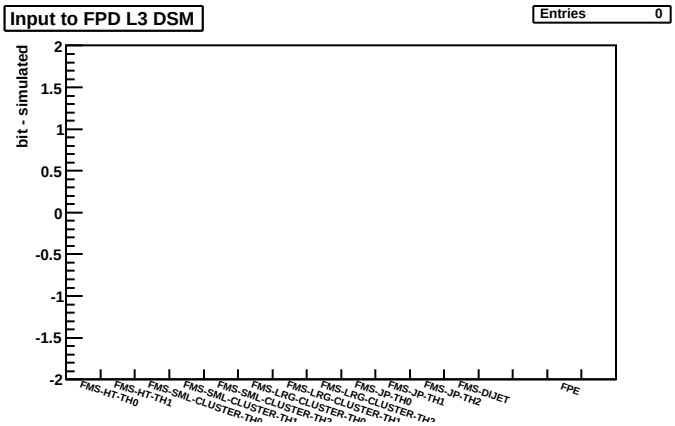

Input to FPD L2 DSM
Entries 154263
CL bits
5175 1690 1839
8091 3027 3077
2563 1505 1869
35592 45199 44636
SMALL LARGE-S LARGE-N

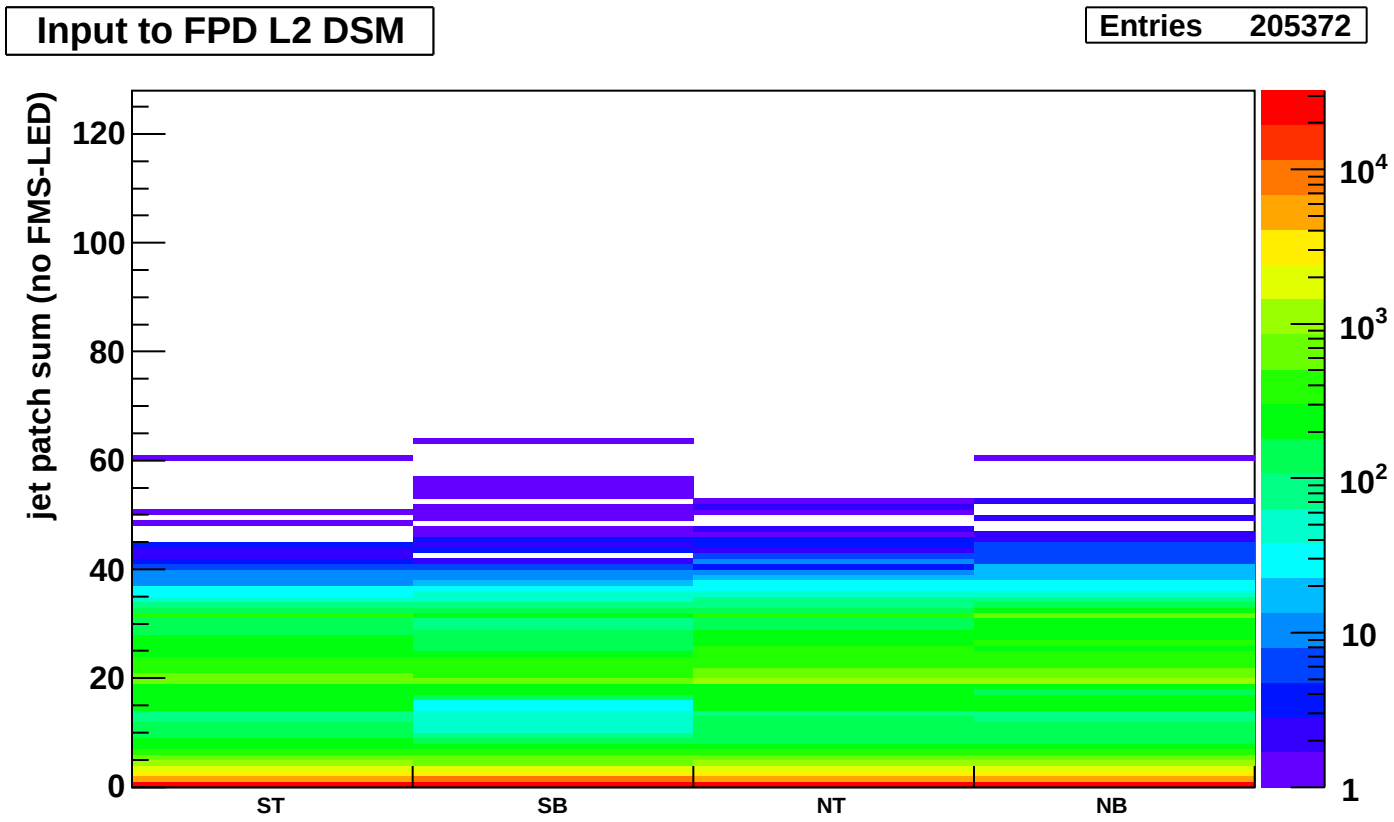

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL LARGE-S LARGE-N

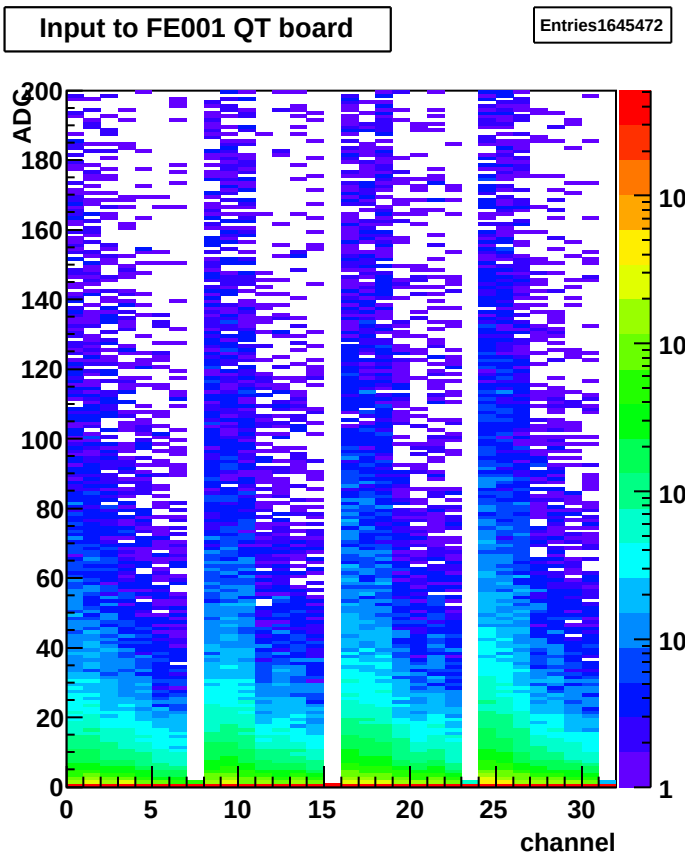

Input to FPD L2 DSM
Entries 154263
HT bits
12192 5238 5474
2824 1004 1207
36405 45179 44740
SMALL LARGE-S LARGE-N

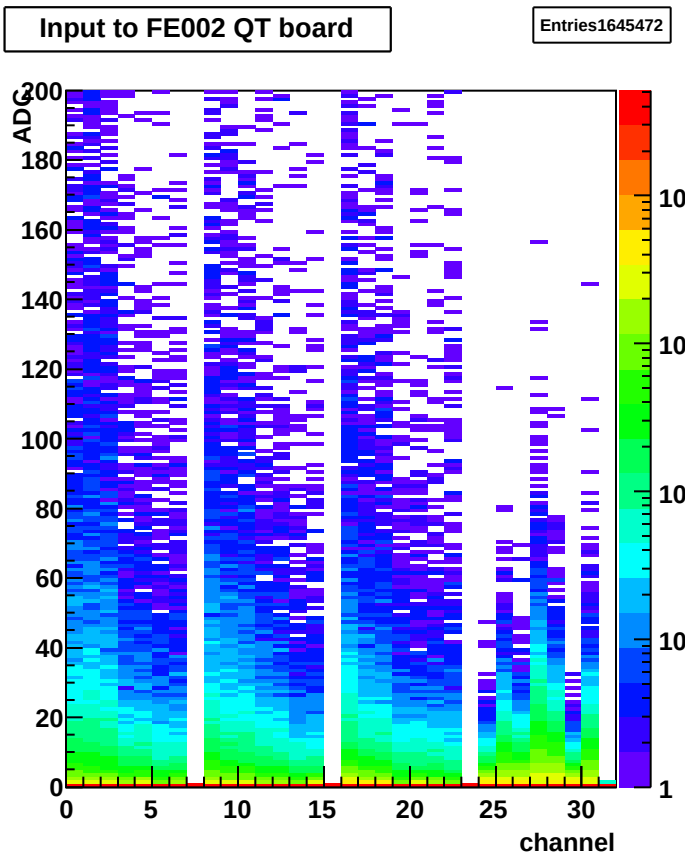

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

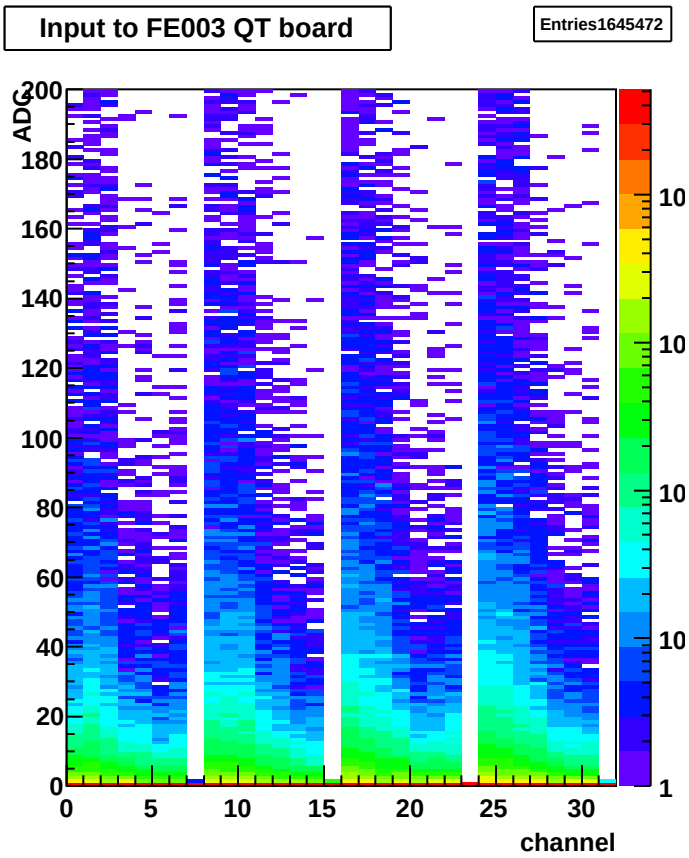

Input to FPD L3 DSM
Entries 822736
bit
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

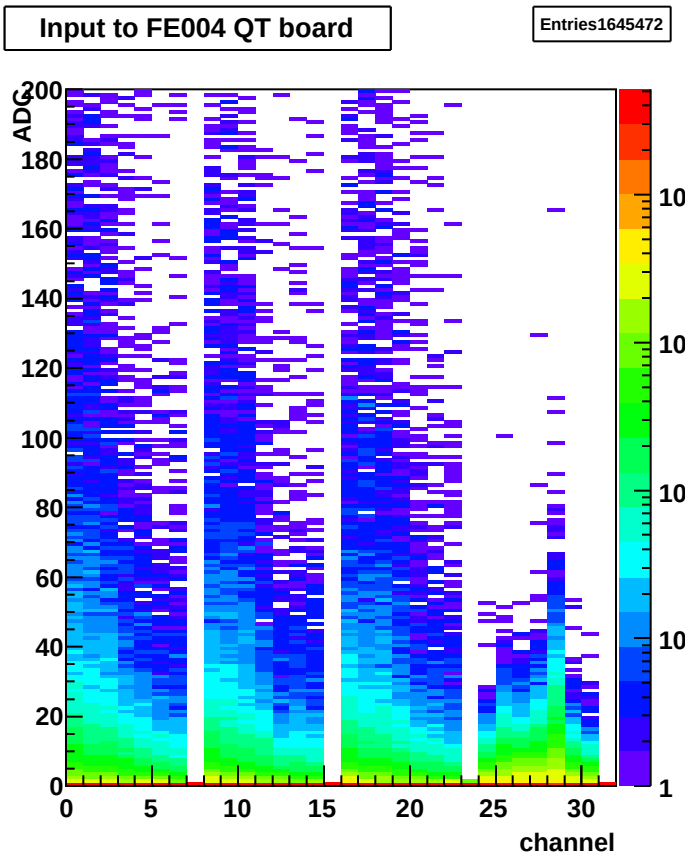

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

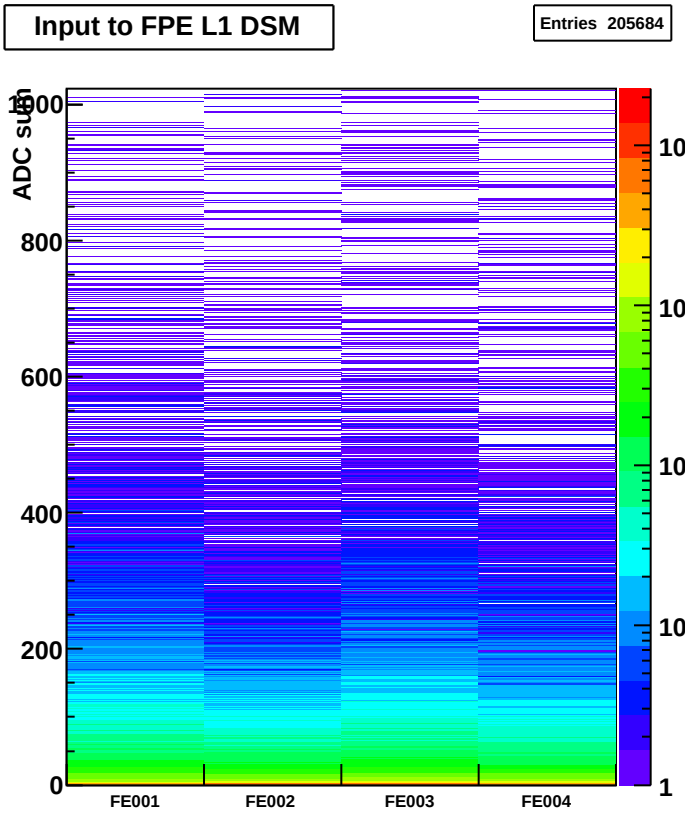

Input to FPD L2 DSM
Entries 205372
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

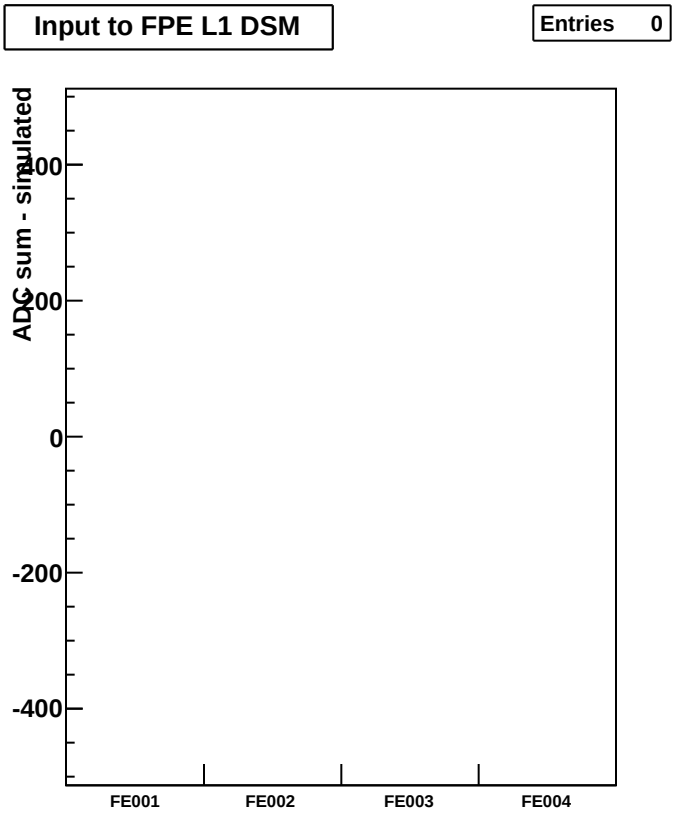

Input to FE001 QT board
Entries1645472
ADC
channel

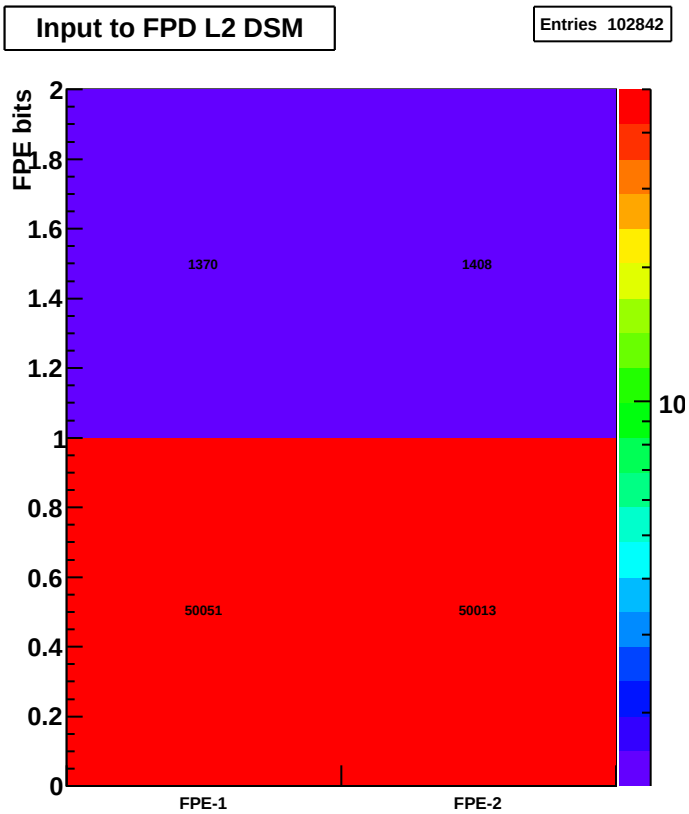

Input to FE002 QT board
Entries1645472
ADC
channel

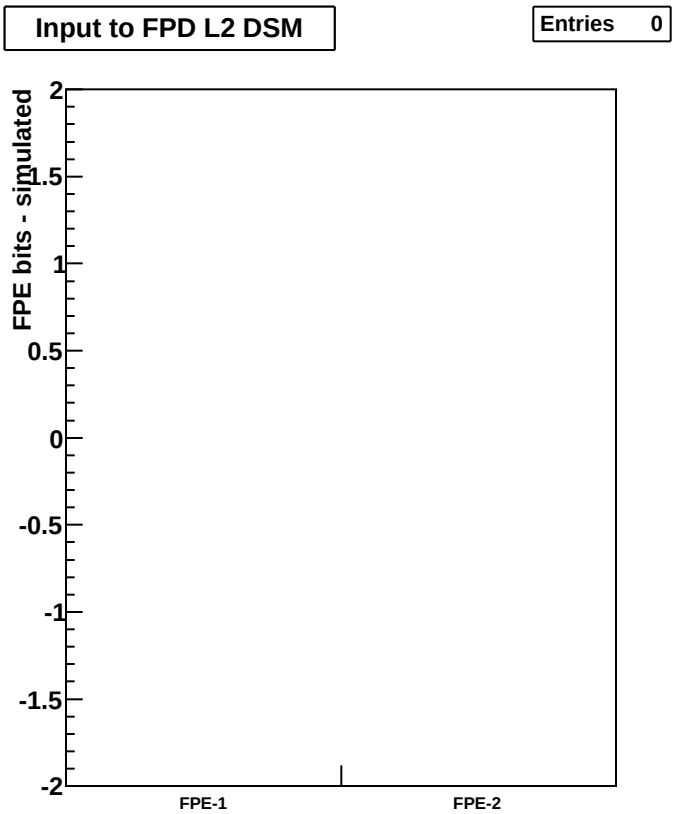

Input to FE003 QT board
Entries1645472
ADC
channel


Input to FE004 QT board
Entries1645472
ADC
channel


Input to FPE L1 DSM
Entries 205684
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 102842
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
1370
1408
50051
50013
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2