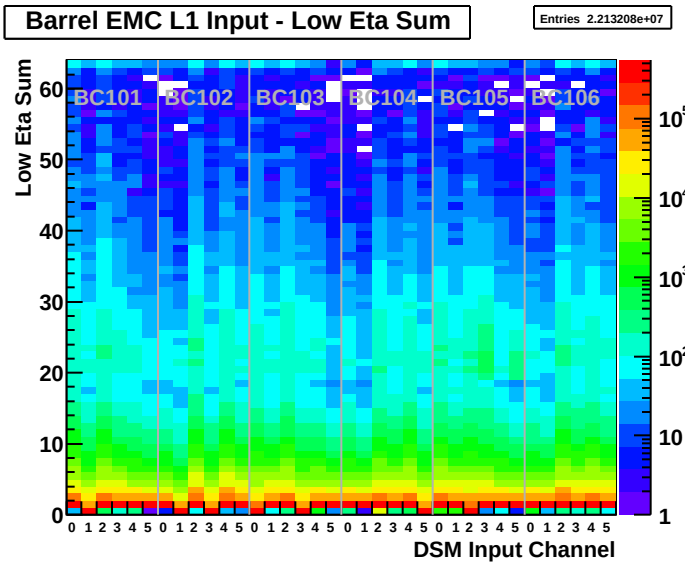
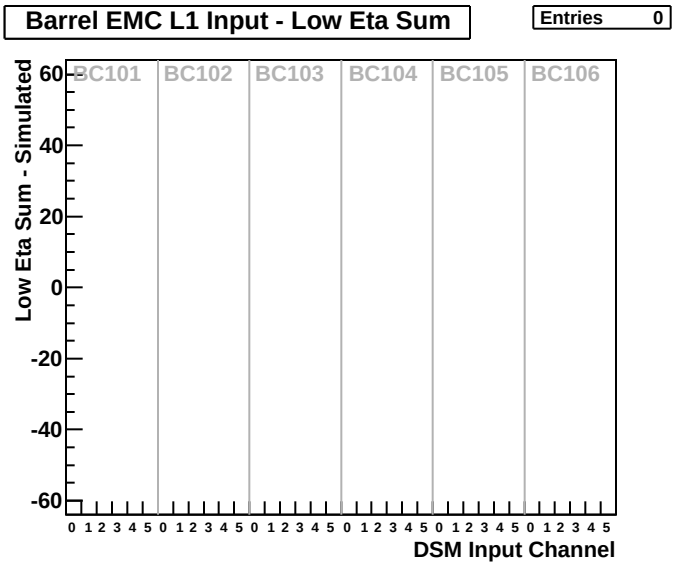
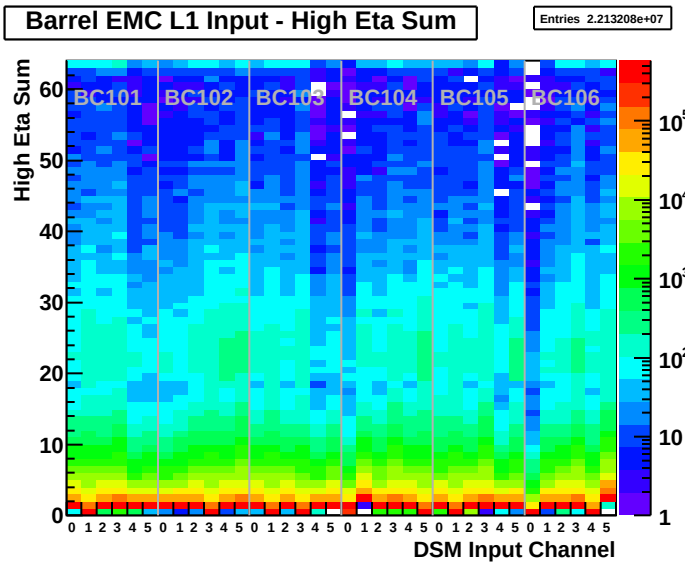
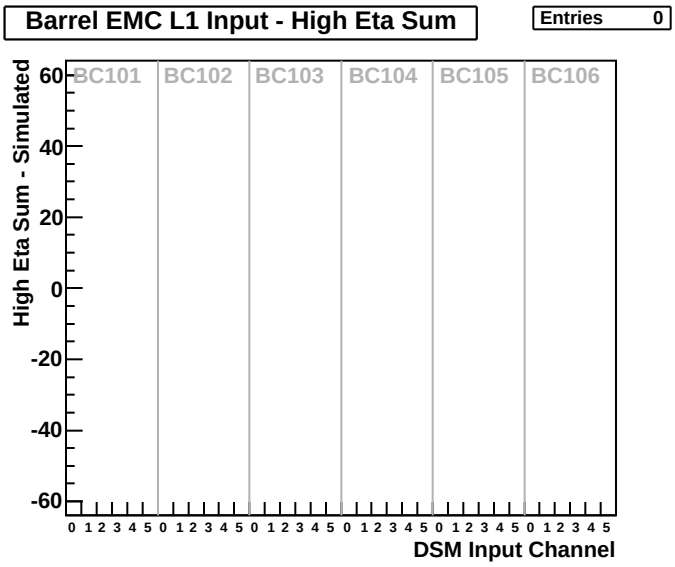
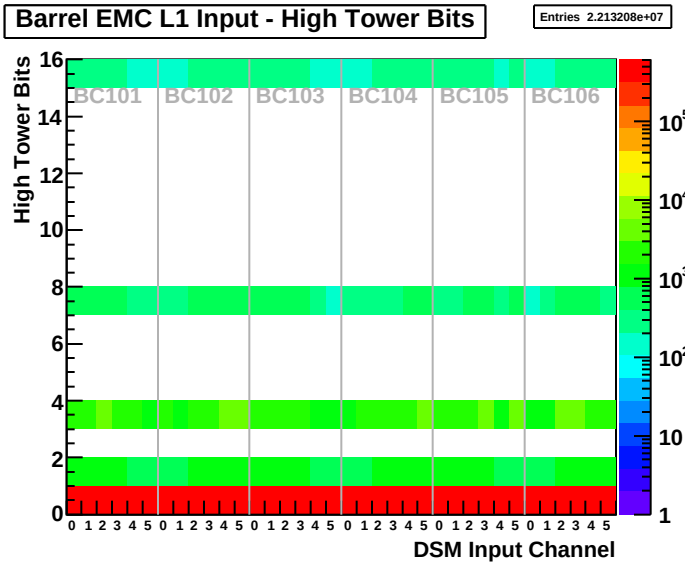
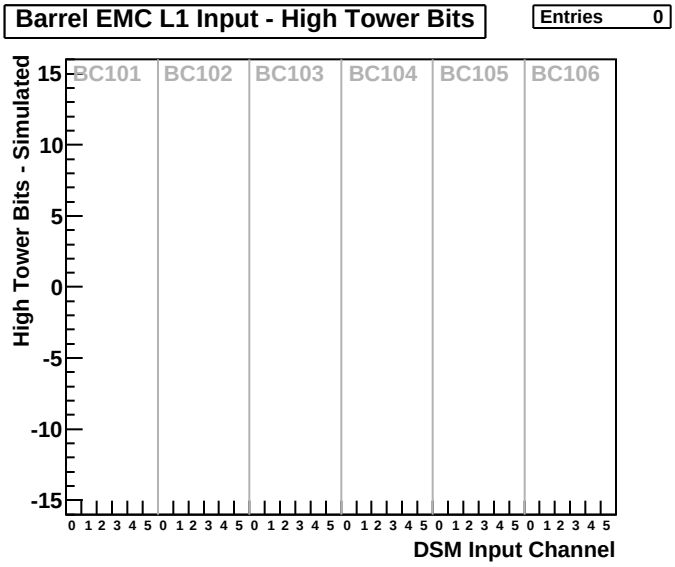

Barrel EMC L1 Input - Low Eta Sum
Entries 2.213208e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

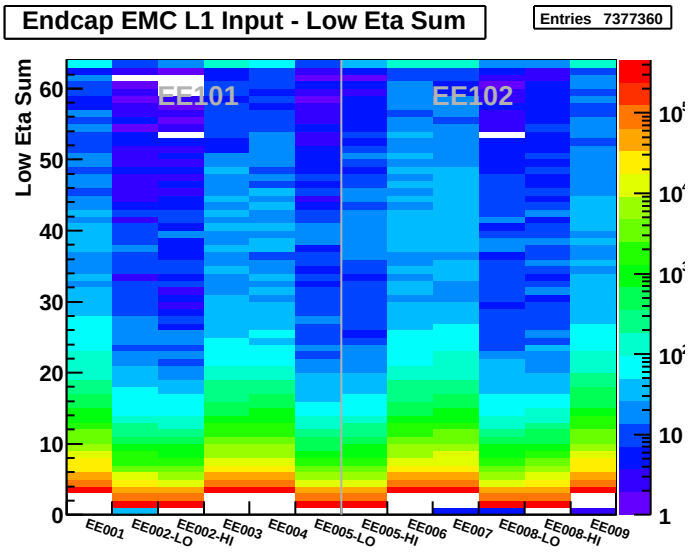

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

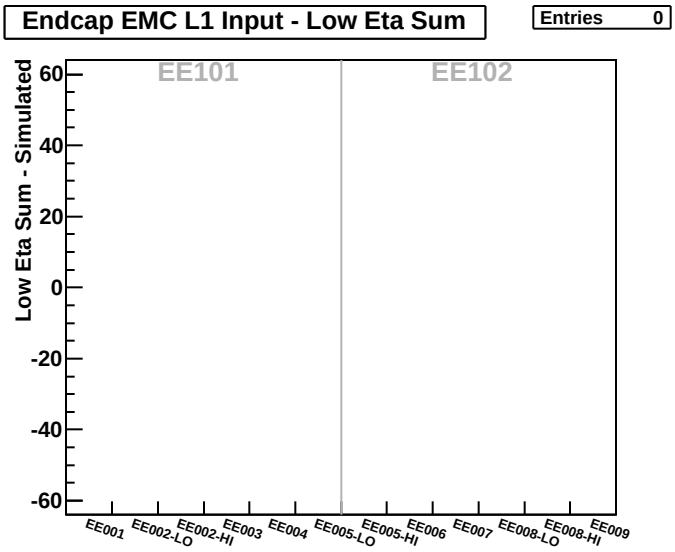

Barrel EMC L1 Input - High Eta Sum
Entries 2.213208e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

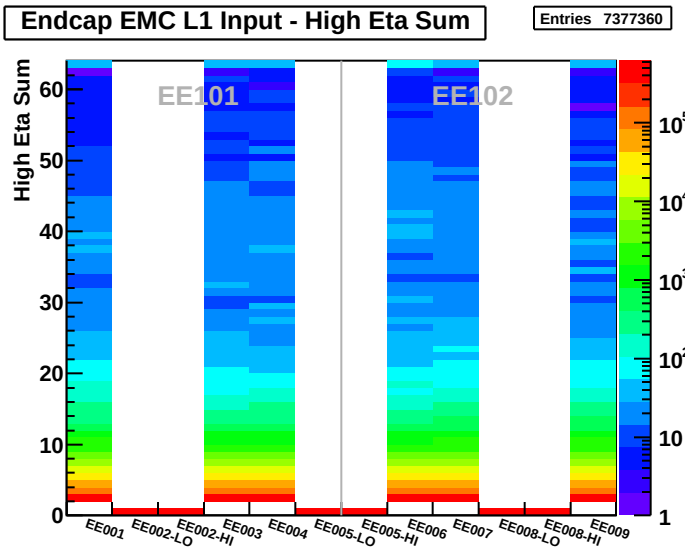

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

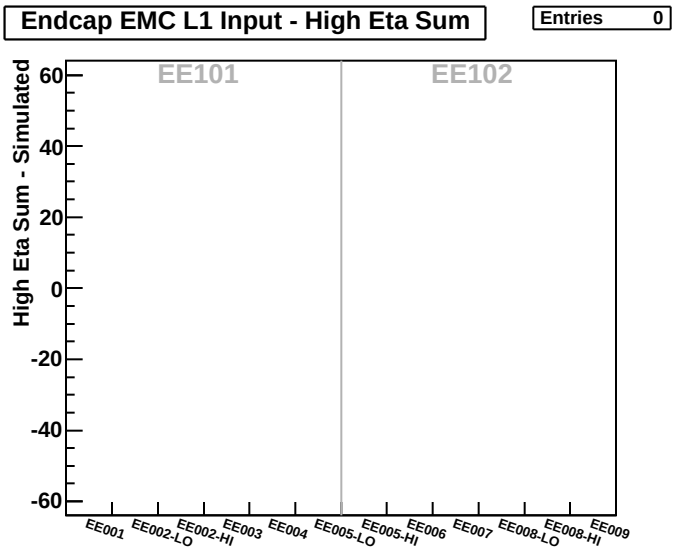

Barrel EMC L1 Input - High Tower Bits
Entries 2.213208e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

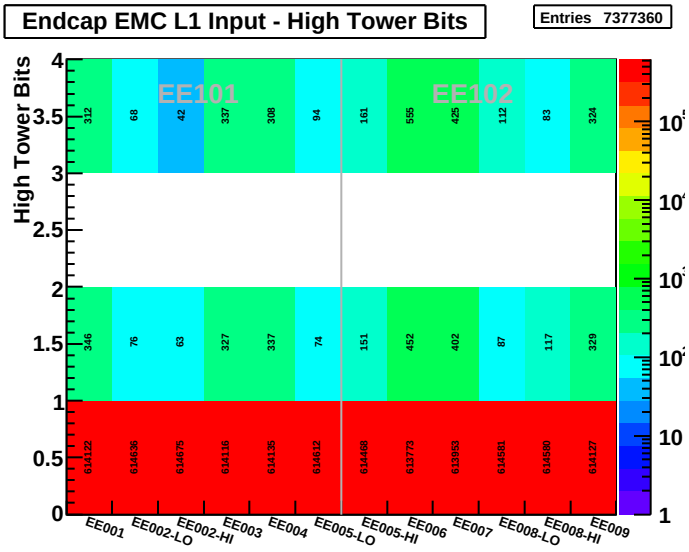

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

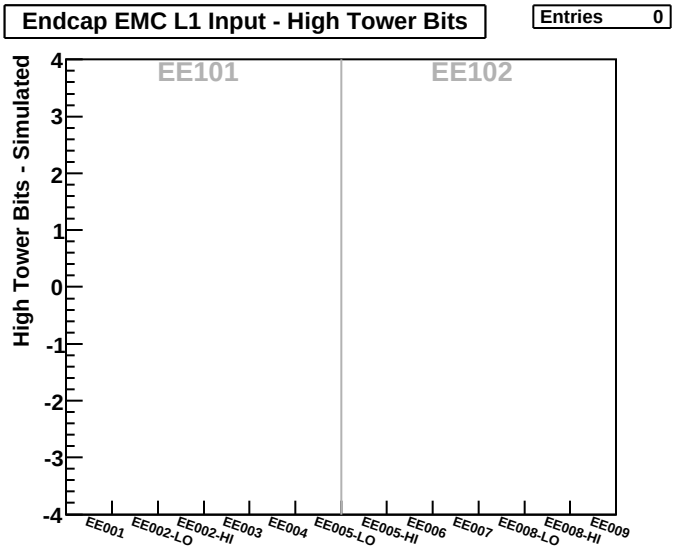

Endcap EMC L1 Input - Low Eta Sum
Entries 7377360
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

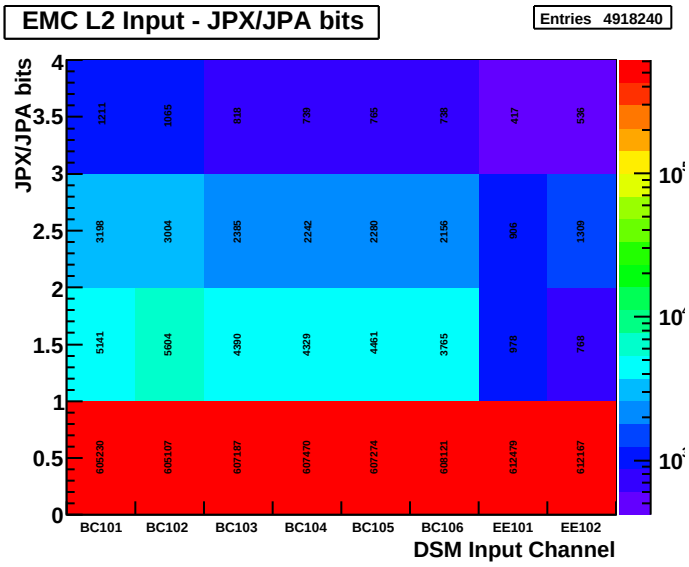

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

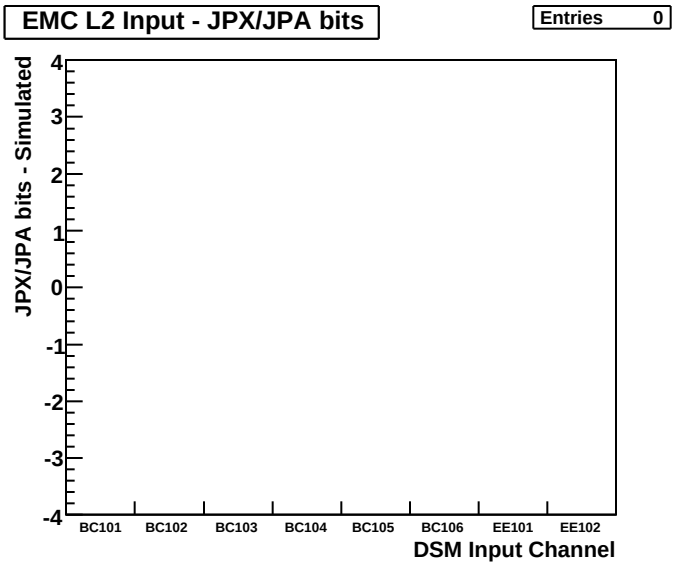

Endcap EMC L1 Input - High Eta Sum
Entries 7377360
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

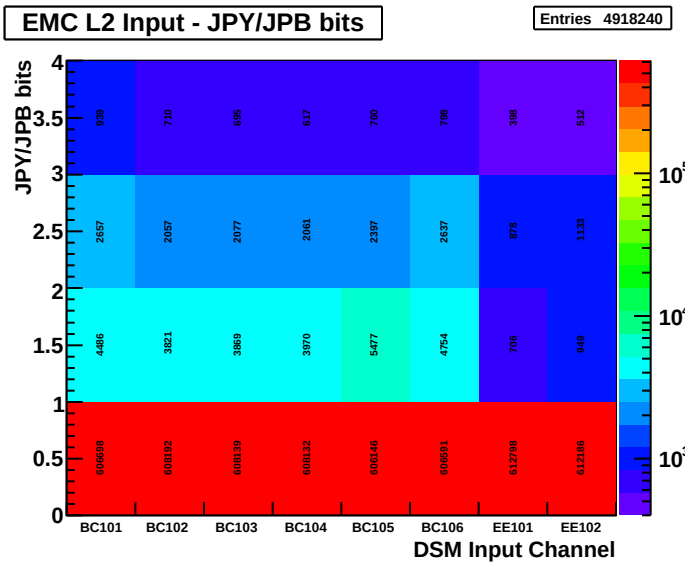

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

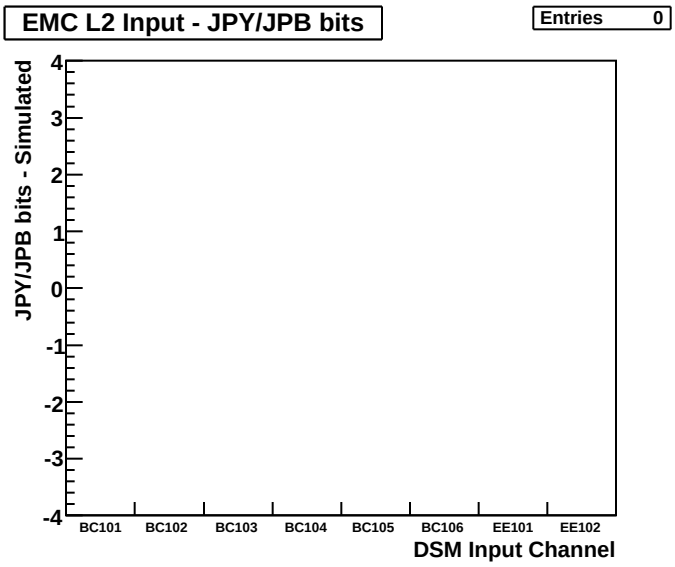

Endcap EMC L1 Input - High Tower Bits
Entries 7377360
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

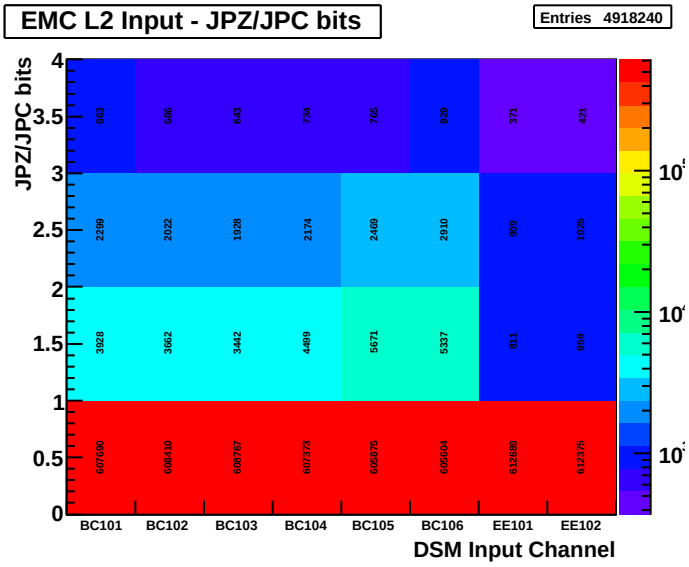

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

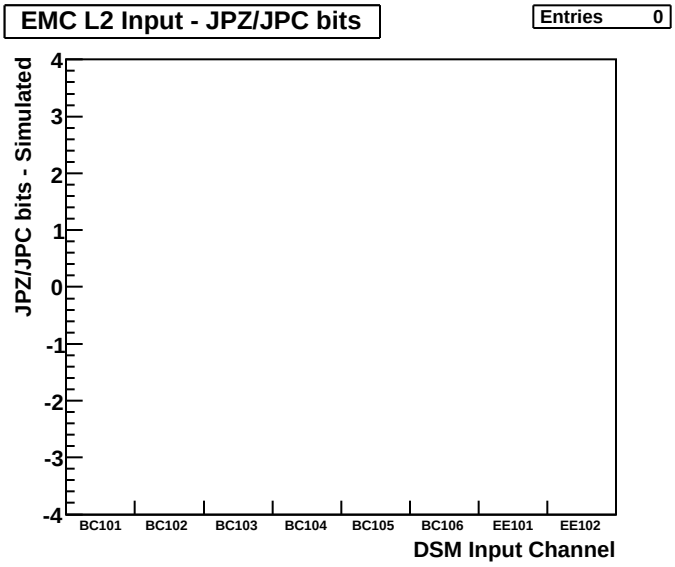

EMC L2 Input - JPX/JPA bits
Entries 4918240
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1211 1065 818 739 765 738 417 536
3198 3004 2385 2242 2280 2156 906 1309
5141 5604 4390 4329 4461 3765 976 768
605230 605107 607187 607470 607274 608121 612479 612167

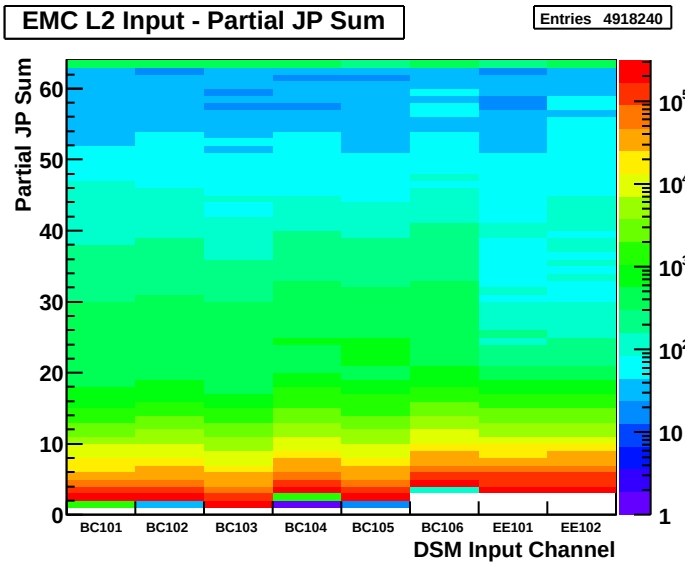

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

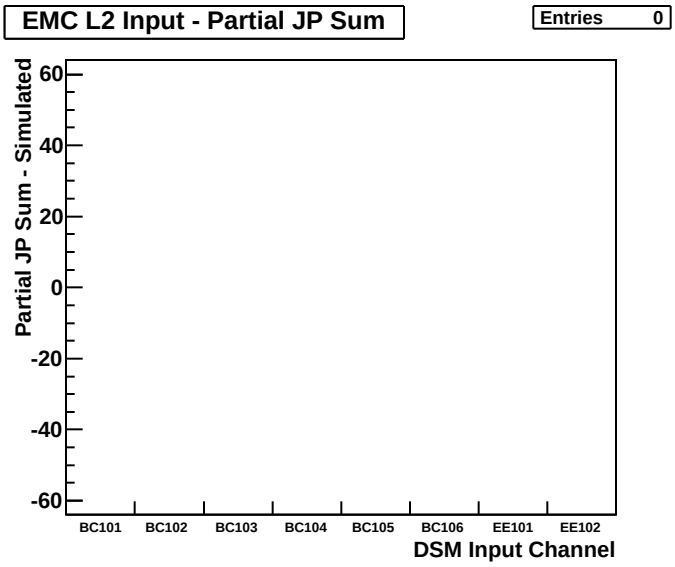

EMC L2 Input - JPY/JPB bits
Entries 4918240
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
938 710 695 617 760 788 398 512
2657 2057 2077 2061 2397 2637 878 1133
4486 3821 3869 3970 5477 4754 706 949
606698 608192 608139 608132 608146 608591 612798 612186

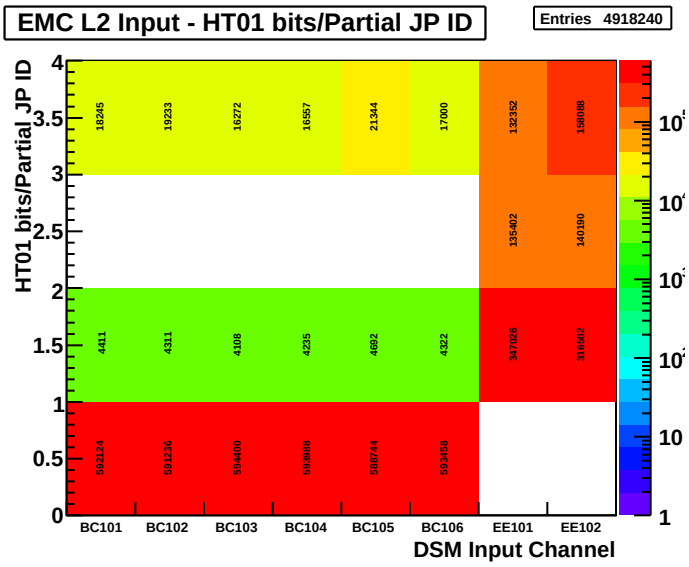

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

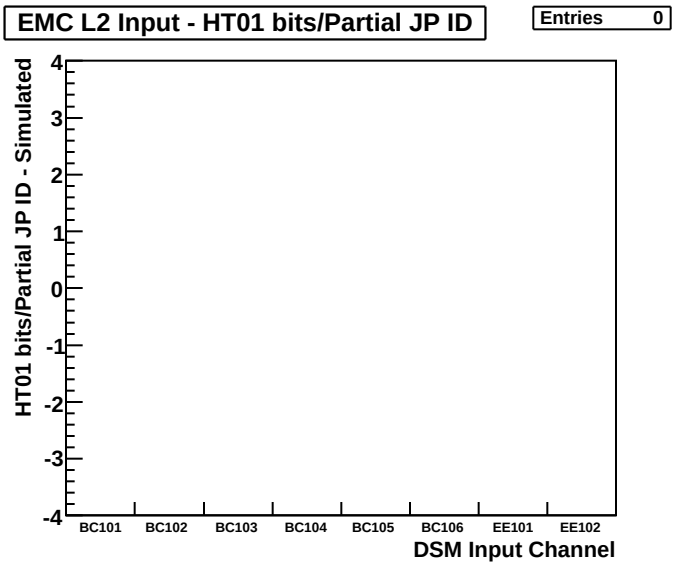

EMC L2 Input - JPZ/JPC bits
Entries 4918240
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
883 686 643 734 765 929 371 421
2299 2022 1928 2174 2469 2910 909 1025
3928 3662 3442 4489 5671 5337 811 959
607590 608410 608767 607373 605875 605604 612689 612375

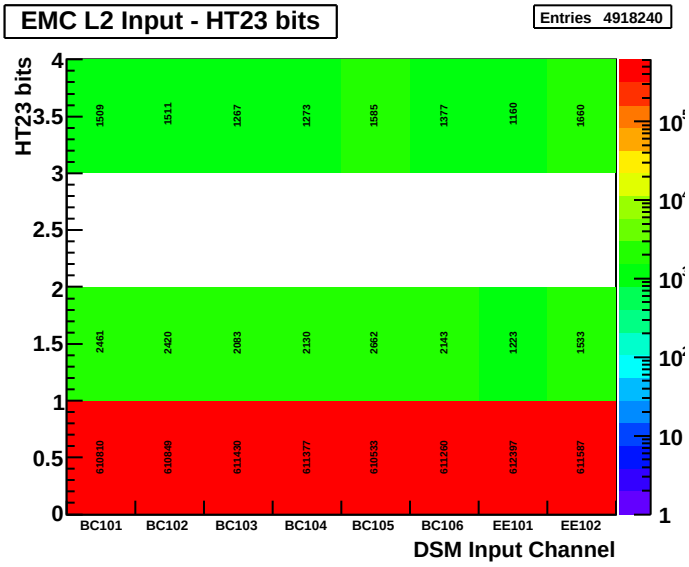

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

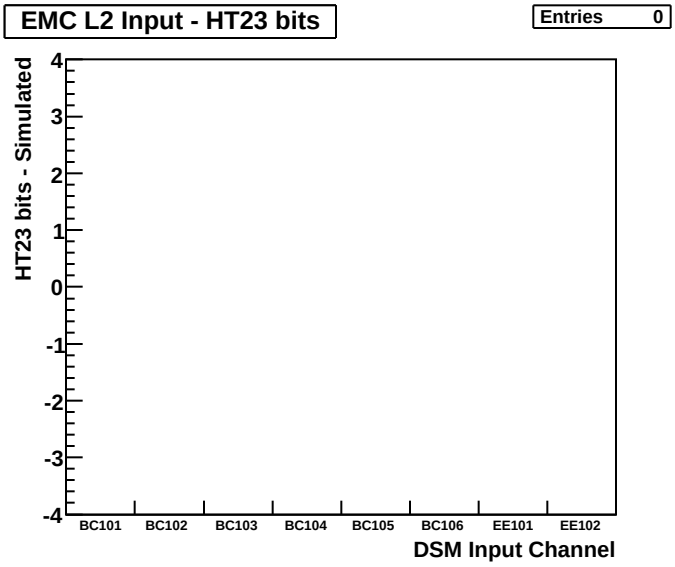

EMC L2 Input - Partial JP Sum
Entries 4918240
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

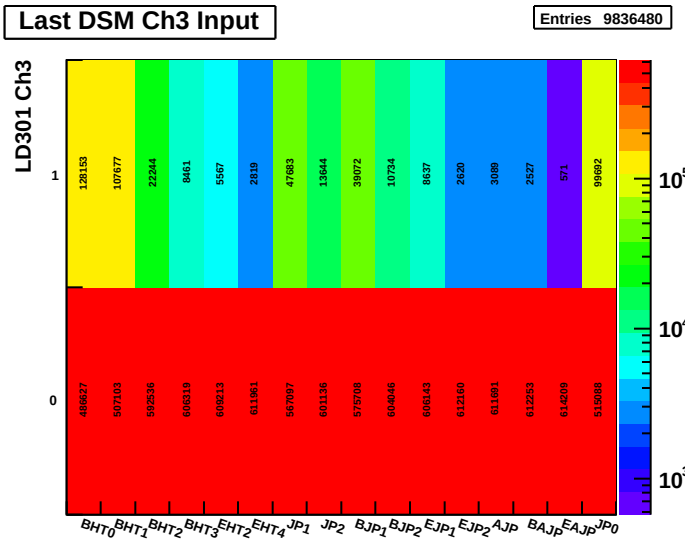

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

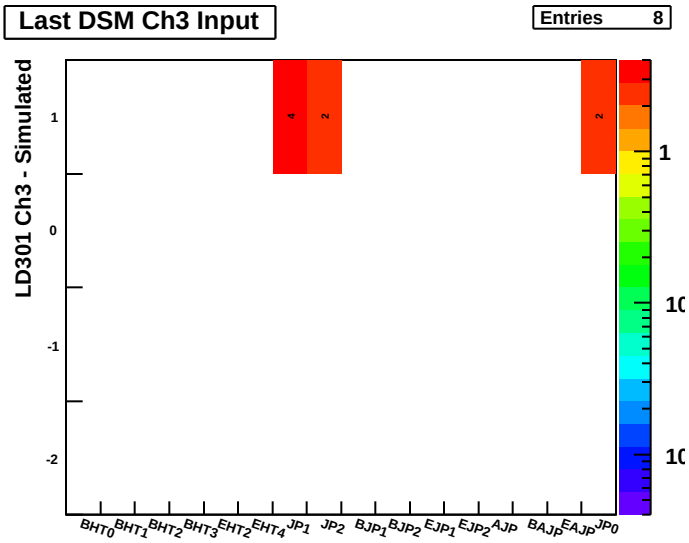

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4918240
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

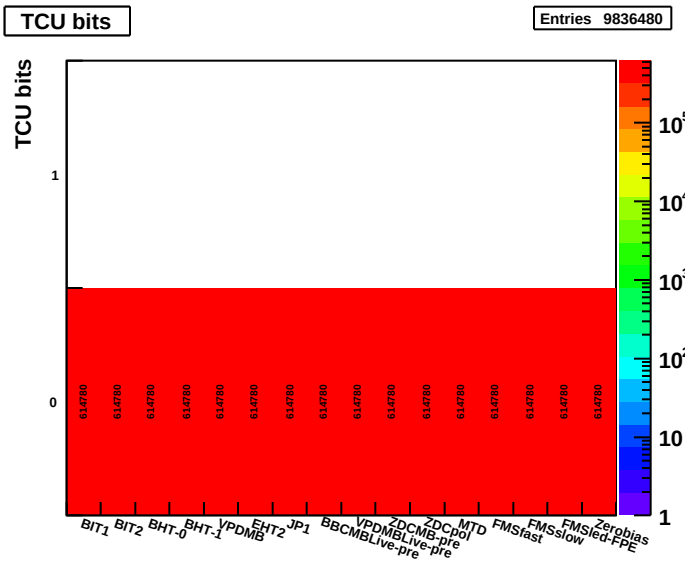

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

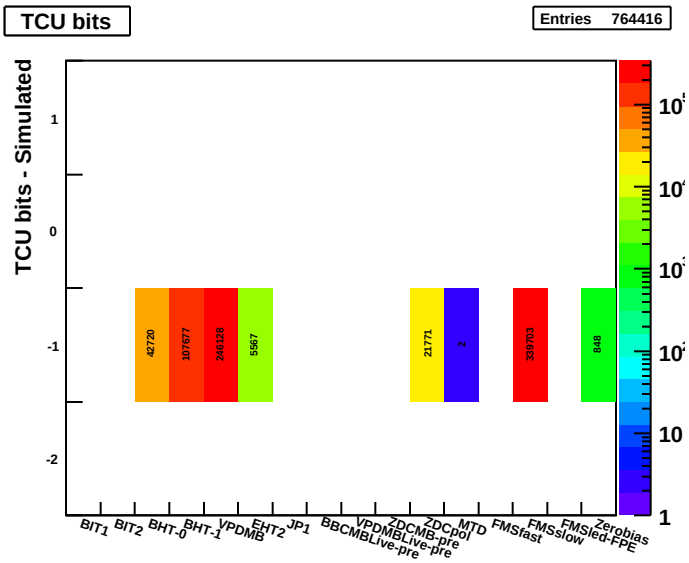

EMC L2 Input - HT23 bits
Entries 4918240
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

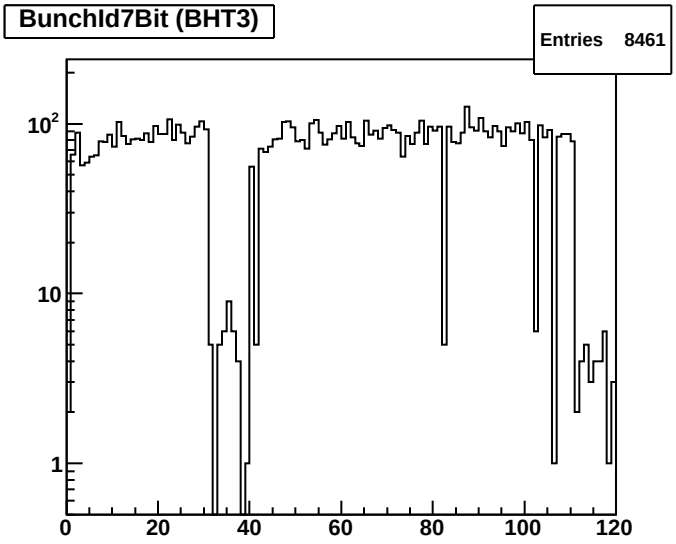

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

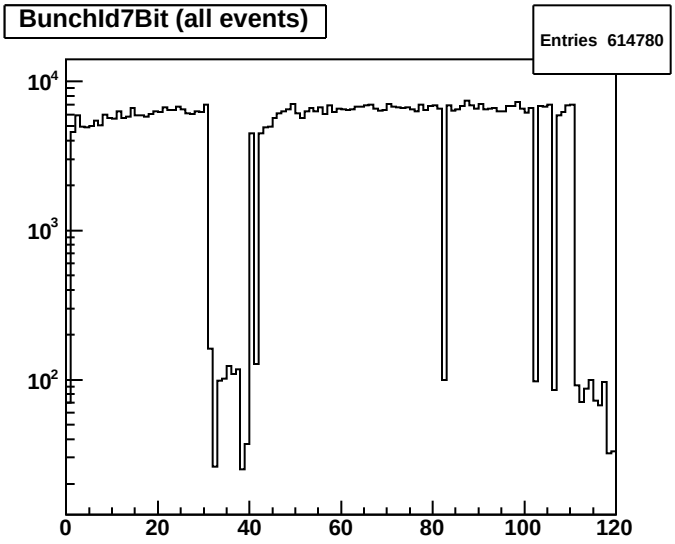

Last DSM Ch3 Input
Entries 9836480
LD301 Ch3
128153 107677 22244 8461 5567 2819 47683 13644 39072 10734 8637 2620 3089 2527 571 99692
486627 507103 592536 606319 609213 611951 567097 601136 575708 604046 606143 612160 611691 612253 614209 515088
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

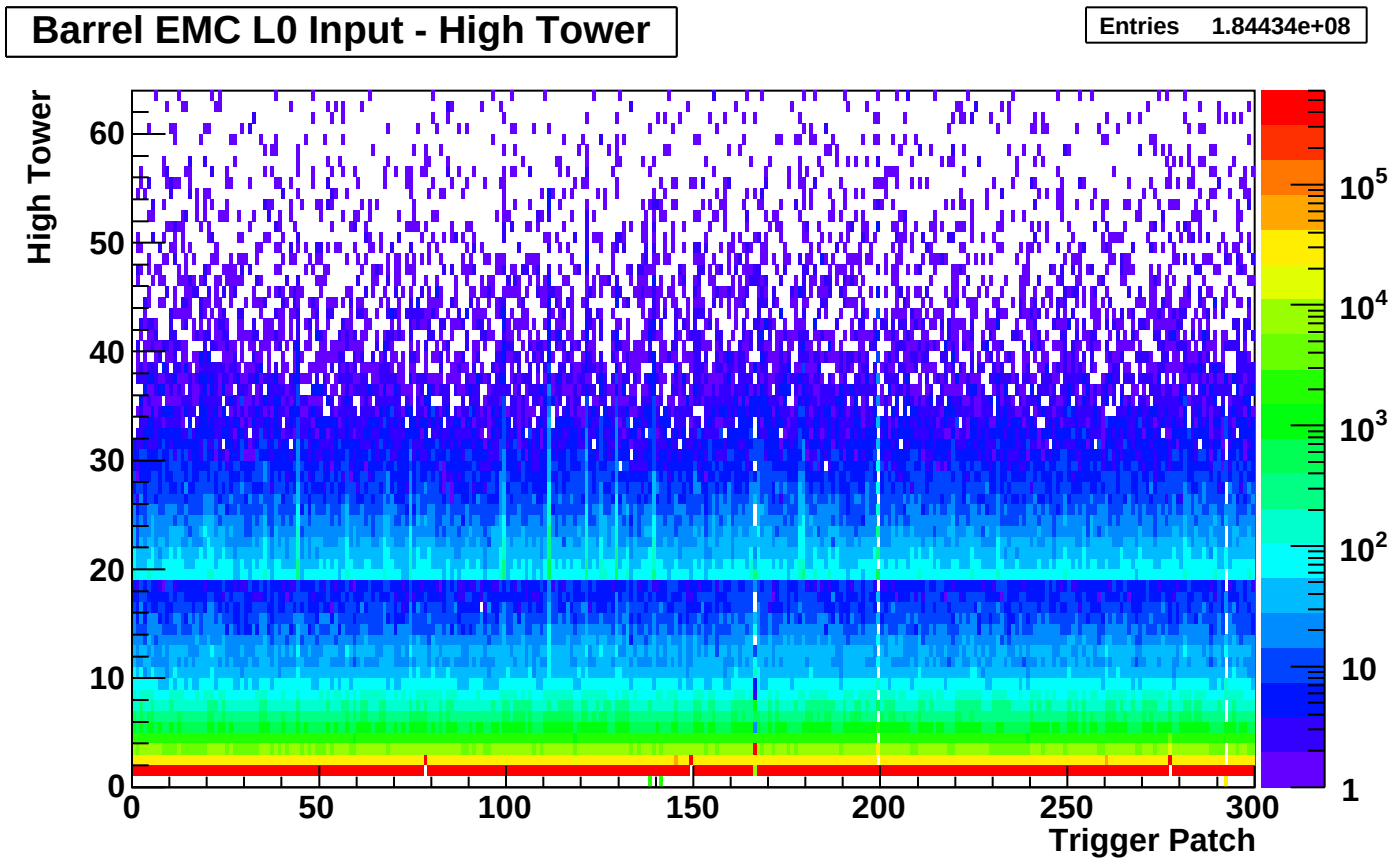

Last DSM Ch3 Input
Entries 8
LD301 Ch3 - Simulated
4 2 2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

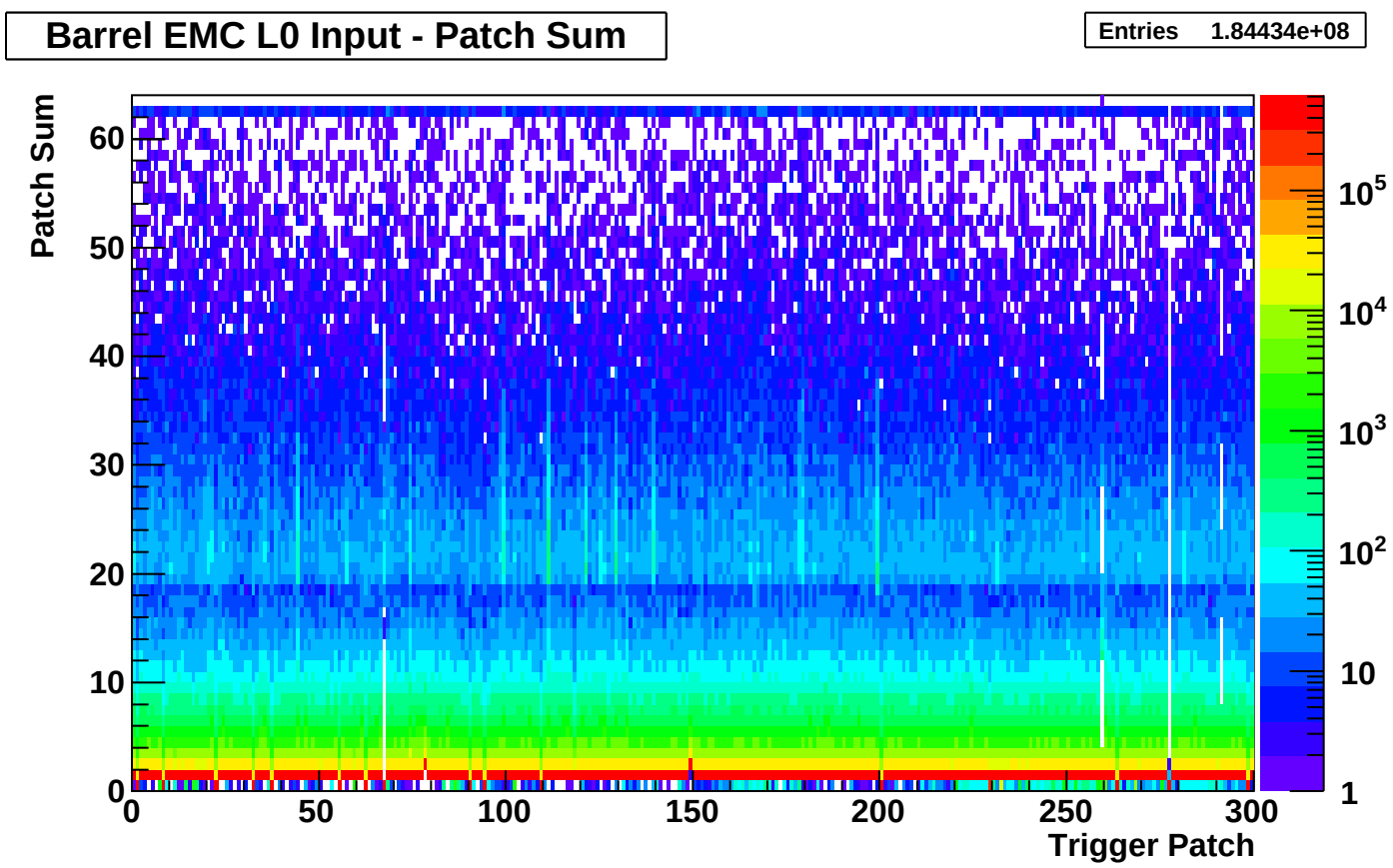

TCU bits
Entries 9836480
TCU bits
614780
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

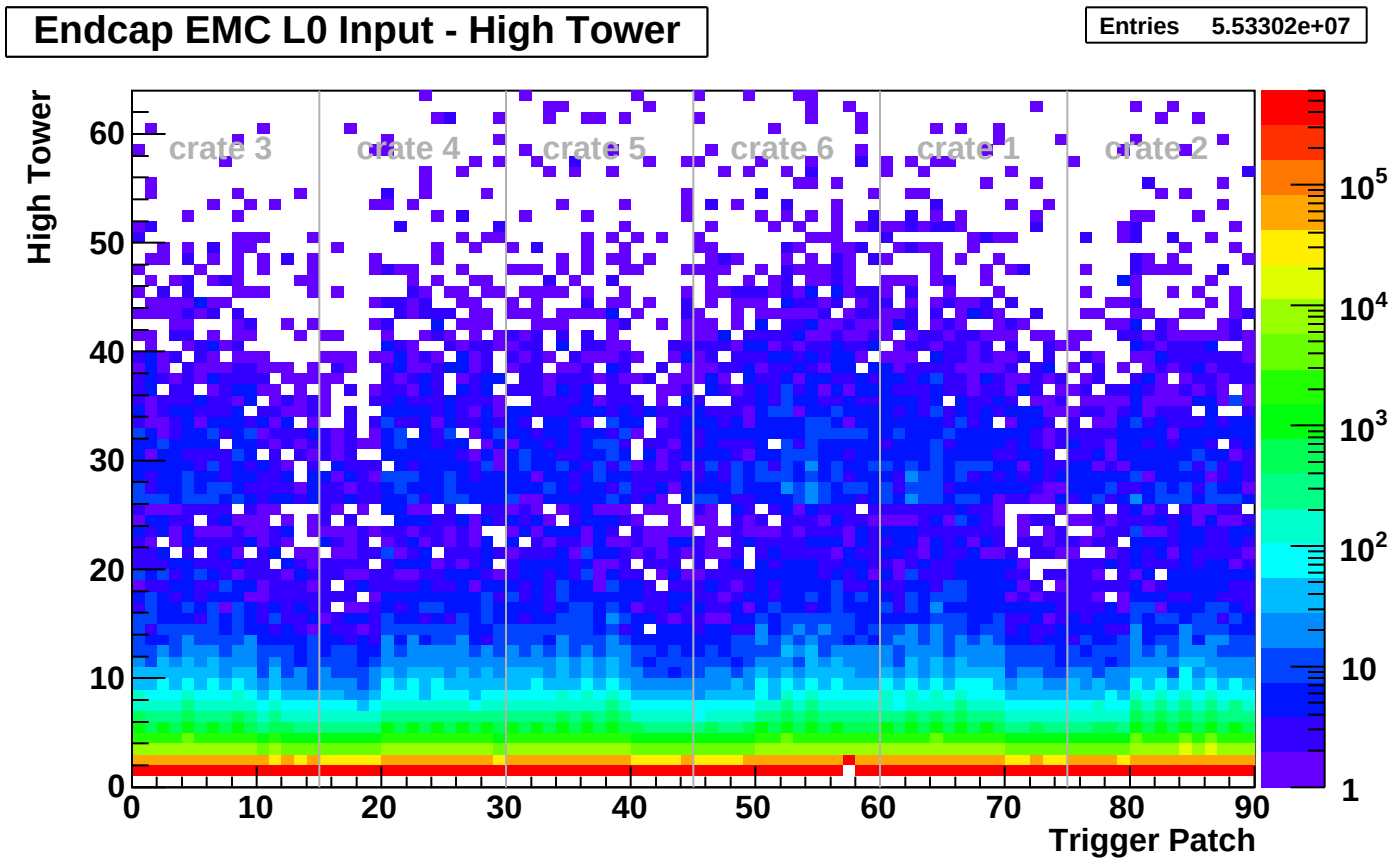

TCU bits
Entries 764416
TCU bits - Simulated
42720 107677 246128 5567 21771 2 339703 848
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

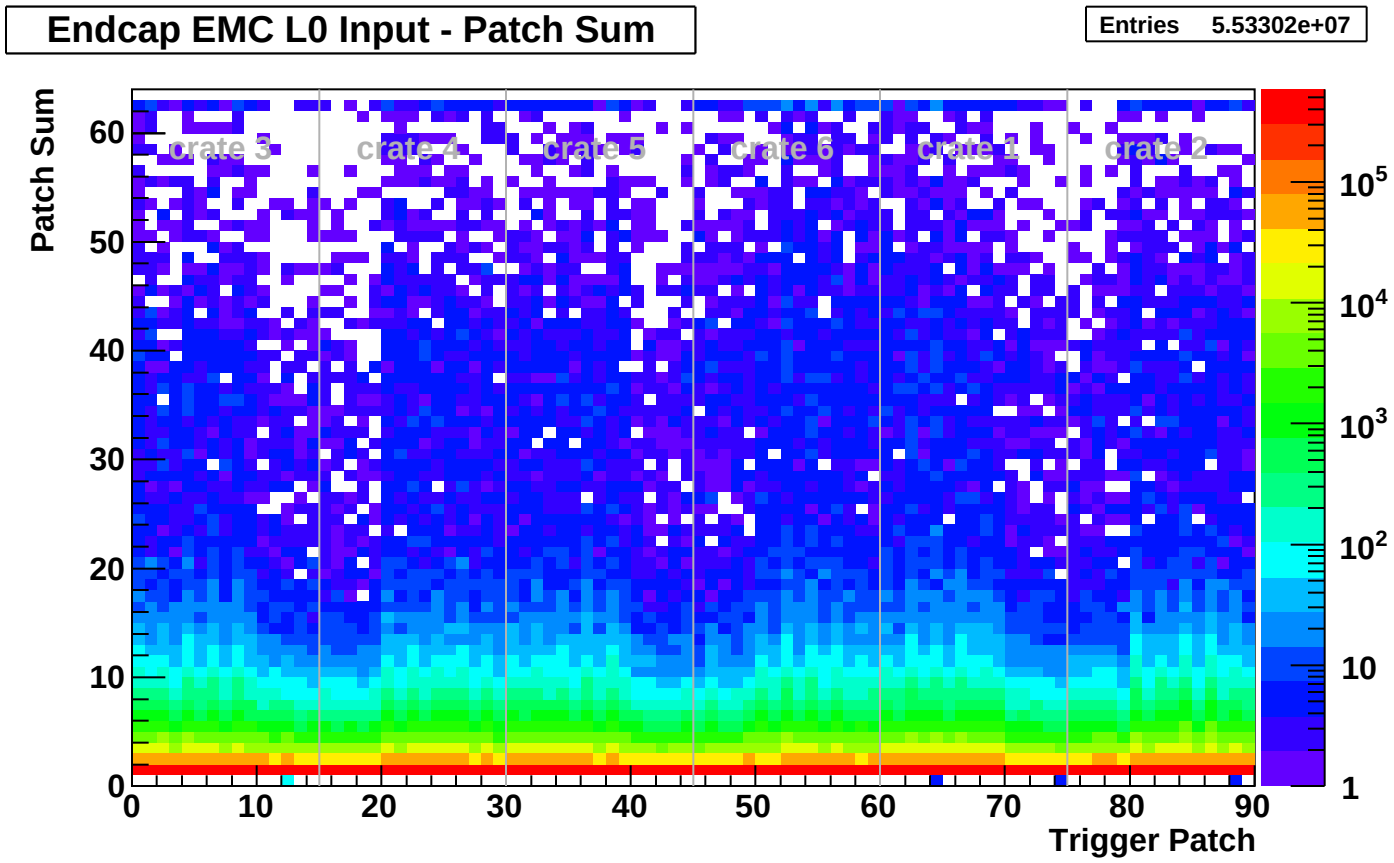

BunchId7Bit (BHT3)
Entries 8461

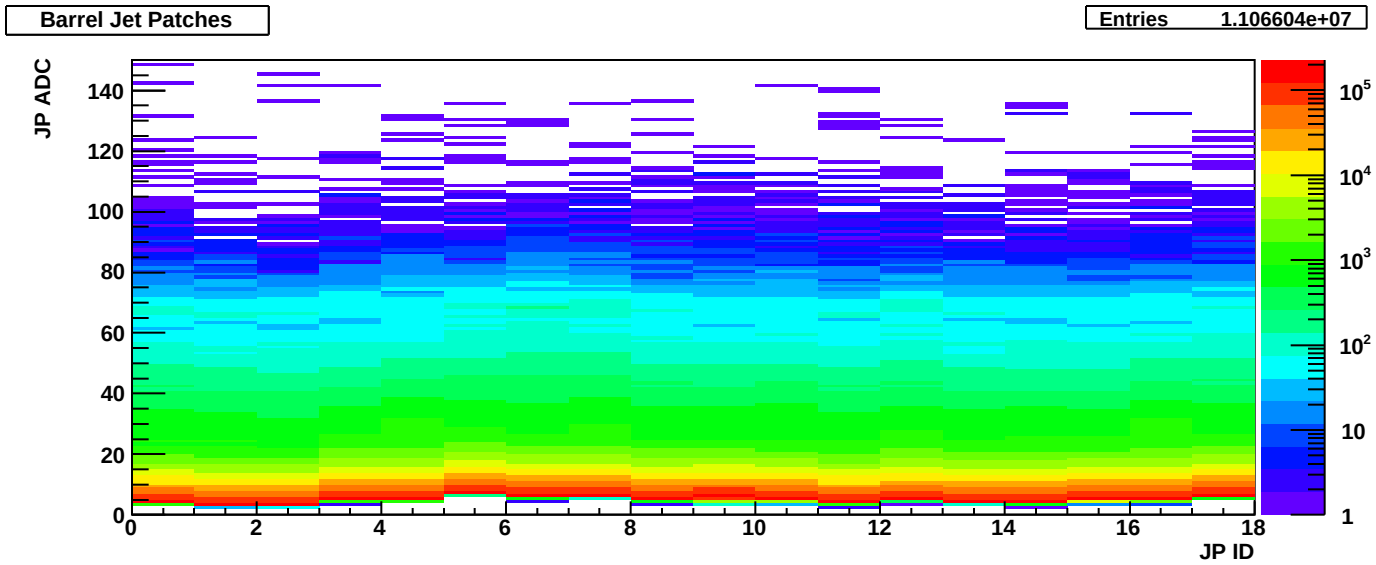

BunchId7Bit (all events)
Entries 614780

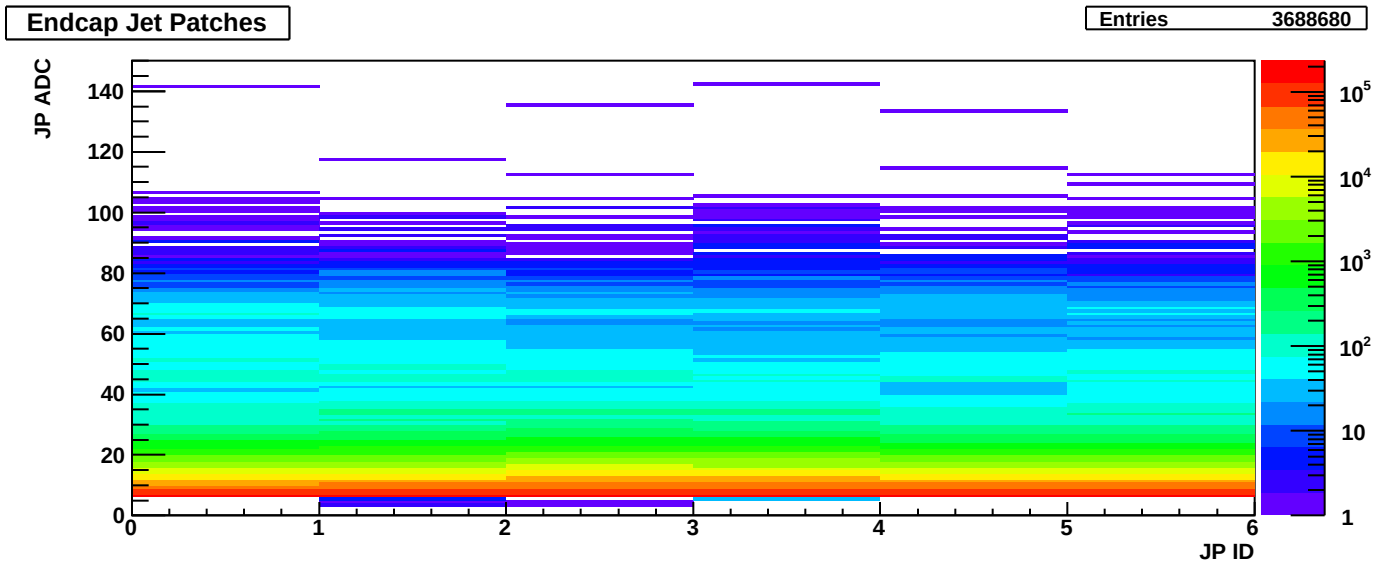

Barrel EMC L0 Input - High Tower
Entries 1.84434e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

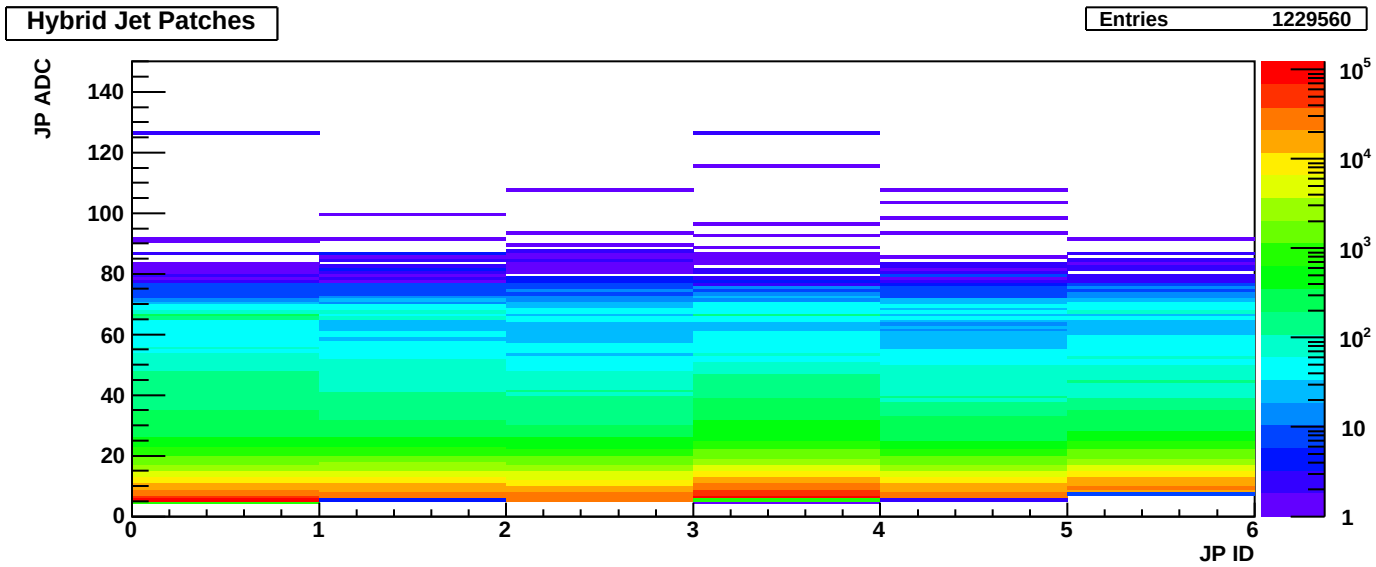

Barrel EMC L0 Input - Patch Sum
Entries 1.84434e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

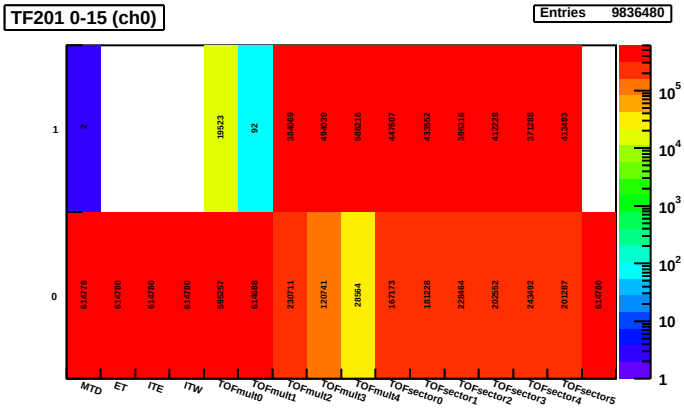

Endcap EMC L0 Input - High Tower
Entries 5.53302e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

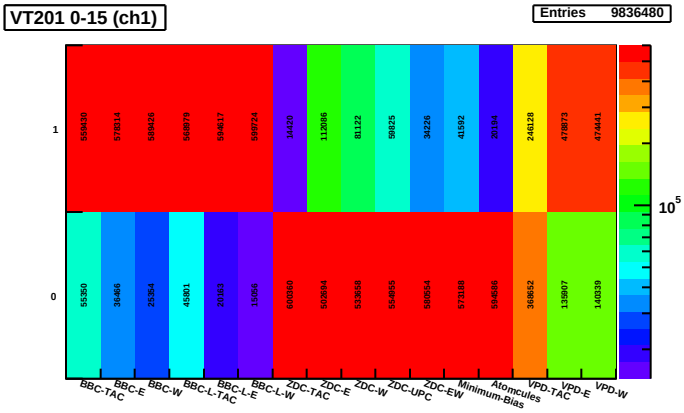

Endcap EMC L0 Input - Patch Sum
Entries 5.53302e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

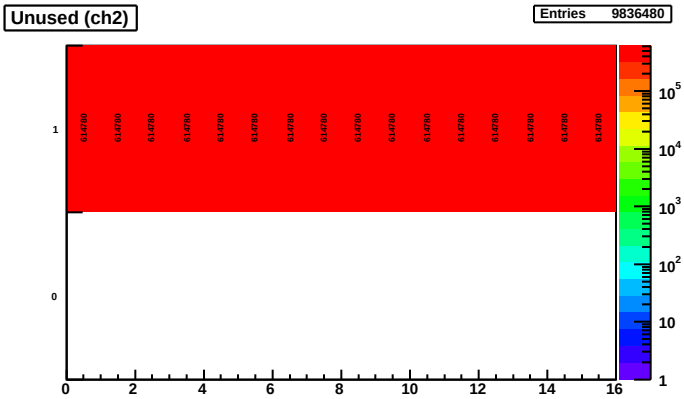

Barrel Jet Patches
Entries 1.106604e+07
JP ADC
JP ID

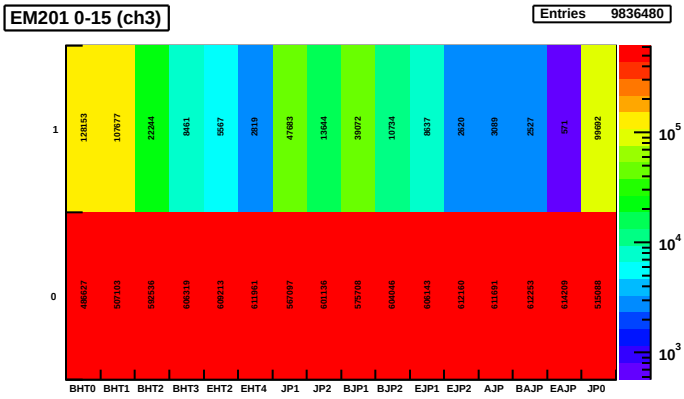

Endcap Jet Patches
Entries 3688680
JP ADC
JP ID

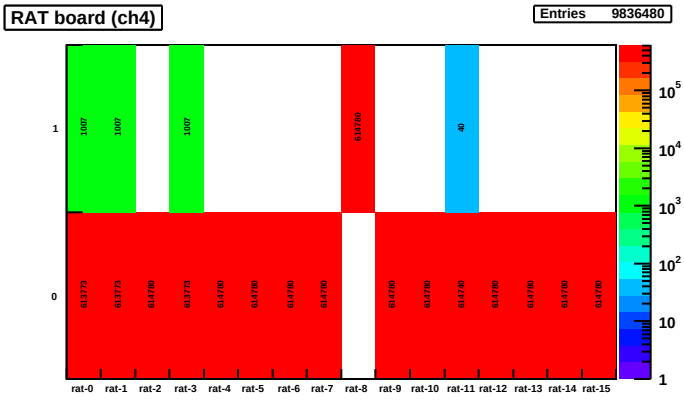

Hybrid Jet Patches
Entries 1229560
JP ADC
JP ID

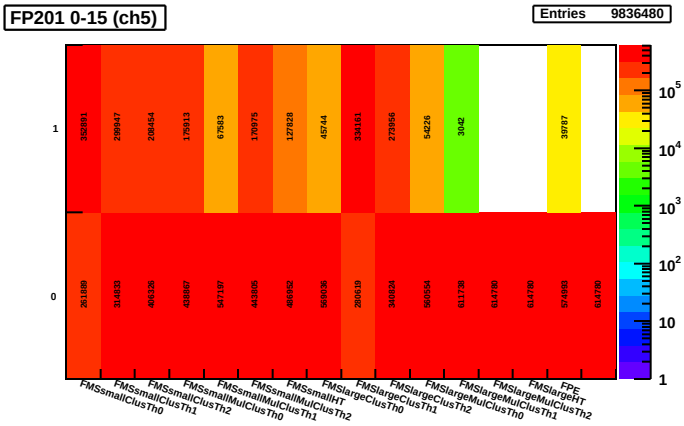

TF201 0-15 (ch0)
Entries 9836480
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

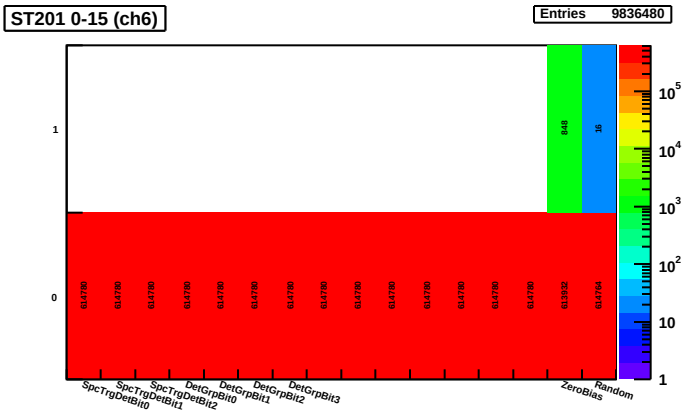

VT201 0-15 (ch1)
Entries 9836480
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

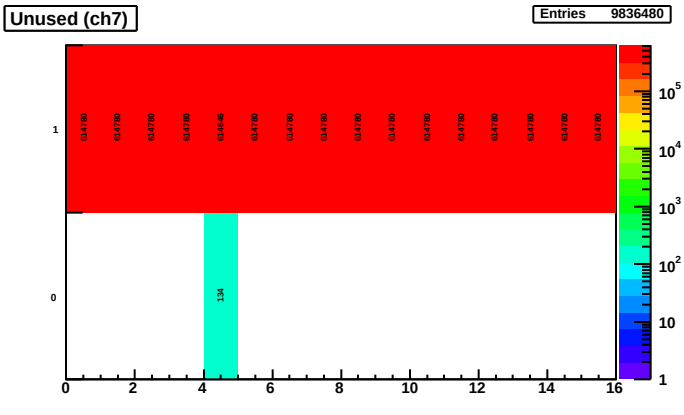

Unused (ch2)
Entries 9836480

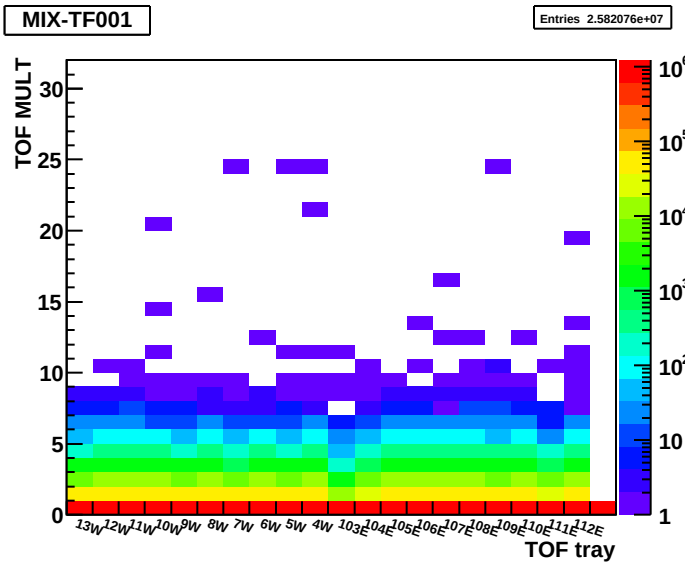

EM201 0-15 (ch3)
Entries 9836480
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

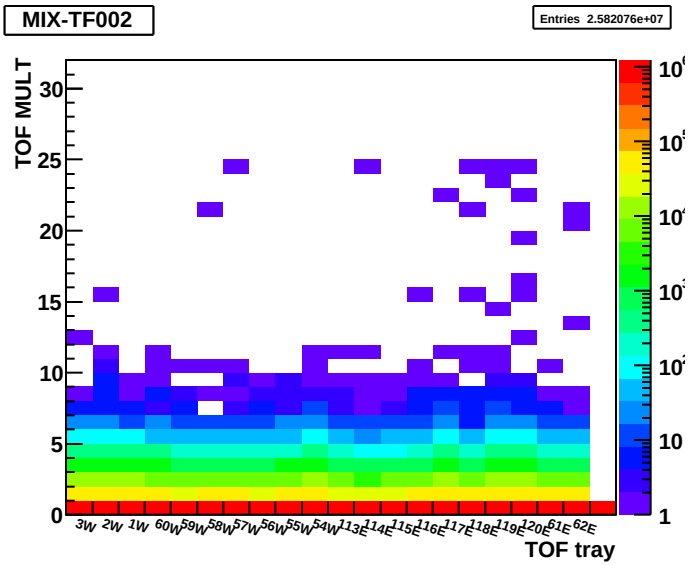

RAT board (ch4)
Entries 9836480
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

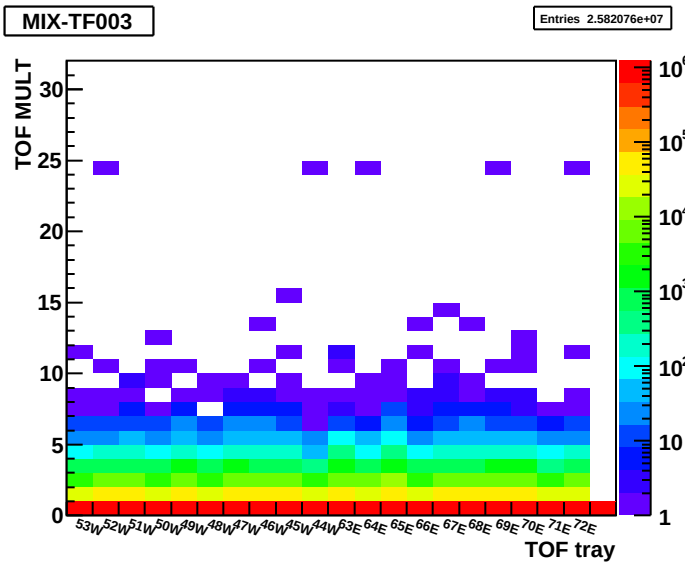

FP201 0-15 (ch5)
Entries 9836480
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

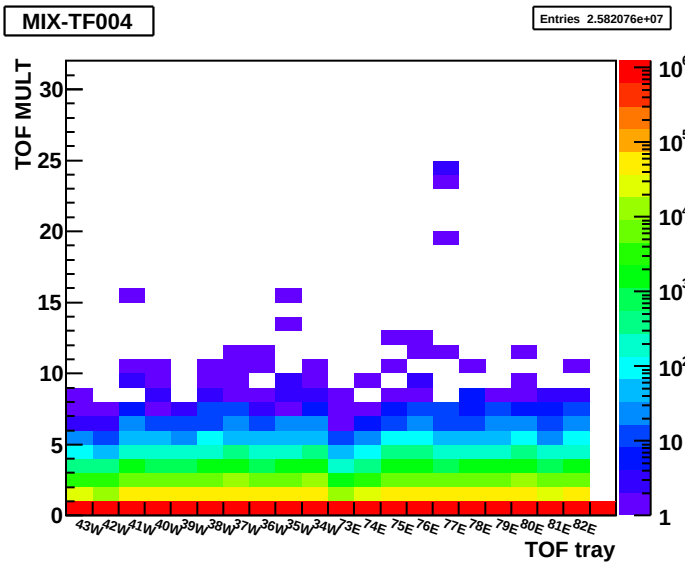

ST201 0-15 (ch6)
Entries 9836480
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

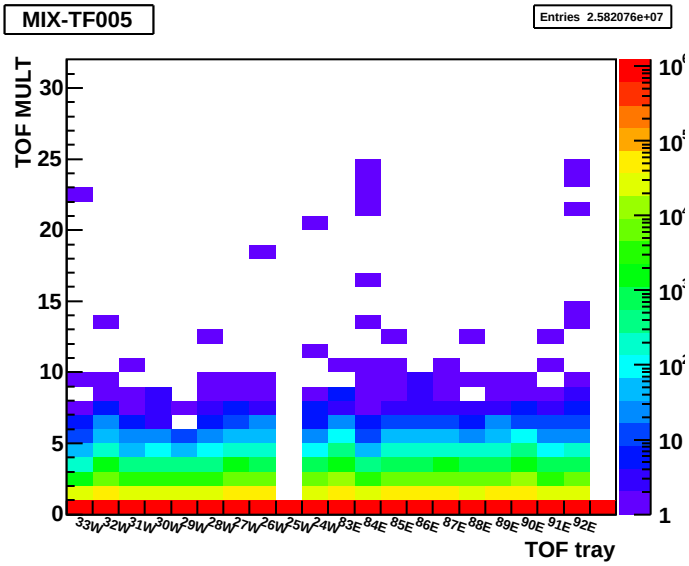

Unused (ch7)
Entries 9836480

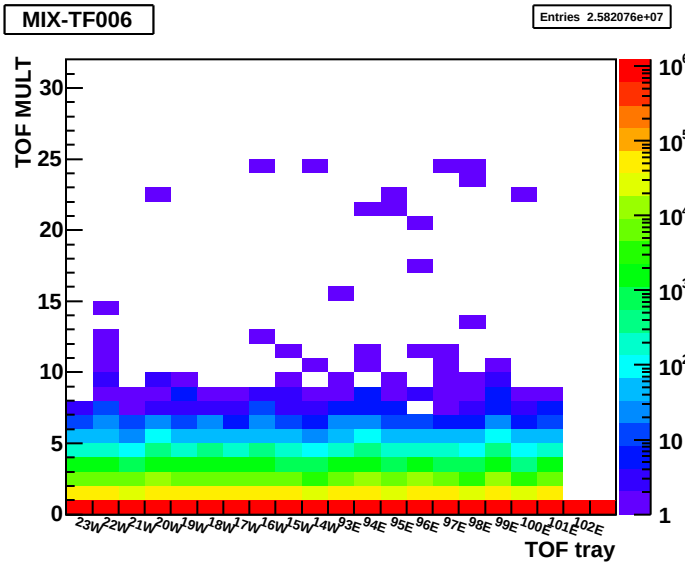

MIX-TF001
Entries 2.582076e+07
TOF MULT
TOF tray

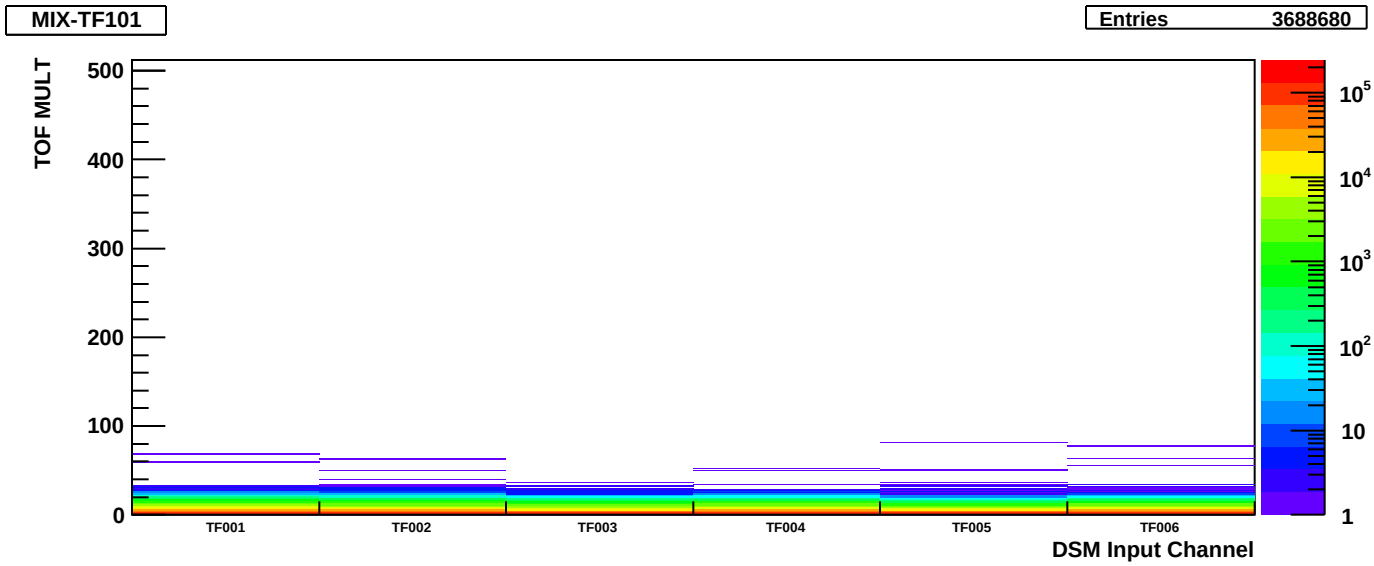

MIX-TF002
Entries 2.582076e+07
TOF MULT
TOF tray

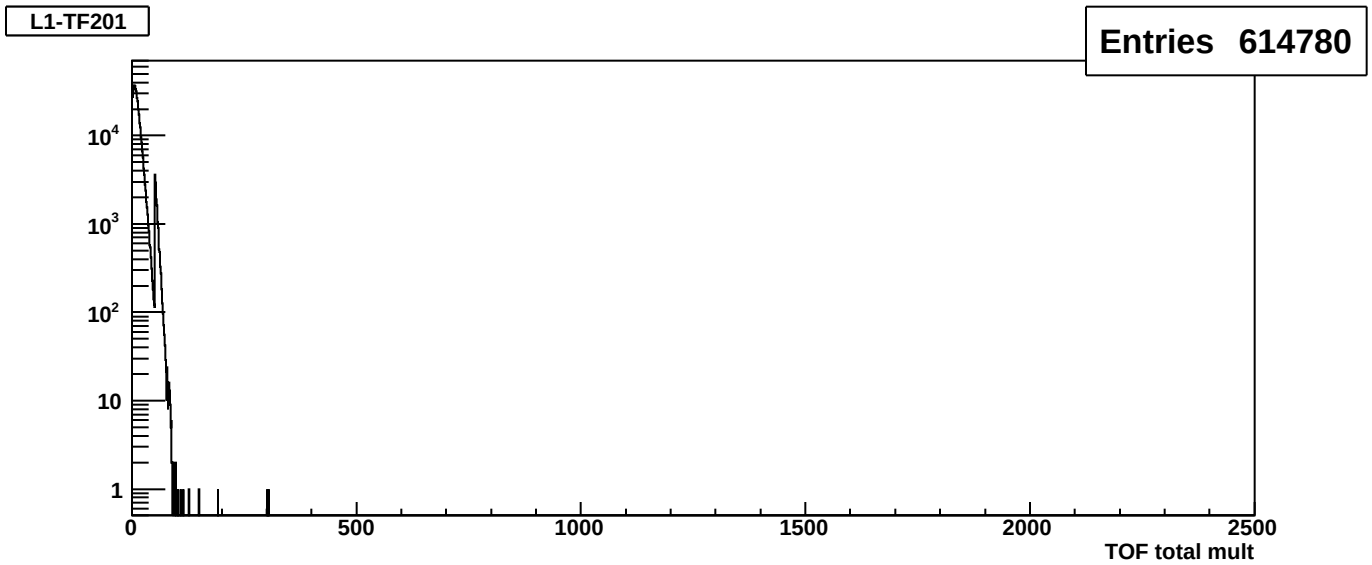

MIX-TF003
Entries 2.582076e+07
TOF MULT
TOF tray

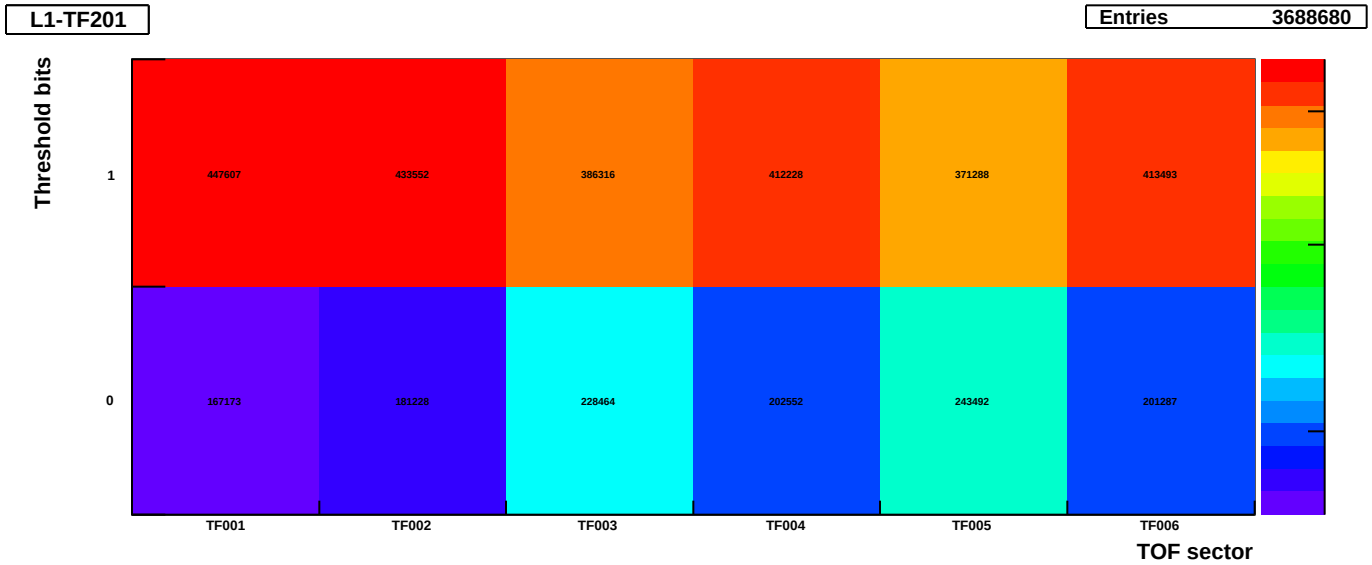

MIX-TF004
Entries 2.582076e+07
TOF MULT
TOF tray

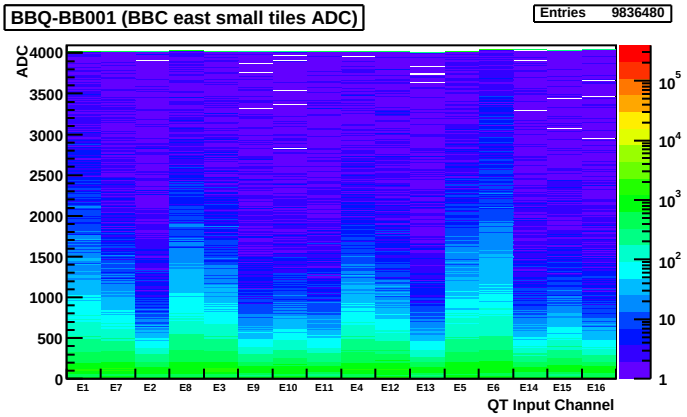

MIX-TF005
Entries 2.582076e+07
TOF MULT
TOF tray

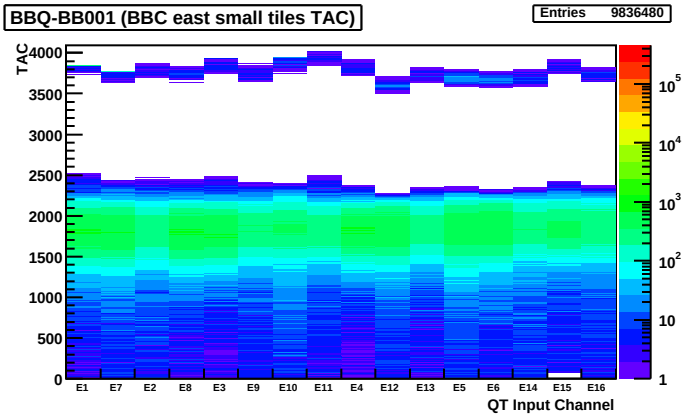

MIX-TF006
Entries 2.582076e+07
TOF MULT
TOF tray

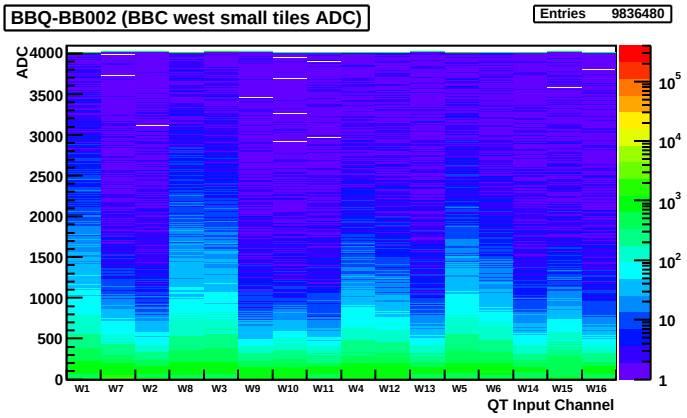

MIX-TF101
Entries 3688680
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

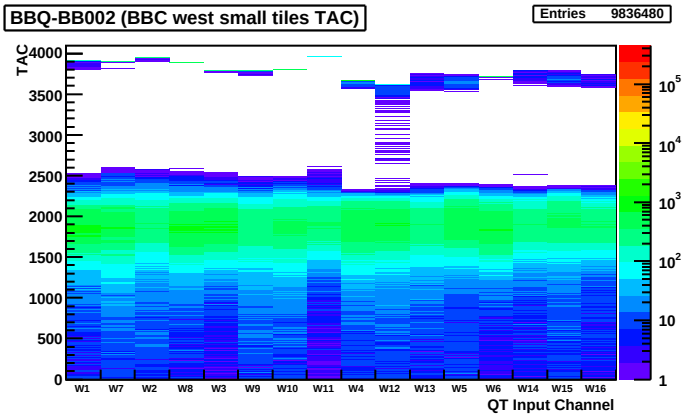

L1-TF201
Entries 614780
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

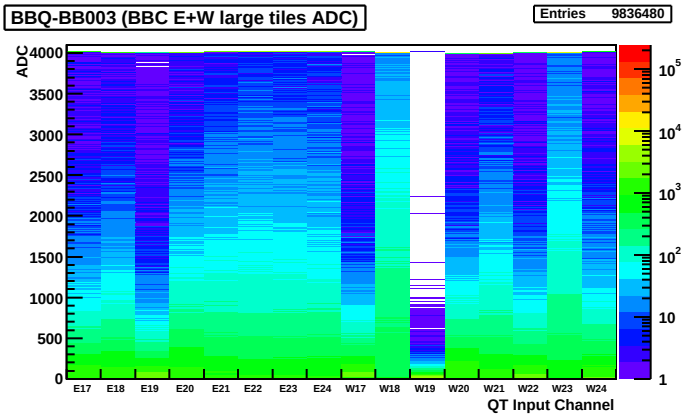

L1-TF201
Entries 3688680
Threshold bits
1
0
447607
433552
386316
412228
371288
413493
167173
181228
228464
202552
243492
201287
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

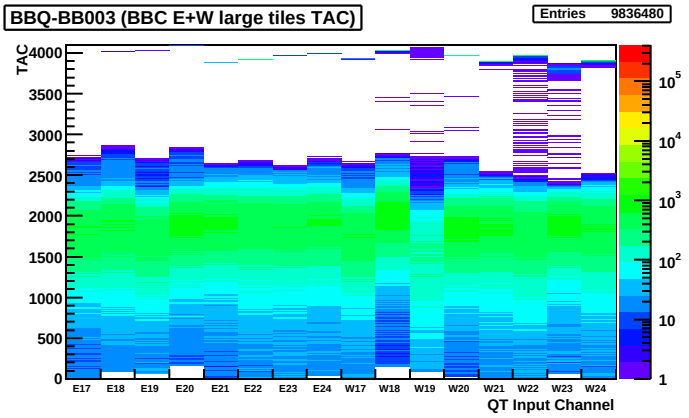

BBQ-BB001 (BBC east small tiles ADC)
Entries 9836480
ADC
QT Input Channel

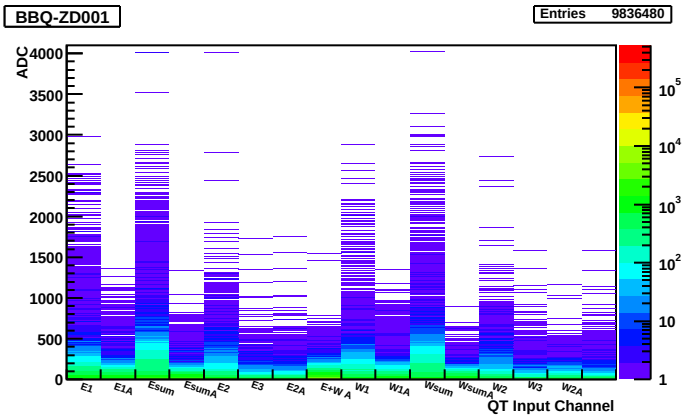

BBQ-BB001 (BBC east small tiles TAC)
Entries 9836480
TAC
QT Input Channel

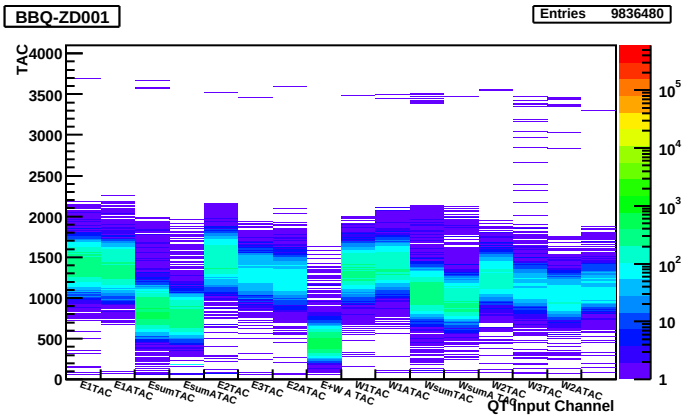

BBQ-BB002 (BBC west small tiles ADC)
Entries 9836480
ADC
QT Input Channel

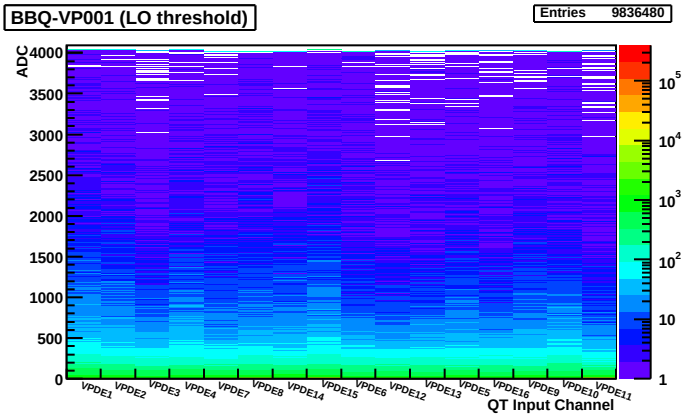

BBQ-BB002 (BBC west small tiles TAC)
Entries 9836480
TAC
QT Input Channel

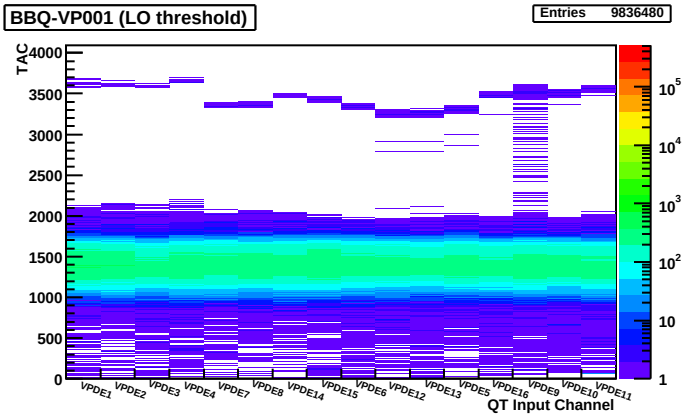

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9836480
ADC
QT Input Channel

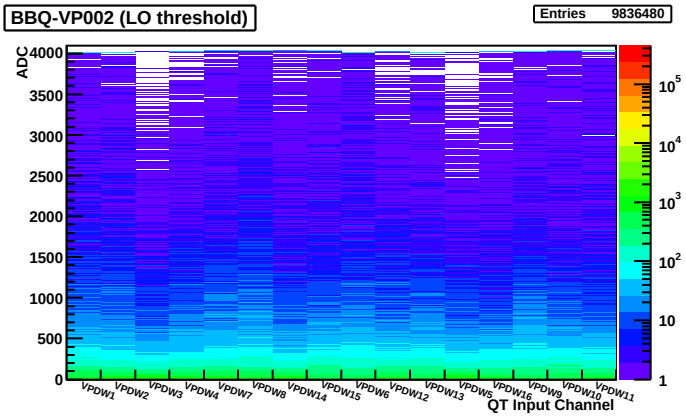

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9836480
TAC
QT Input Channel

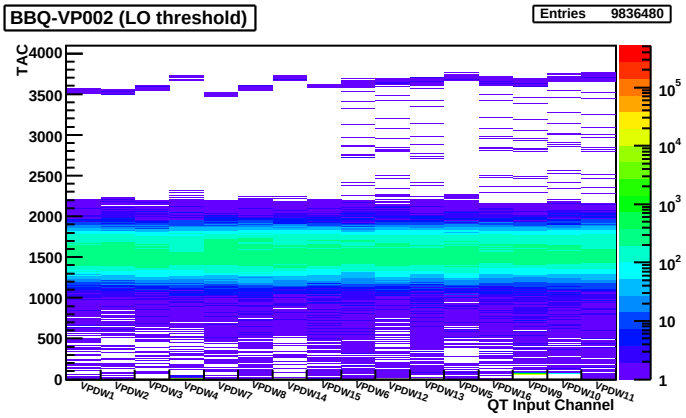

BBQ-ZD001
Entries 9836480
ADC
QT Input Channel

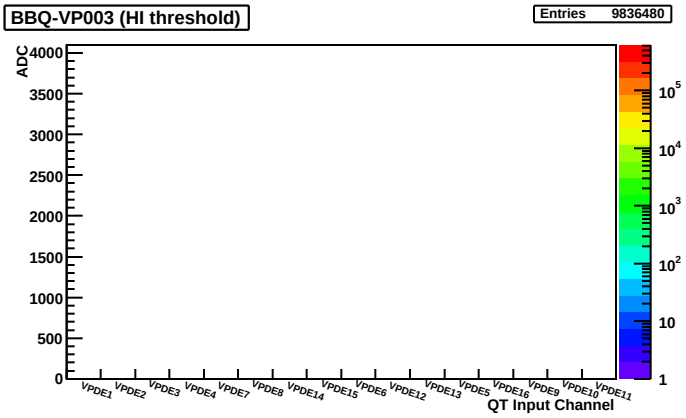

BBQ-ZD001
Entries 9836480
TAC
QT Input Channel

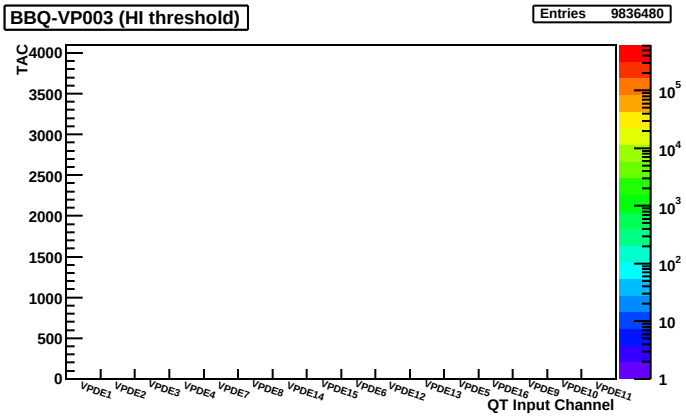

BBQ-VP001 (LO threshold)
Entries 9836480
ADC
QT Input Channel

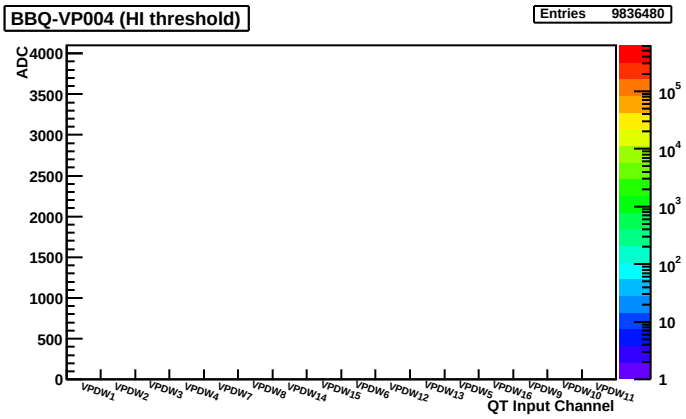

BBQ-VP001 (LO threshold)
Entries 9836480
TAC
QT Input Channel

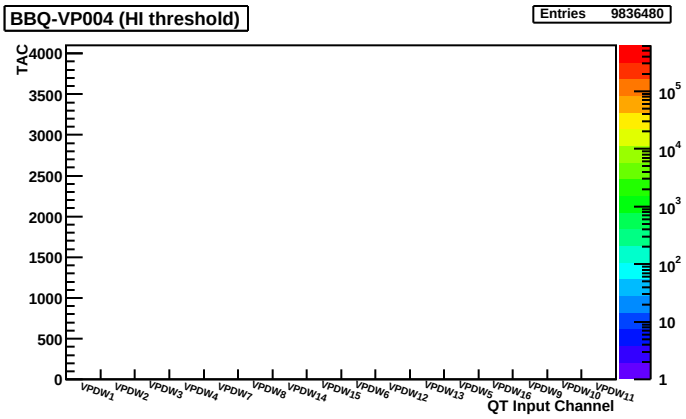

BBQ-VP002 (LO threshold)
Entries 9836480
ADC
QT Input Channel

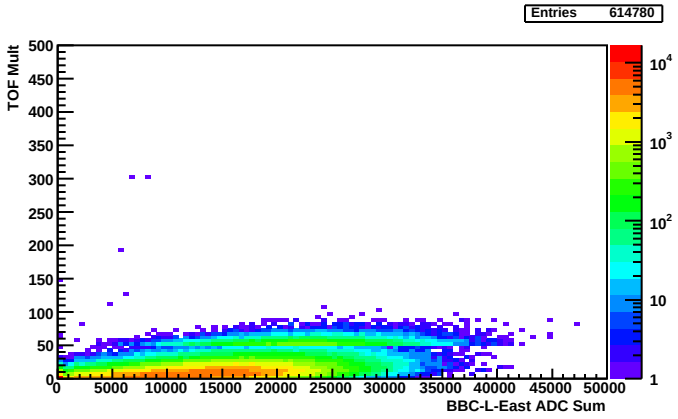

BBQ-VP002 (LO threshold)
Entries 9836480
TAC
QT Input Channel

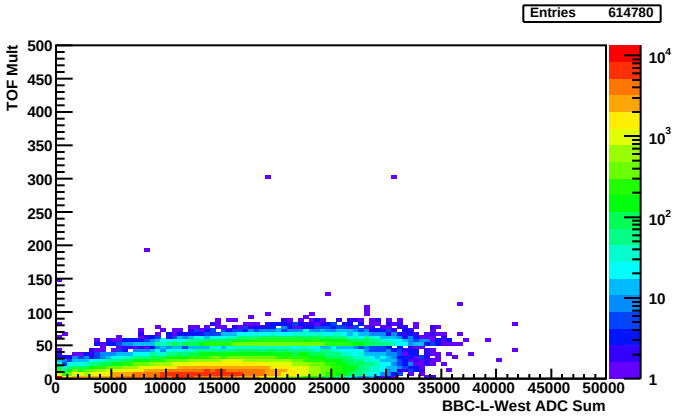

BBQ-VP003 (HI threshold)
Entries 9836480
ADC
QT Input Channel

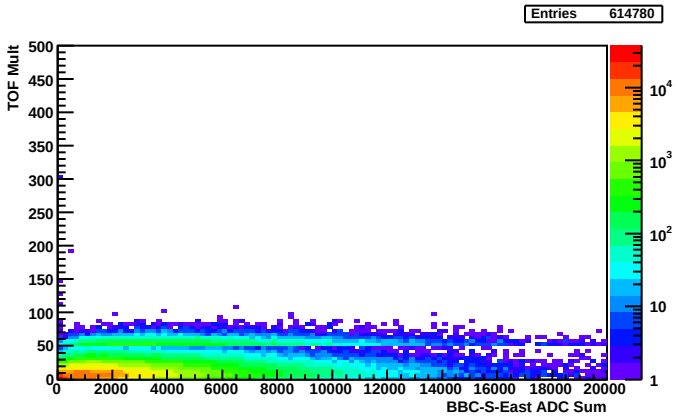

BBQ-VP003 (HI threshold)
Entries 9836480
TAC
QT Input Channel

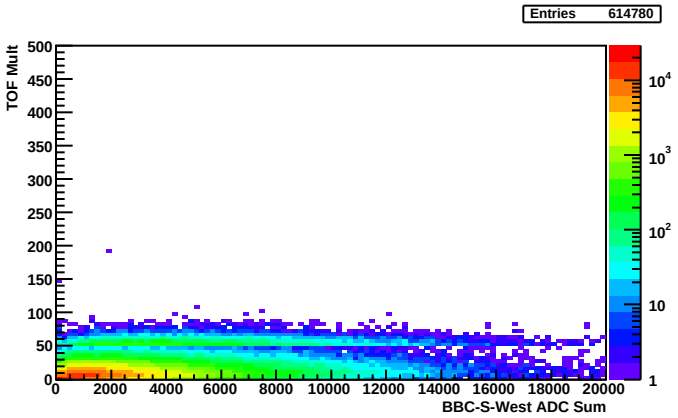

BBQ-VP004 (HI threshold)
Entries 9836480
ADC
QT Input Channel

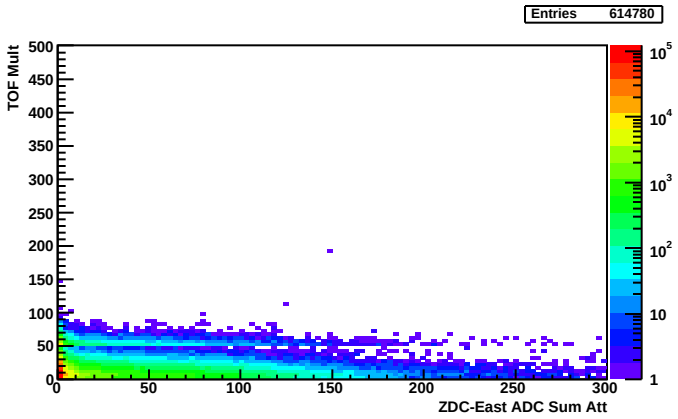

BBQ-VP004 (HI threshold)
Entries 9836480
TAC
QT Input Channel

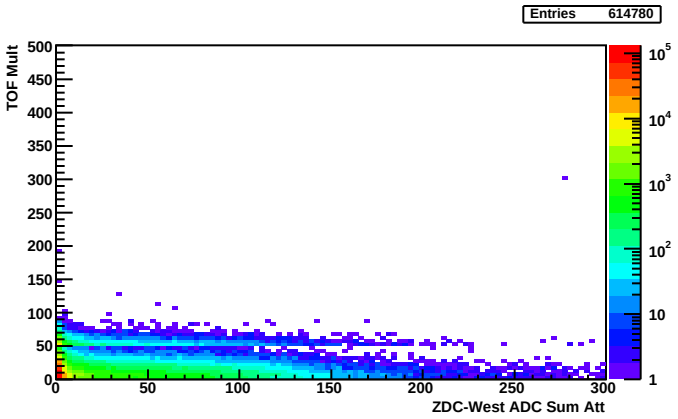

Entries 614780
TOF Mult
BBC-L-East ADC Sum

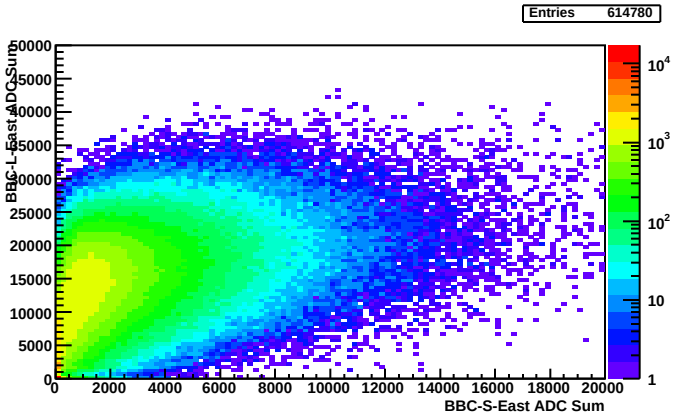

Entries 614780
TOF Mult
BBC-L-West ADC Sum

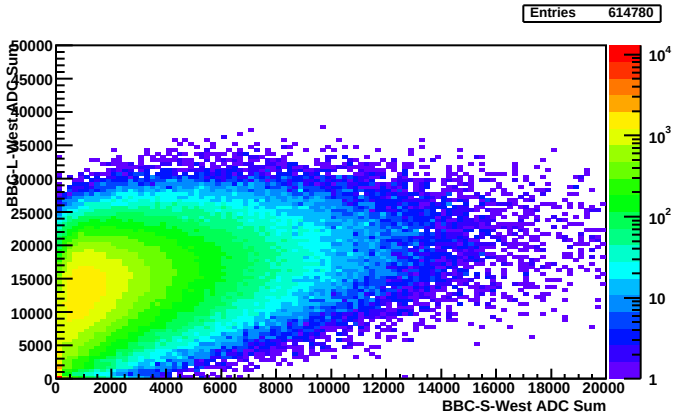

Entries 614780
TOF Mult
BBC-S-East ADC Sum

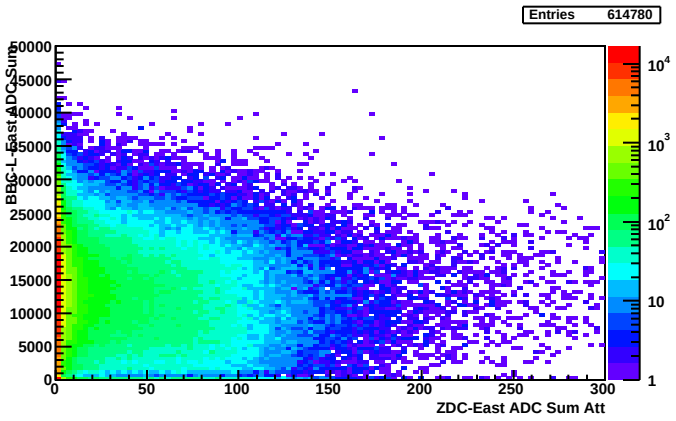

Entries 614780
TOF Mult
BBC-S-West ADC Sum

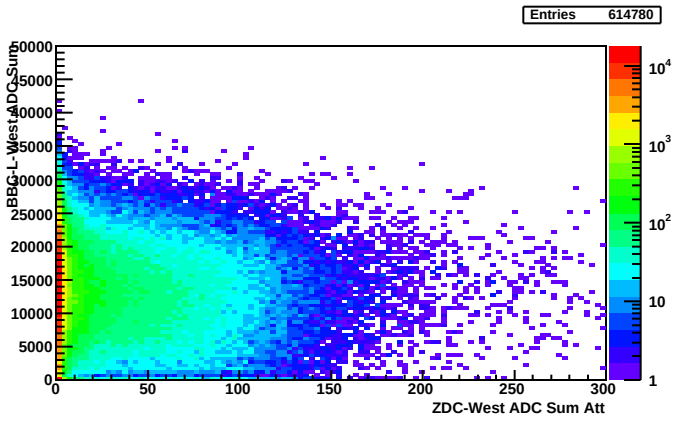

Entries 614780
TOF Mult
ZDC-East ADC Sum Att

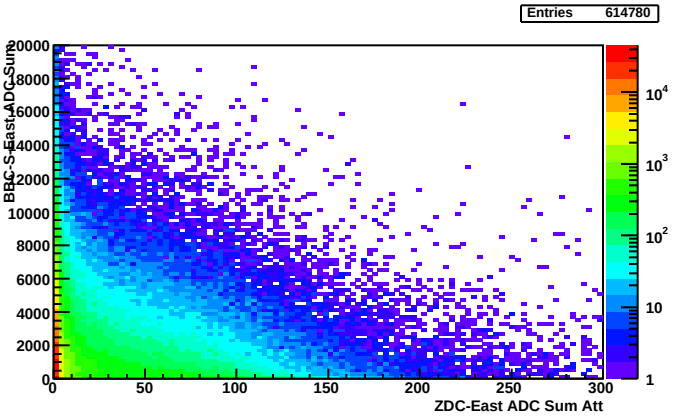

Entries 614780
TOF Mult
ZDC-West ADC Sum Att

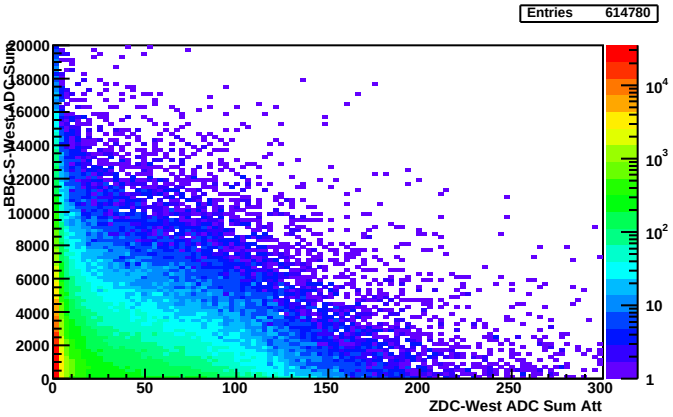

Entries 614780
BBC-L-East ADC Sum
BBC-S-East ADC Sum

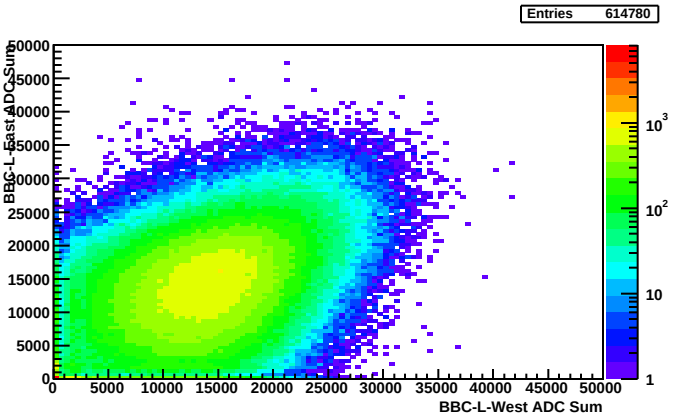

Entries 614780
BBC-L-West ADC Sum
BBC-S-West ADC Sum

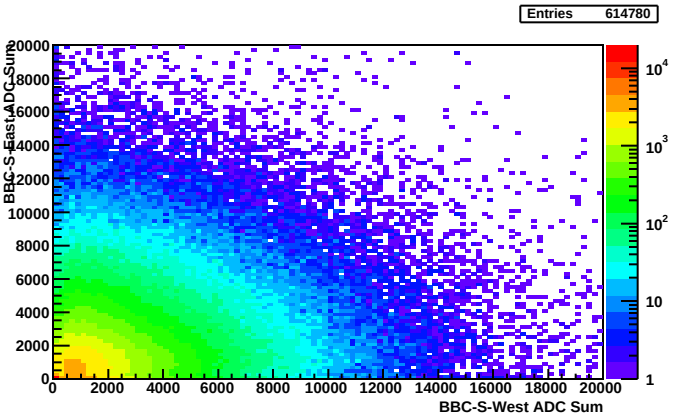

Entries 614780
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

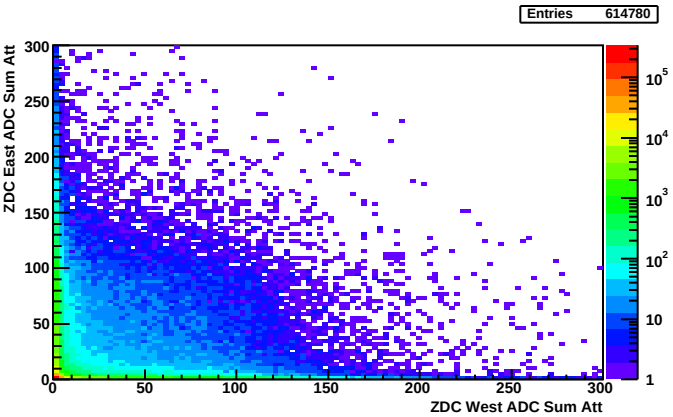

Entries 614780
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

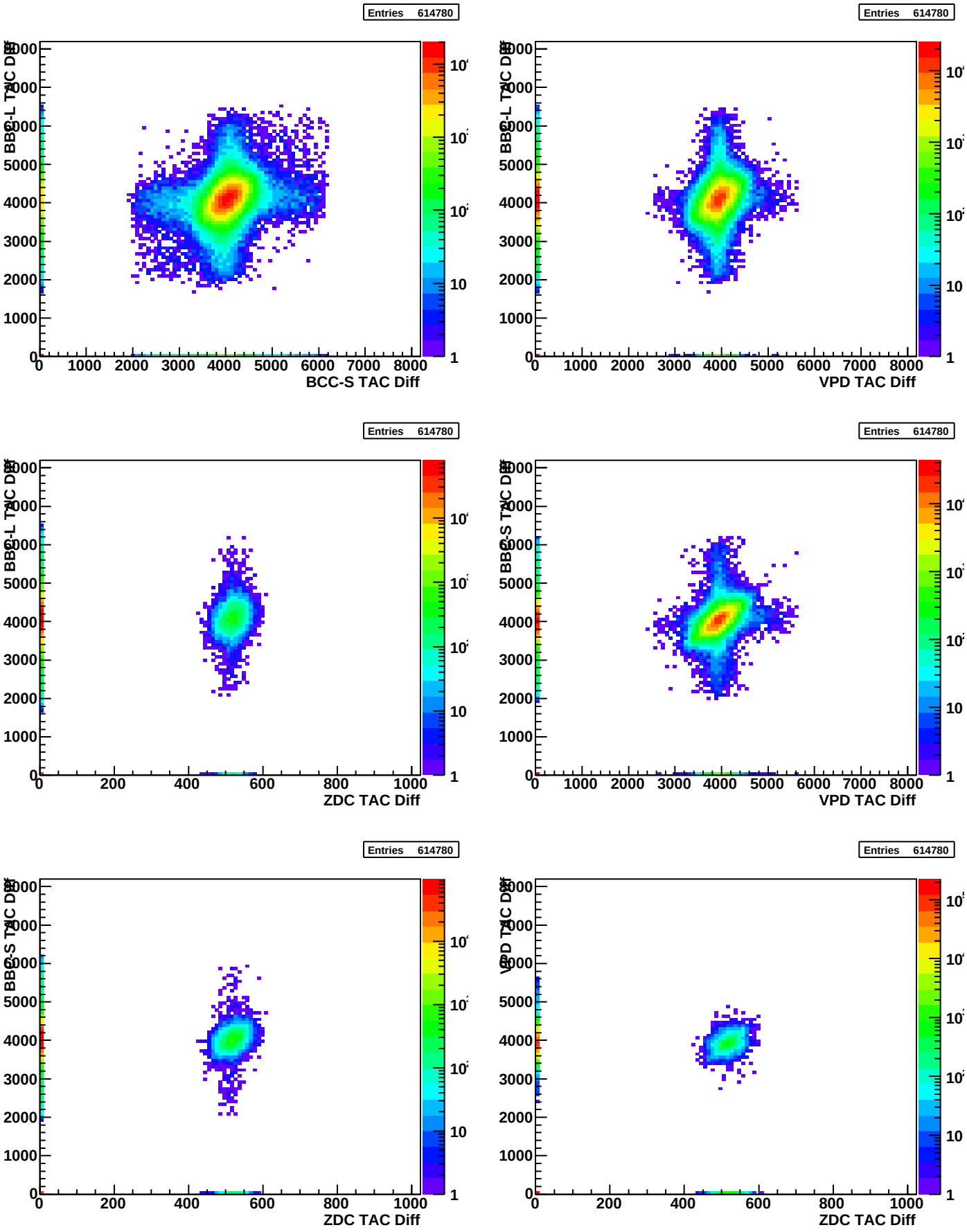

Entries 614780
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

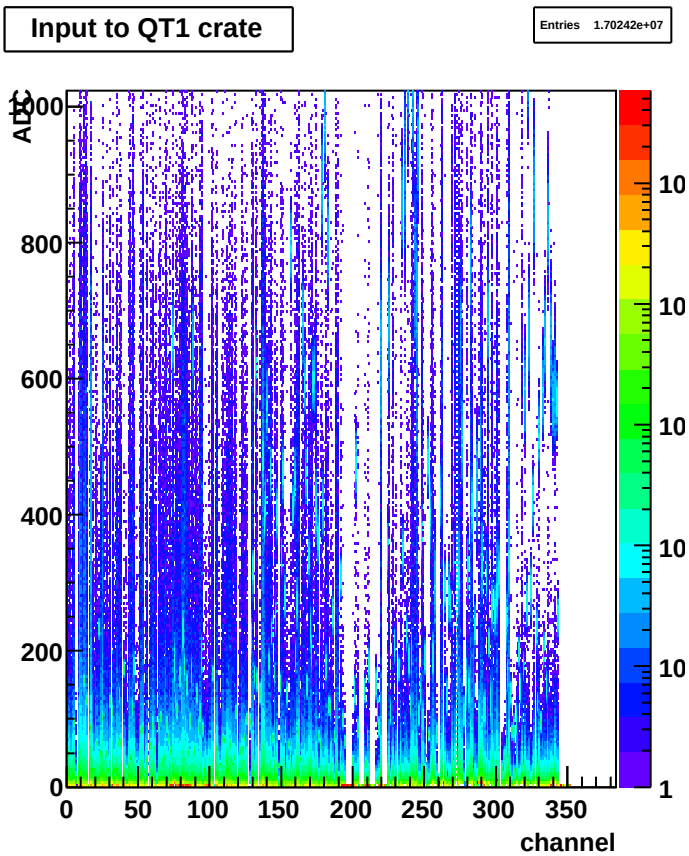

Entries 614780
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

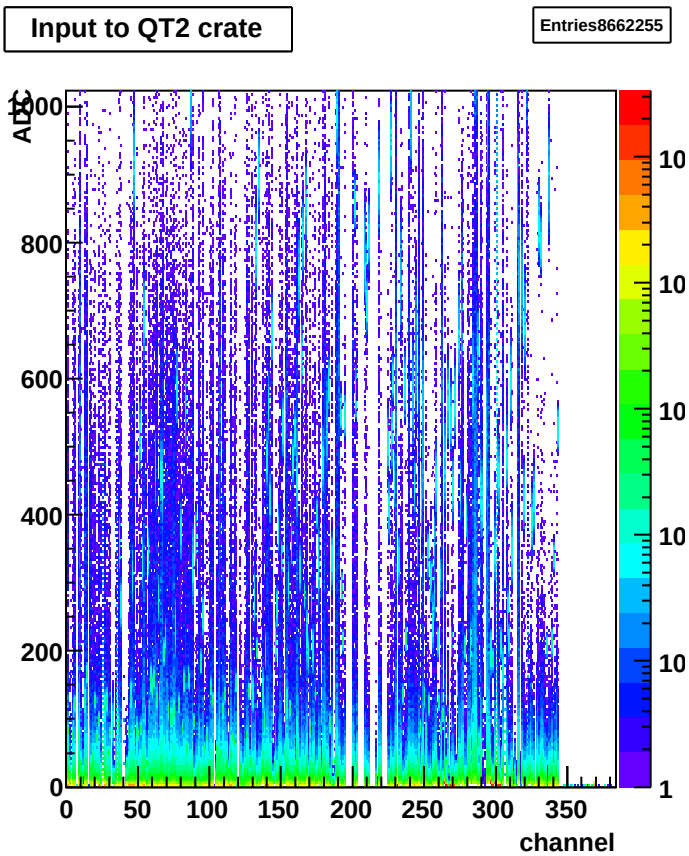

Entries 614780
BBC-L-East ADC Sum
BBC-L-West ADC Sum

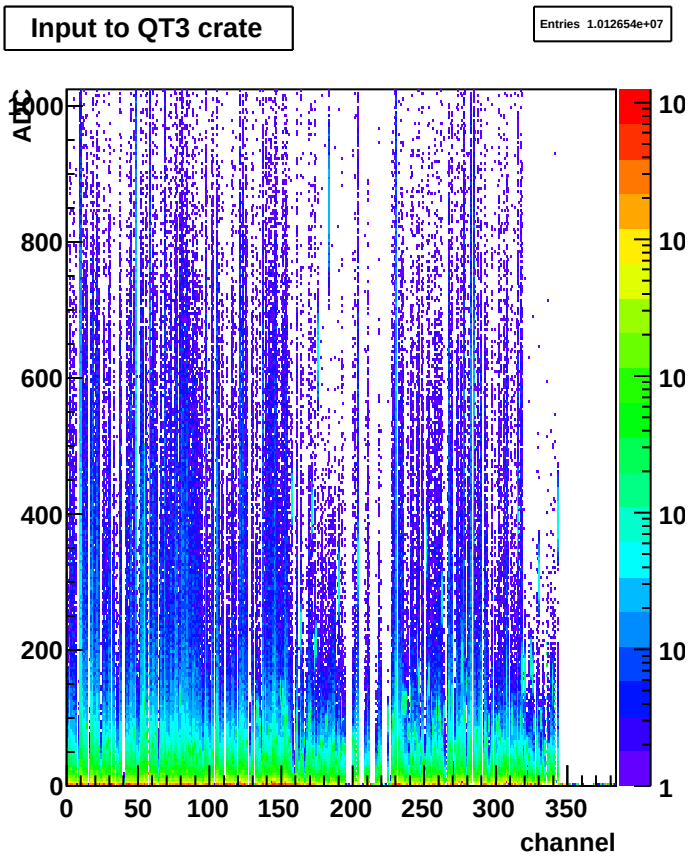

Entries 614780
BBC-S-East ADC Sum
BBC-S-West ADC Sum

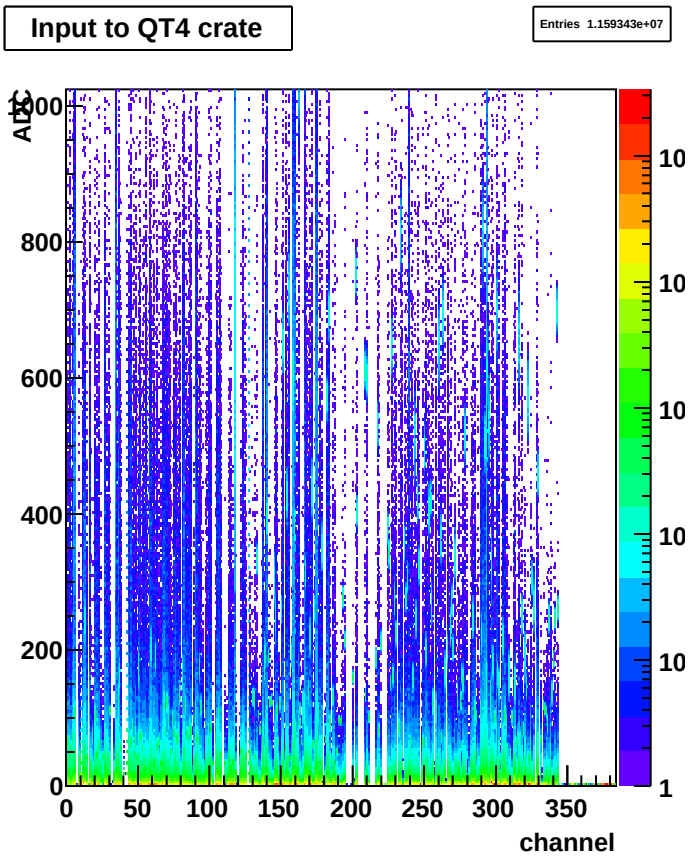

Entries 614780
ZDC East ADC Sum Att
ZDC West ADC Sum Att

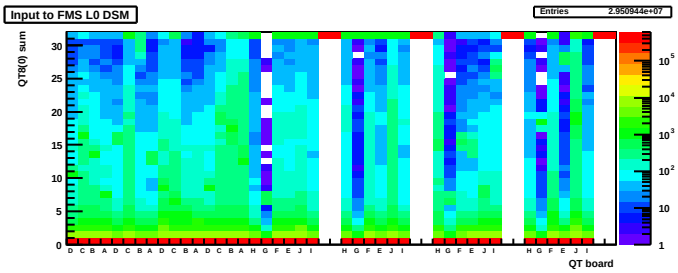

Entries 614780
BBC-L TAC Diff
BCC-S TAC Diff
Entries 614780
BBC-L TAC Diff
VPD TAC Diff
Entries 614780
BBC-L TAC Diff
ZDC TAC Diff
Entries 614780
BBC-S TAC Diff
VPD TAC Diff
Entries 614780
BBC-S TAC Diff
ZDC TAC Diff
Entries 614780
VPD TAC Diff
ZDC TAC Diff

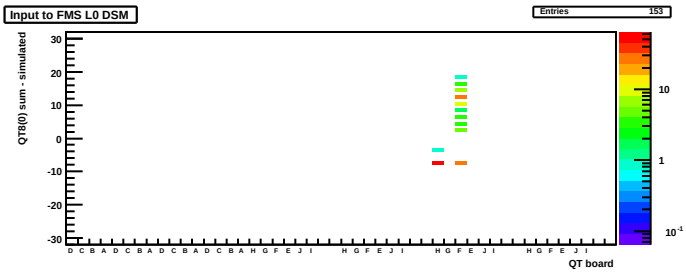

Input to QT1 crate
Entries 1.70242e+07
ADC
channel

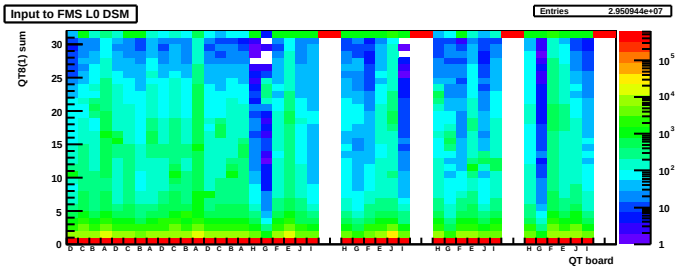

Input to QT2 crate
Entries8662255
ADC
channel

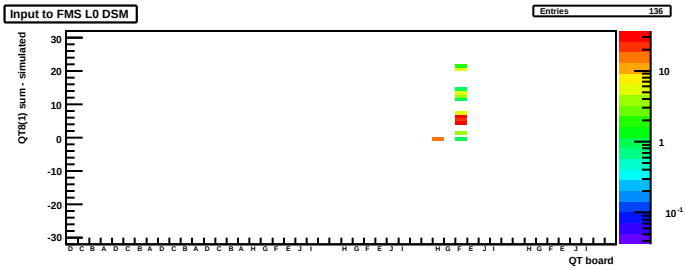

Input to QT3 crate
Entries 1.012654e+07
ADC
channel

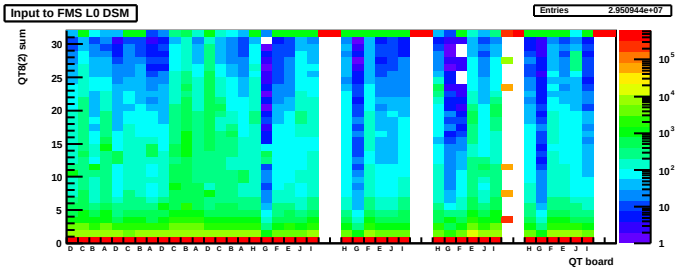

Input to QT4 crate
Entries 1.159343e+07
ADC
channel

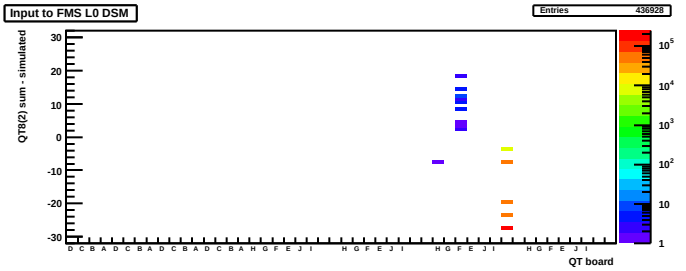

Input to FMS L0 DSM
Entries 2.950944e+07
QT8(0) sum
QT board

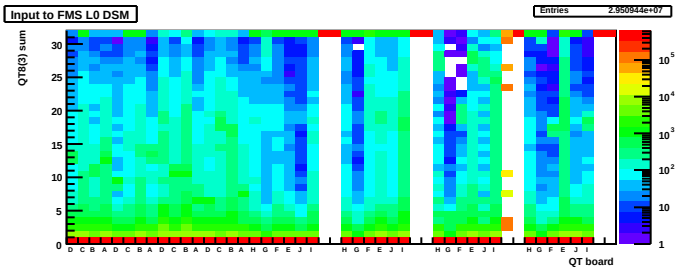

Input to FMS L0 DSM
Entries 153
QT8(0) sum - simulated
QT board

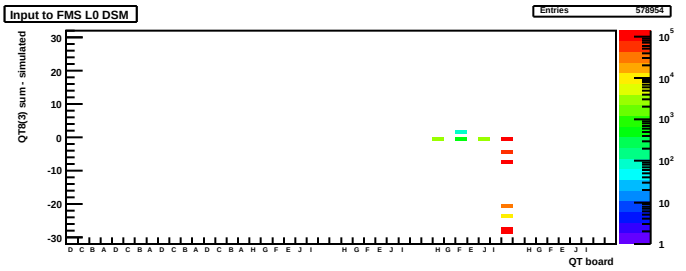

Input to FMS L0 DSM
Entries 2.950944e+07
QT8(1) sum
QT board

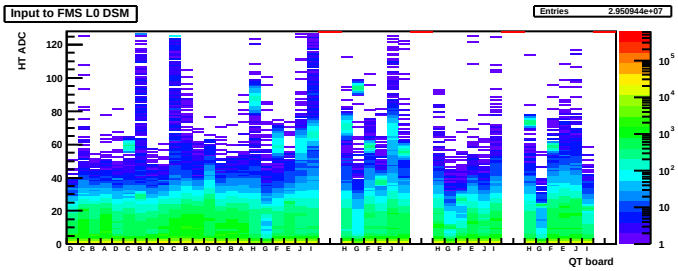

Input to FMS L0 DSM
Entries 136
QT8(1) sum - simulated
QT board

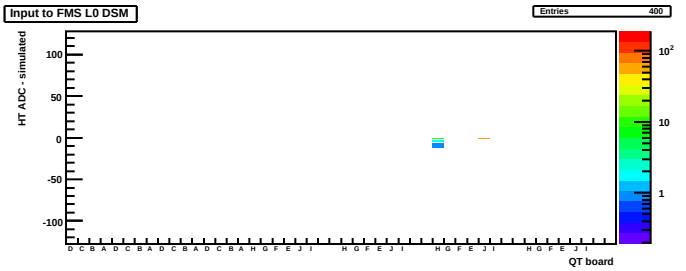

Input to FMS L0 DSM
Entries 2.950944e+07
QT8(2) sum
QT board

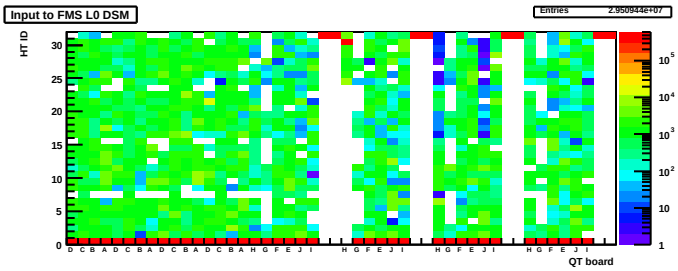

Input to FMS L0 DSM
Entries 436928
QT8(2) sum - simulated
QT board

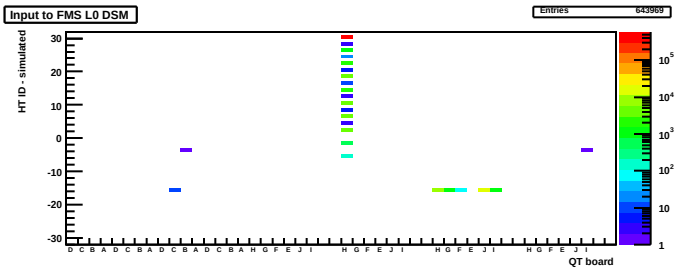

Input to FMS L0 DSM
Entries 2.950944e+07
QT8(3) sum
QT board

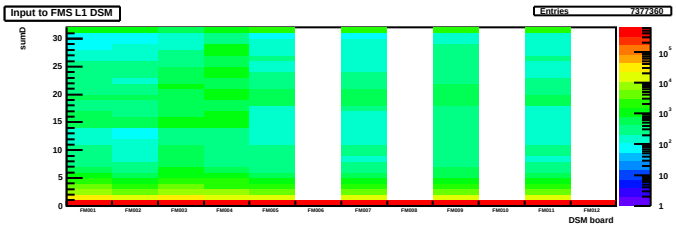

Input to FMS L0 DSM
Entries 578954
QT8(3) sum - simulated
QT board

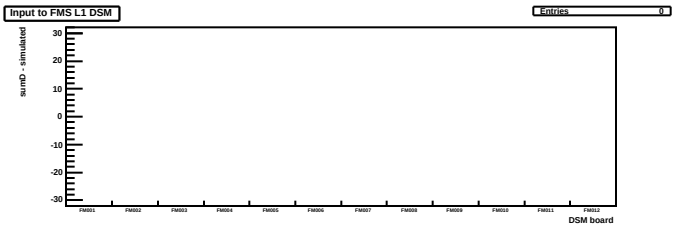

Input to FMS L0 DSM
Entries 2.950944e+07
HT ADC
QT board

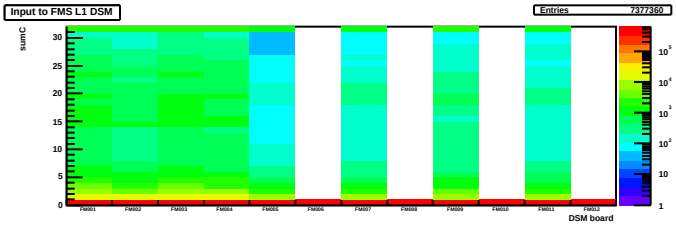

Input to FMS L0 DSM
Entries 400
HT ADC - simulated
QT board

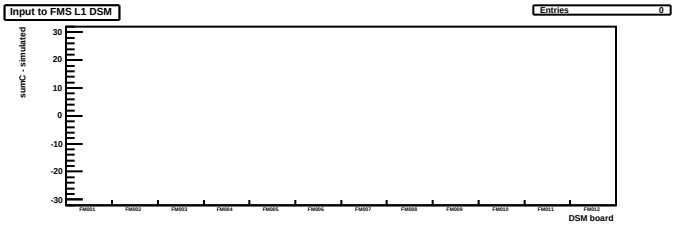

Input to FMS L0 DSM
Entries 2.950944e+07
HT ID
QT board

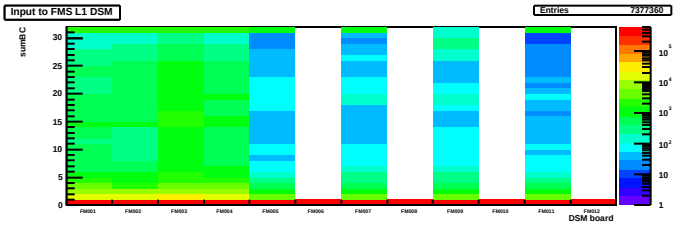

Input to FMS L0 DSM
Entries 643969
HT ID - simulated
QT board

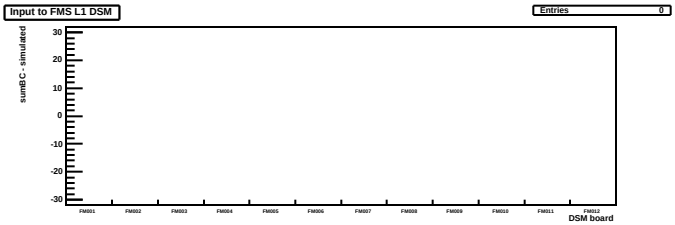

Input to FMS L1 DSM
Entries 7377360
sumD
DSM board

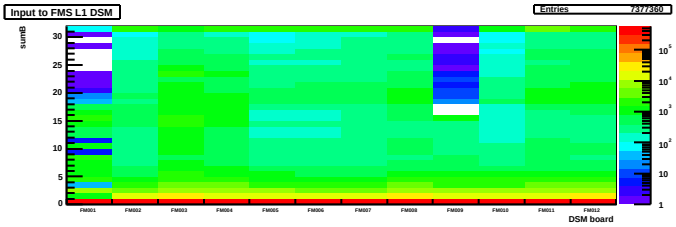

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

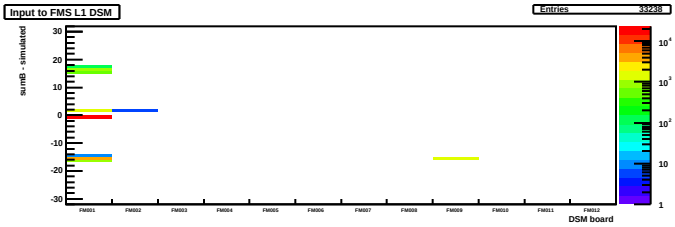

Input to FMS L1 DSM
Entries 7377360
sumC
DSM board

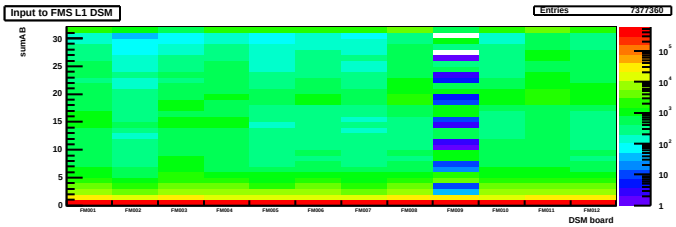

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

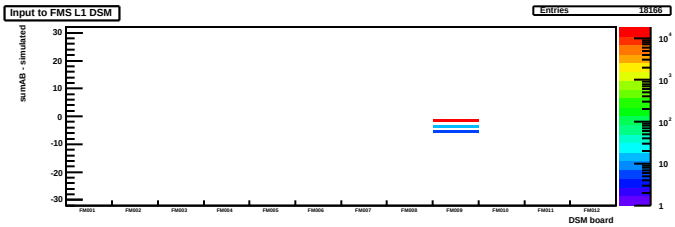

Input to FMS L1 DSM
Entries 7377360
sumBC
DSM board

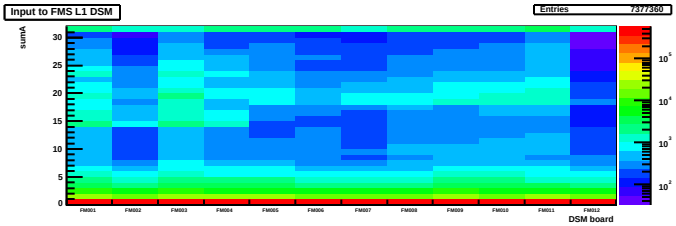

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

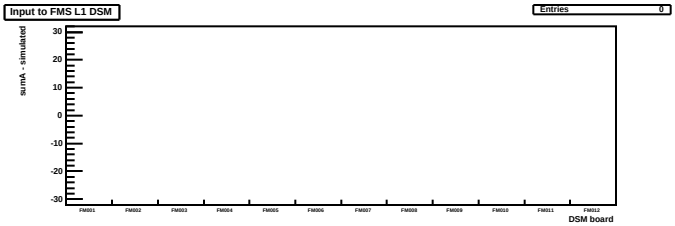

Input to FMS L1 DSM
Entries 7377360
sumB
DSM board

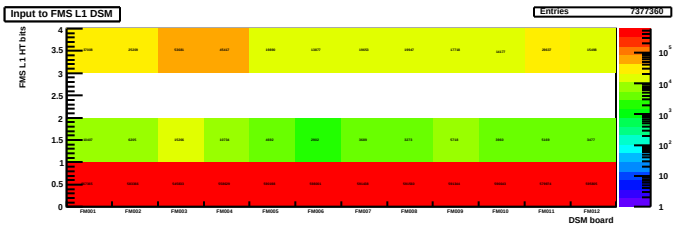

Input to FMS L1 DSM
Entries 33238
sumB - simulated
DSM board

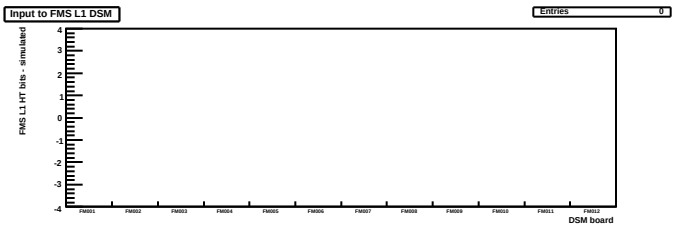

Input to FMS L1 DSM
Entries 7377360
sumAB
DSM board

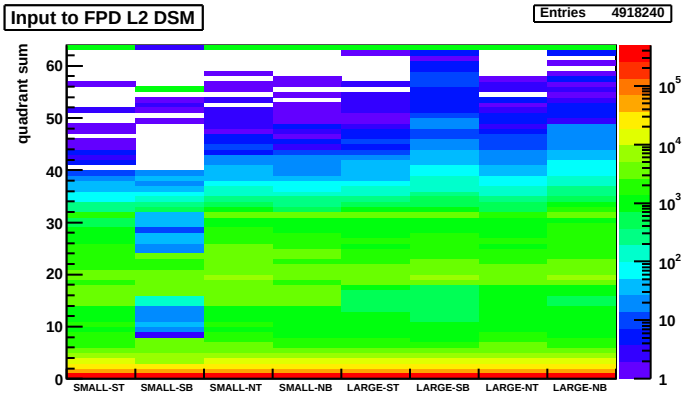

Input to FMS L1 DSM
Entries 18166
sumAB - simulated
DSM board

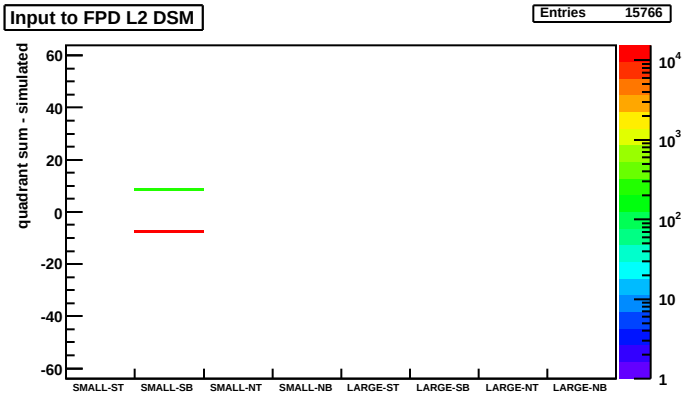

Input to FMS L1 DSM
Entries 7377360
sumA
DSM board

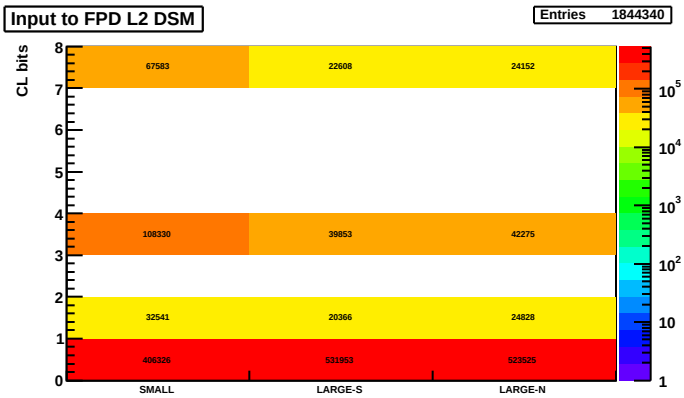

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

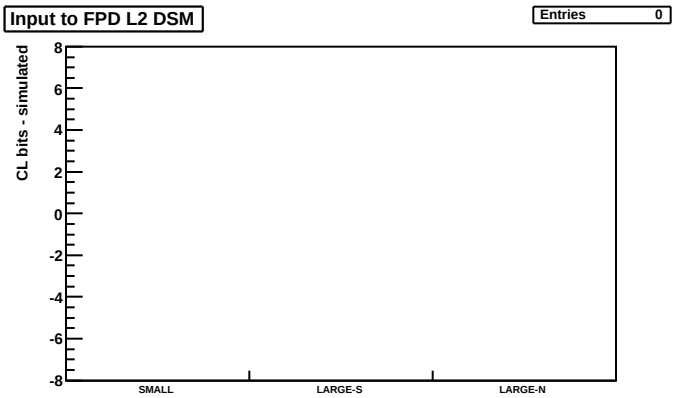

Input to FMS L1 DSM
Entries 7377360
FMS L1 HT bits
DSM board

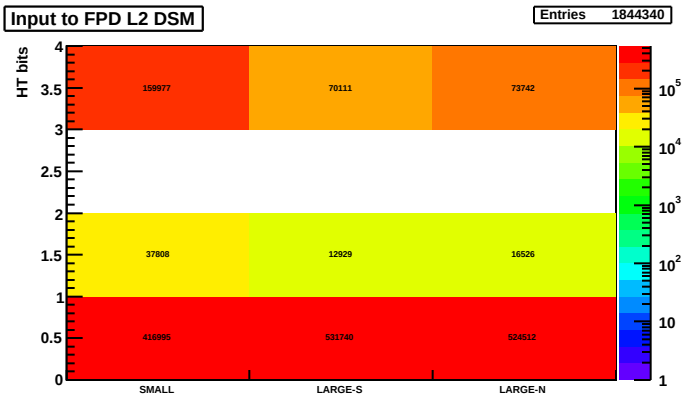

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

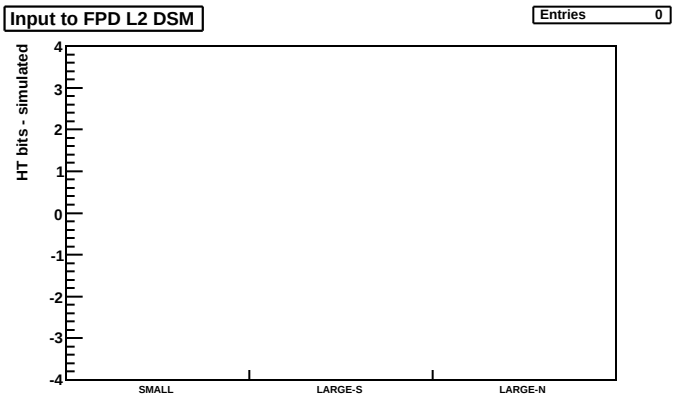

Input to FPD L2 DSM
Entries 4918240
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

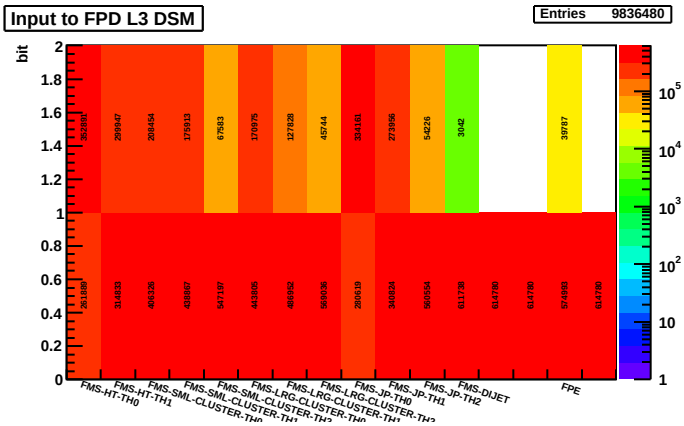

Input to FPD L2 DSM
Entries 15766
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

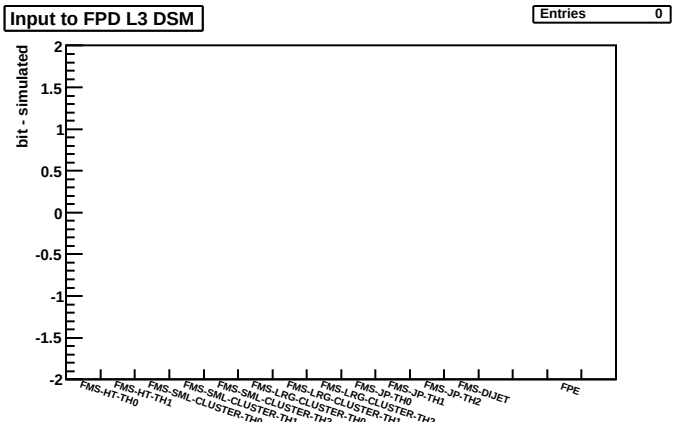

Input to FPD L2 DSM
Entries 1844340
CL bits
67583
22608
24152
108330
39853
42275
32541
20366
24828
406326
531953
523525
SMALL LARGE-S LARGE-N

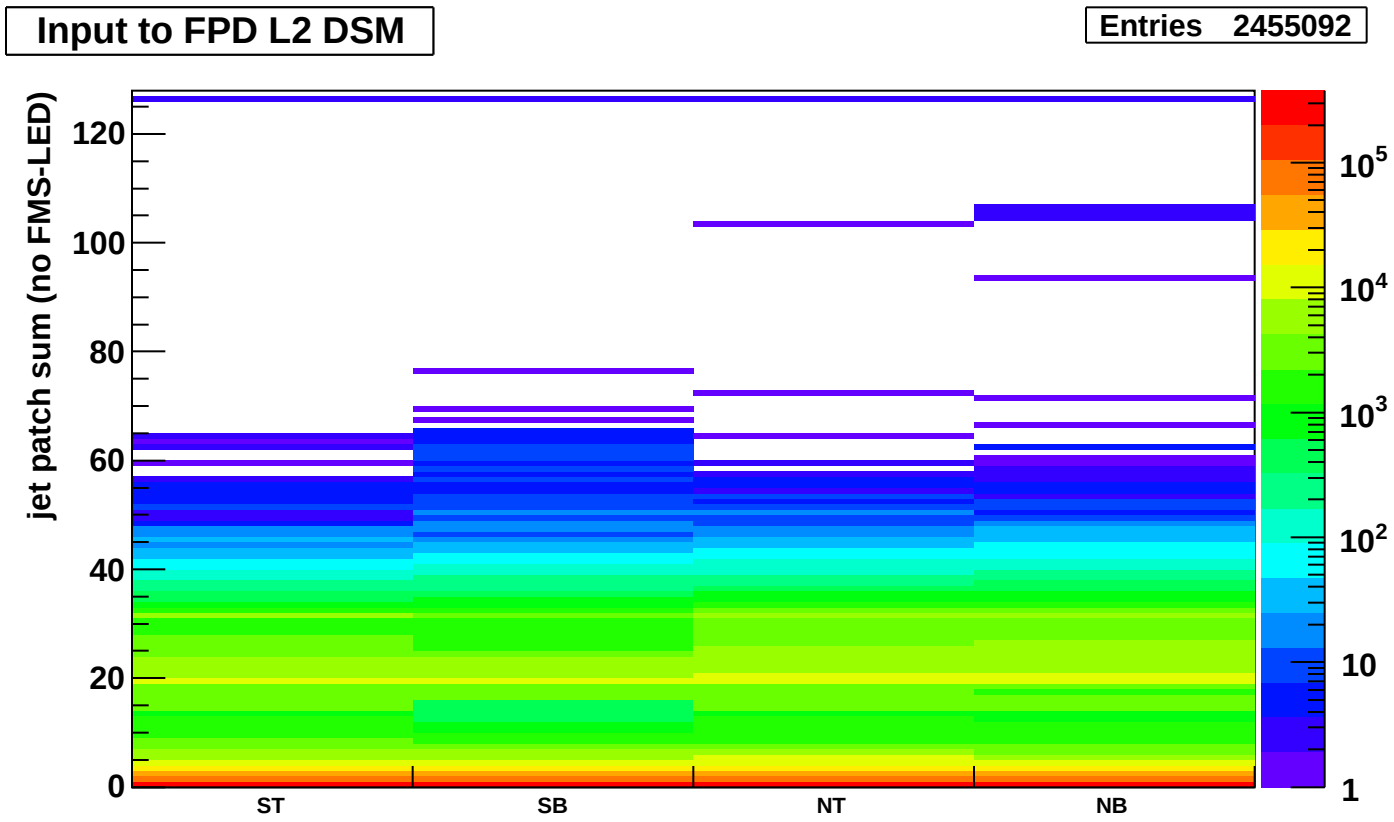

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL LARGE-S LARGE-N

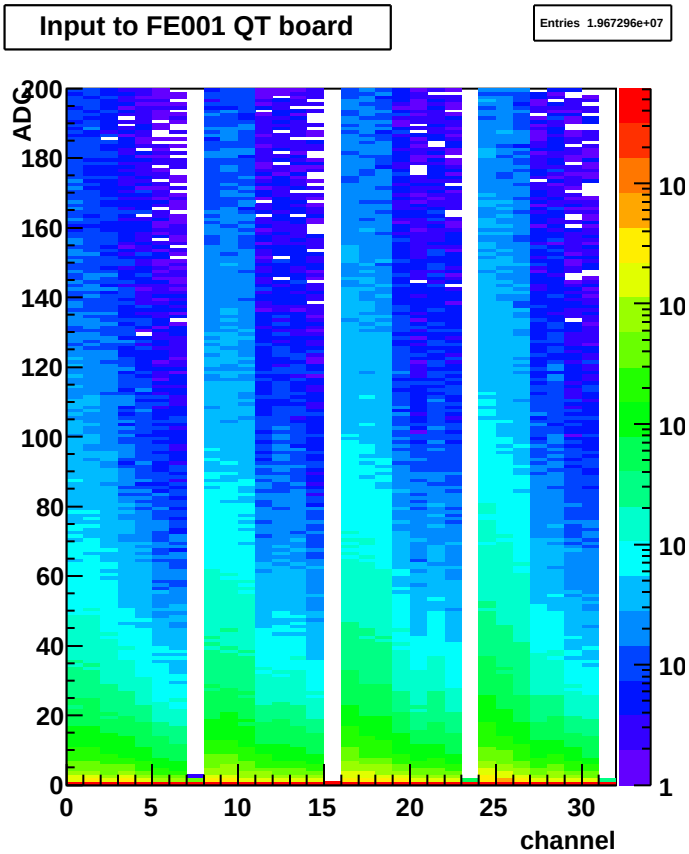

Input to FPD L2 DSM
Entries 1844340
HT bits
159977
70111
73742
37808
12929
16526
416995
531740
524512
SMALL LARGE-S LARGE-N

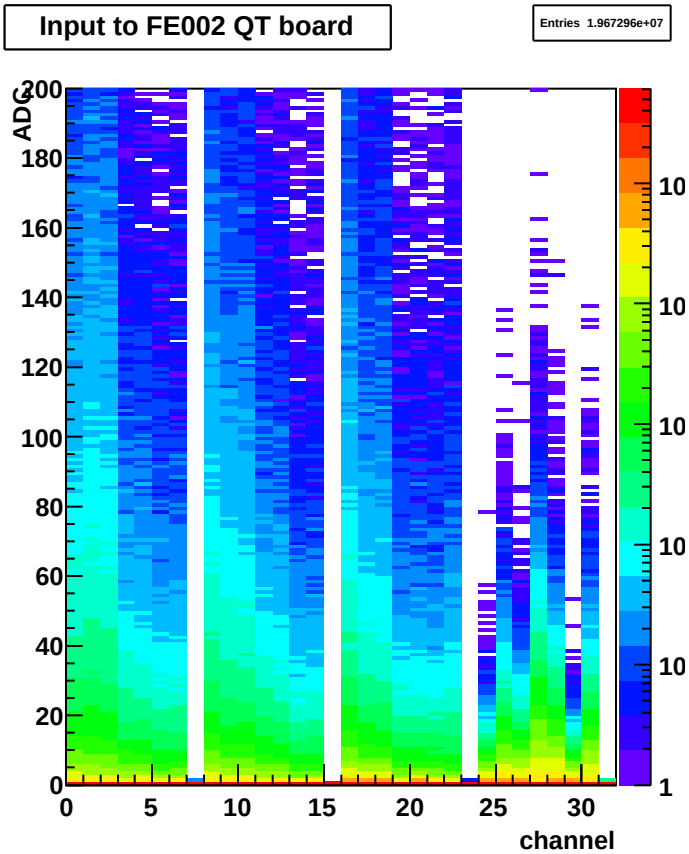

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

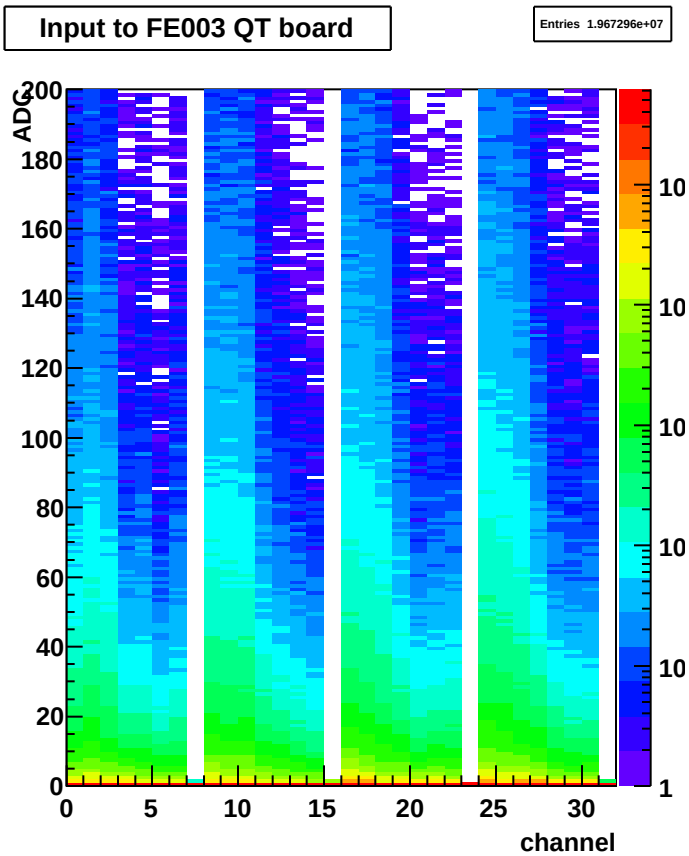

Input to FPD L3 DSM
Entries 9836480
bit
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

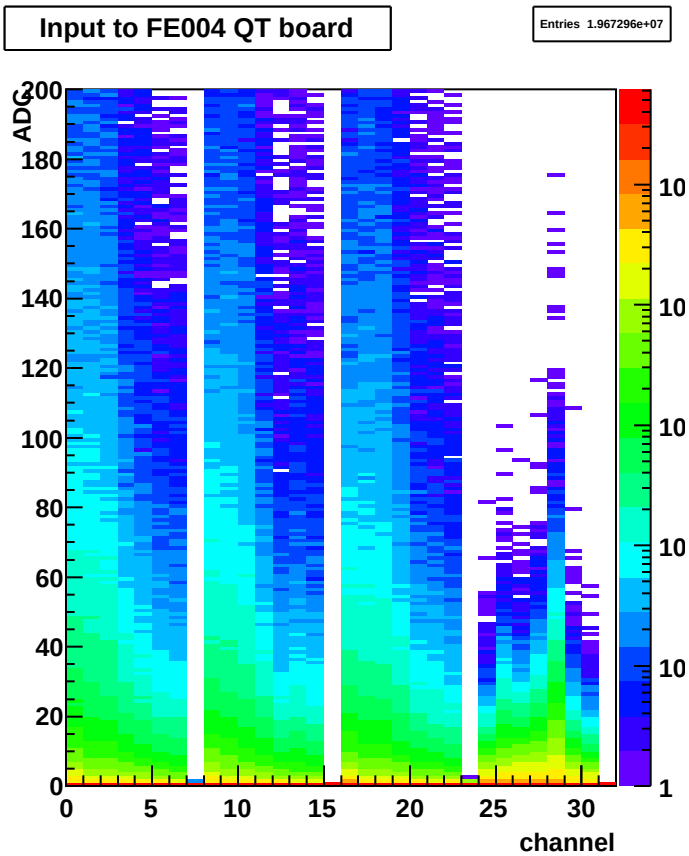

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

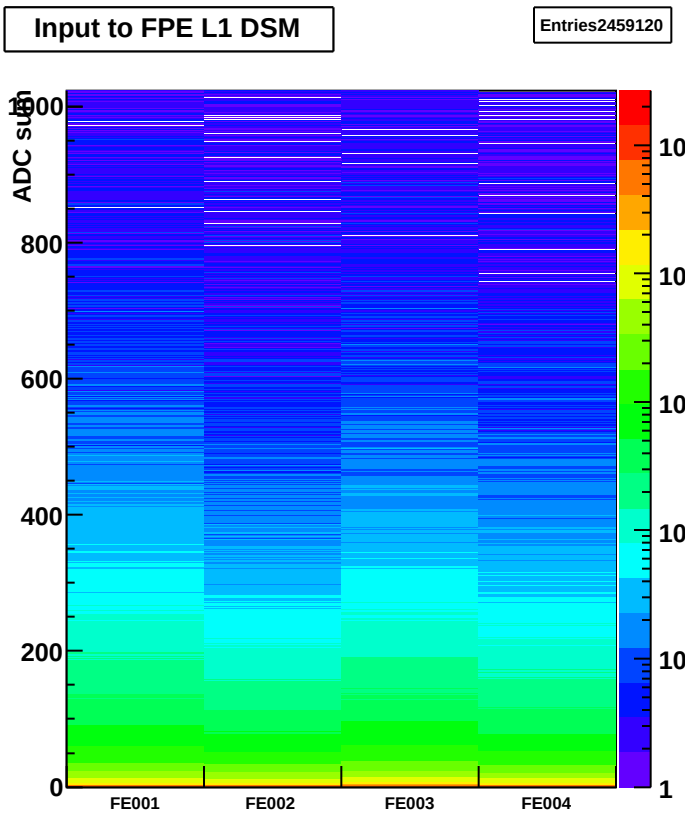

Input to FPD L2 DSM
Entries 2455092
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

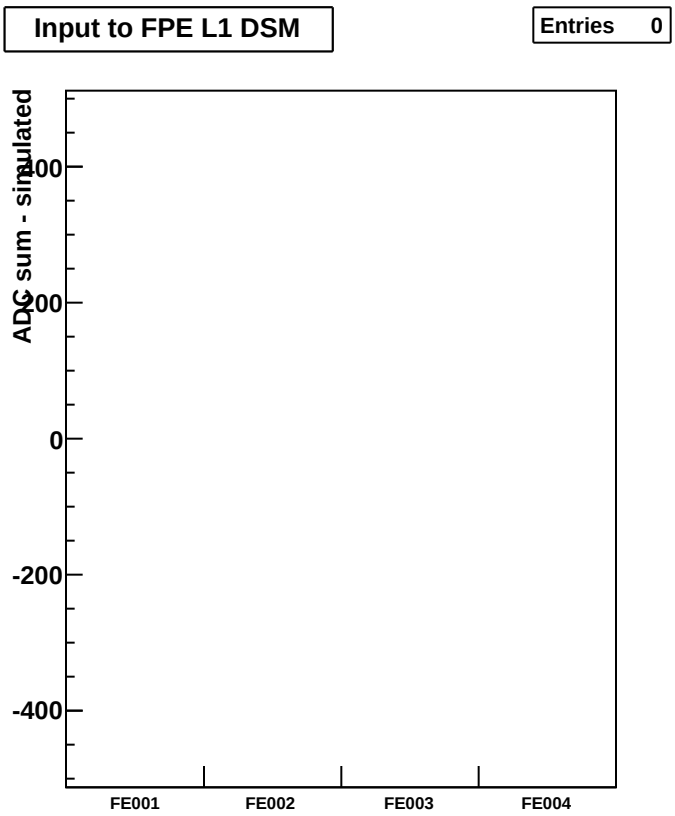

Input to FE001 QT board
Entries 1.967296e+07
ADC
channel

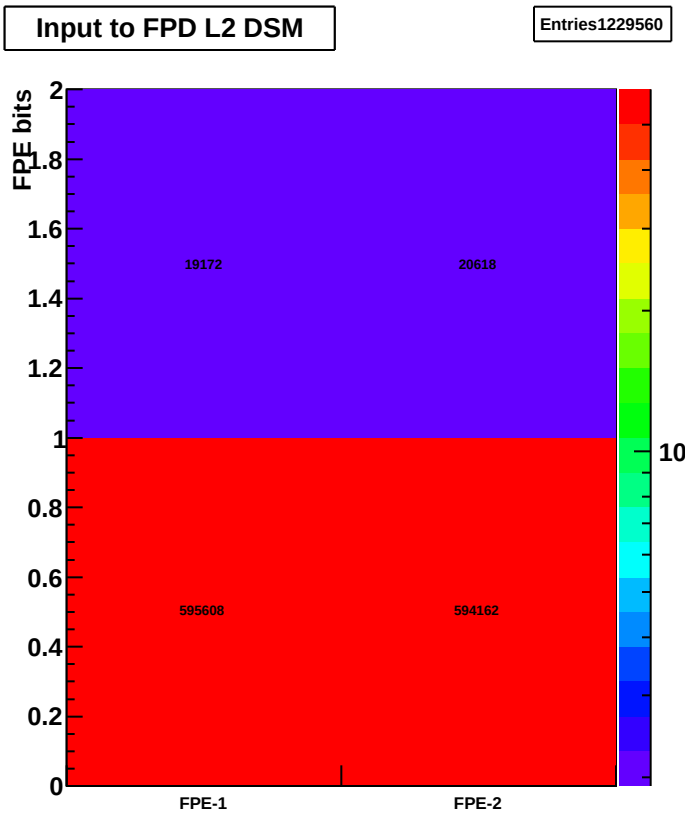

Input to FE002 QT board
Entries 1.967296e+07
ADC
channel

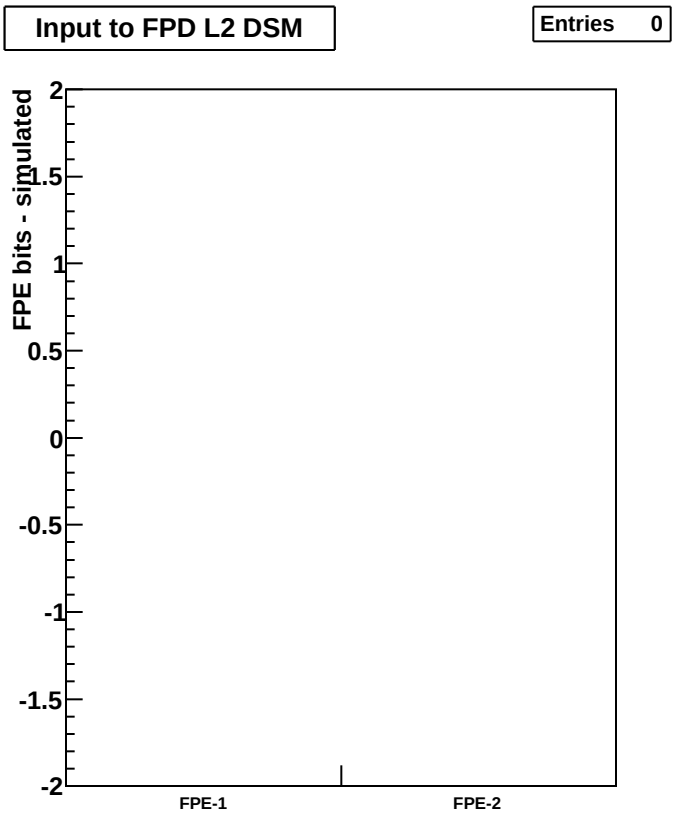

Input to FE003 QT board
Entries 1.967296e+07
ADC
channel


Input to FE004 QT board
Entries 1.967296e+07
ADC
channel


Input to FPE L1 DSM
Entries2459120
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1229560
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
19172
20618
595608
594162
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2