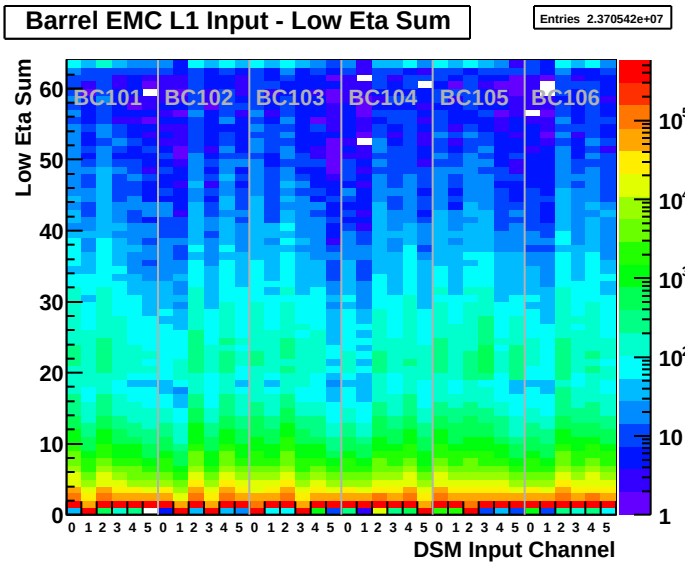
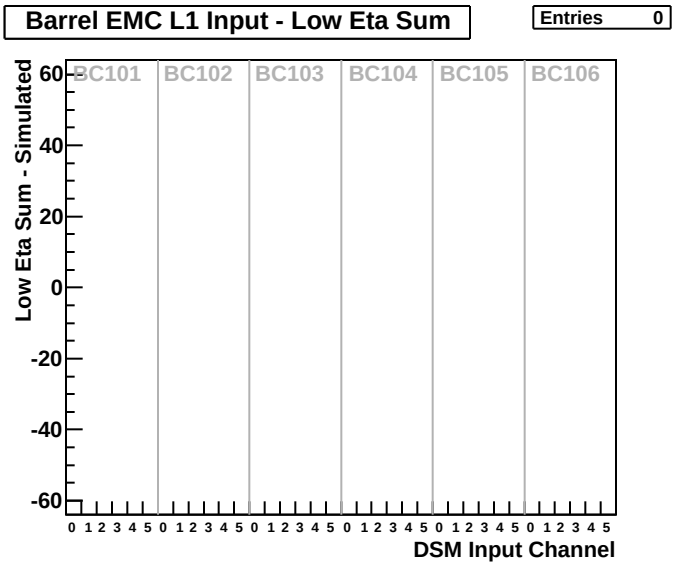
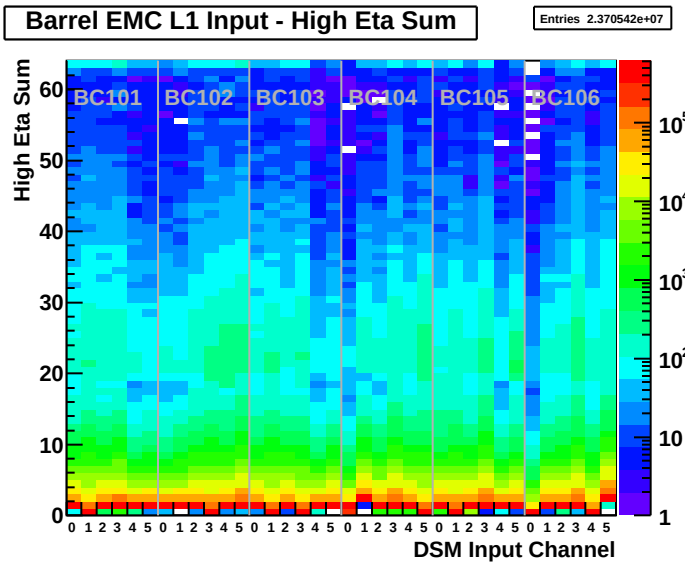
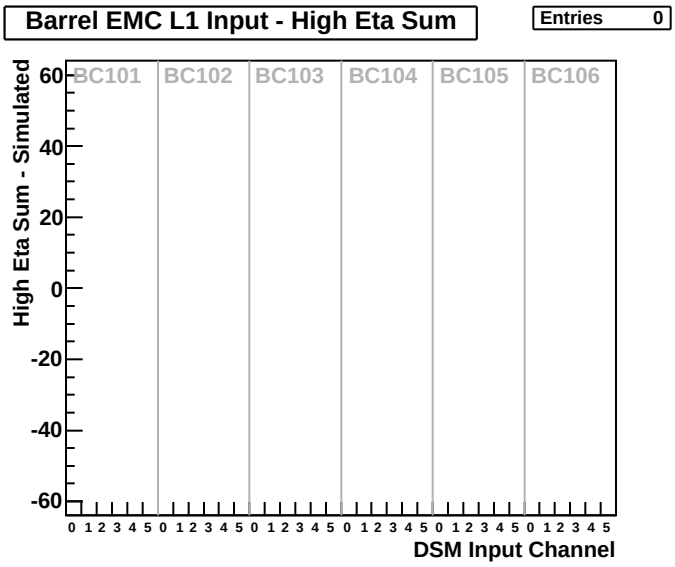
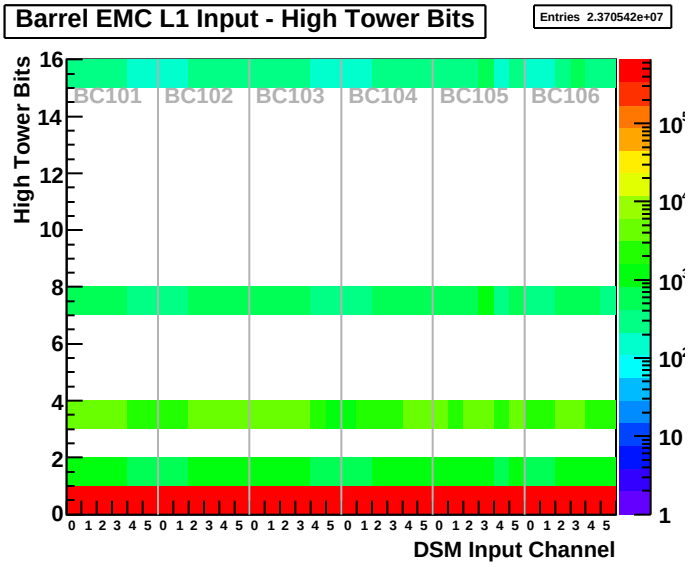
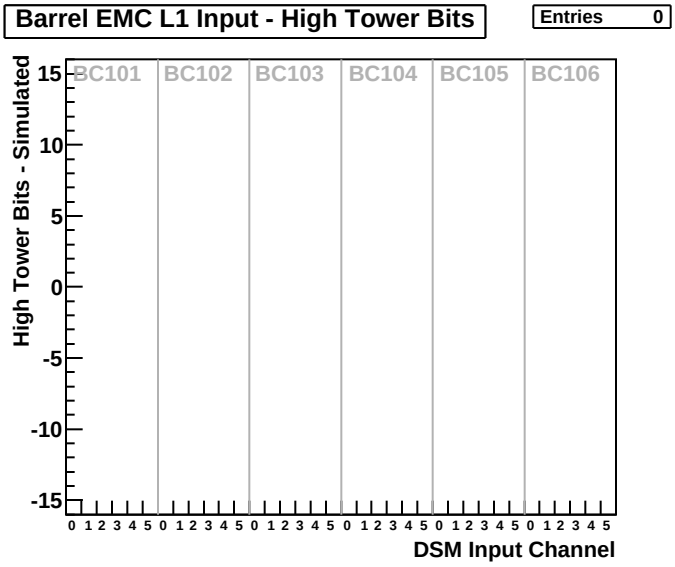

Barrel EMC L1 Input - Low Eta Sum
Entries 2.370542e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

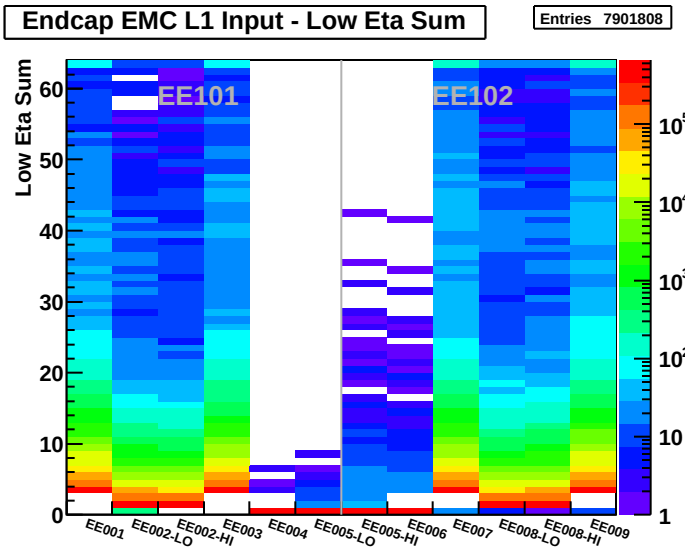

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

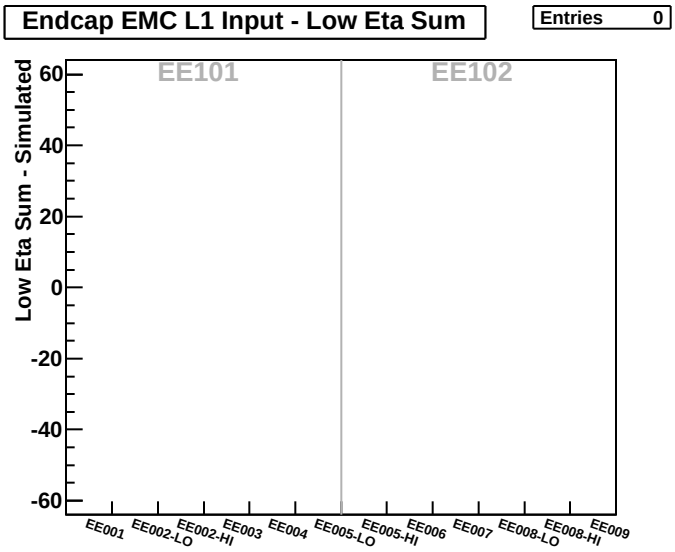

Barrel EMC L1 Input - High Eta Sum
Entries 2.370542e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

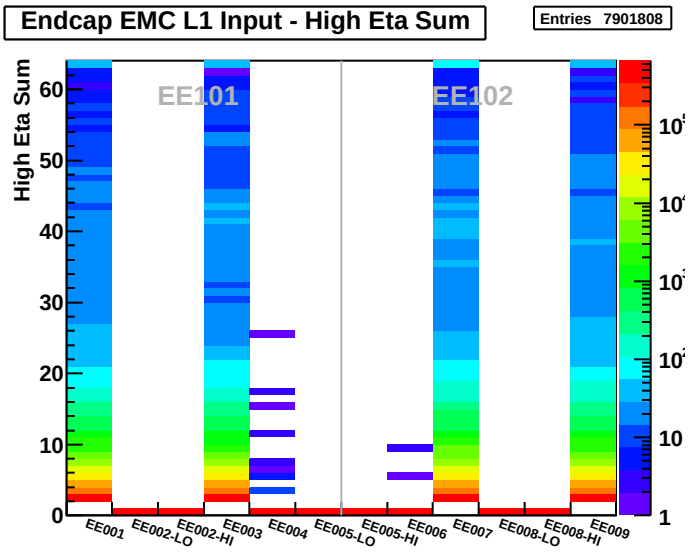

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

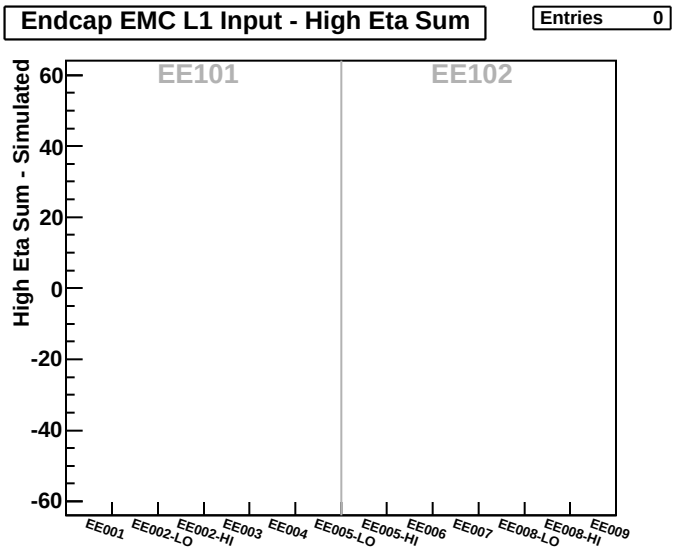

Barrel EMC L1 Input - High Tower Bits
Entries 2.370542e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

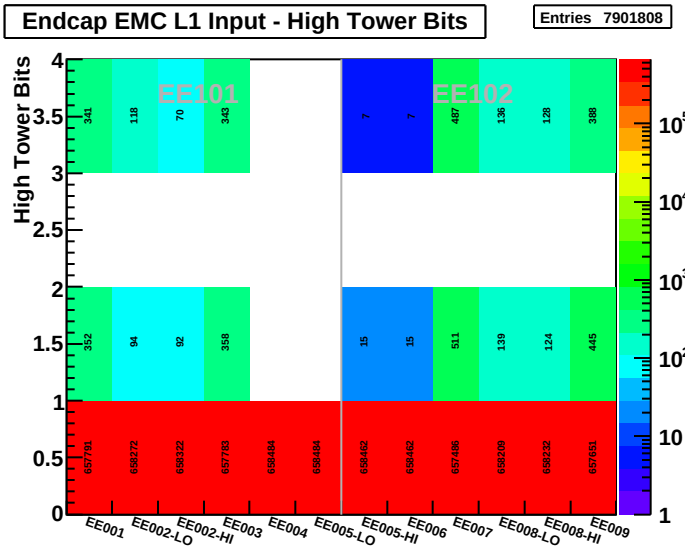

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

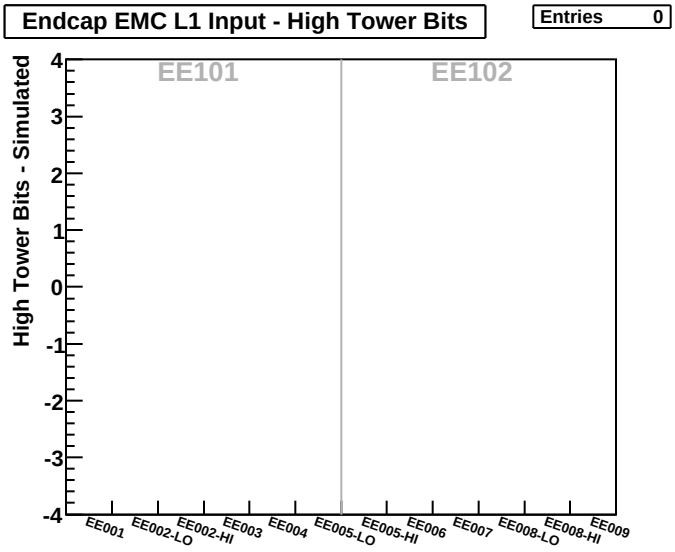

Endcap EMC L1 Input - Low Eta Sum
Entries 7901808
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

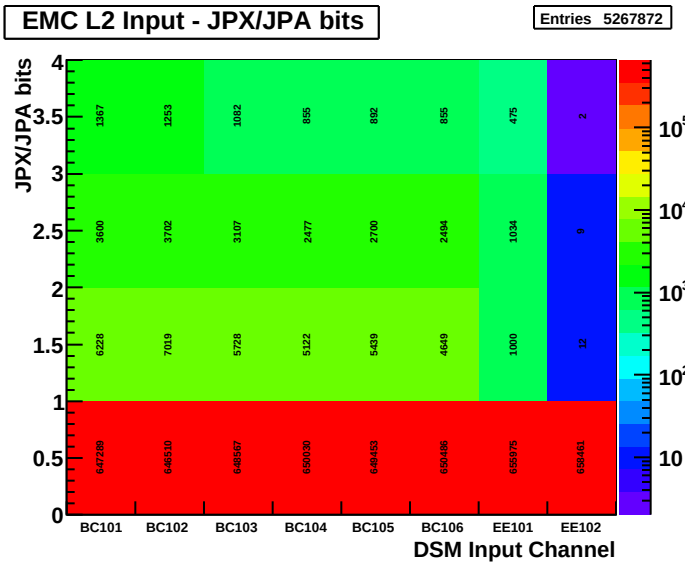

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

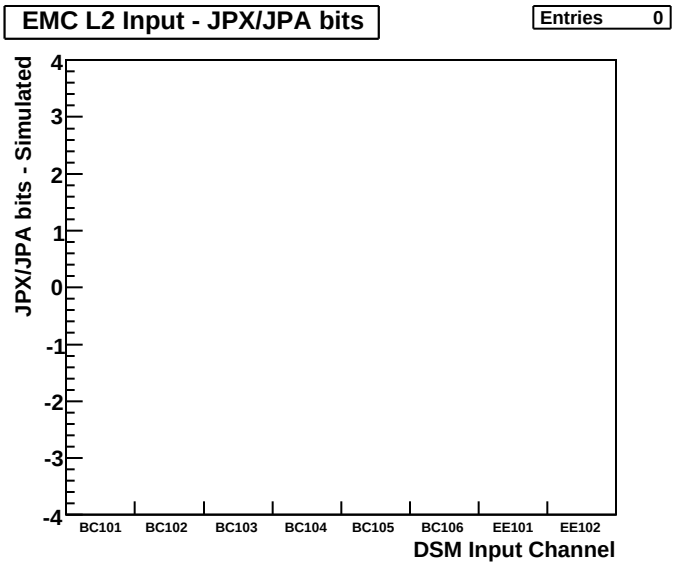

Endcap EMC L1 Input - High Eta Sum
Entries 7901808
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

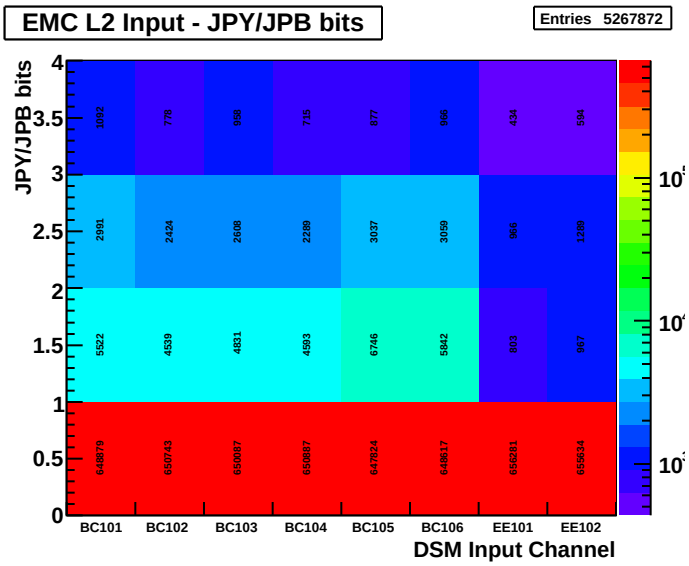

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

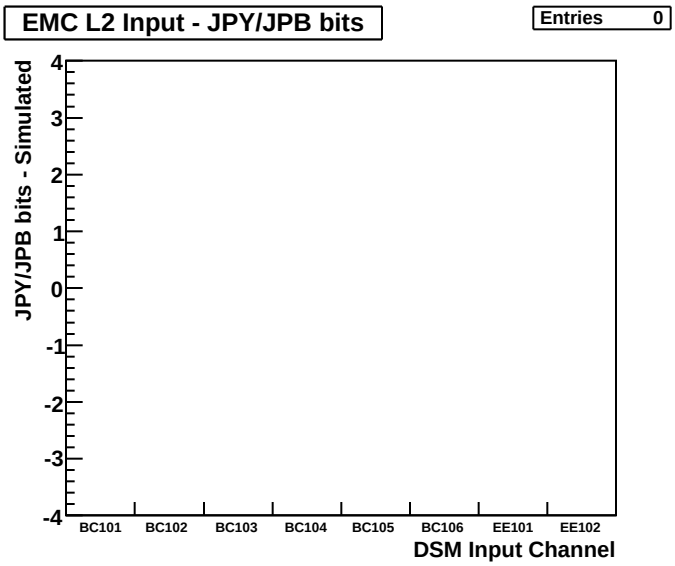

Endcap EMC L1 Input - High Tower Bits
Entries 7901808
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

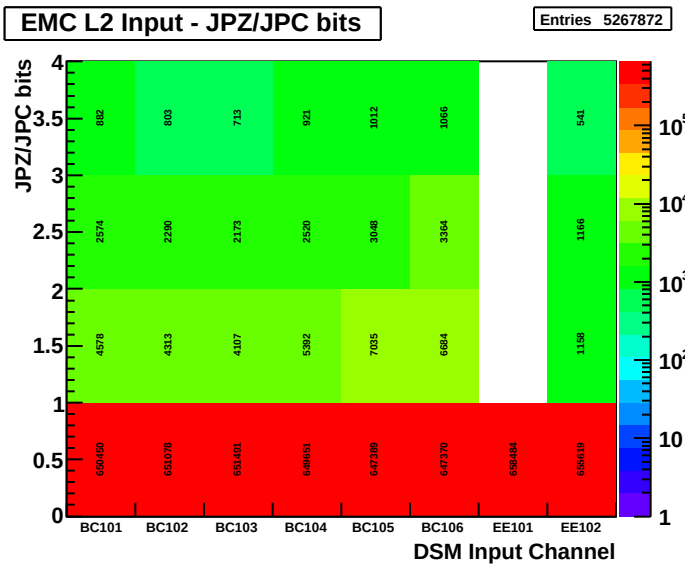

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

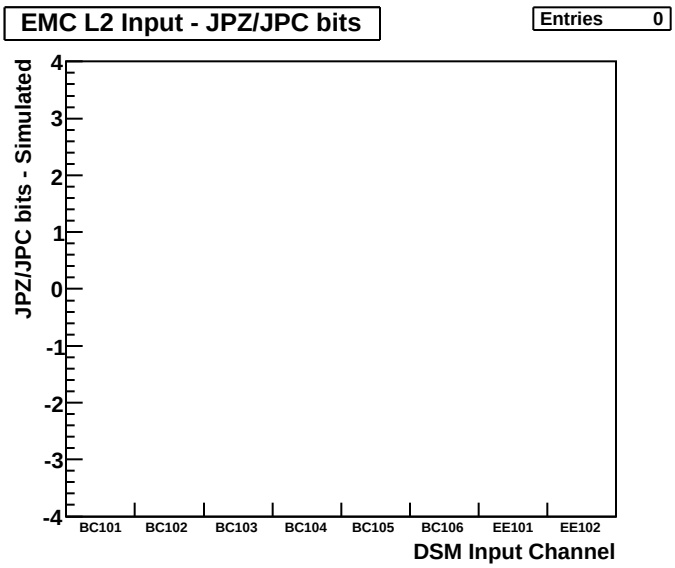

EMC L2 Input - JPX/JPA bits
Entries 5267872
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1367 1253 1062 855 892 855 475 2
3600 3702 3107 2477 2700 2494 1034 9
6228 7019 5728 5122 5439 4649 1000 12
647269 646510 648567 650030 649453 650486 655975 658461

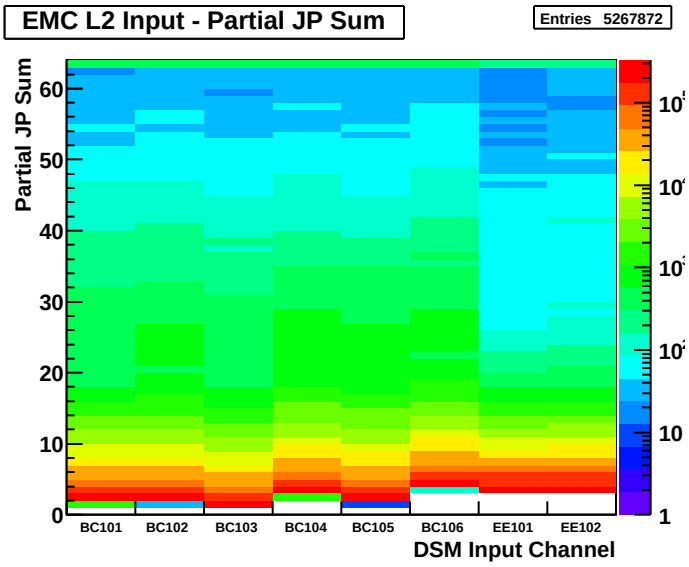

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

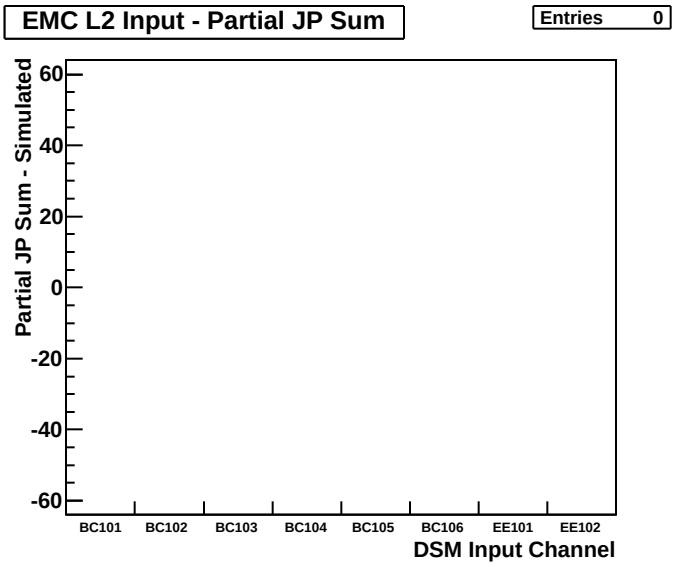

EMC L2 Input - JPY/JPB bits
Entries 5267872
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1092 778 958 715 877 966 434 594
2991 2424 2608 2289 3037 3059 966 1289
5522 4539 4831 4593 6746 5942 803 967
646879 650743 650087 650987 647824 648617 656281 655634

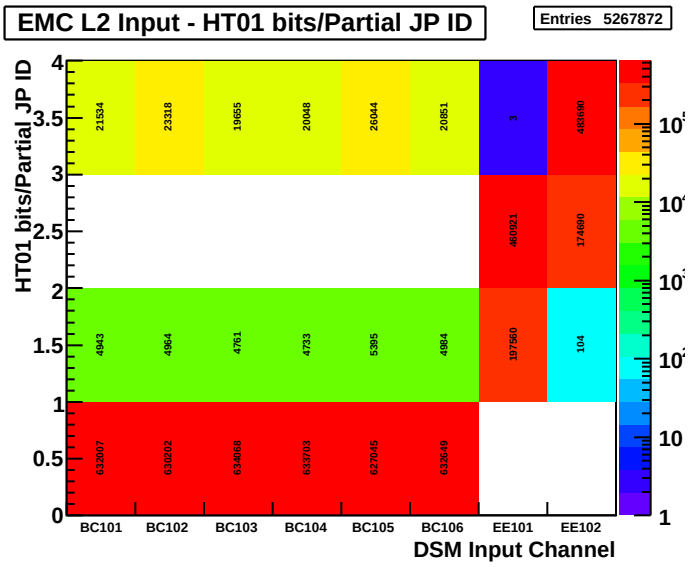

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

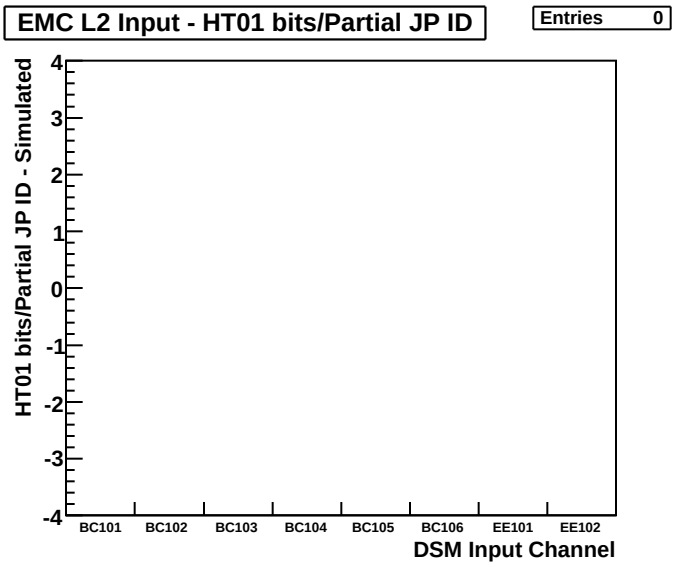

EMC L2 Input - JPZ/JPC bits
Entries 5267872
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
882 803 713 921 1012 1066 541
2574 2290 2173 2520 3048 3364 1166
4578 4313 4107 5392 7035 6684 1158
650450 651078 651491 649651 647389 647370 658484 655619

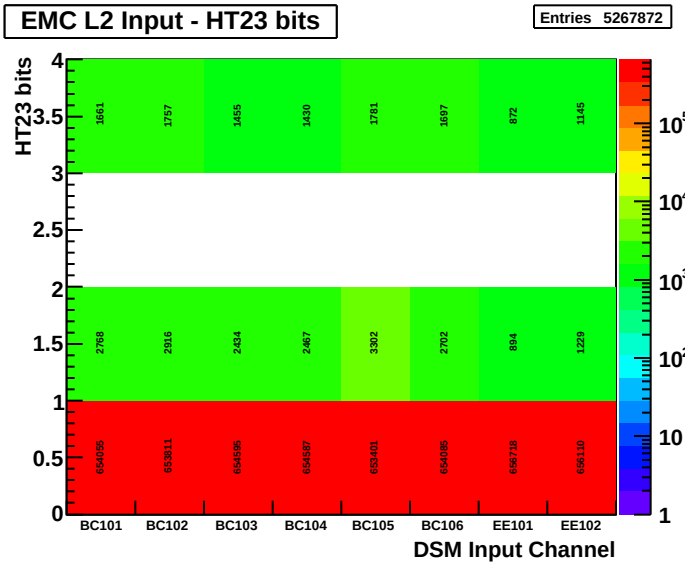

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

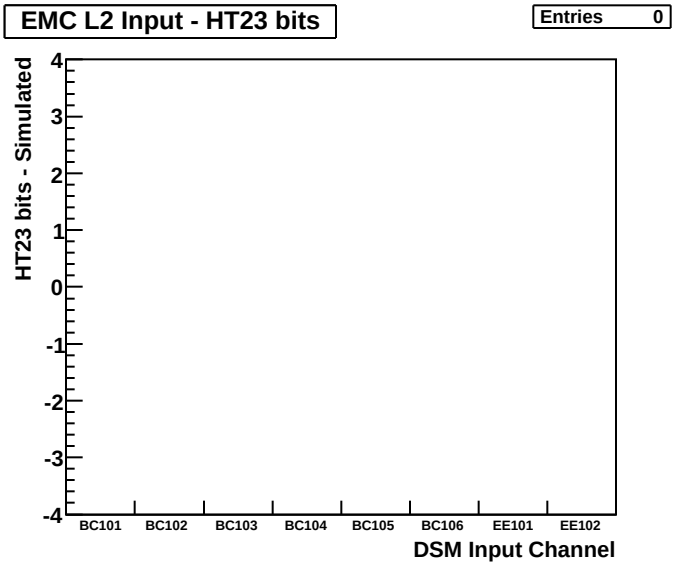

EMC L2 Input - Partial JP Sum
Entries 5267872
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

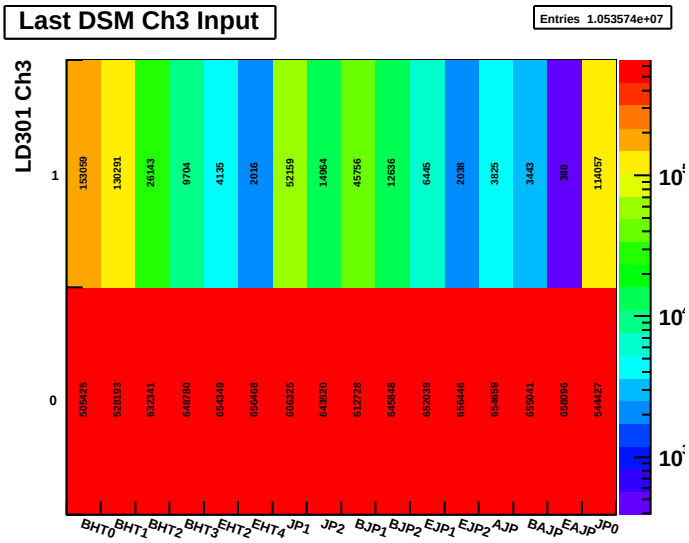

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

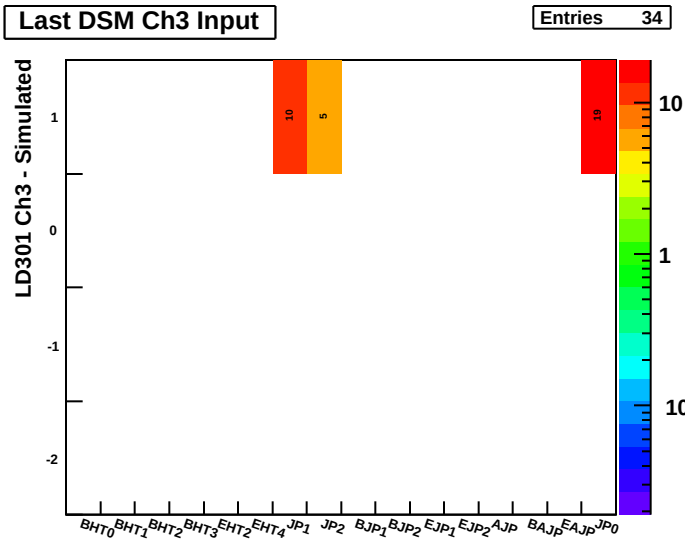

EMC L2 Input - HT01 bits/Partial JP ID
Entries 5267872
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

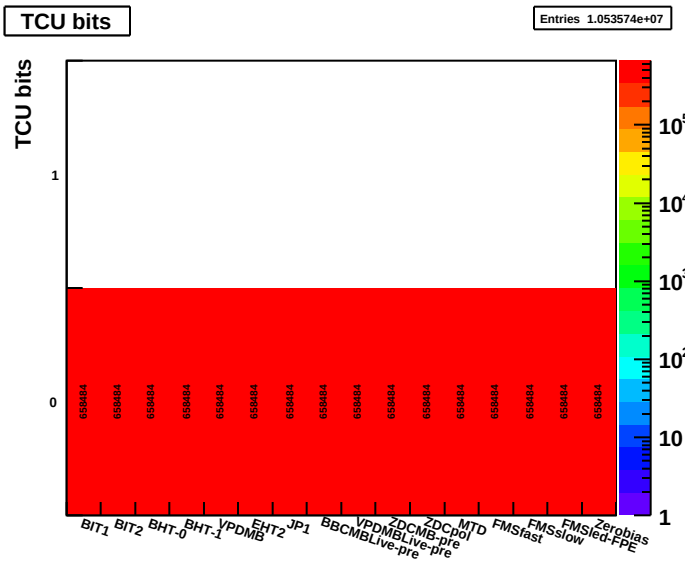

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

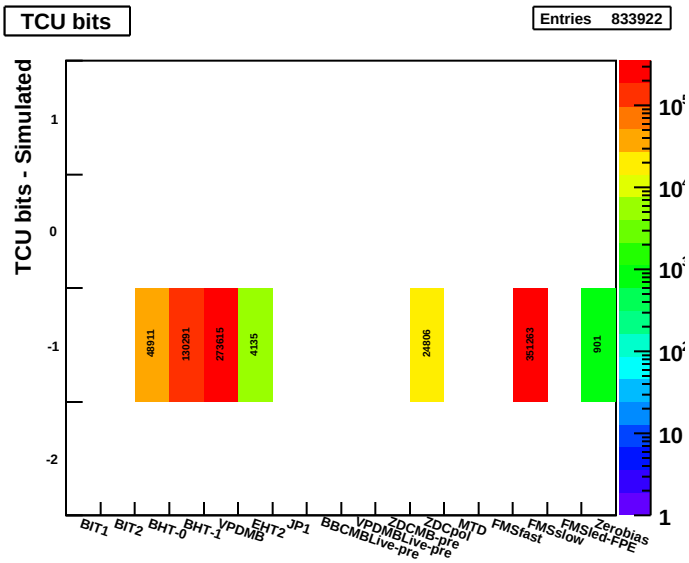

EMC L2 Input - HT23 bits
Entries 5267872
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

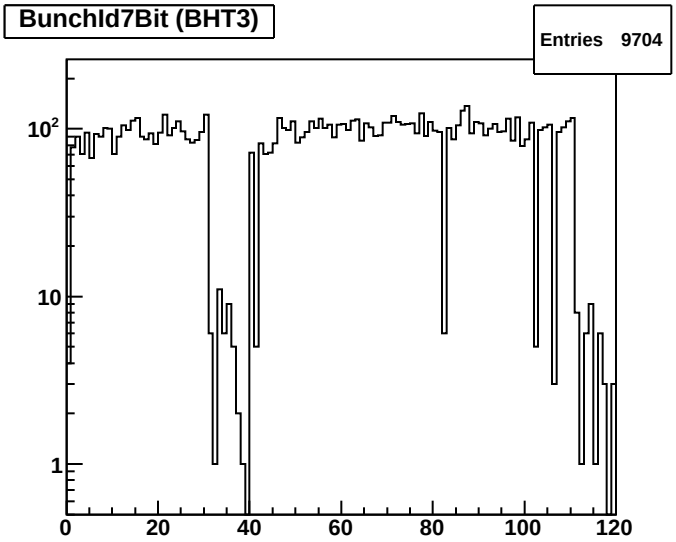

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

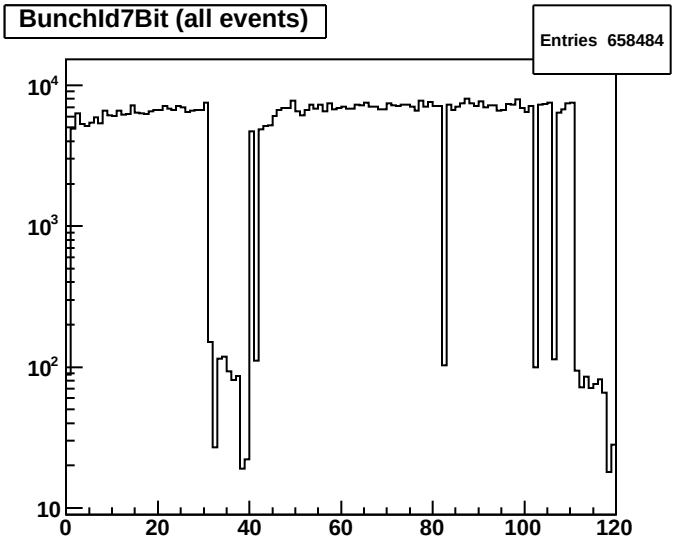

Last DSM Ch3 Input
Entries 1.053574e+07
LD301 Ch3
153059 130291 26343 9704 4135 2016 52159 14964 45756 12636 6445 2038 3825 3443 388 114057
505425 528193 632341 648780 654349 656468 606325 643520 612728 648048 652039 656446 654659 655041 658096 544427
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

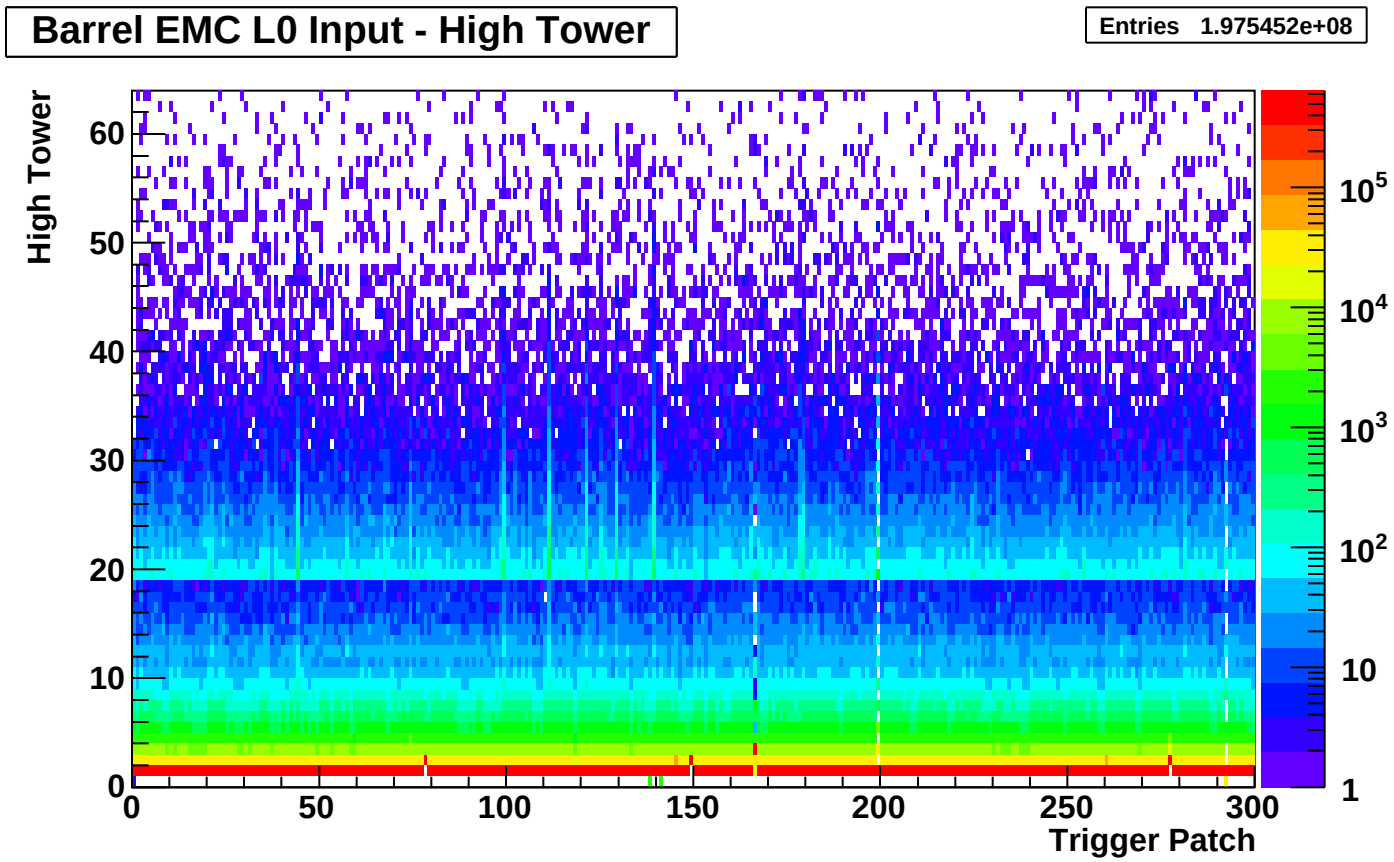

Last DSM Ch3 Input
Entries 34
LD301 Ch3 - Simulated
10 5 19
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

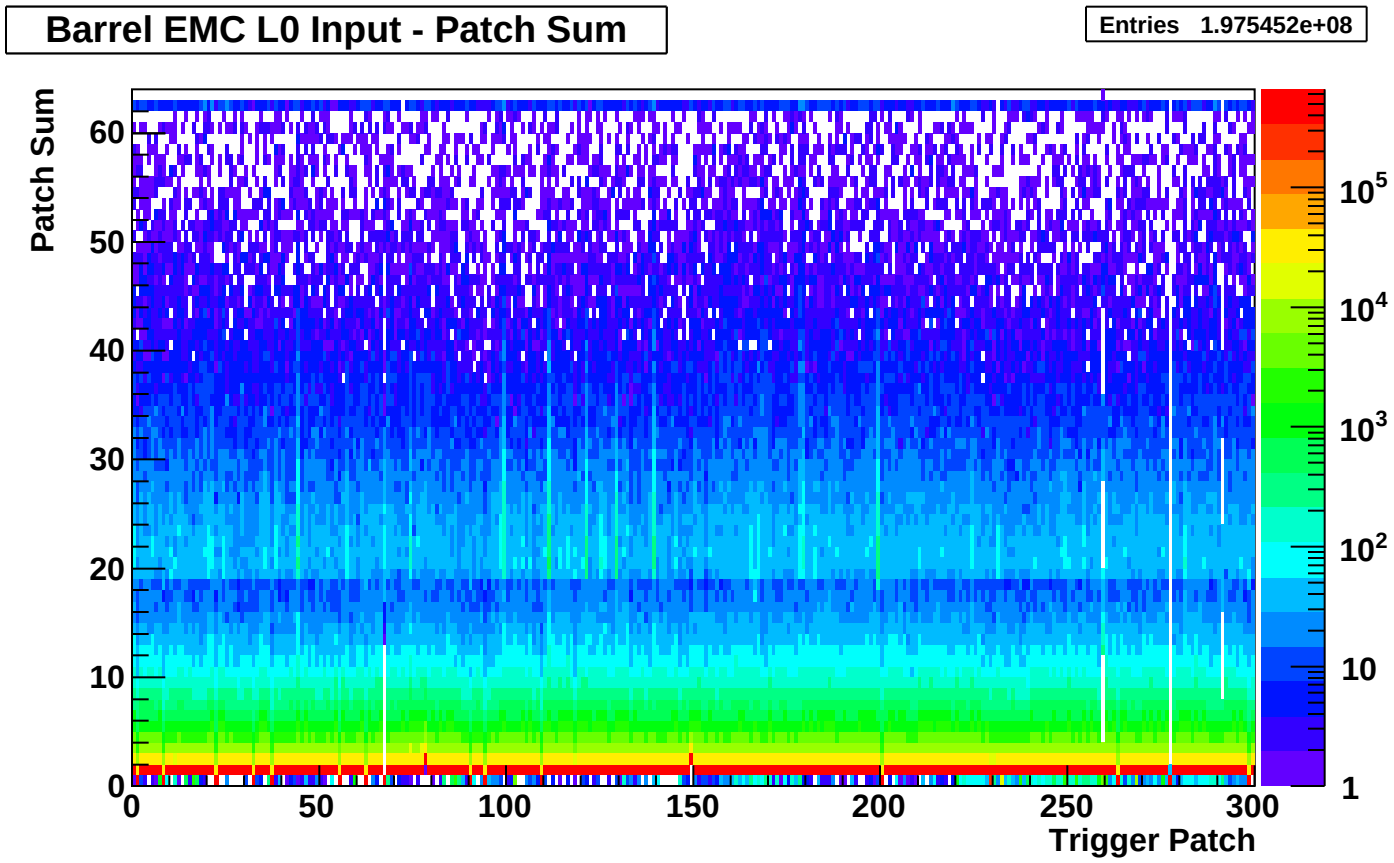

TCU bits
Entries 1.053574e+07
TCU bits
655484 655484 655484 655484 655484 655484 655484 655484 655484 655484 655484 655484 655484 655484 655484 655484
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

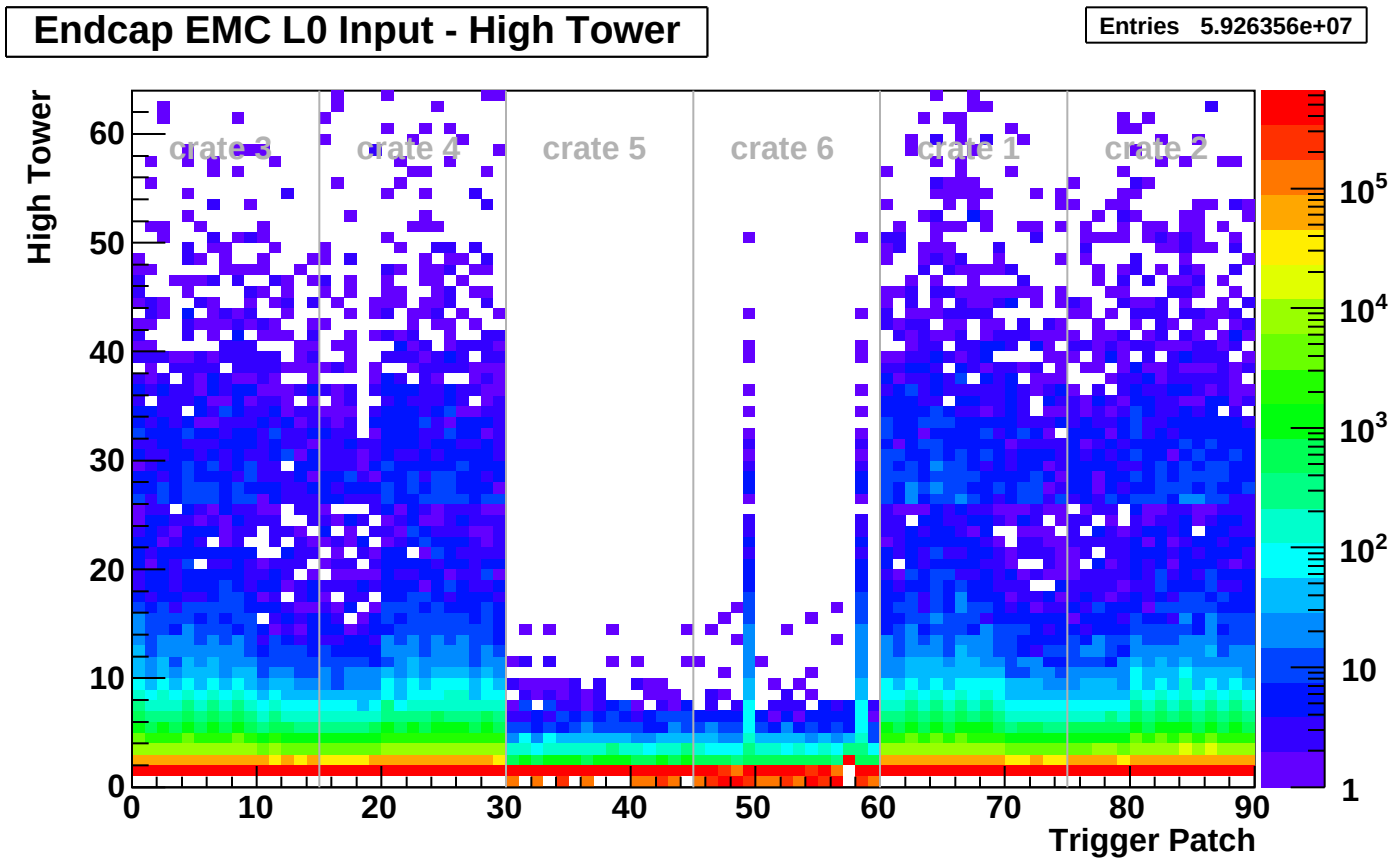

TCU bits
Entries 833922
TCU bits - Simulated
48911 130291 273615 4135 24806 351263 901
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

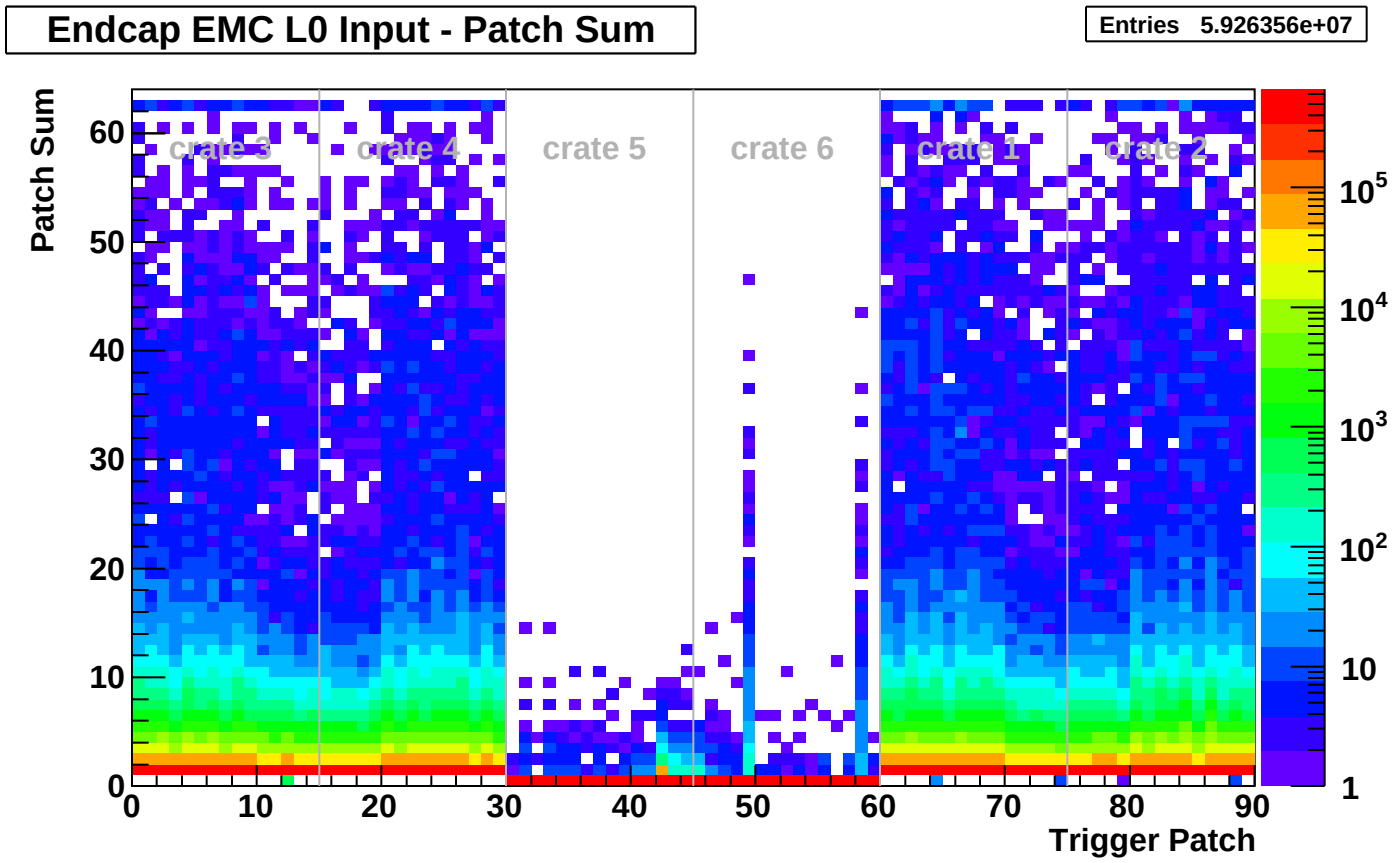

BunchId7Bit (BHT3)
Entries 9704

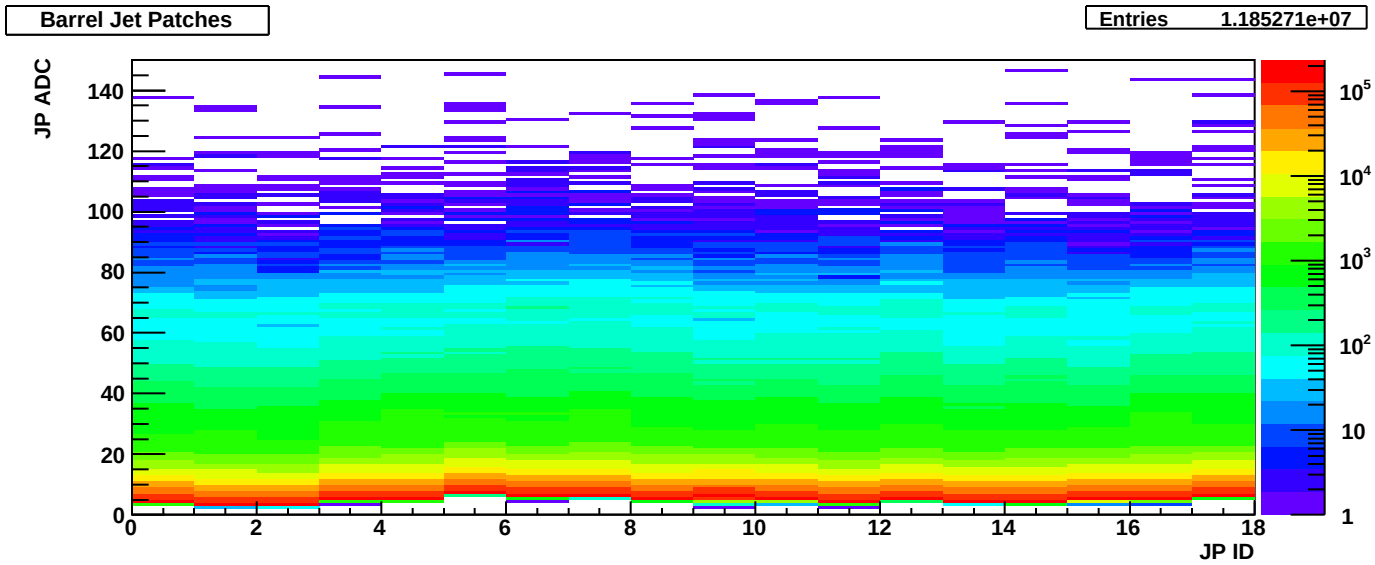

BunchId7Bit (all events)
Entries 658484

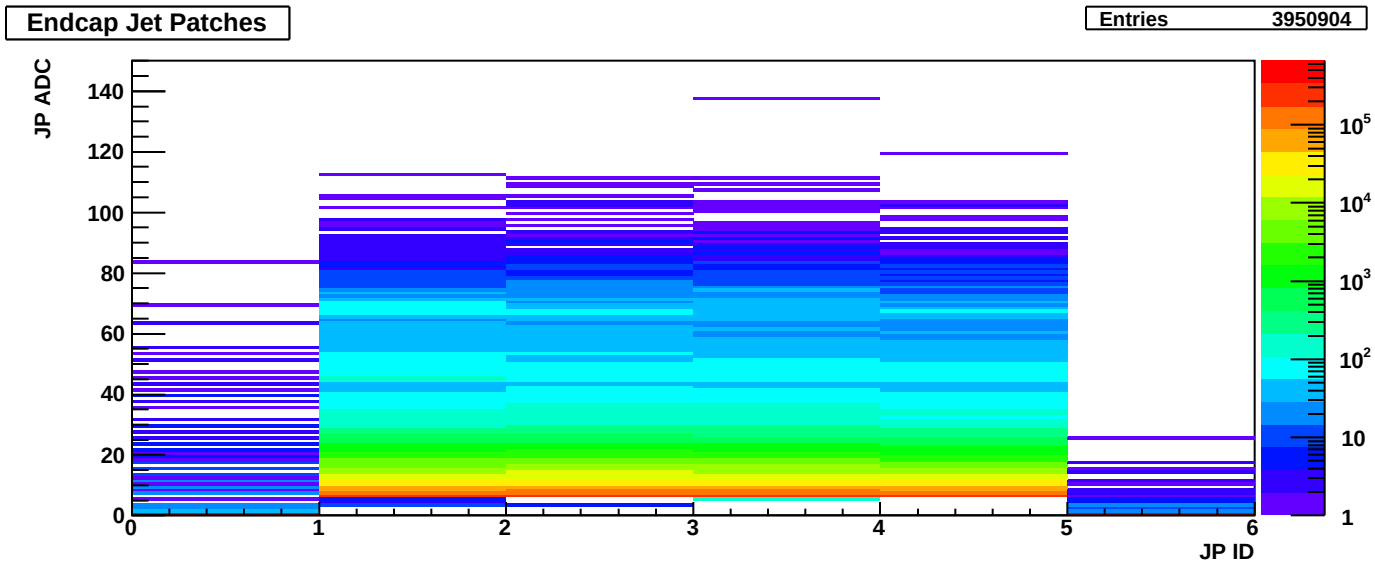

Barrel EMC L0 Input - High Tower
Entries 1.975452e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

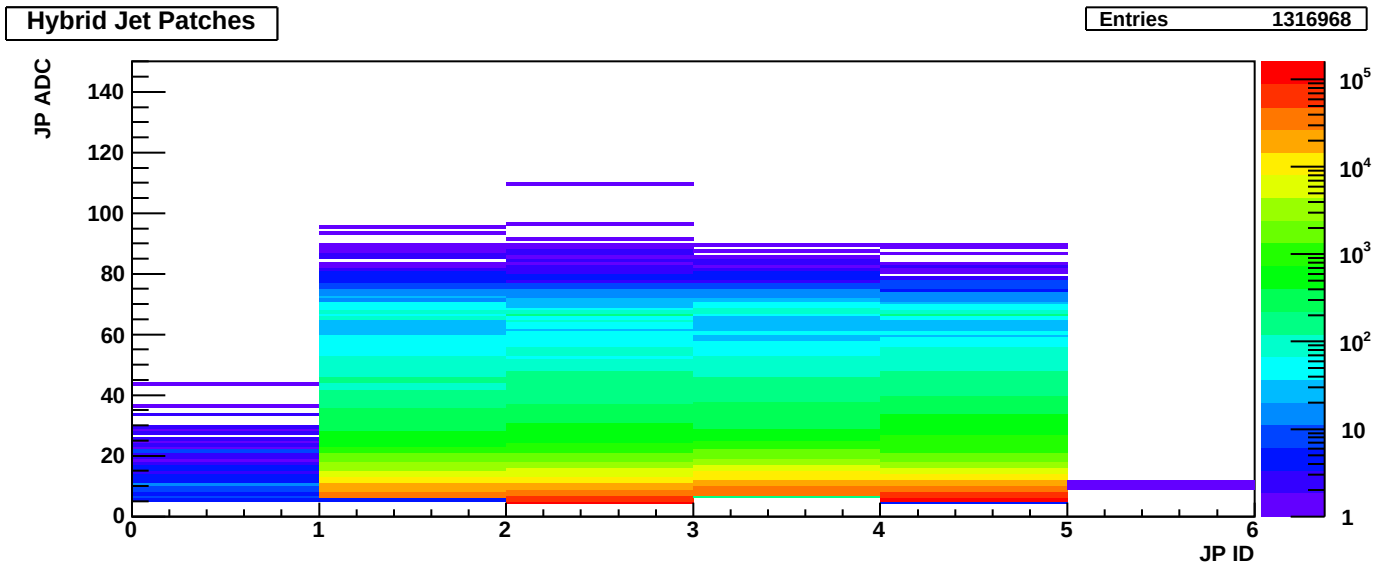

Barrel EMC L0 Input - Patch Sum
Entries 1.975452e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

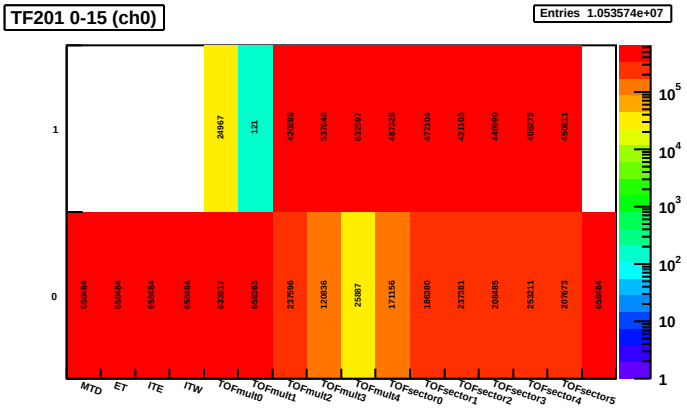

Endcap EMC L0 Input - High Tower
Entries 5.926356e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

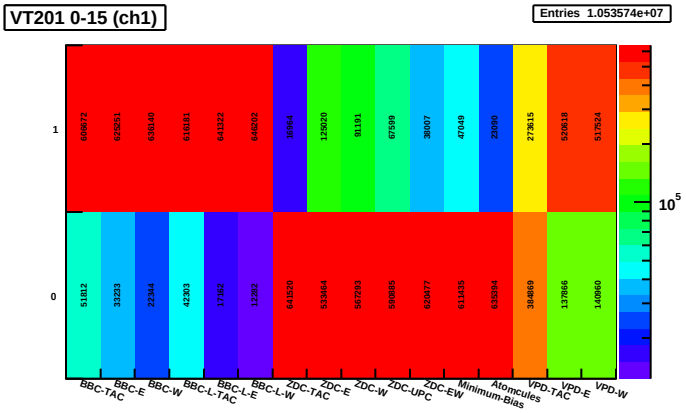

Endcap EMC L0 Input - Patch Sum
Entries 5.926356e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

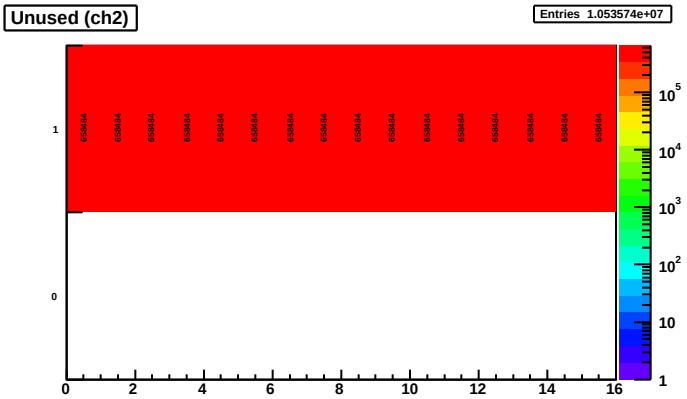

Barrel Jet Patches
Entries 1.185271e+07
JP ADC
JP ID

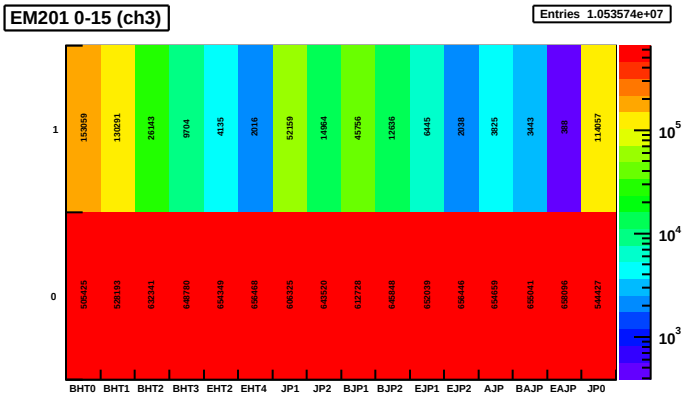

Endcap Jet Patches
Entries 3950904
JP ADC
JP ID

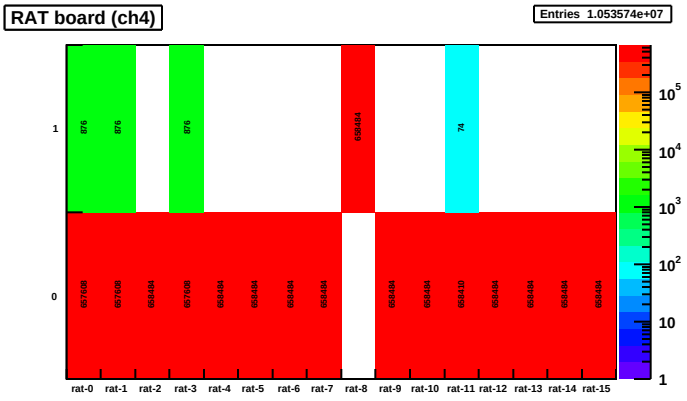

Hybrid Jet Patches
Entries 1316968
JP ADC
JP ID

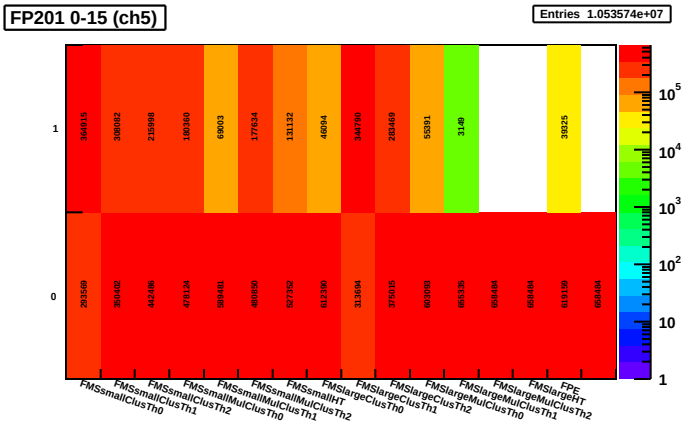

TF201 0-15 (ch0)
Entries 1.053574e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

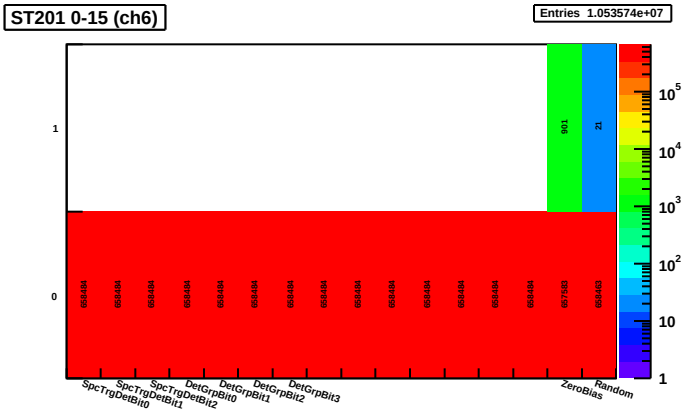

VT201 0-15 (ch1)
Entries 1.053574e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

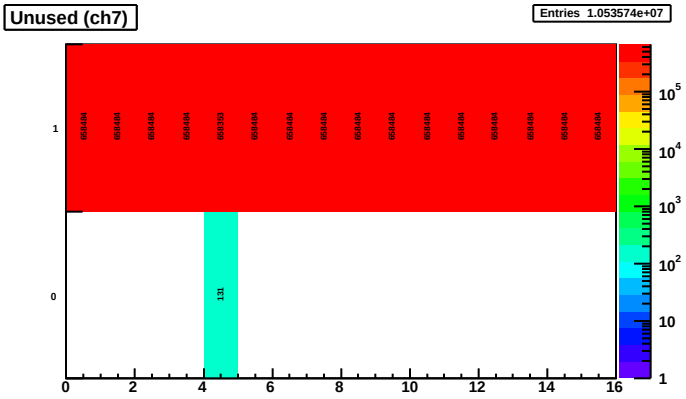

Unused (ch2)
Entries 1.053574e+07

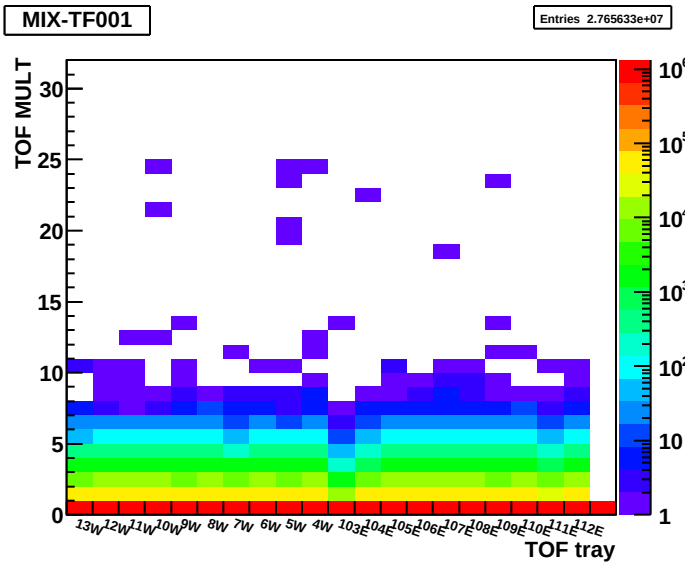

EM201 0-15 (ch3)
Entries 1.053574e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

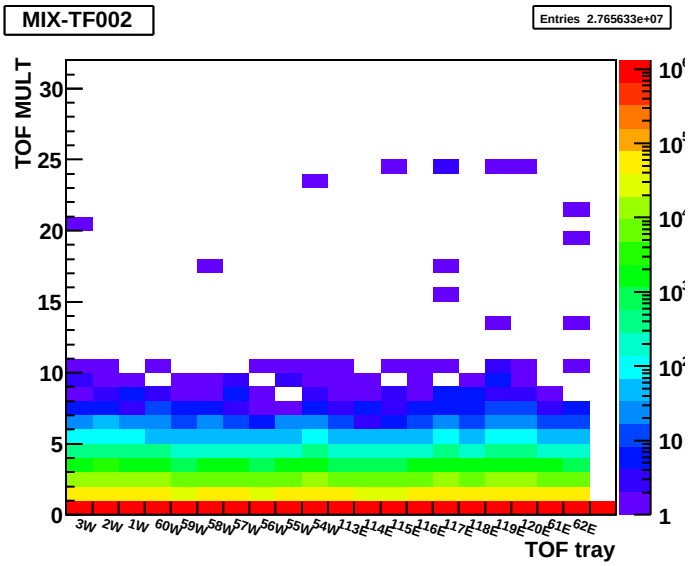

RAT board (ch4)
Entries 1.053574e+07
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

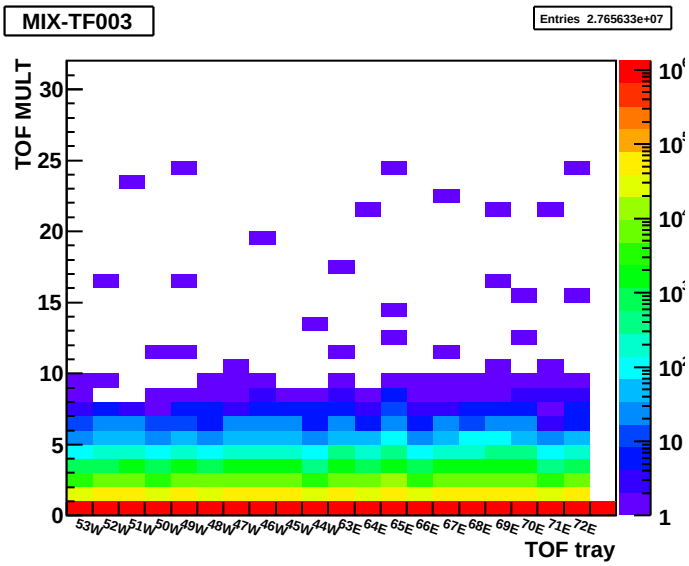

FP201 0-15 (ch5)
Entries 1.053574e+07
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

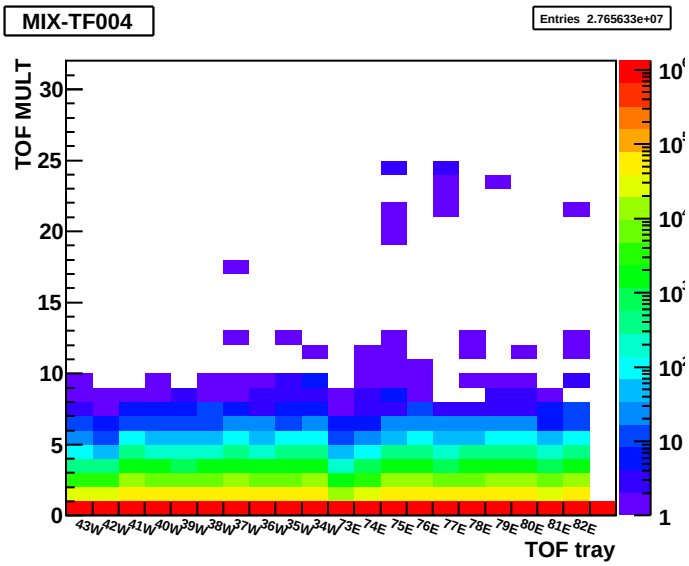

ST201 0-15 (ch6)
Entries 1.053574e+07
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

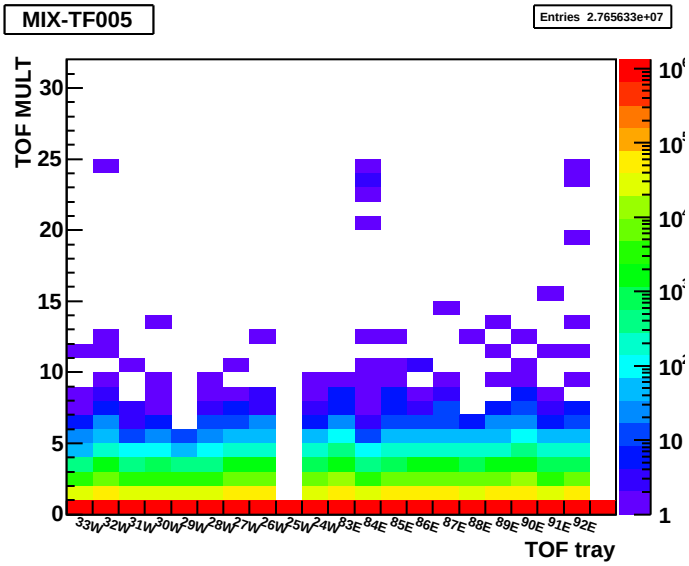

Unused (ch7)
Entries 1.053574e+07

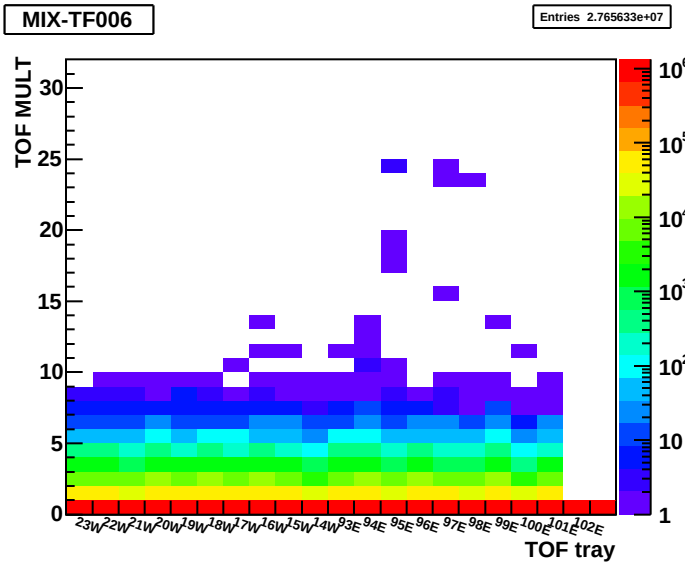

MIX-TF001
Entries 2.765633e+07
TOF MULT
TOF tray

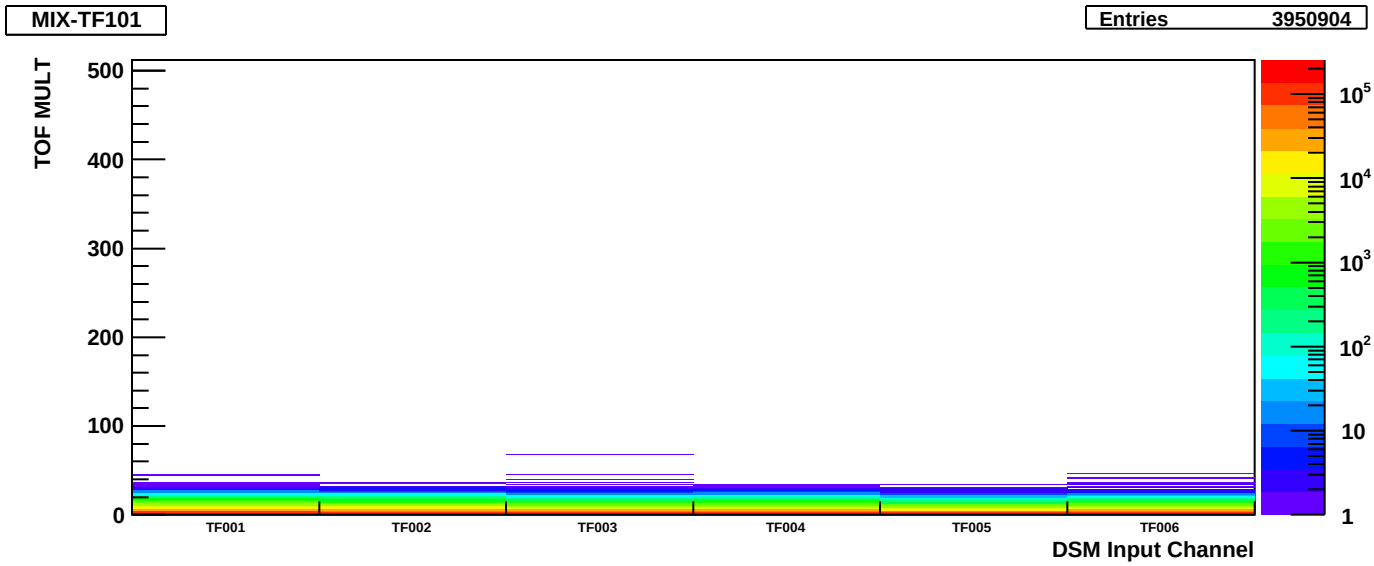

MIX-TF002
Entries 2.765633e+07
TOF MULT
TOF tray

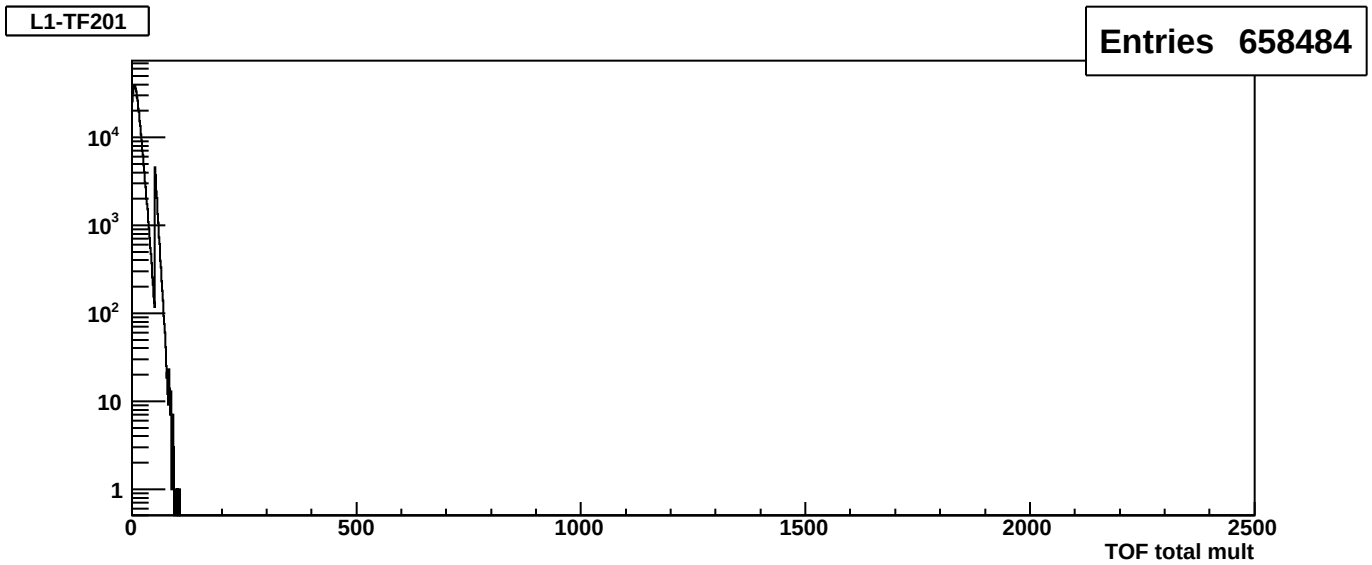

MIX-TF003
Entries 2.765633e+07
TOF MULT
TOF tray

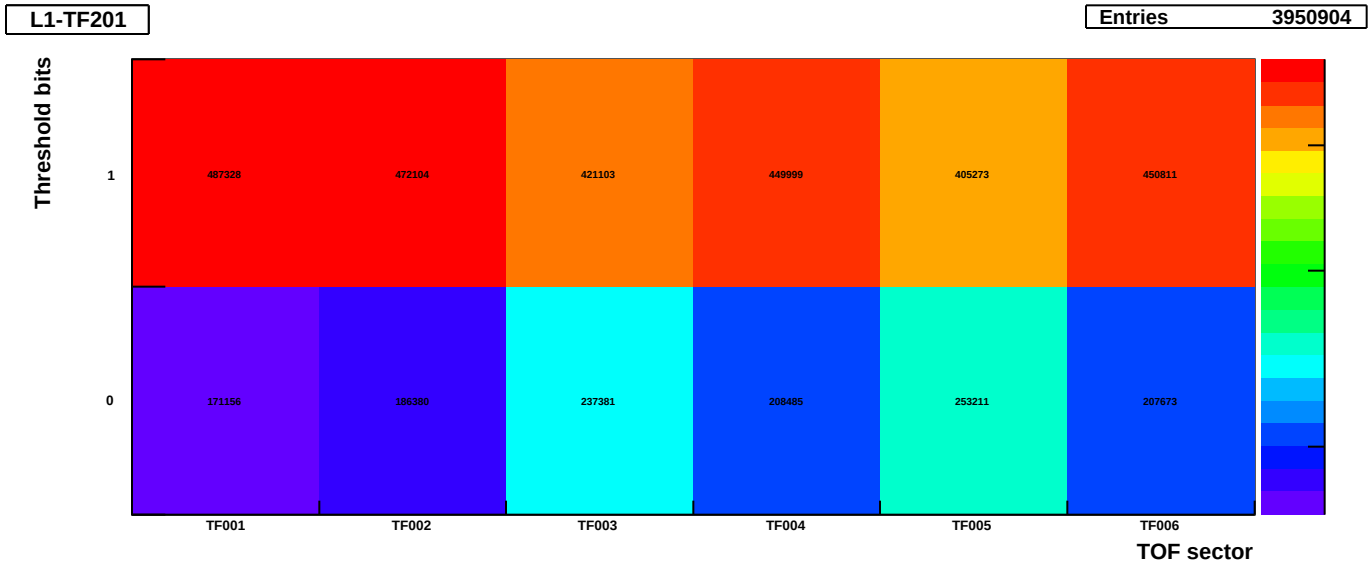

MIX-TF004
Entries 2.765633e+07
TOF MULT
TOF tray

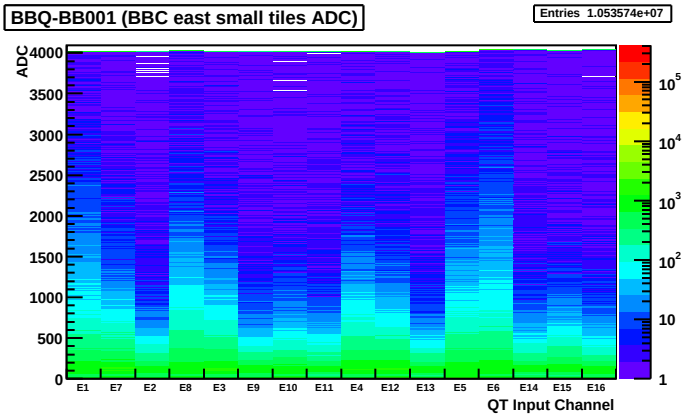

MIX-TF005
Entries 2.765633e+07
TOF MULT
TOF tray

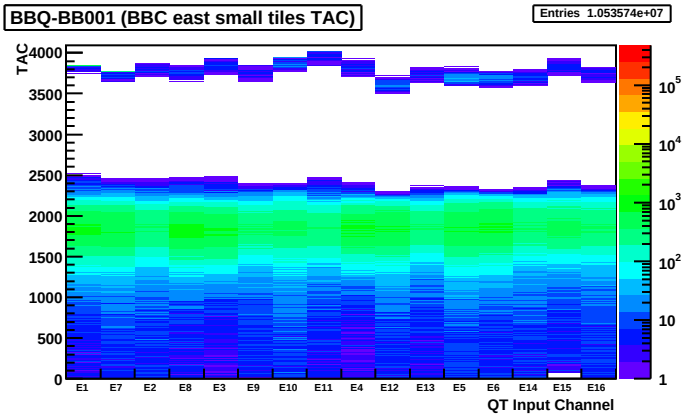

MIX-TF006
Entries 2.765633e+07
TOF MULT
TOF tray

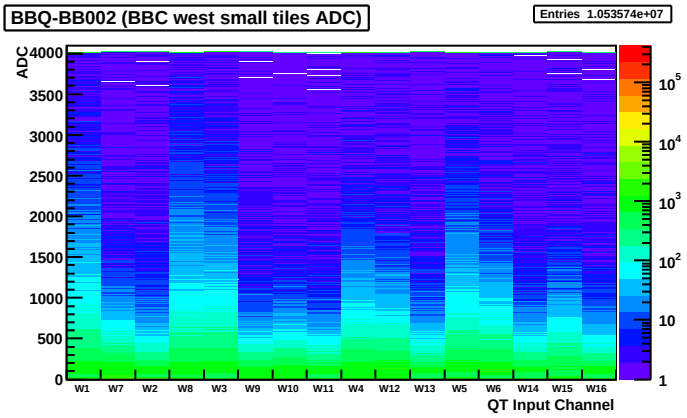

MIX-TF101
Entries 3950904
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

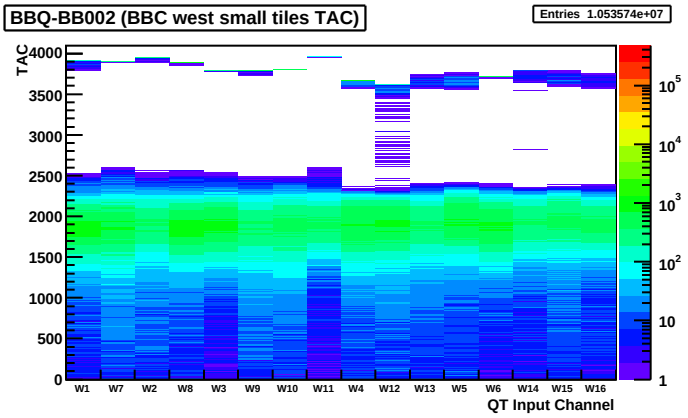

L1-TF201
Entries 658484
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

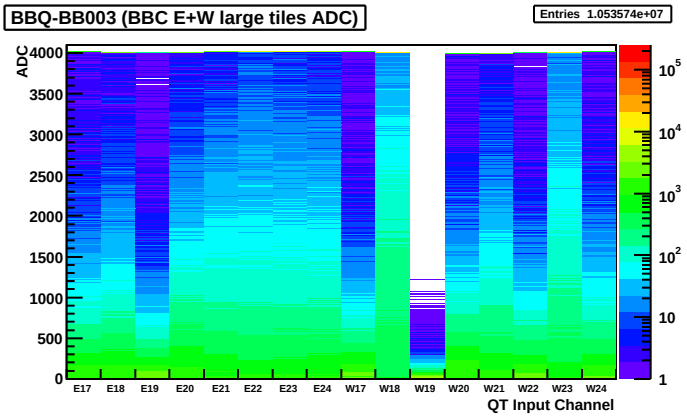

L1-TF201
Entries 3950904
Threshold bits
1
0
487328
472104
421103
449999
405273
450811
171156
186380
237381
208485
253211
207673
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

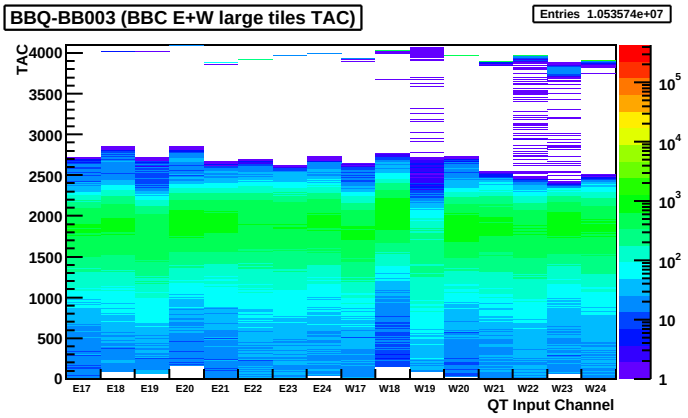

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.053574e+07
ADC
QT Input Channel

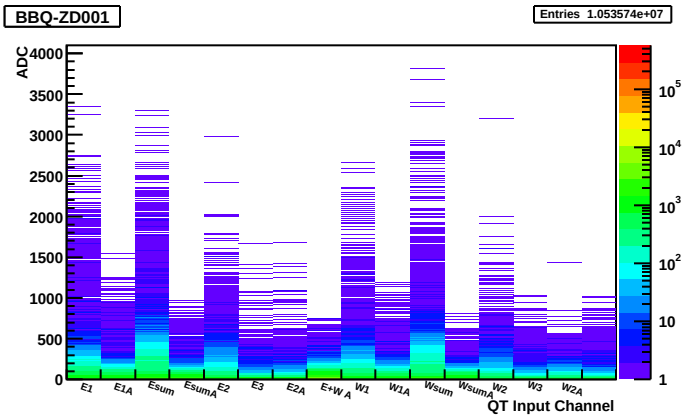

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.053574e+07
TAC
QT Input Channel

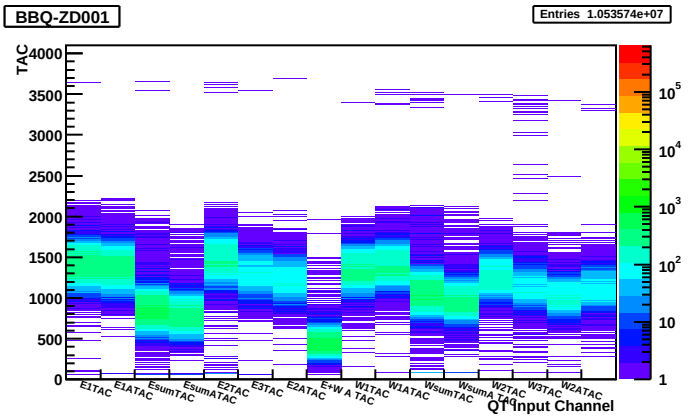

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.053574e+07
ADC
QT Input Channel

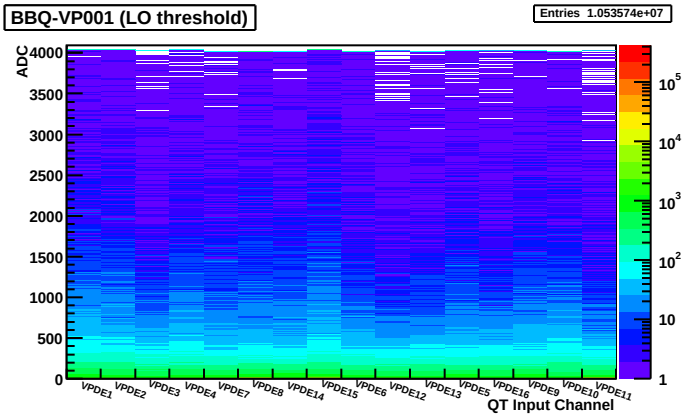

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.053574e+07
TAC
QT Input Channel

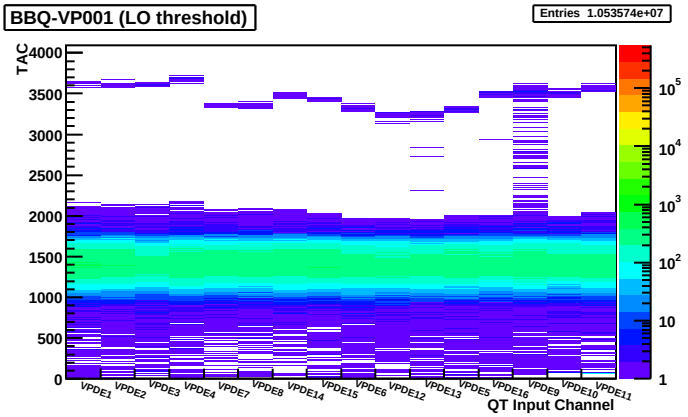

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.053574e+07
ADC
QT Input Channel

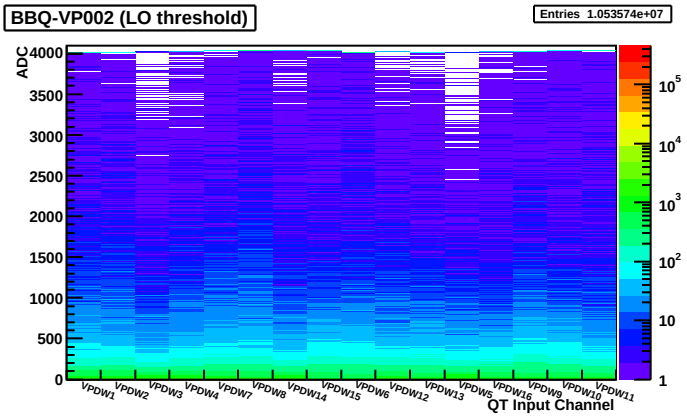

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.053574e+07
TAC
QT Input Channel

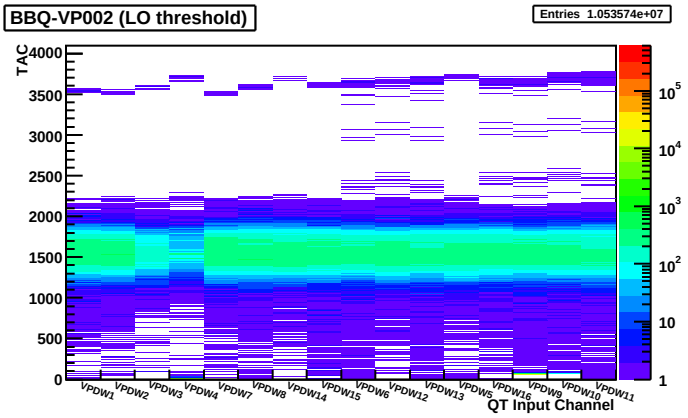

BBQ-ZD001
Entries 1.053574e+07
ADC
QT Input Channel

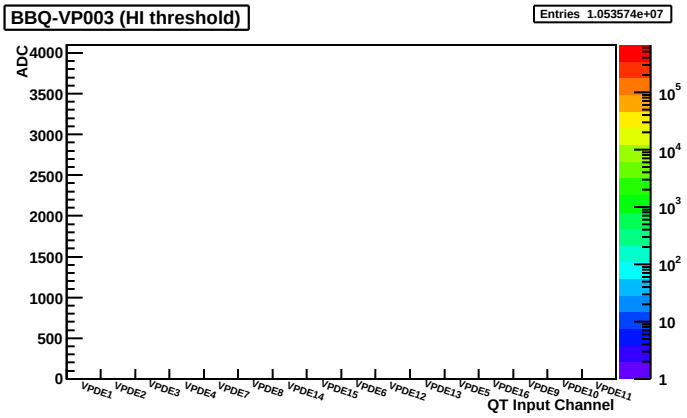

BBQ-ZD001
Entries 1.053574e+07
TAC
QT Input Channel

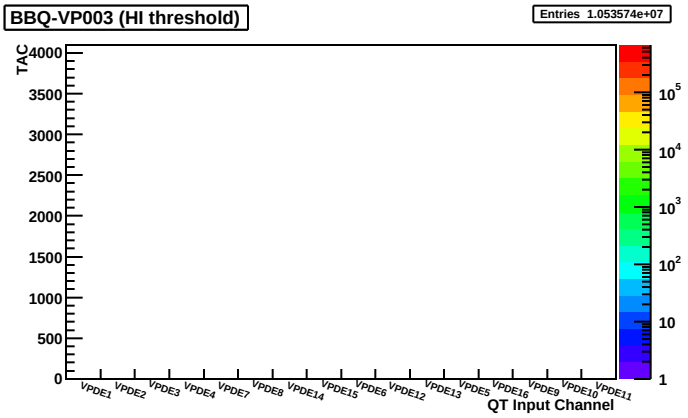

BBQ-VP001 (LO threshold)
Entries 1.053574e+07
ADC
QT Input Channel

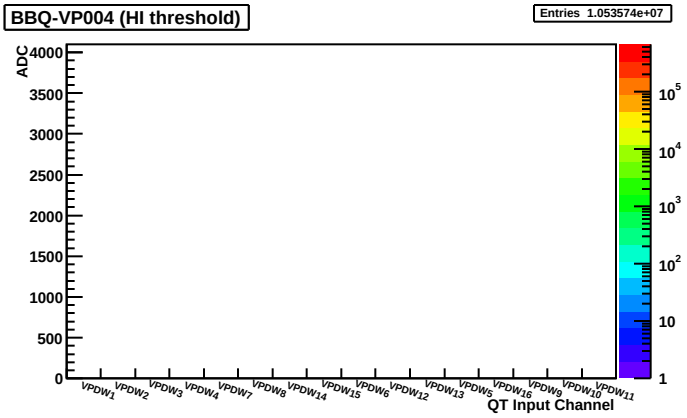

BBQ-VP001 (LO threshold)
Entries 1.053574e+07
TAC
QT Input Channel

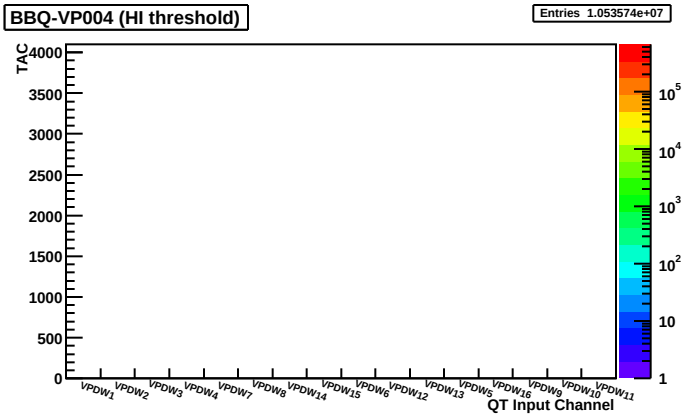

BBQ-VP002 (LO threshold)
Entries 1.053574e+07
ADC
QT Input Channel

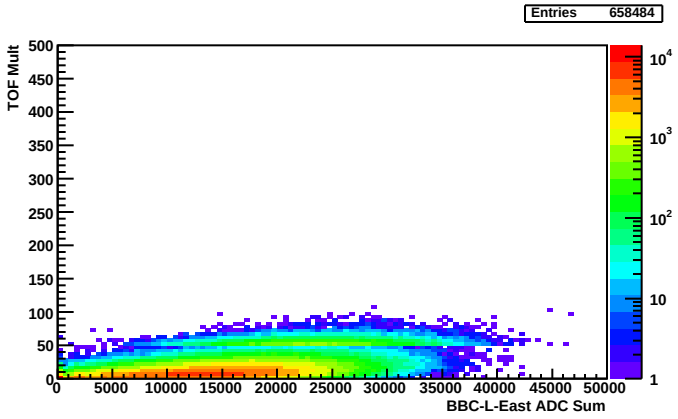

BBQ-VP002 (LO threshold)
Entries 1.053574e+07
TAC
QT Input Channel

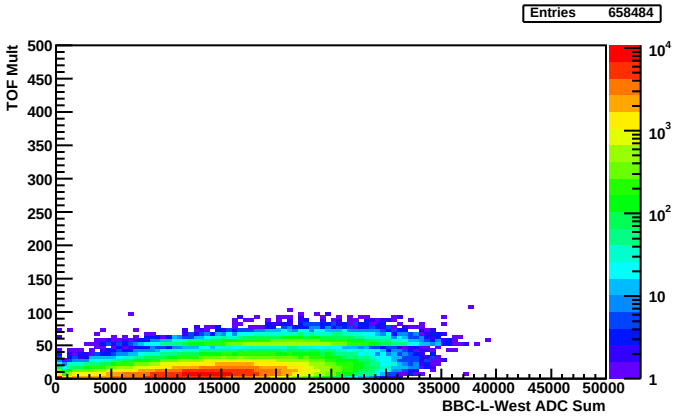

BBQ-VP003 (HI threshold)
Entries 1.053574e+07
ADC
QT Input Channel

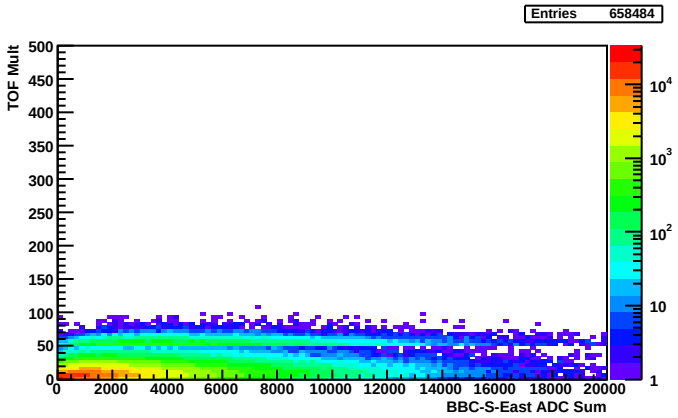

BBQ-VP003 (HI threshold)
Entries 1.053574e+07
TAC
QT Input Channel

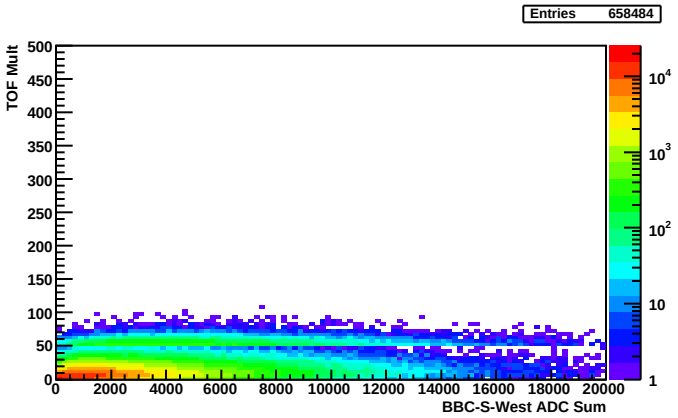

BBQ-VP004 (HI threshold)
Entries 1.053574e+07
ADC
QT Input Channel

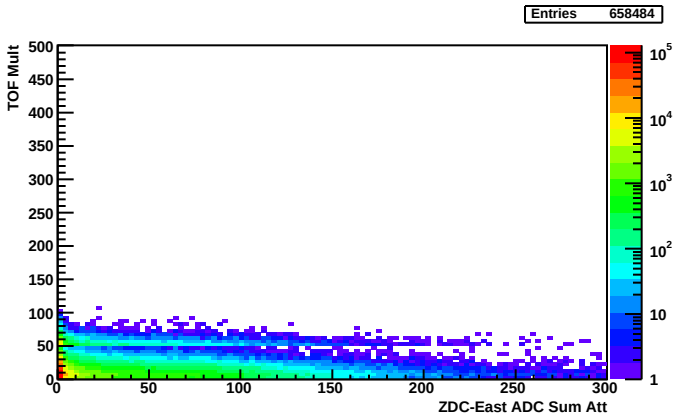

BBQ-VP004 (HI threshold)
Entries 1.053574e+07
TAC
QT Input Channel

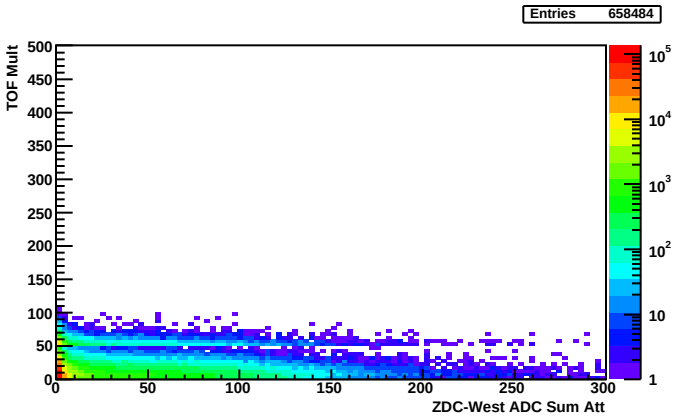

Entries 658484
TOF Mult
BBC-L-East ADC Sum

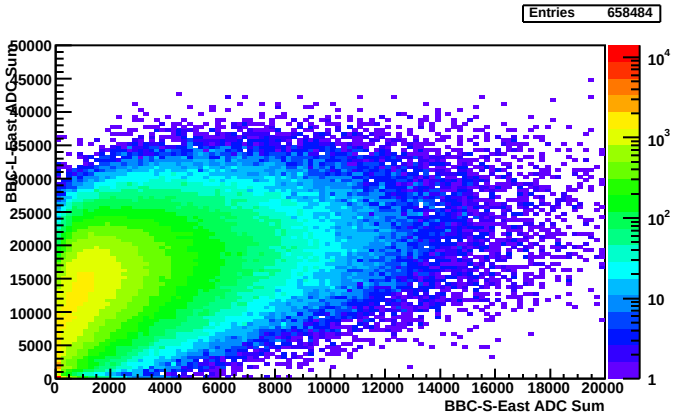

Entries 658484
TOF Mult
BBC-L-West ADC Sum

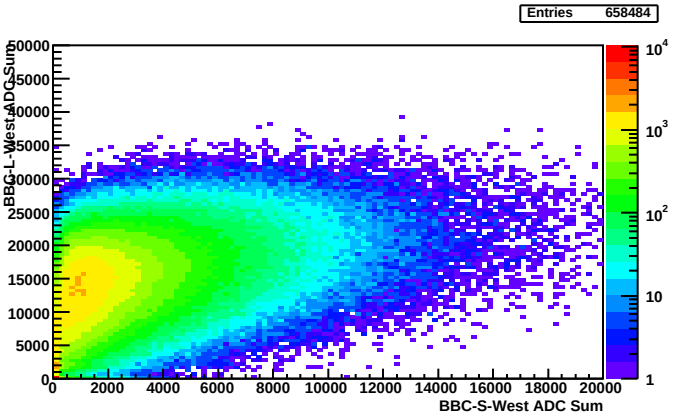

Entries 658484
TOF Mult
BBC-S-East ADC Sum

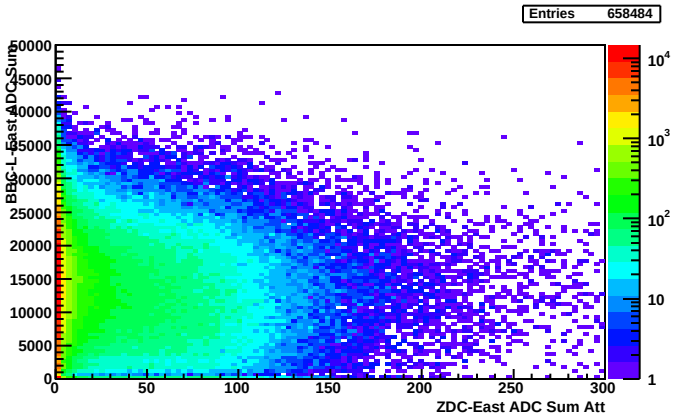

Entries 658484
TOF Mult
BBC-S-West ADC Sum

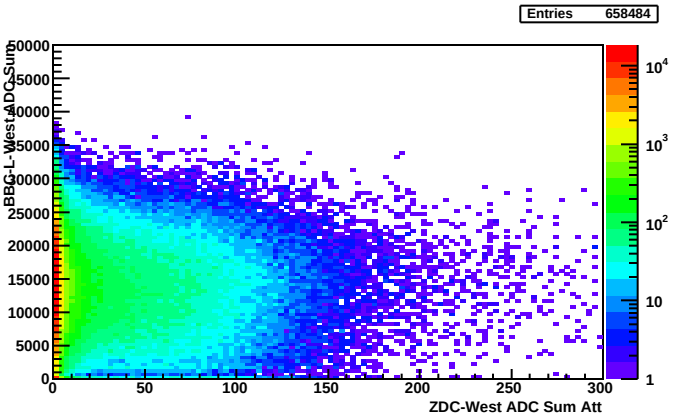

Entries 658484
TOF Mult
ZDC-East ADC Sum Att

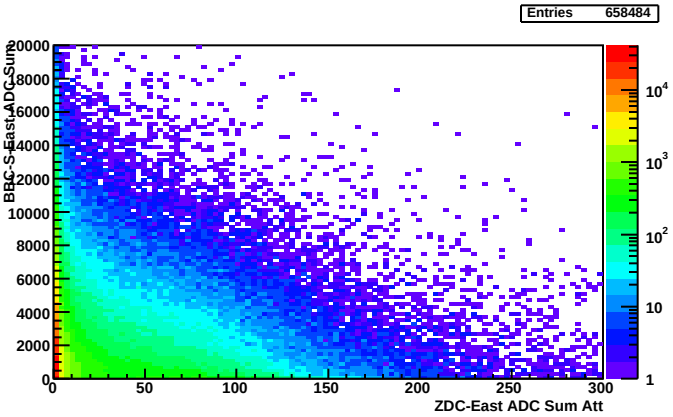

Entries 658484
TOF Mult
ZDC-West ADC Sum Att

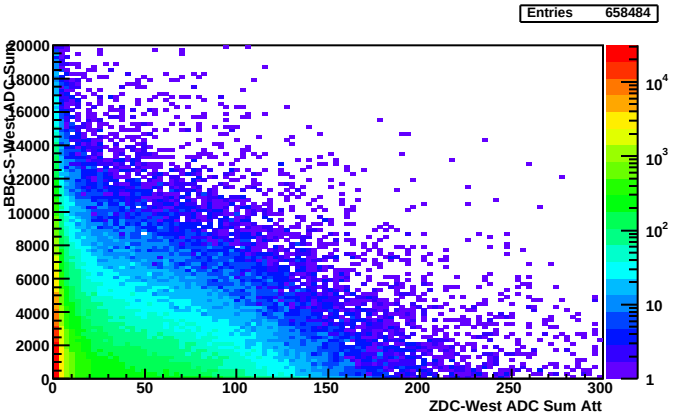

Entries 658484
BBC-L-East ADC Sum
BBC-S-East ADC Sum

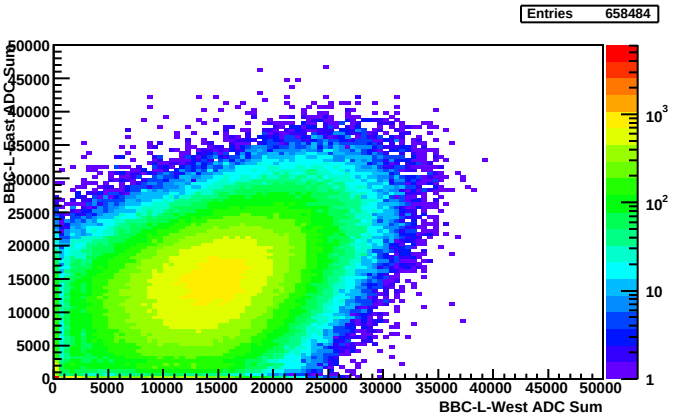

Entries 658484
BBC-L-West ADC Sum
BBC-S-West ADC Sum

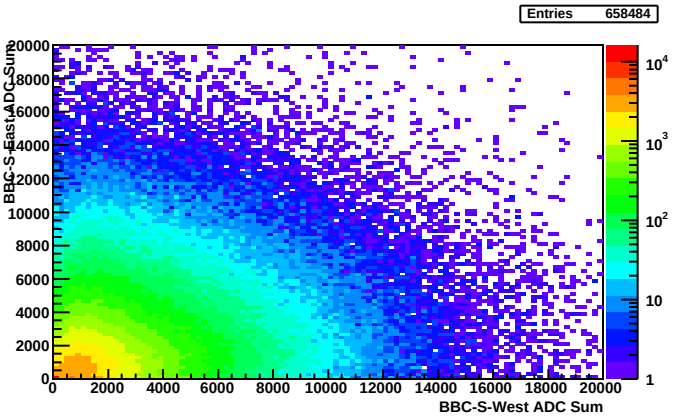

Entries 658484
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

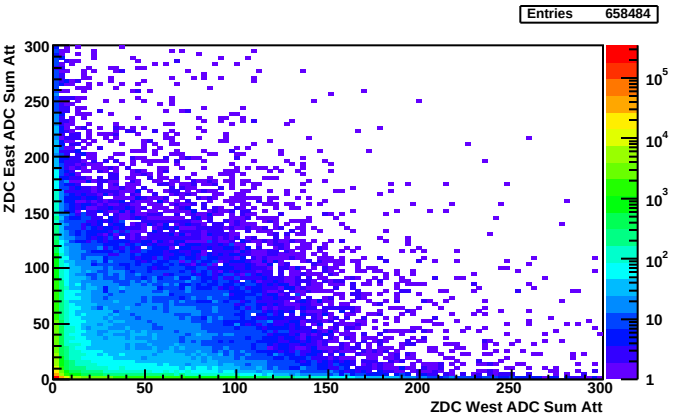

Entries 658484
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

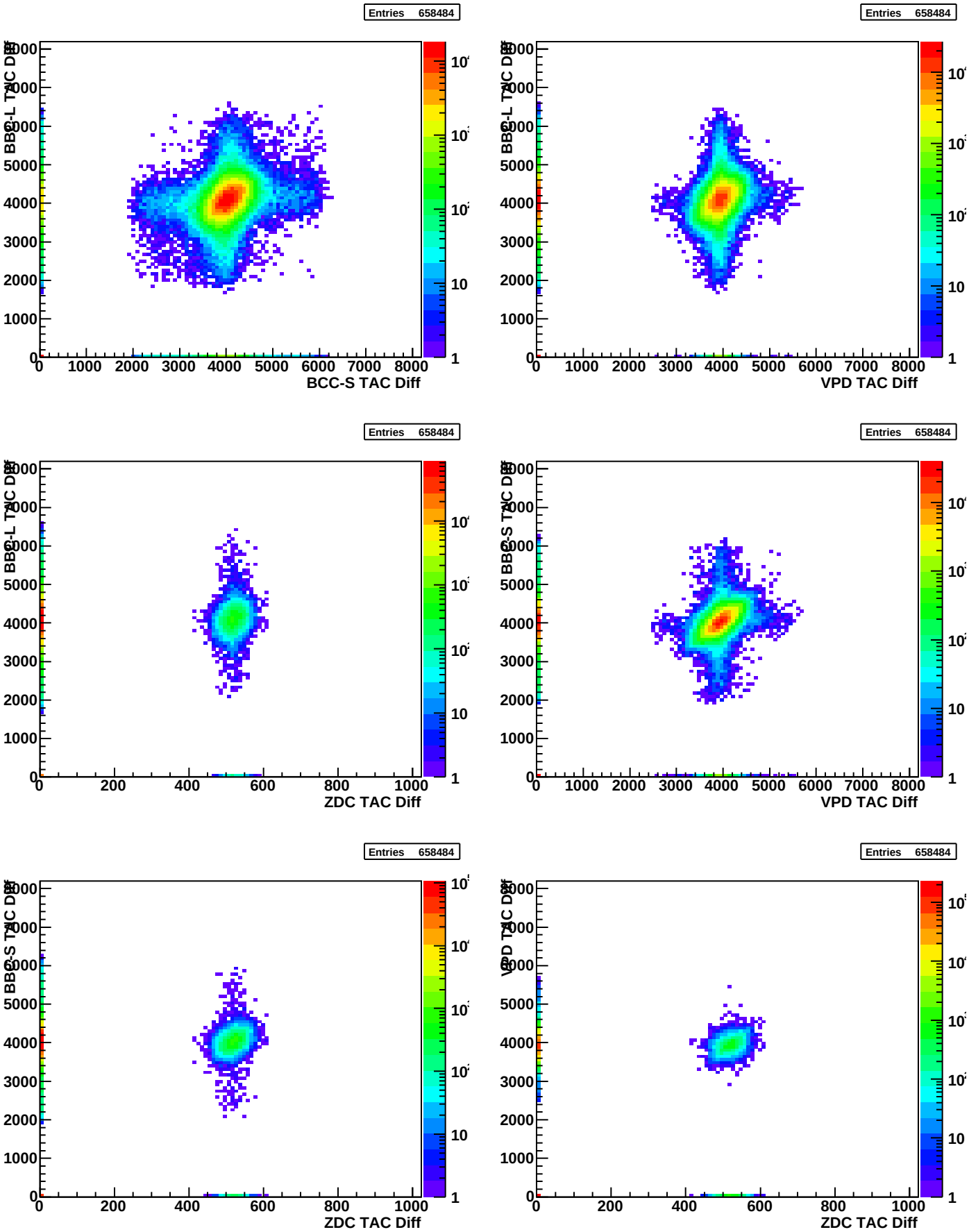

Entries 658484
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

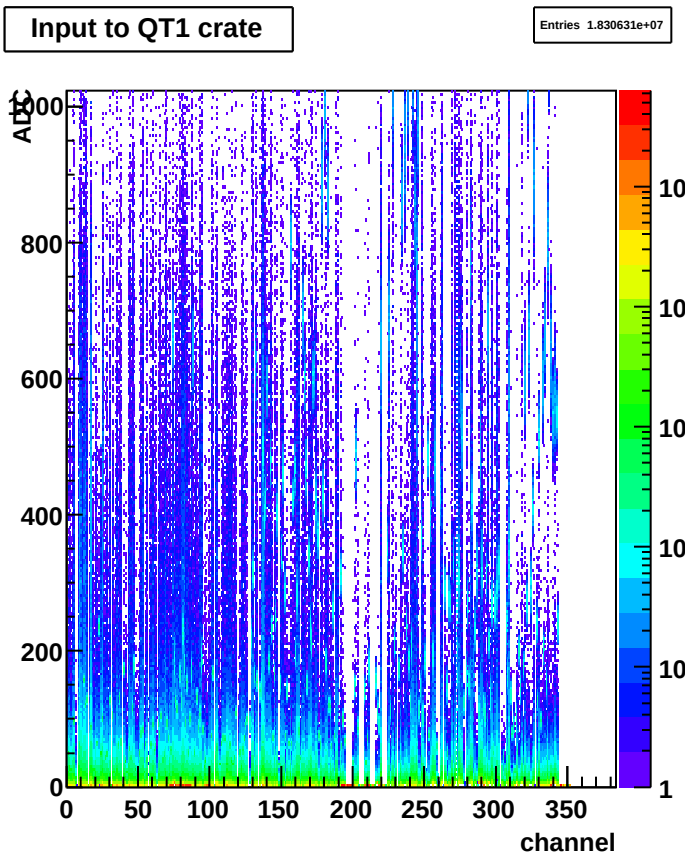

Entries 658484
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

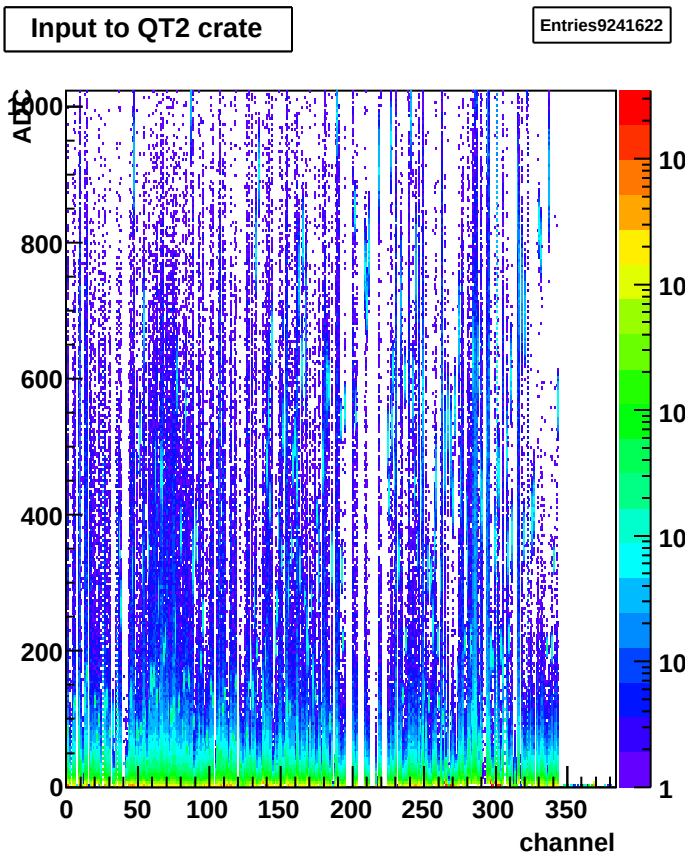

Entries 658484
BBC-L-East ADC Sum
BBC-L-West ADC Sum

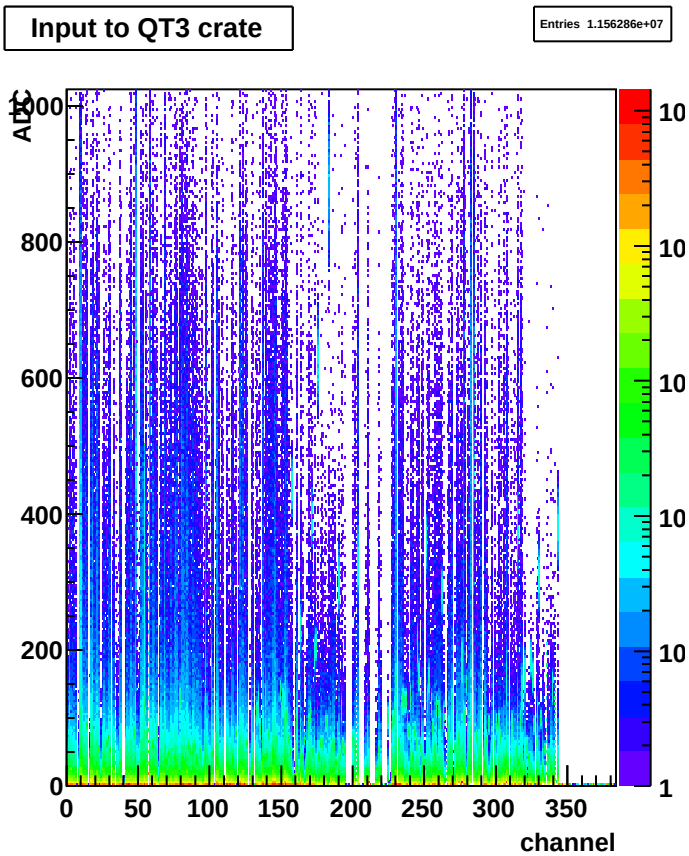

Entries 658484
BBC-S-East ADC Sum
BBC-S-West ADC Sum

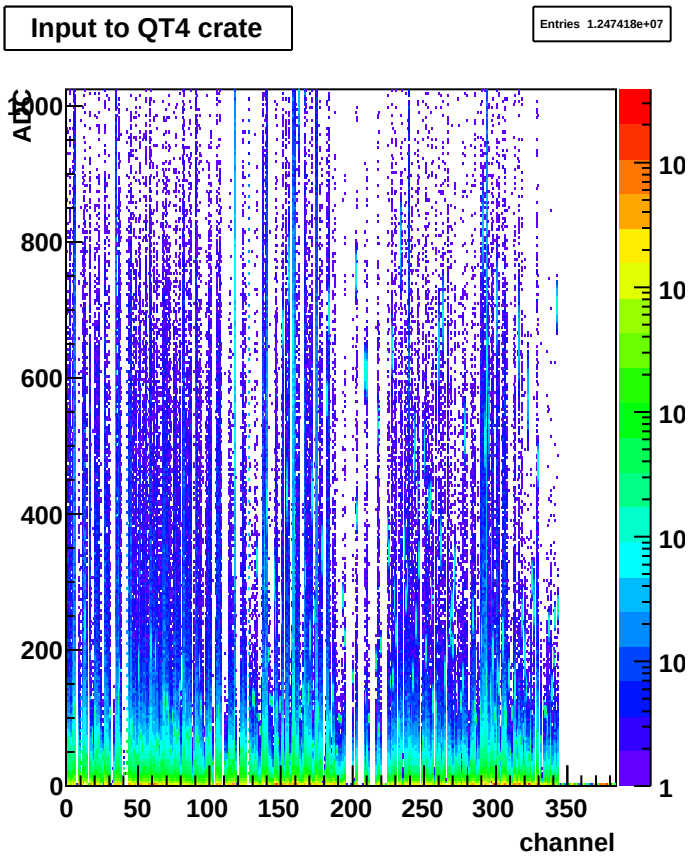

Entries 658484
ZDC East ADC Sum Att
ZDC West ADC Sum Att

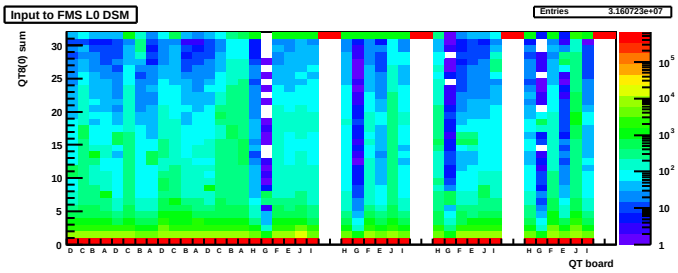

Entries 658484
BBC-L TAC Diff
BCC-S TAC Diff
Entries 658484
BBC-L TAC Diff
VPD TAC Diff
Entries 658484
BBC-L TAC Diff
ZDC TAC Diff
Entries 658484
BBC-S TAC Diff
VPD TAC Diff
Entries 658484
BBC-S TAC Diff
ZDC TAC Diff
Entries 658484
VPD TAC Diff
ZDC TAC Diff

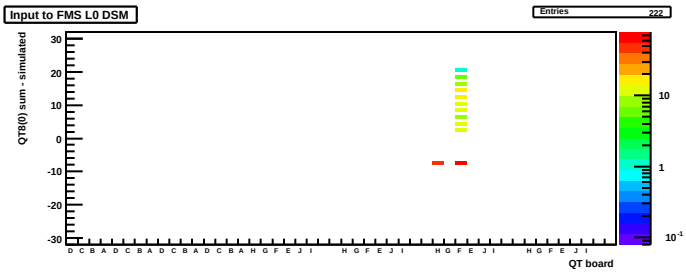

Input to QT1 crate
Entries 1.830631e+07
ADC
channel

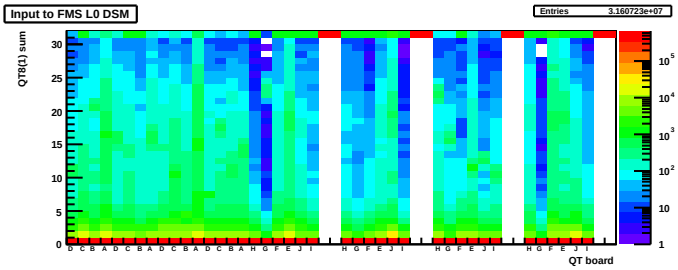

Input to QT2 crate
Entries9241622
ADC
channel

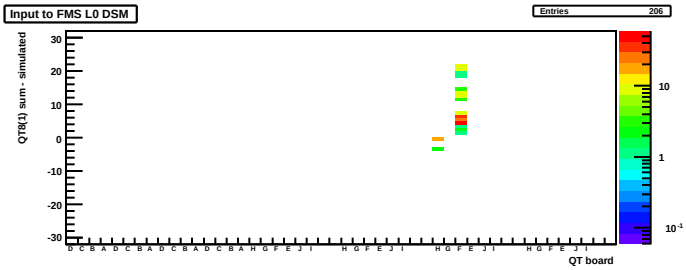

Input to QT3 crate
Entries 1.156286e+07
ADC
channel

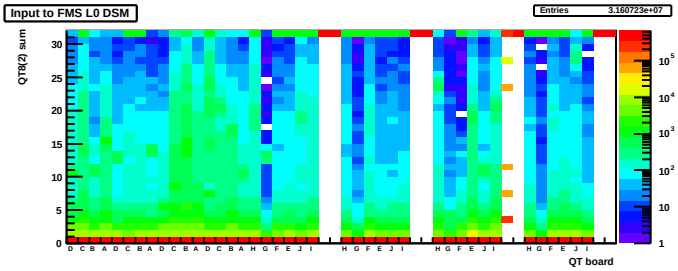

Input to QT4 crate
Entries 1.247418e+07
ADC
channel

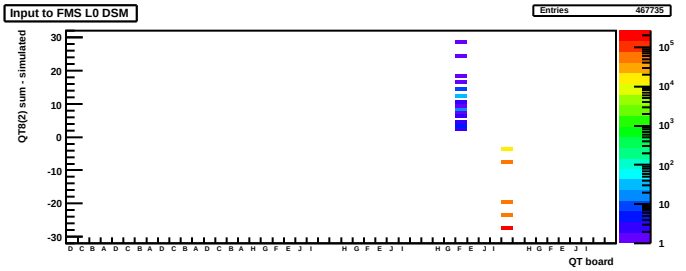

Input to FMS L0 DSM
Entries 3.160723e+07
QT8(0) sum
QT board

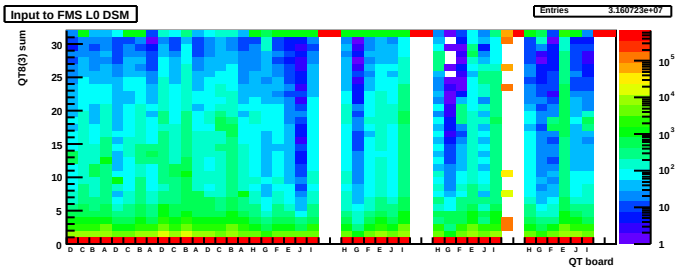

Input to FMS L0 DSM
Entries 222
QT8(0) sum - simulated
QT board

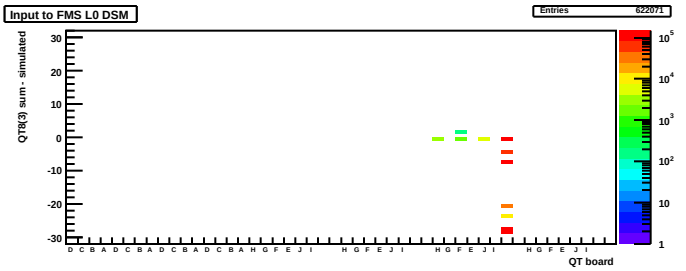

Input to FMS L0 DSM
Entries 3.160723e+07
QT8(1) sum
QT board

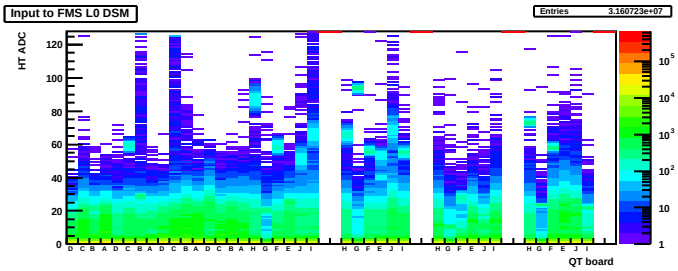

Input to FMS L0 DSM
Entries 206
QT8(1) sum - simulated
QT board

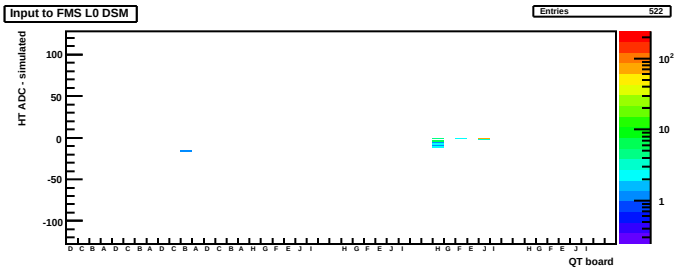

Input to FMS L0 DSM
Entries 3.160723e+07
QT8(2) sum
QT board

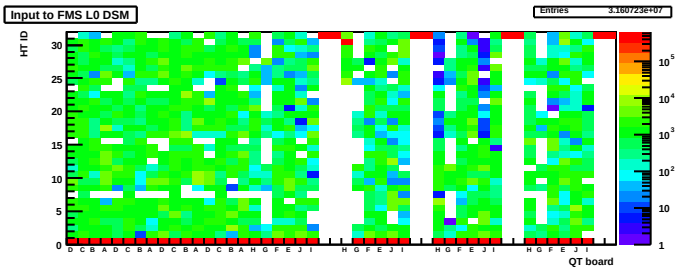

Input to FMS L0 DSM
Entries 467735
QT8(2) sum - simulated
QT board

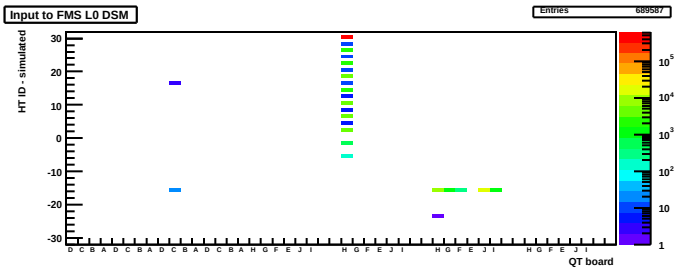

Input to FMS L0 DSM
Entries 3.160723e+07
QT8(3) sum
QT board

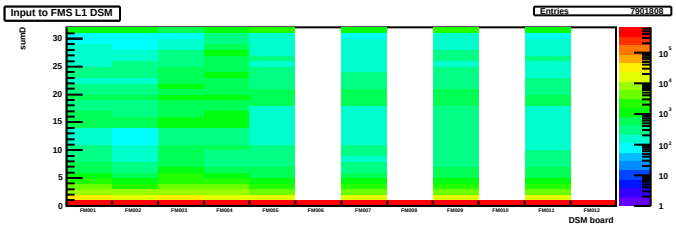

Input to FMS L0 DSM
Entries 622071
QT8(3) sum - simulated
QT board

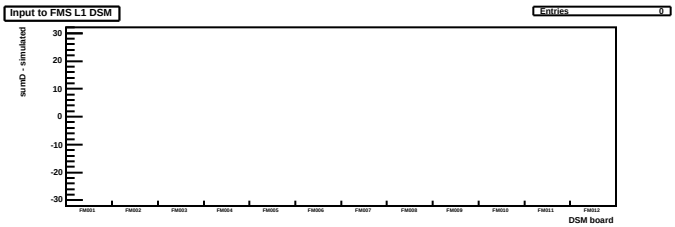

Input to FMS L0 DSM
Entries 3.160723e+07
HT ADC
QT board

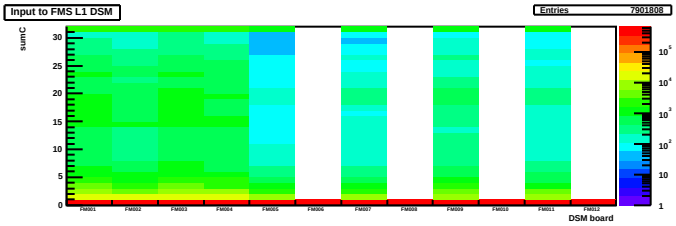

Input to FMS L0 DSM
Entries 522
HT ADC - simulated
QT board

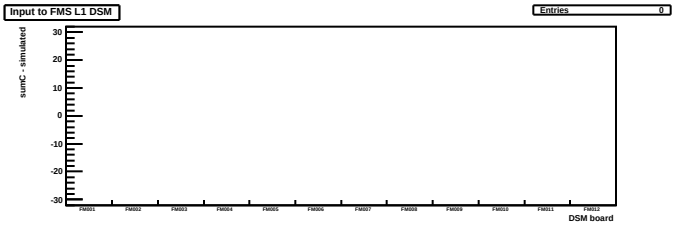

Input to FMS L0 DSM
Entries 3.160723e+07
HT ID
QT board

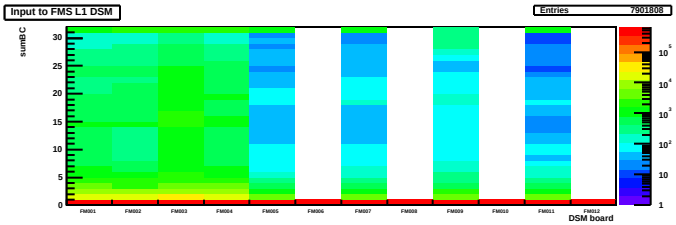

Input to FMS L0 DSM
Entries 689587
HT ID - simulated
QT board

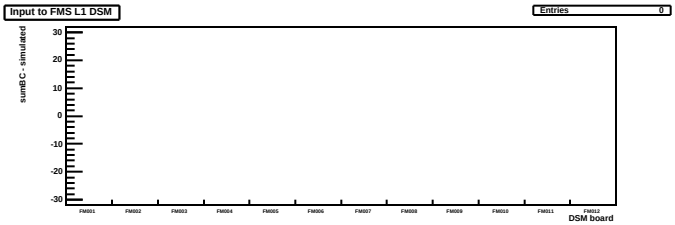

Input to FMS L1 DSM
Entries 7901808
sumD
DSM board

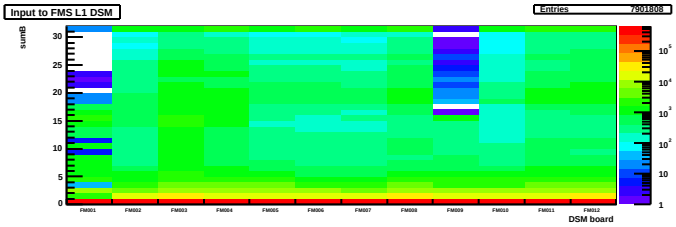

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

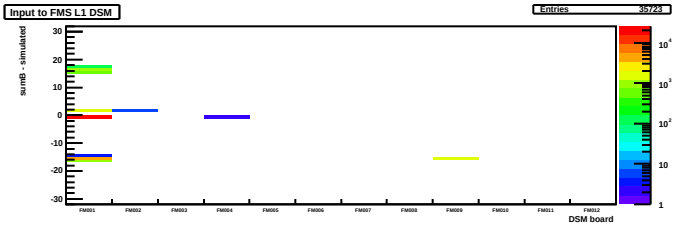

Input to FMS L1 DSM
Entries 7901808
sumC
DSM board

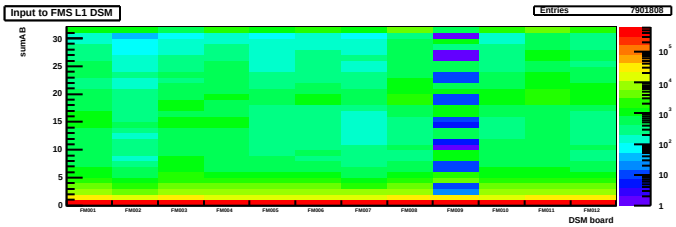

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

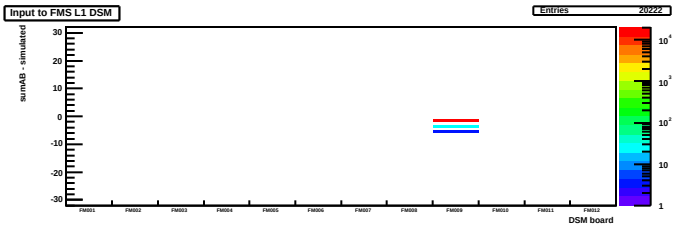

Input to FMS L1 DSM
Entries 7901808
sumBC
DSM board

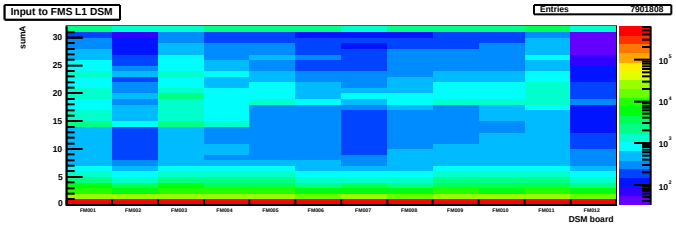

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

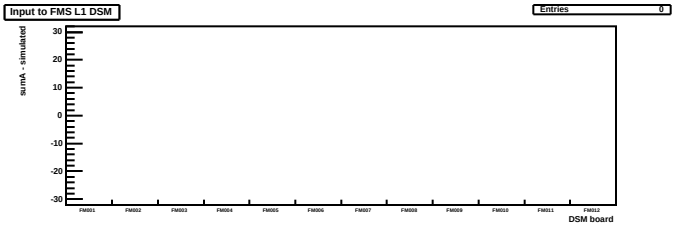

Input to FMS L1 DSM
Entries 7901808
sumB
DSM board

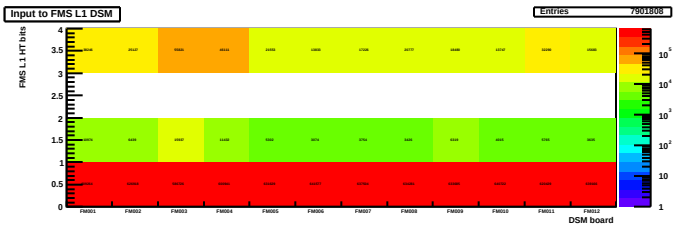

Input to FMS L1 DSM
Entries 35723
sumB - simulated
DSM board

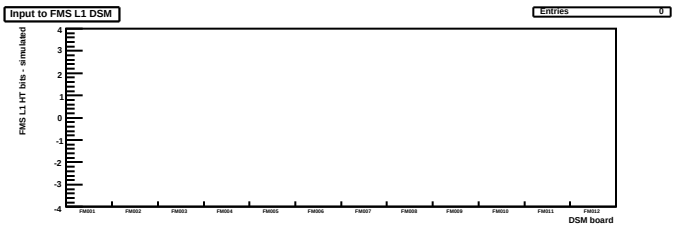

Input to FMS L1 DSM
Entries 7901808
sumAB
DSM board

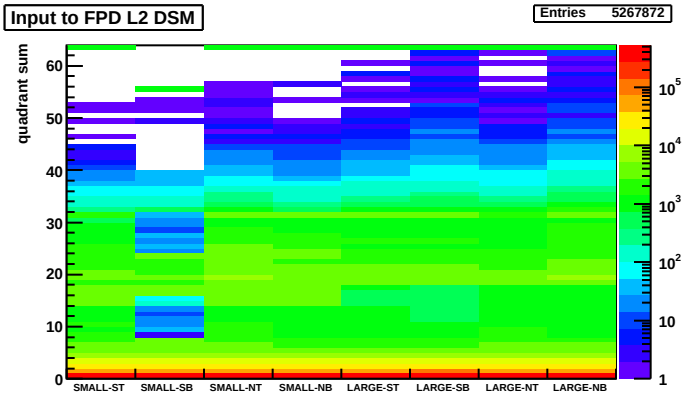

Input to FMS L1 DSM
Entries 20222
sumAB - simulated
DSM board

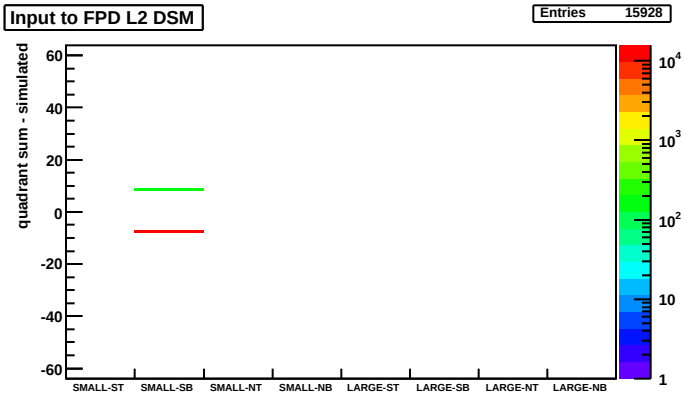

Input to FMS L1 DSM
Entries 7901808
sumA
DSM board

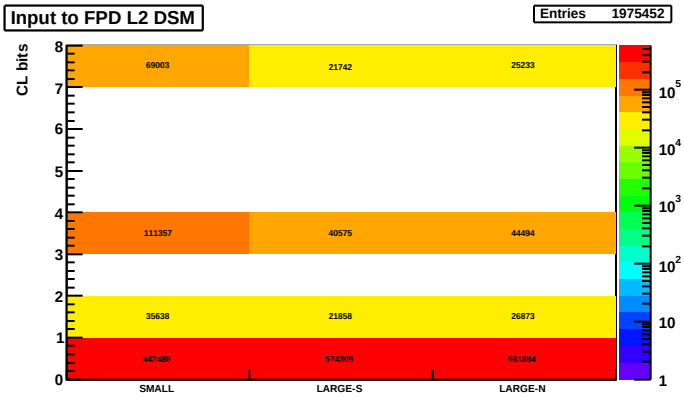

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

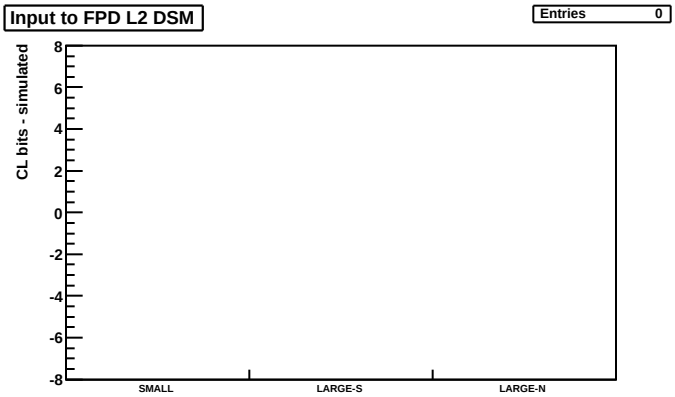

Input to FMS L1 DSM
Entries 7901808
FMS L1 HT bits
DSM board

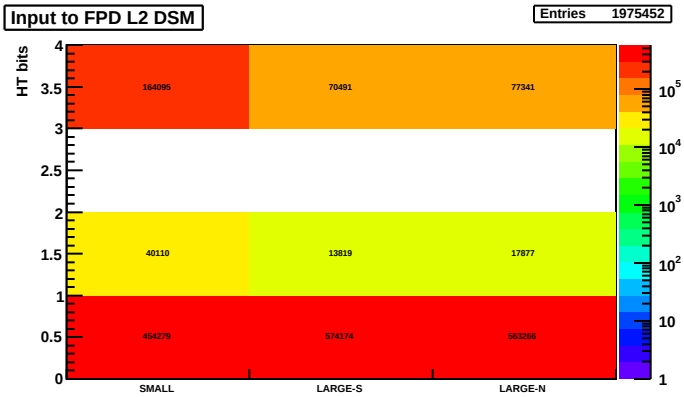

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

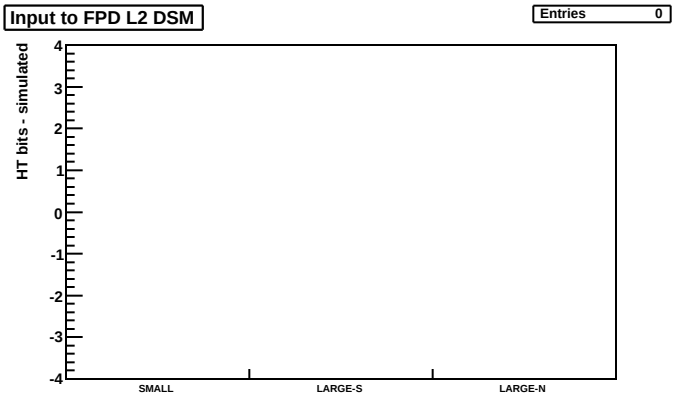

Input to FPD L2 DSM
Entries 5267872
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

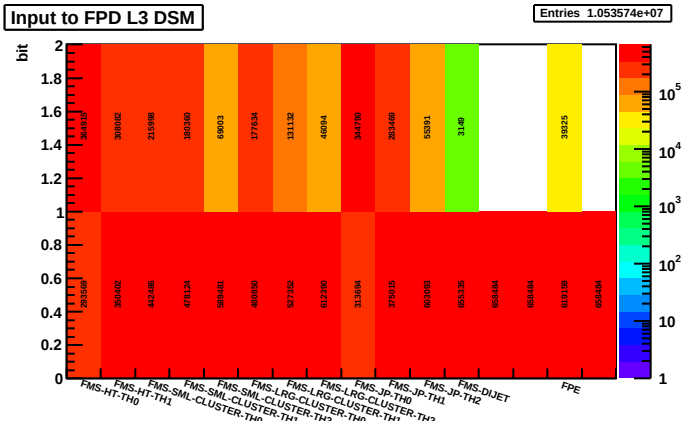

Input to FPD L2 DSM
Entries 15928
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

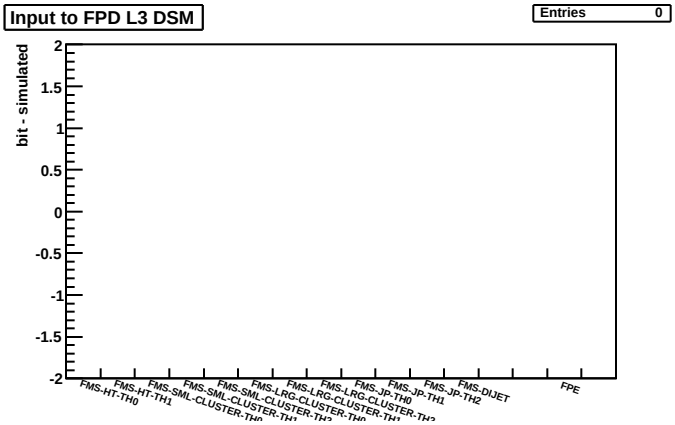

Input to FPD L2 DSM
Entries 1975452
CL bits
69003
21742
25233
111357
40575
44494
35638
21858
26873
442486
574309
561884
SMALL
LARGE-S
LARGE-N

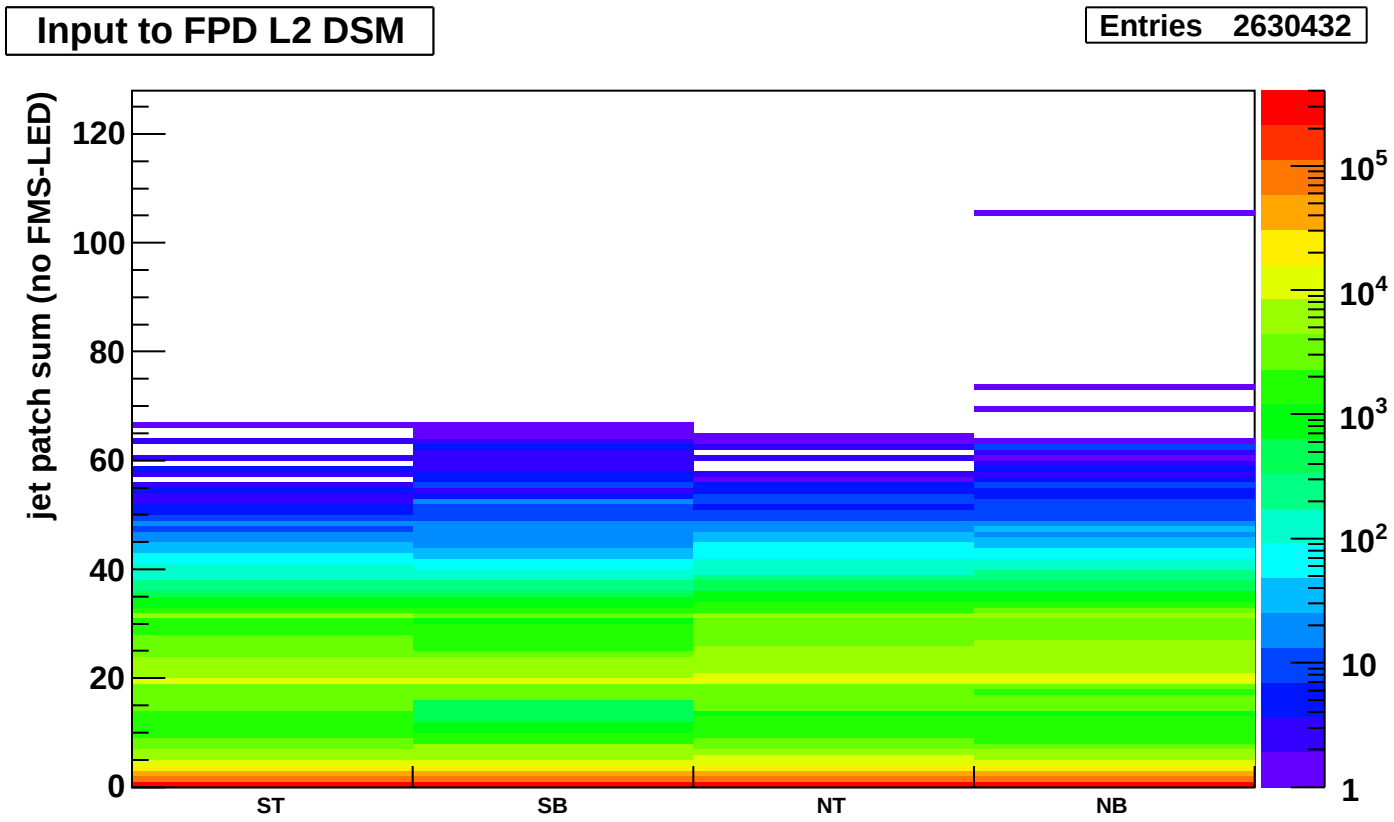

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

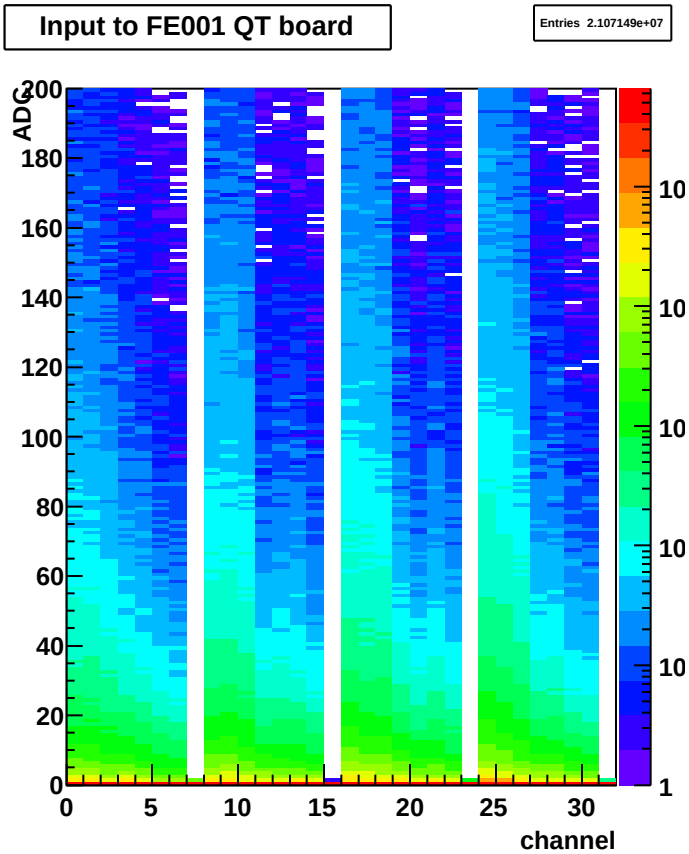

Input to FPD L2 DSM
Entries 1975452
HT bits
164095
70491
77341
40110
13819
17877
454279
574174
563266
SMALL
LARGE-S
LARGE-N

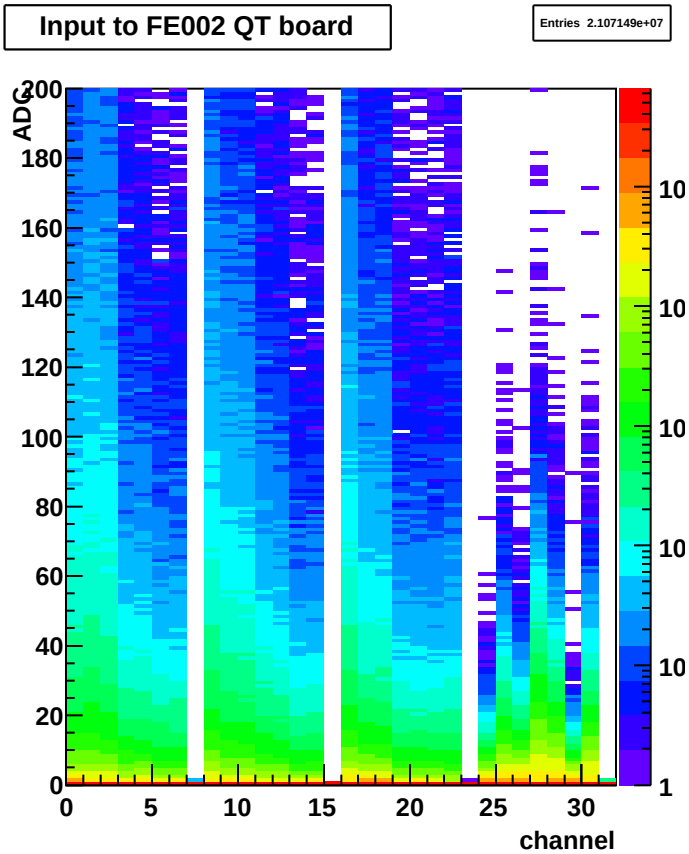

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

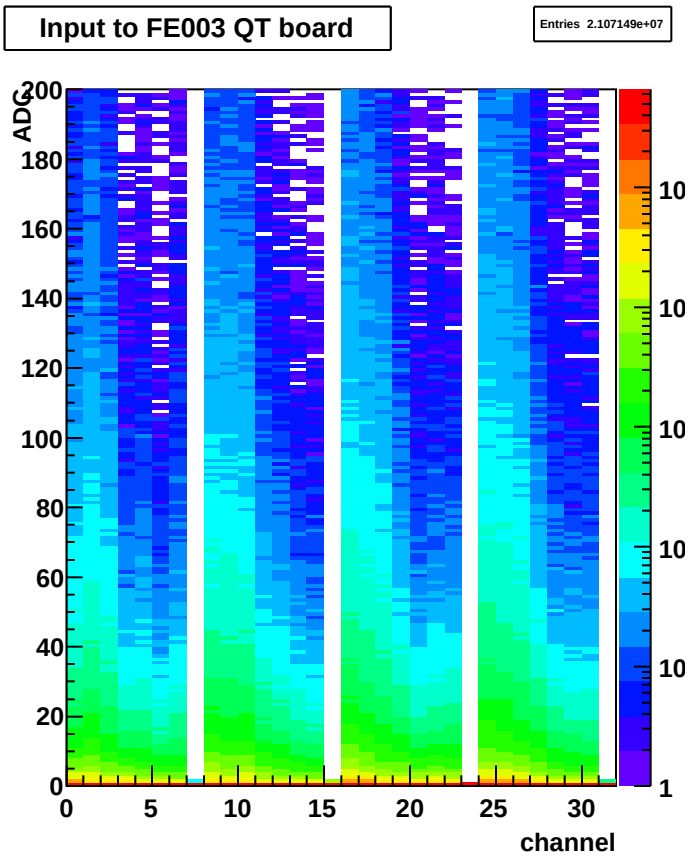

Input to FPD L3 DSM
Entries 1.053574e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

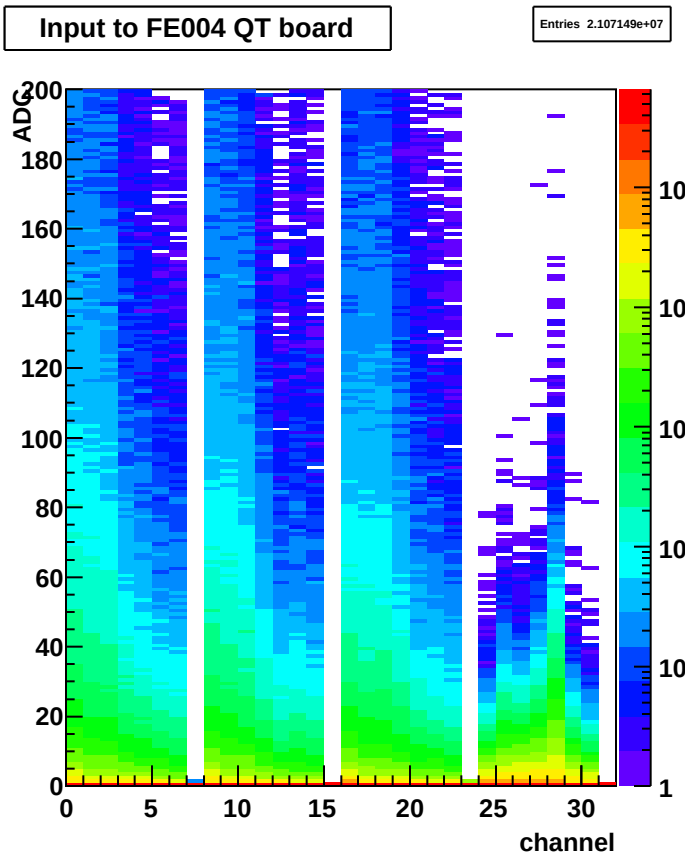

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

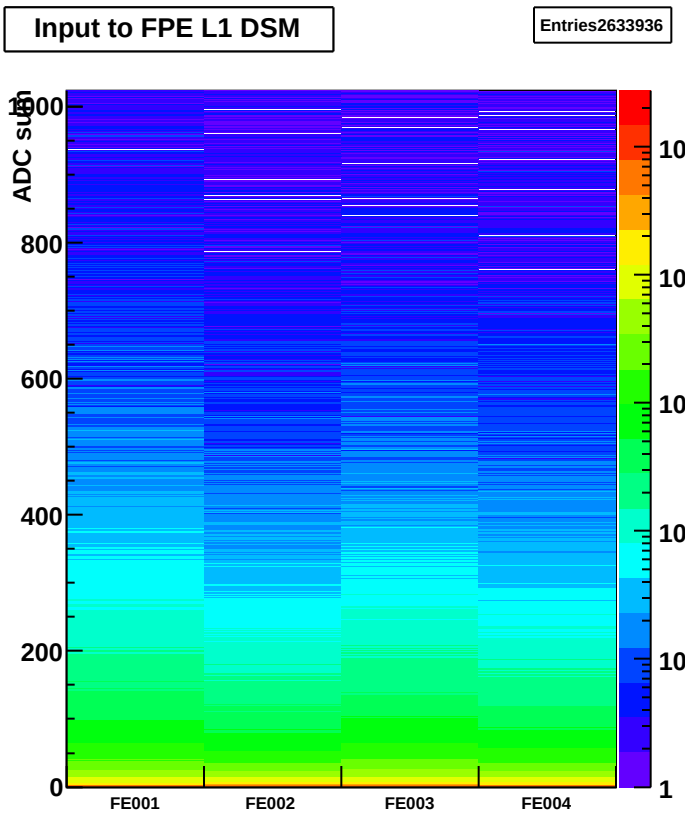

Input to FPD L2 DSM
Entries 2630432
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

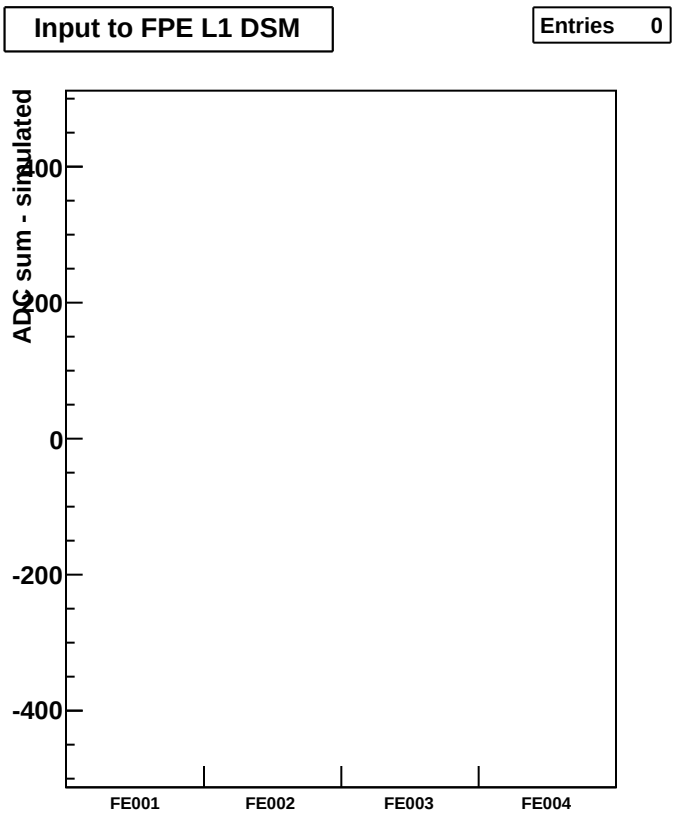

Input to FE001 QT board
Entries 2.107149e+07
ADC
channel

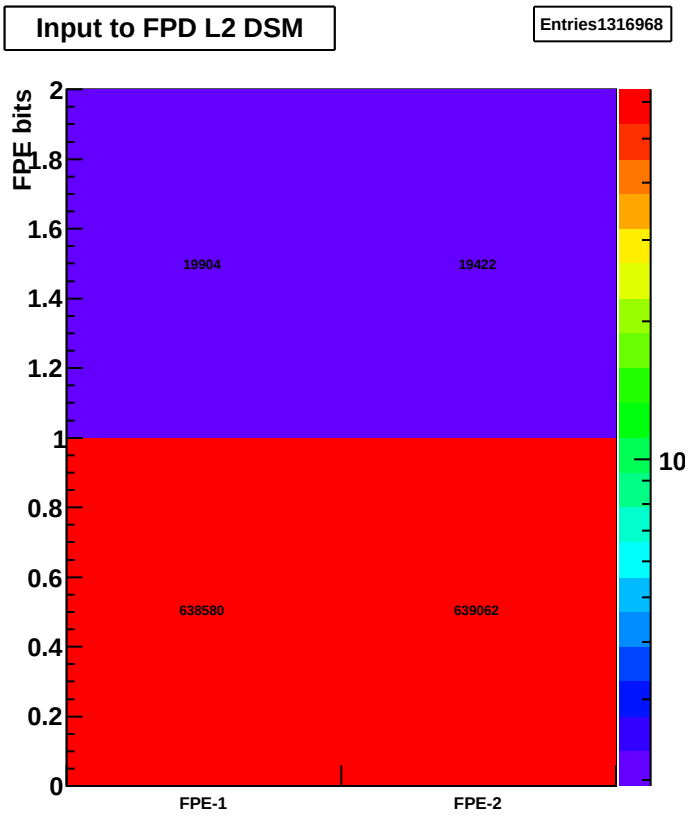

Input to FE002 QT board
Entries 2.107149e+07
ADC
channel

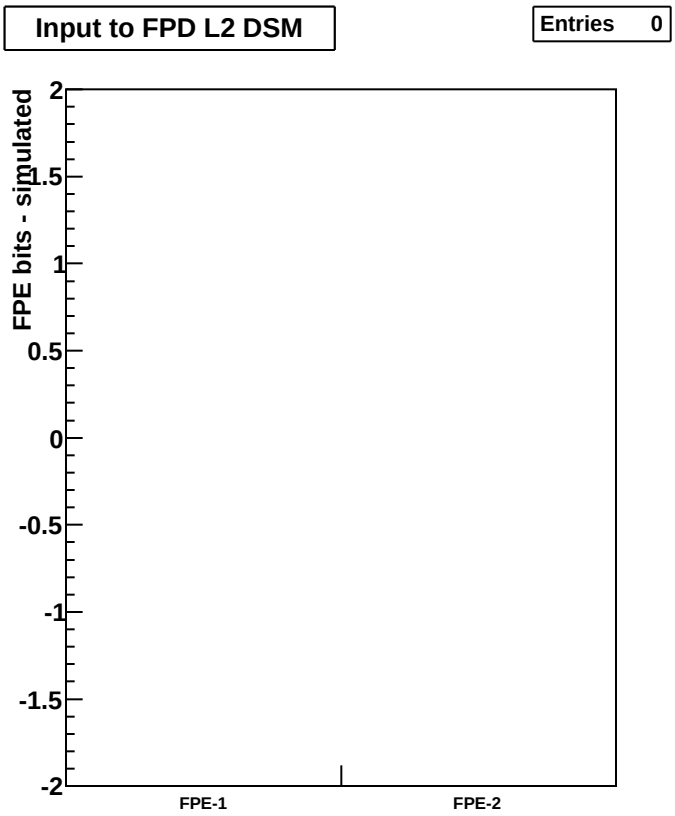

Input to FE003 QT board
Entries 2.107149e+07
ADC
channel


Input to FE004 QT board
Entries 2.107149e+07
ADC
channel


Input to FPE L1 DSM
Entries2633936
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1316968
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
19904
19422
638580
639062
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2