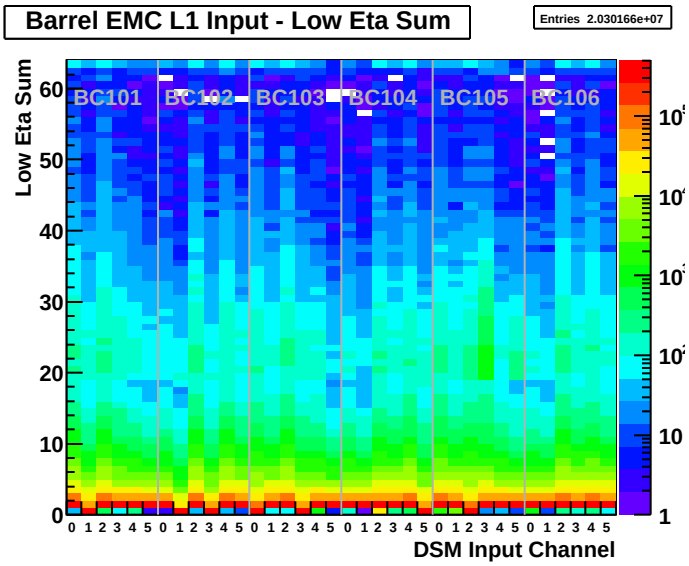
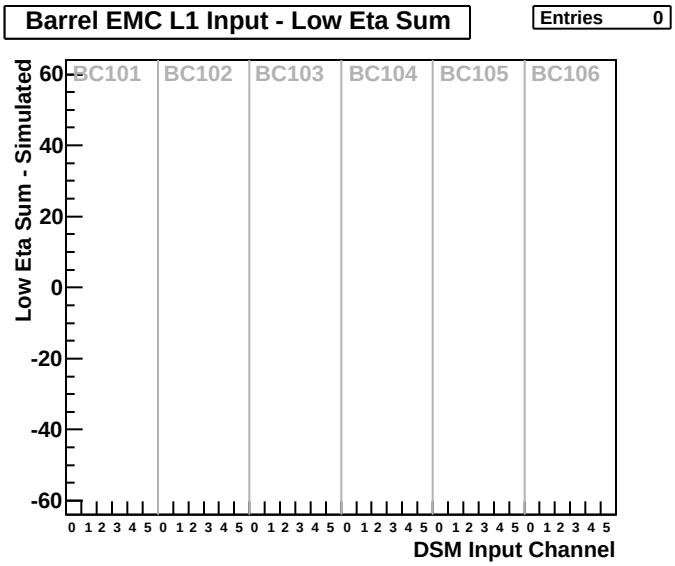
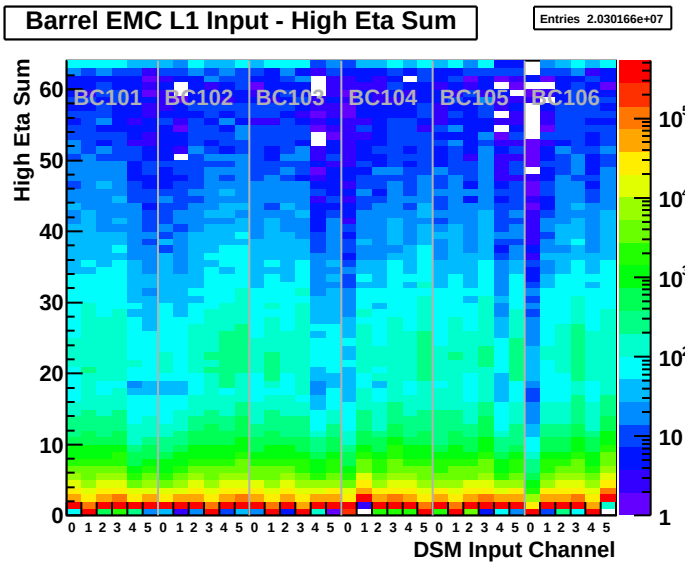
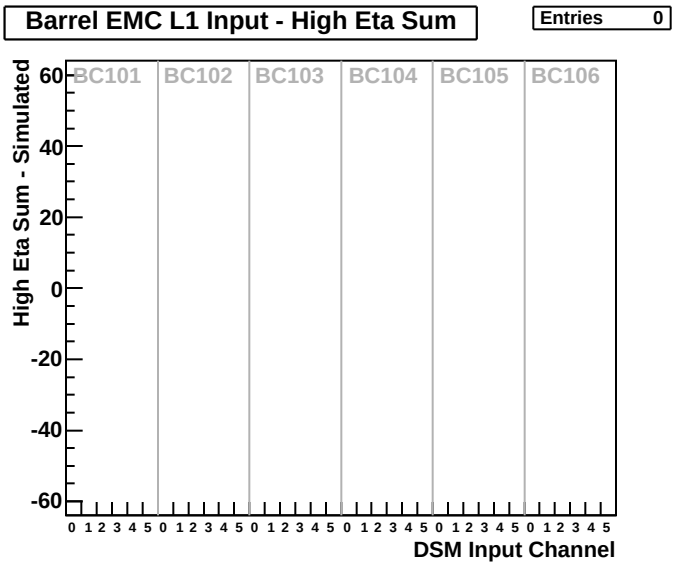
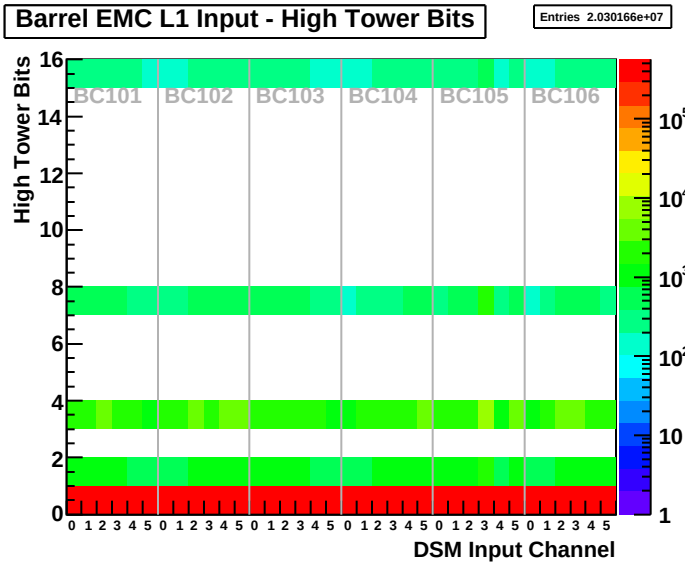
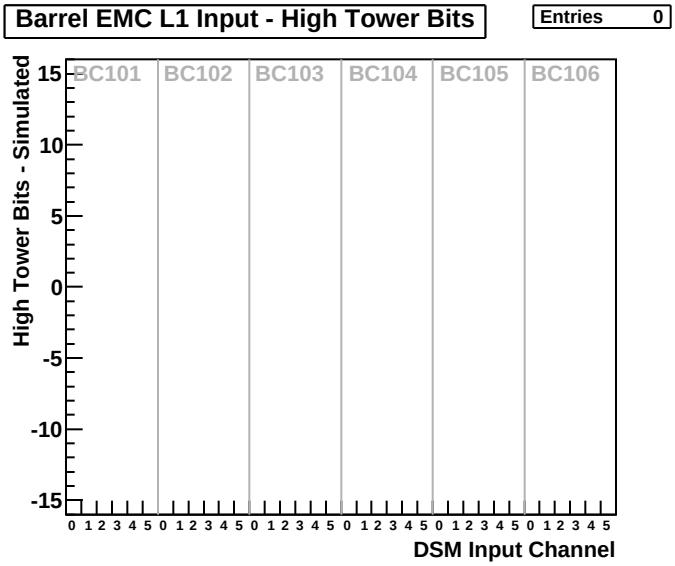

Barrel EMC L1 Input - Low Eta Sum
Entries 2.030166e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

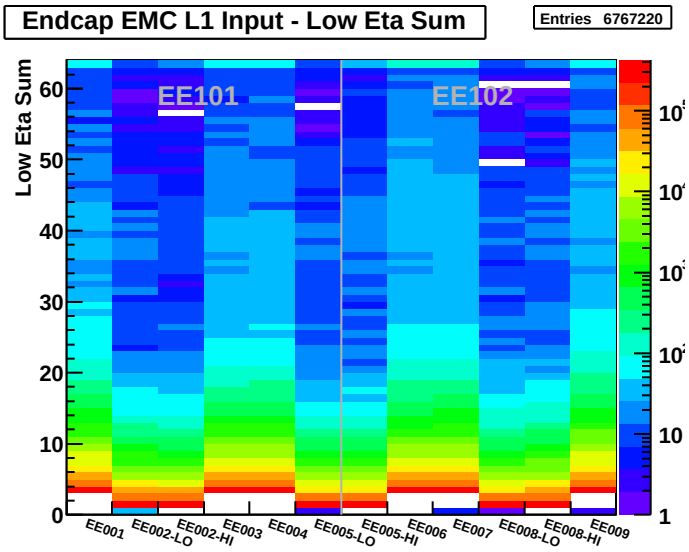

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

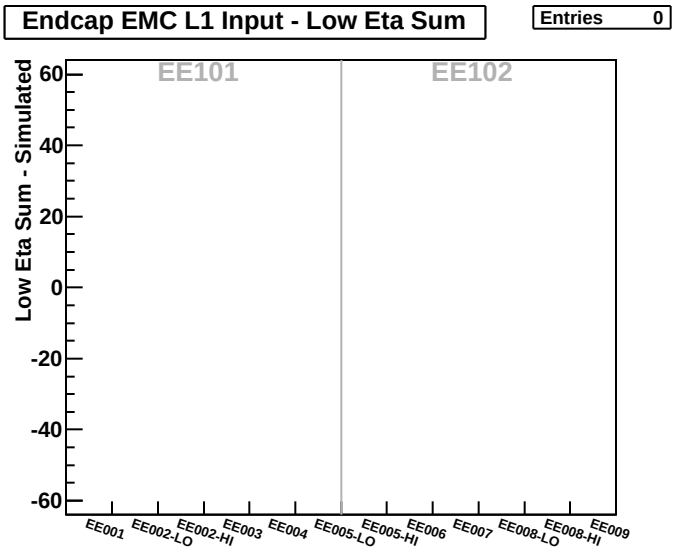

Barrel EMC L1 Input - High Eta Sum
Entries 2.030166e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

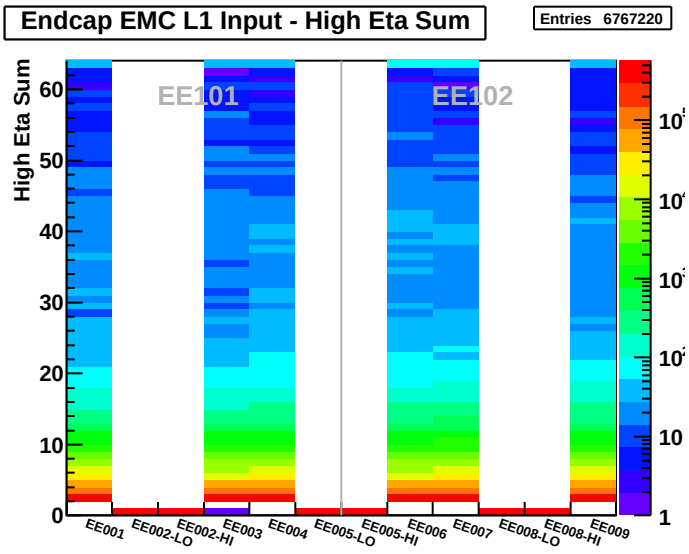

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

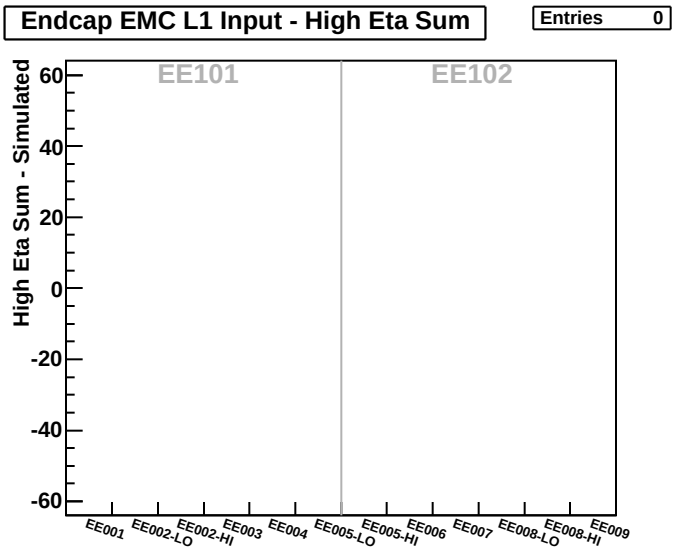

Barrel EMC L1 Input - High Tower Bits
Entries 2.030166e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

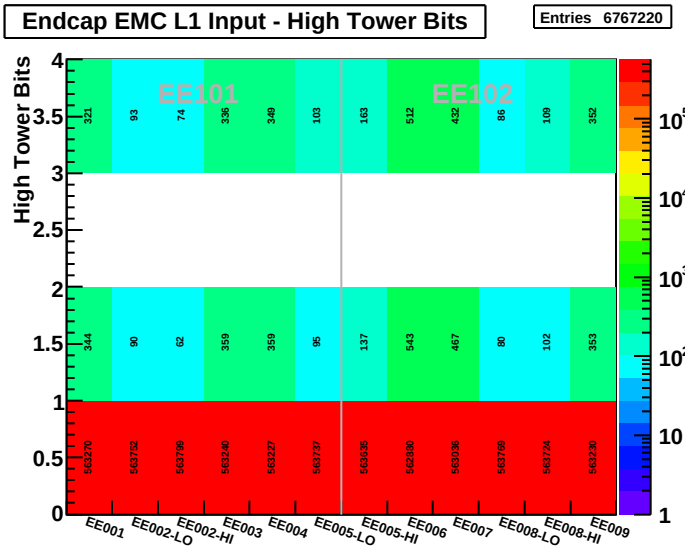

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

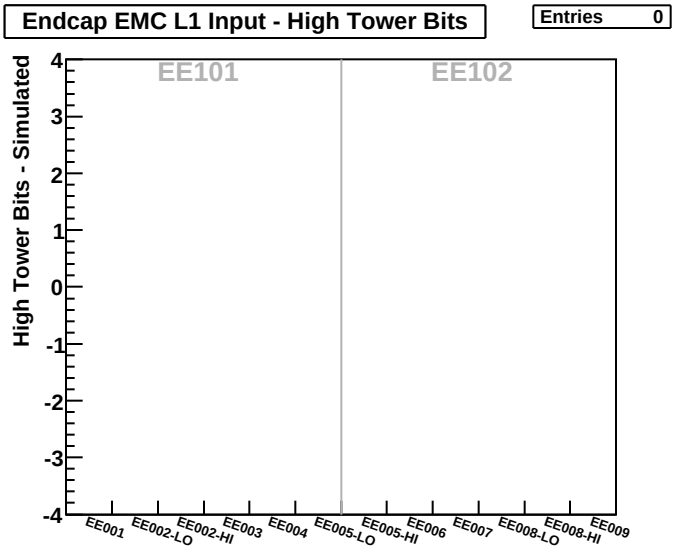

Endcap EMC L1 Input - Low Eta Sum
Entries 6767220
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

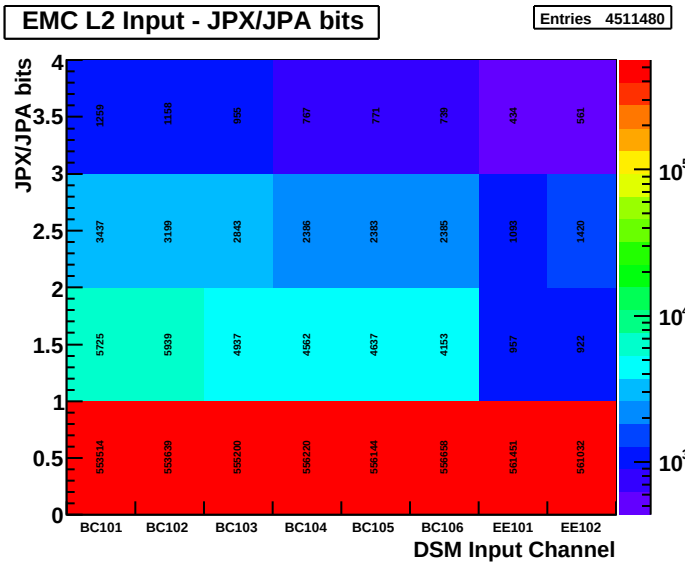

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

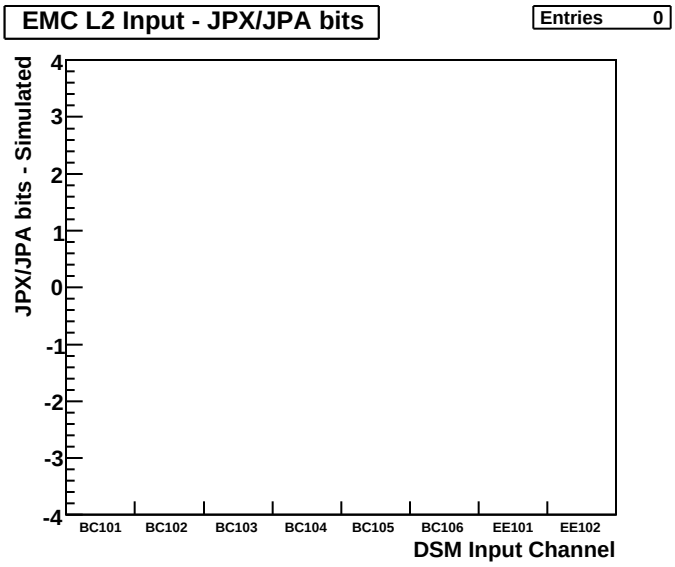

Endcap EMC L1 Input - High Eta Sum
Entries 6767220
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

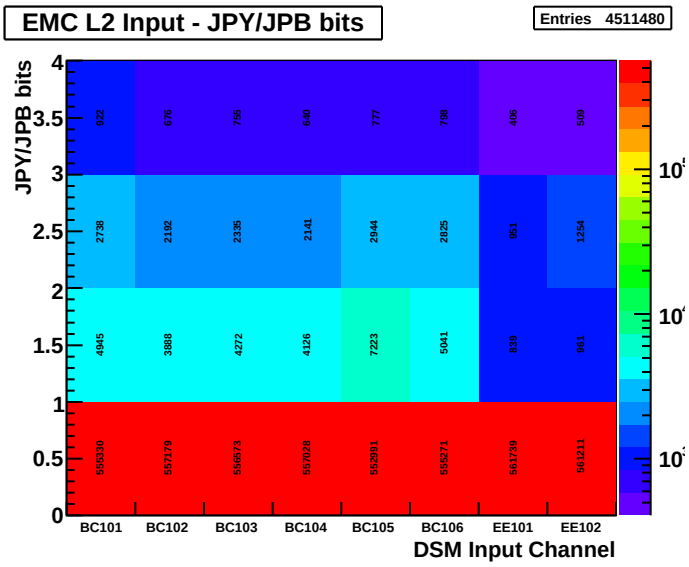

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

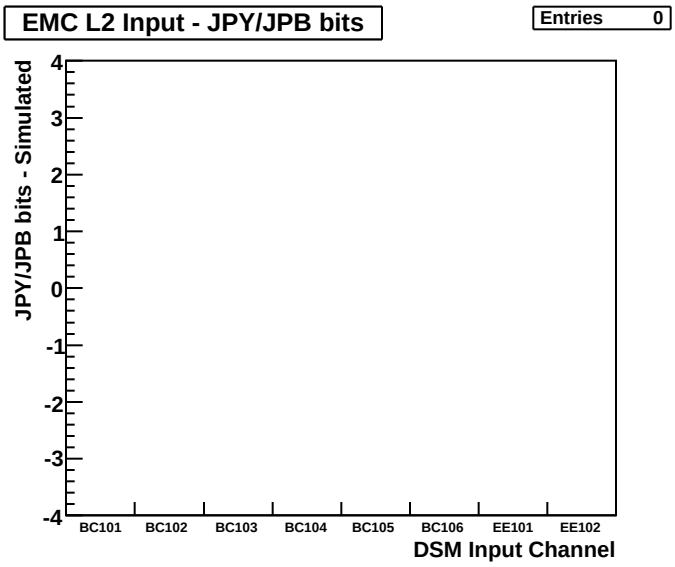

Endcap EMC L1 Input - High Tower Bits
Entries 6767220
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

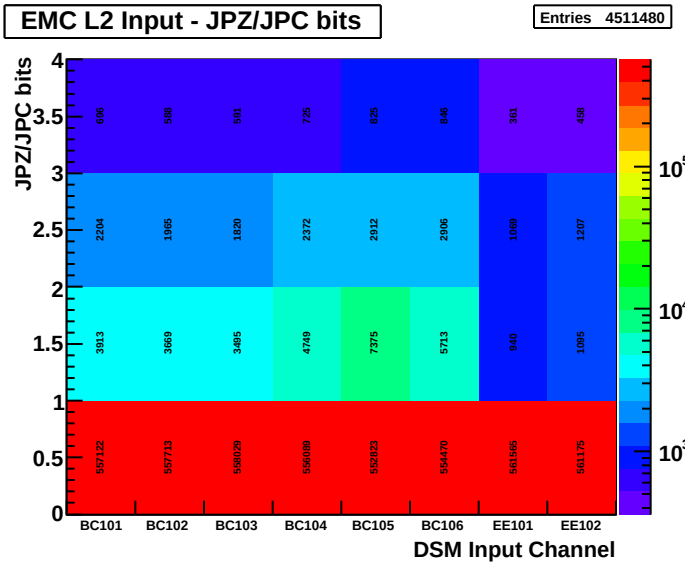

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

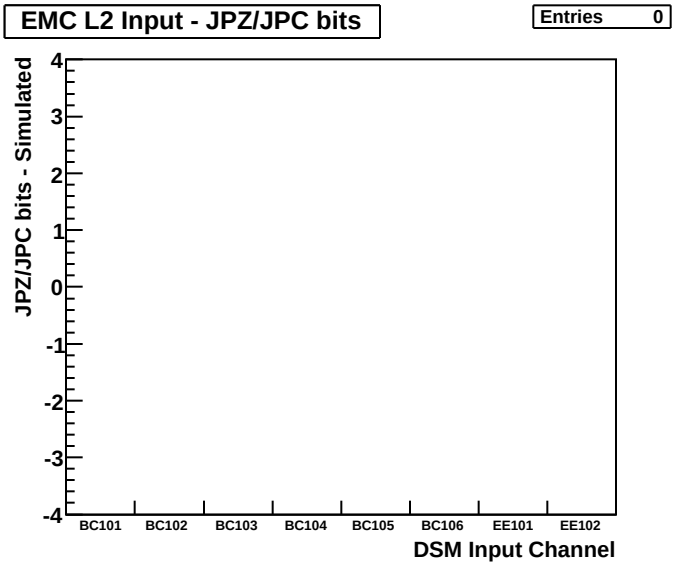

EMC L2 Input - JPX/JPA bits
Entries 4511480
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1259 1158 955 767 771 739 434 561
3437 3199 2843 2386 2383 2385 1093 1420
5725 5839 4937 4562 4637 4153 957 922
553514 553639 558200 558220 556144 556658 561451 561032

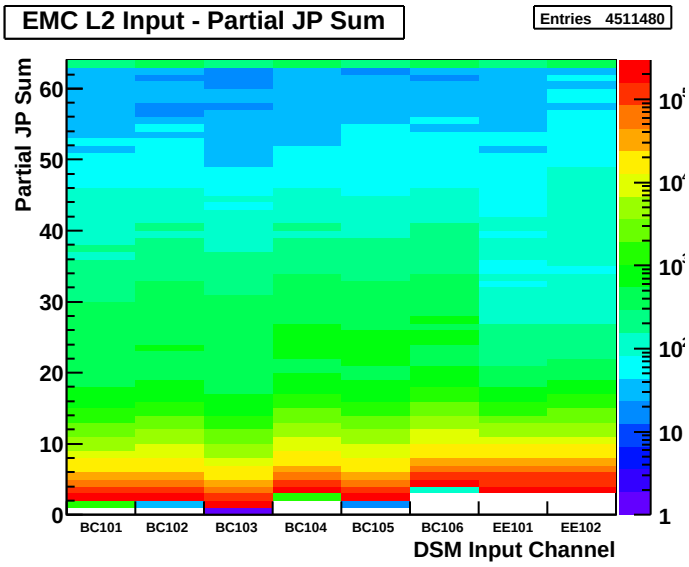

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

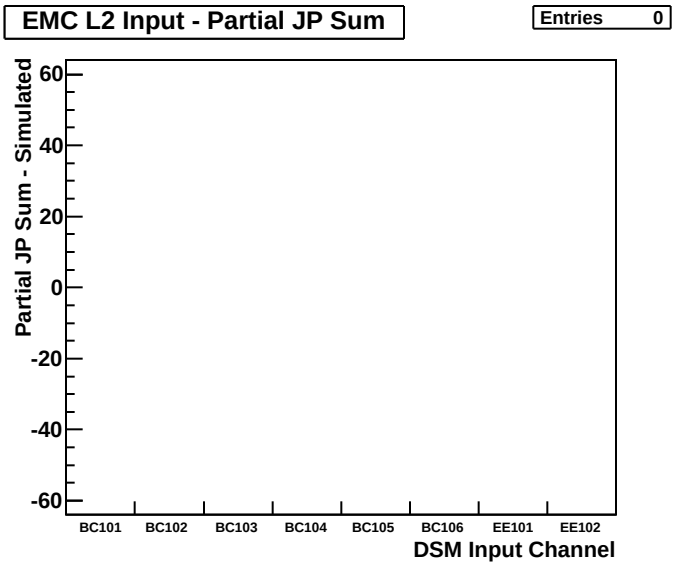

EMC L2 Input - JPY/JPB bits
Entries 4511480
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
922 676 755 640 777 786 406 509
2738 2192 2335 2141 2944 2825 951 1254
4945 3888 4272 4126 7223 5041 839 961
555330 557179 556573 557028 552991 555271 561739 561211

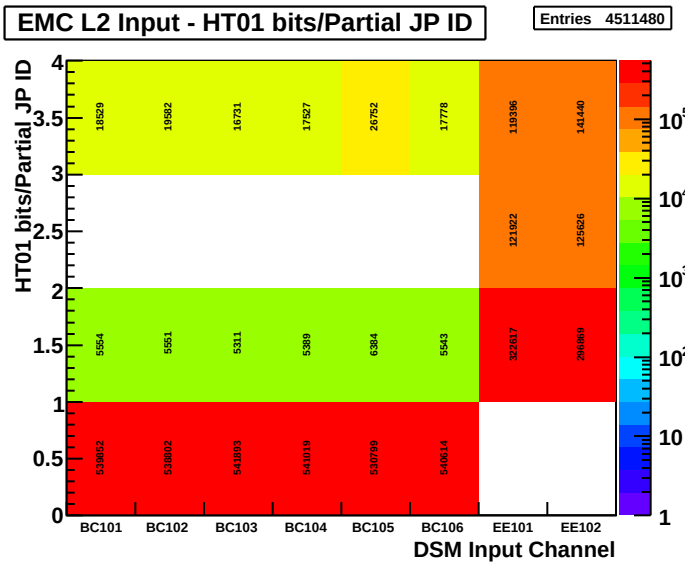

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

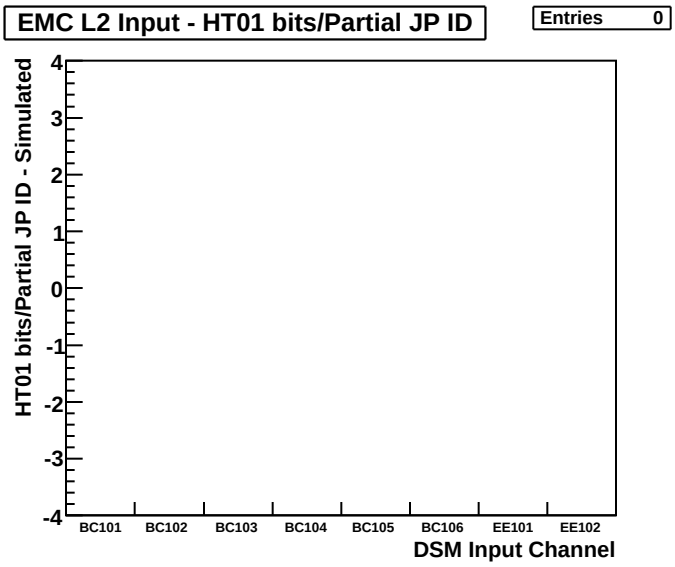

EMC L2 Input - JPZ/JPC bits
Entries 4511480
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
686 588 591 725 825 846 361 458
2204 1965 1820 2372 2912 2906 1069 1207
3913 3669 3495 4749 7375 5713 940 1095
557122 557713 558029 556089 552923 554470 561565 561175

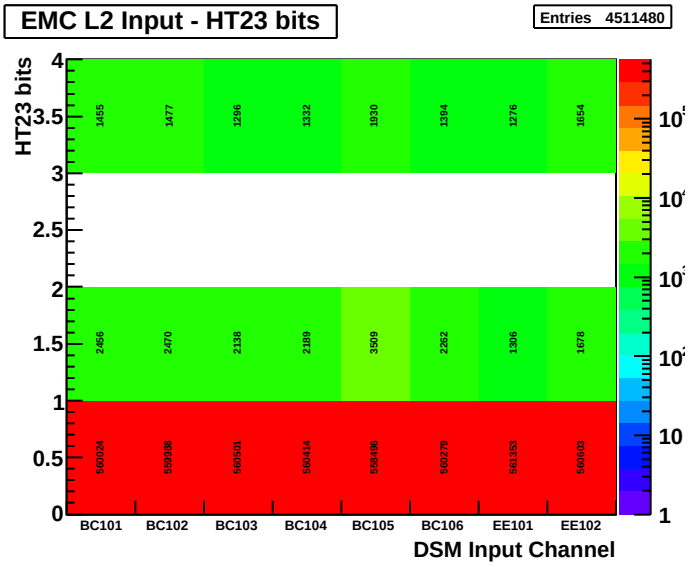

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

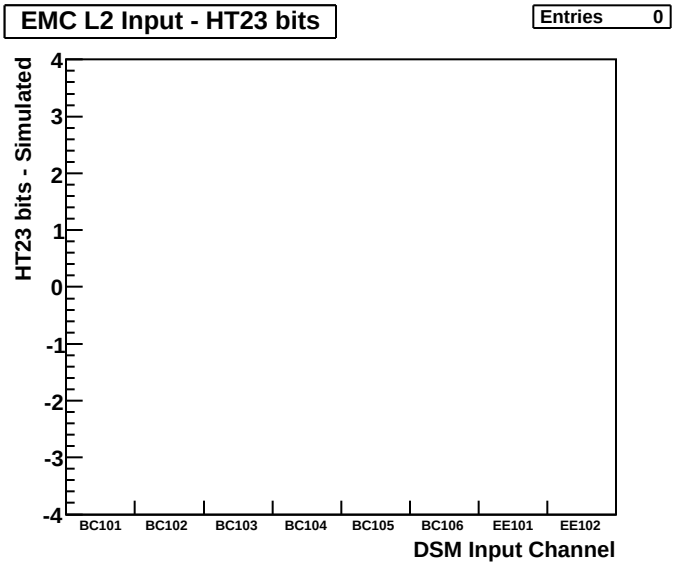

EMC L2 Input - Partial JP Sum
Entries 4511480
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

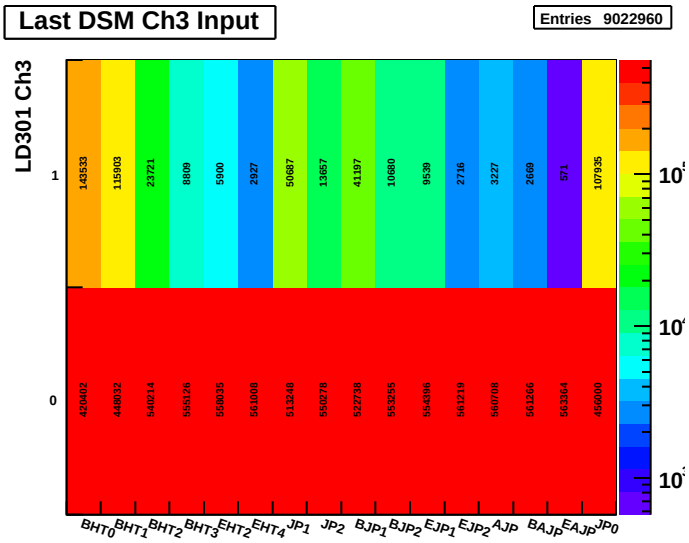

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

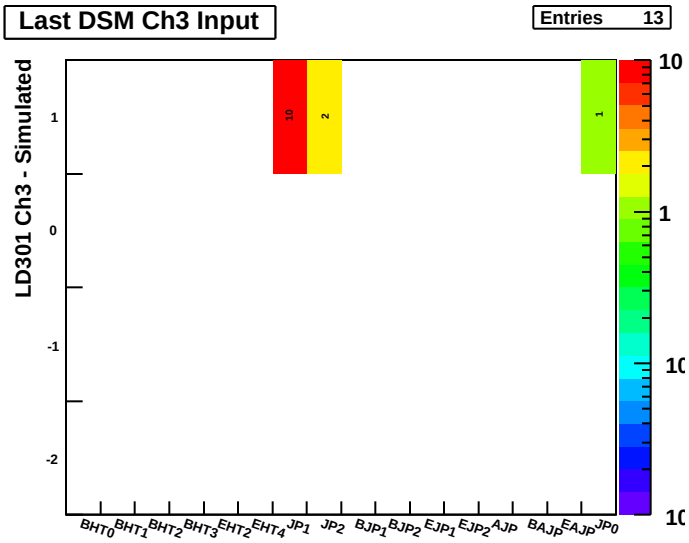

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4511480
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

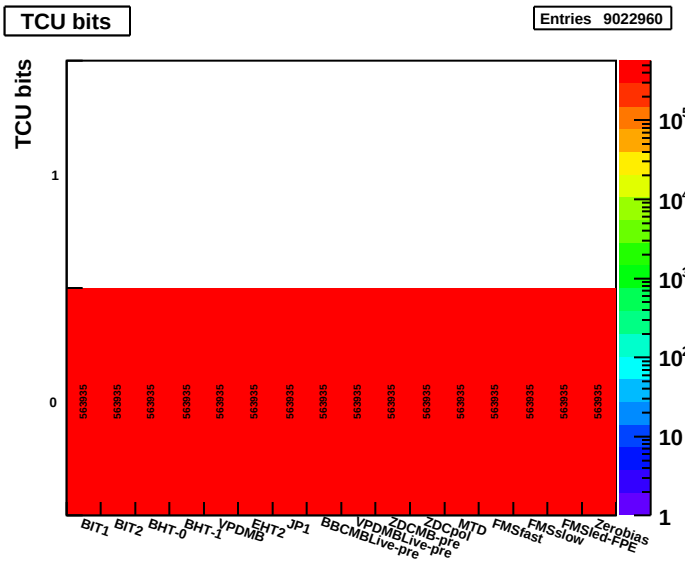

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

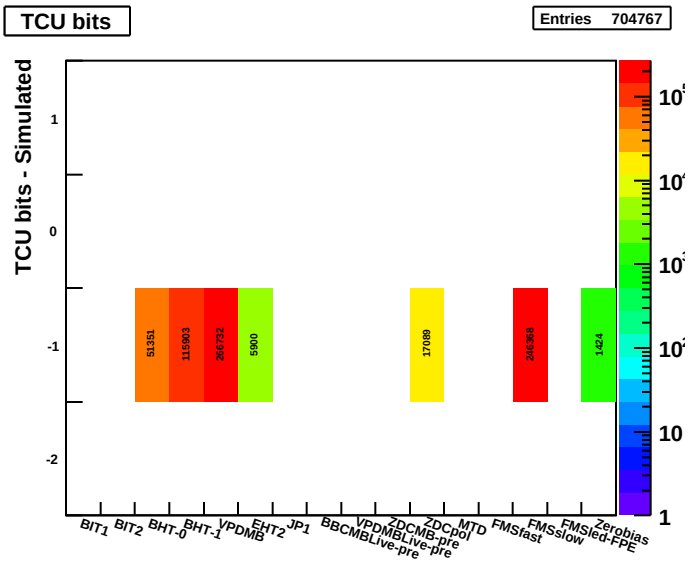

EMC L2 Input - HT23 bits
Entries 4511480
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

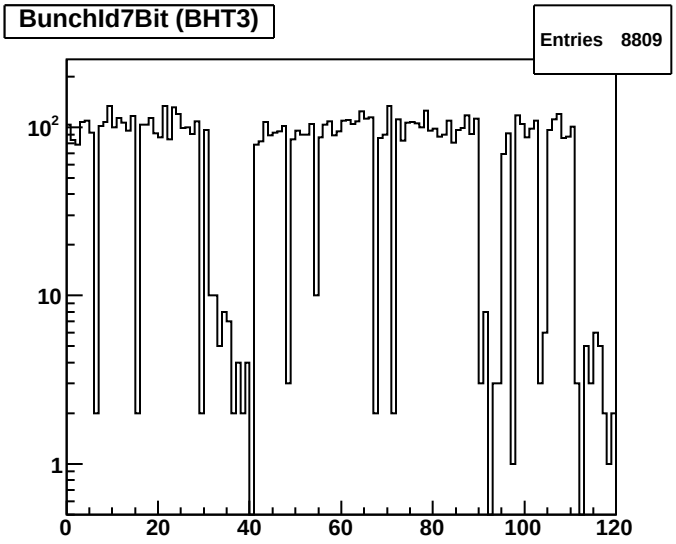

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

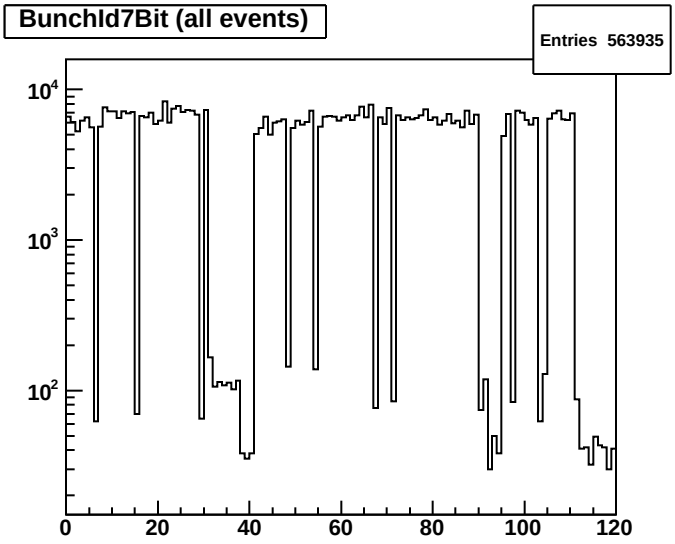

Last DSM Ch3 Input
Entries 9022960
LD301 Ch3
143533 115903 23721 8809 5900 2927 50687 13657 41197 10680 9539 2716 3227 2669 571 107935
420402 448032 540214 555126 558035 561008 513248 550278 522738 553255 554396 561219 560708 561266 563354 456000
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

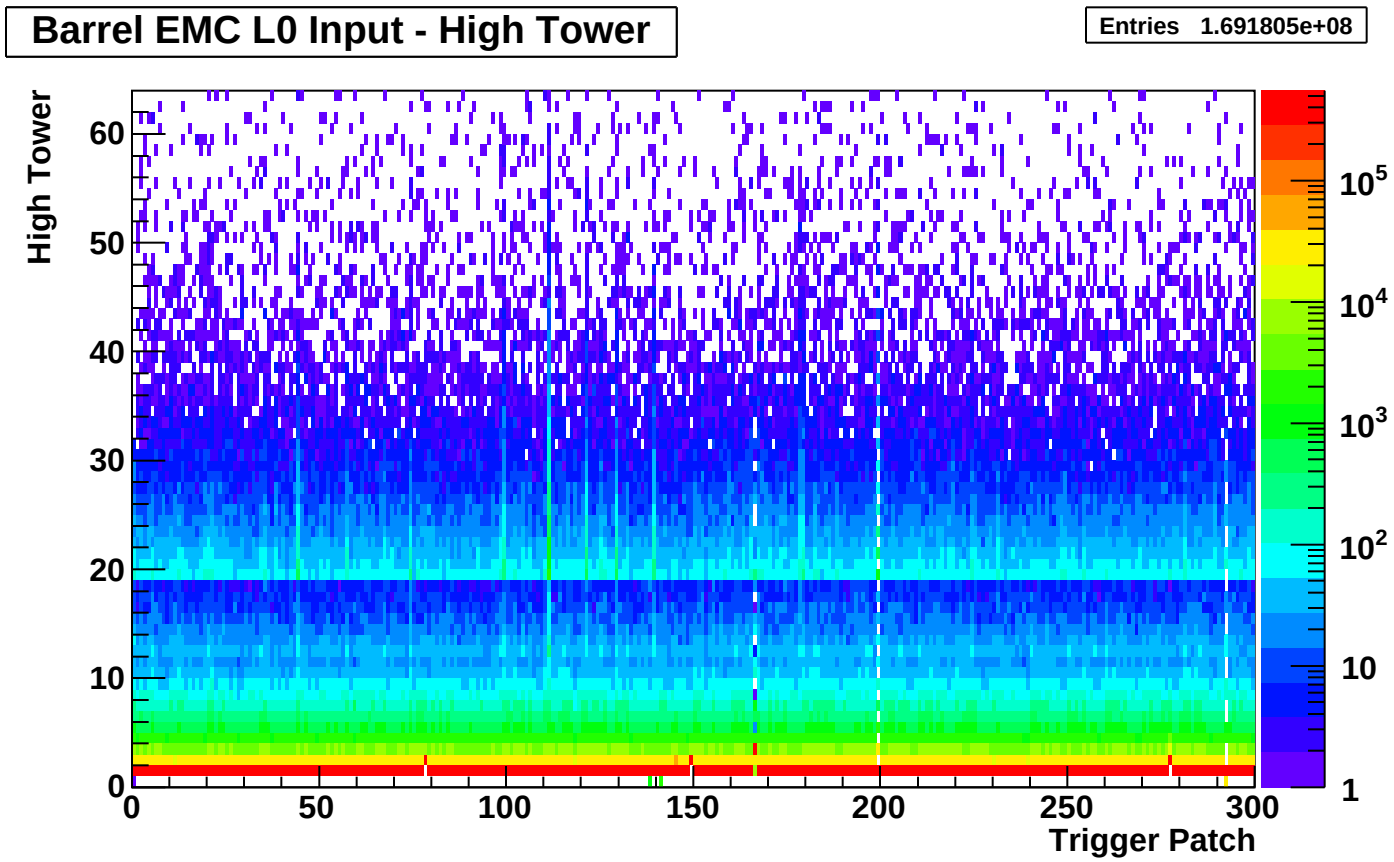

Last DSM Ch3 Input
Entries 13
LD301 Ch3 - Simulated
10 2 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

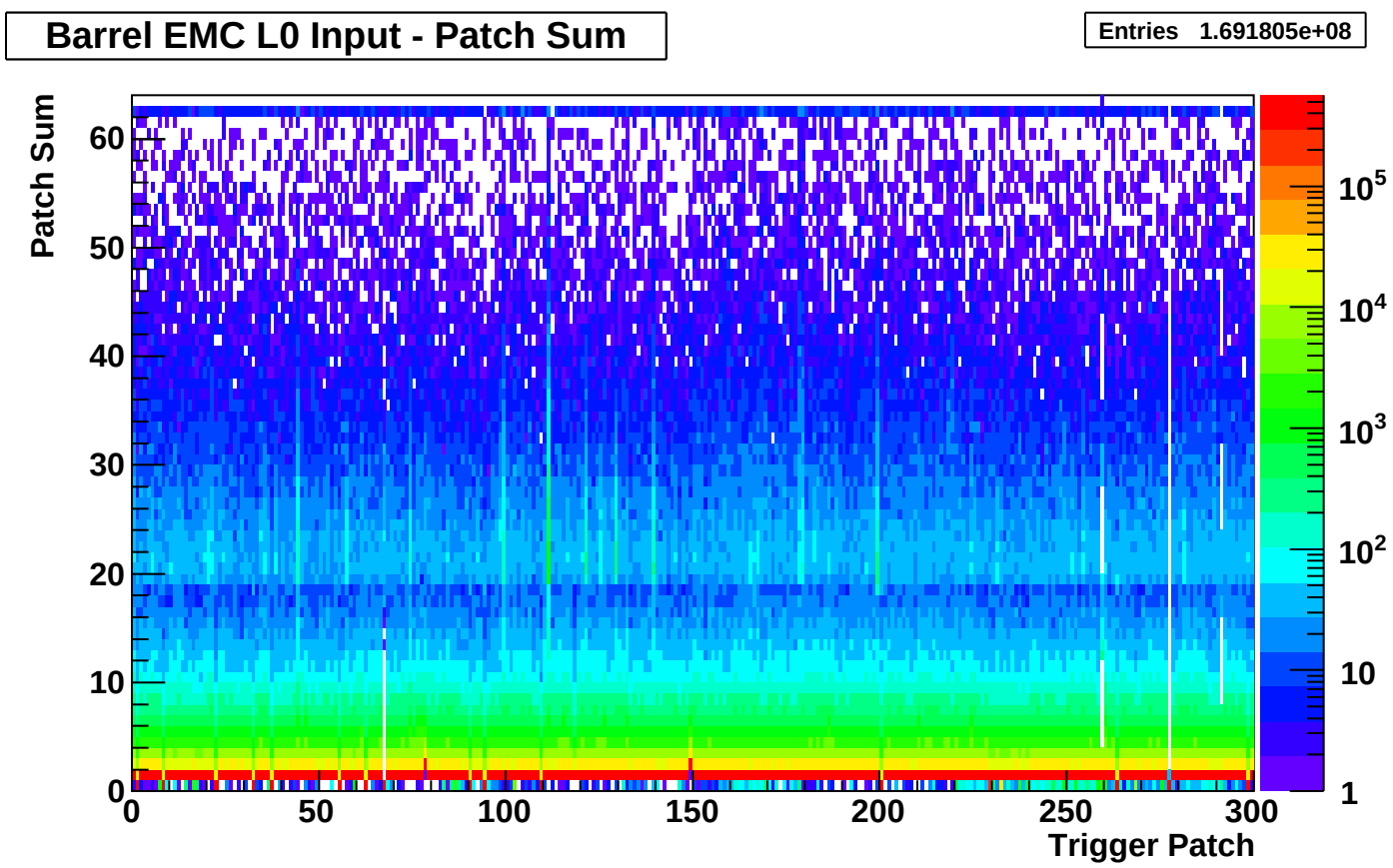

TCU bits
Entries 9022960
TCU bits
563935
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

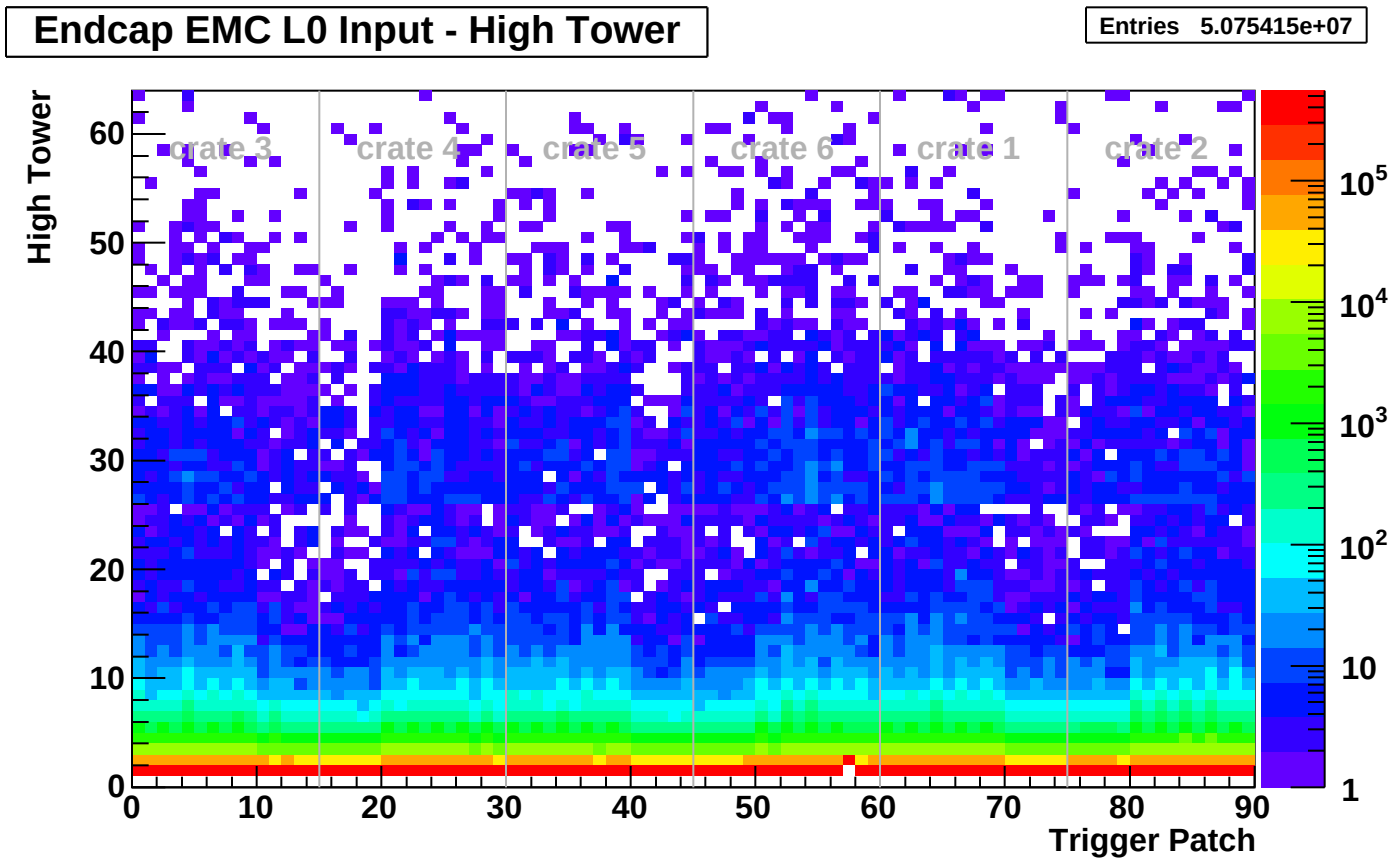

TCU bits
Entries 704767
TCU bits - Simulated
51351 115903 266732 5900 17089 246368 1424
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

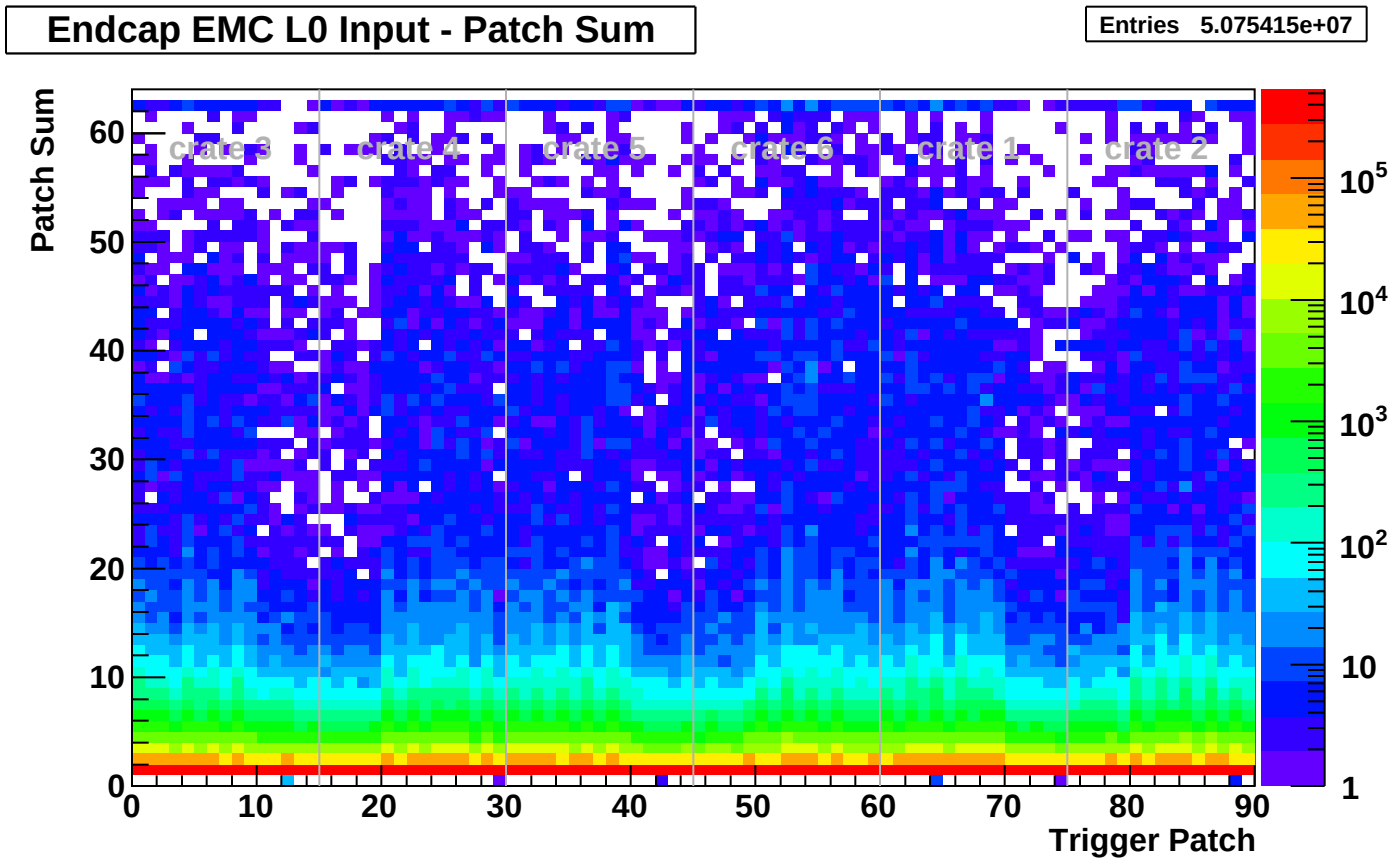

BunchId7Bit (BHT3)
Entries 8809

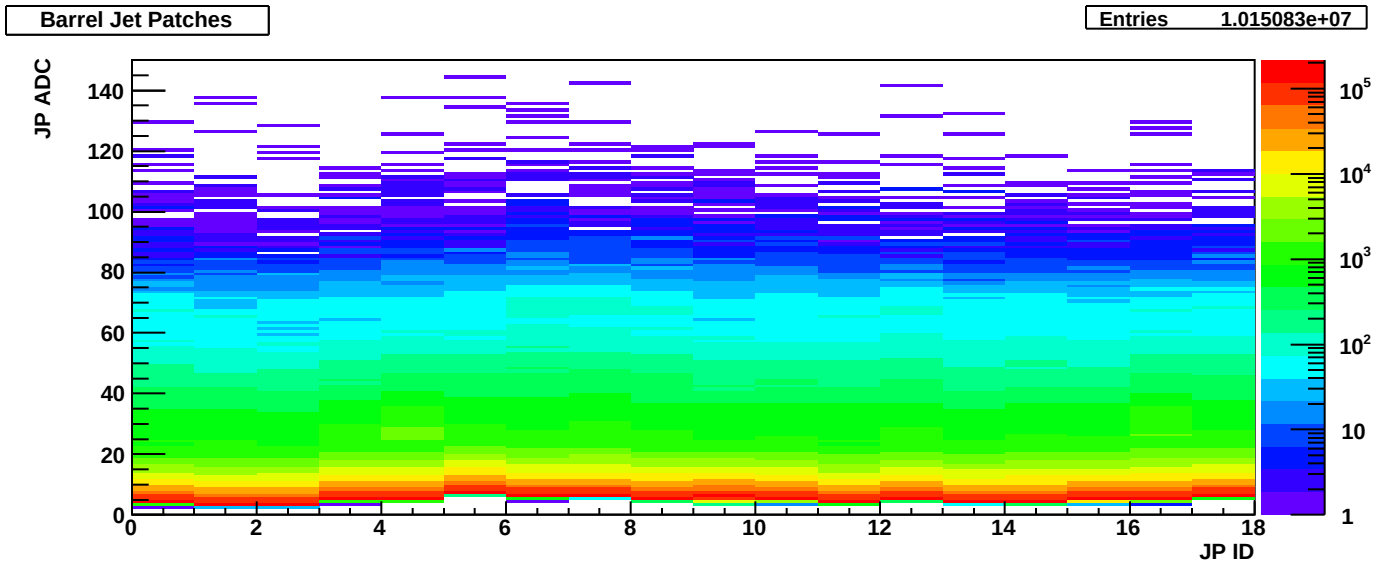

BunchId7Bit (all events)
Entries 563935

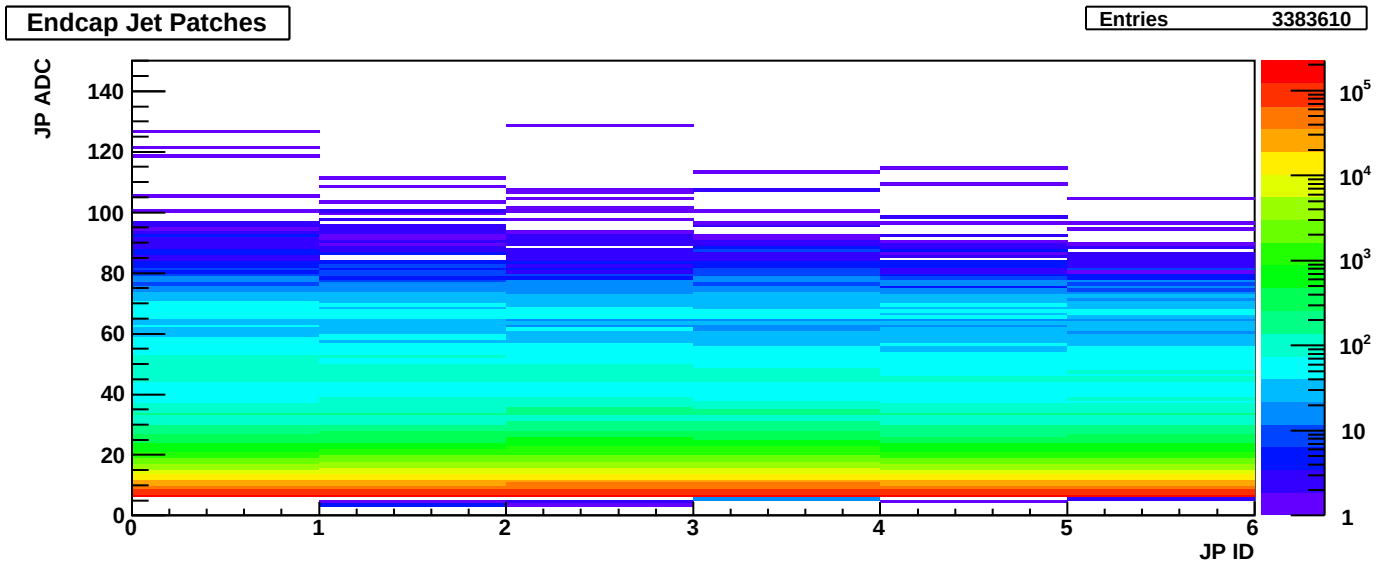

Barrel EMC L0 Input - High Tower
Entries 1.691805e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

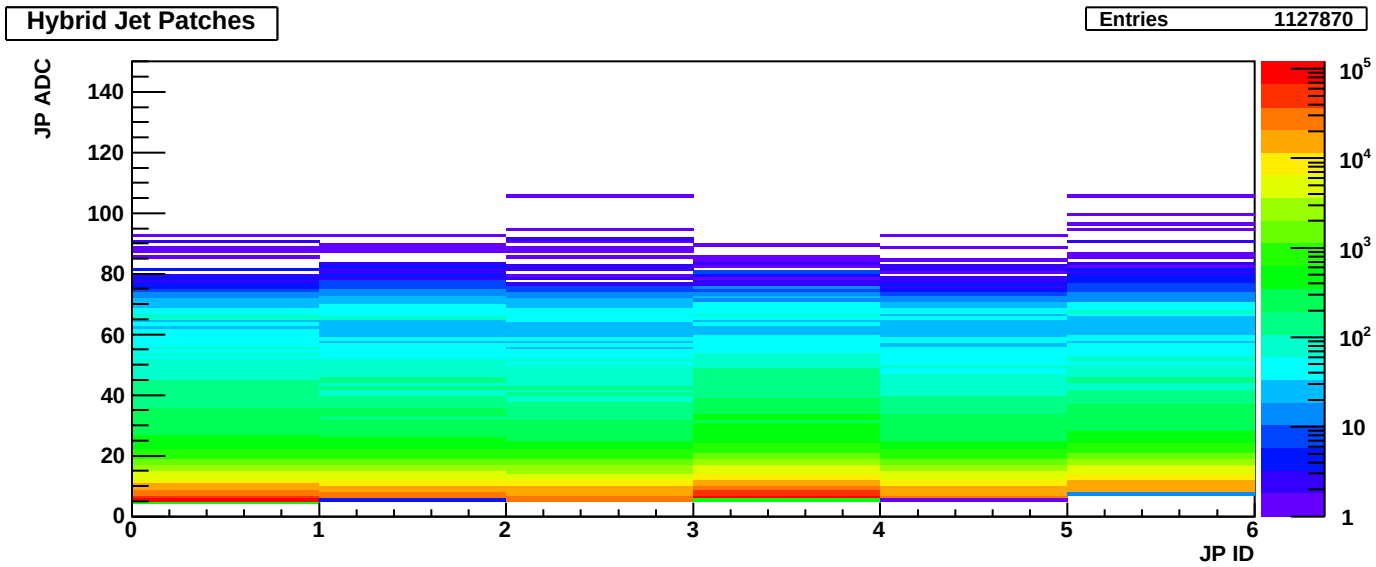

Barrel EMC L0 Input - Patch Sum
Entries 1.691805e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

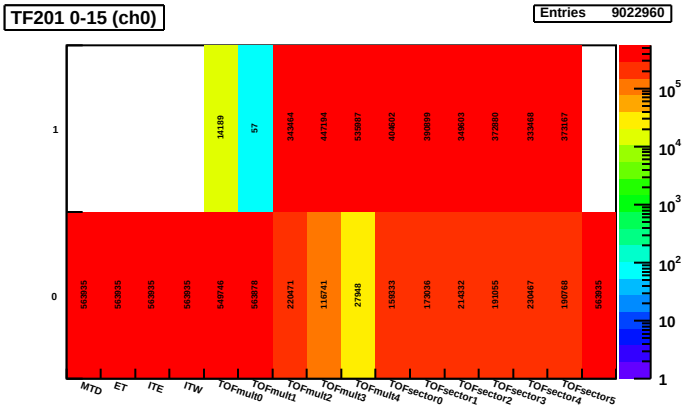

Endcap EMC L0 Input - High Tower
Entries 5.075415e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

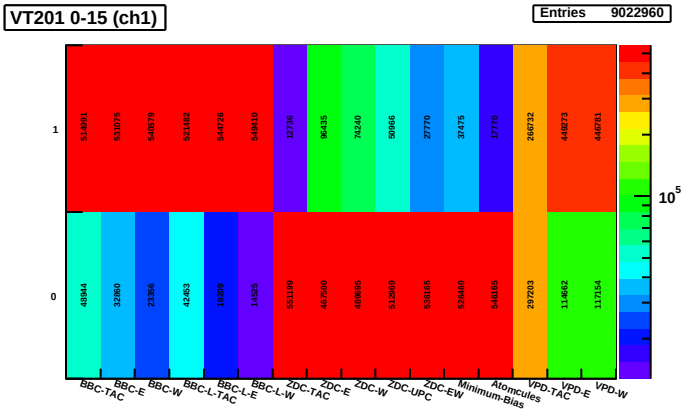

Endcap EMC L0 Input - Patch Sum
Entries 5.075415e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

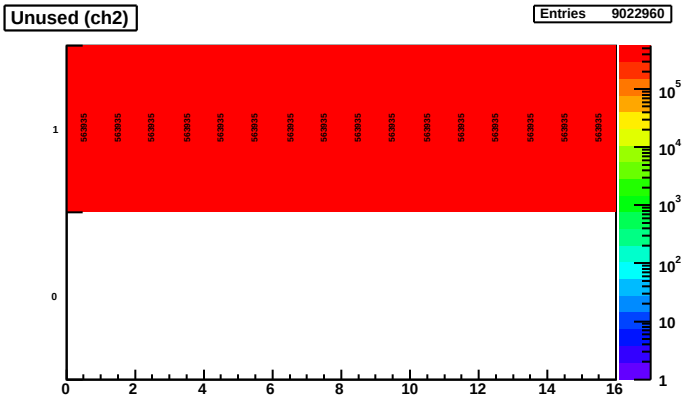

Barrel Jet Patches
Entries 1.015083e+07
JP ADC
JP ID

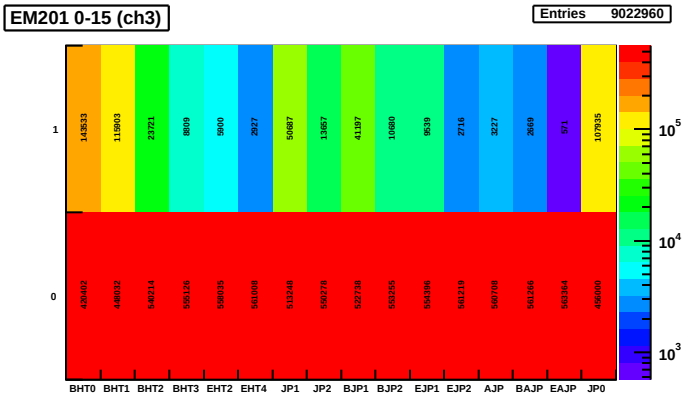

Endcap Jet Patches
Entries 3383610
JP ADC
JP ID

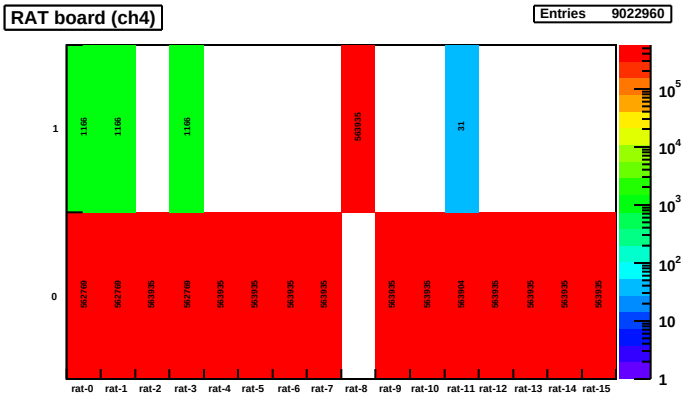

Hybrid Jet Patches
Entries 1127870
JP ADC
JP ID

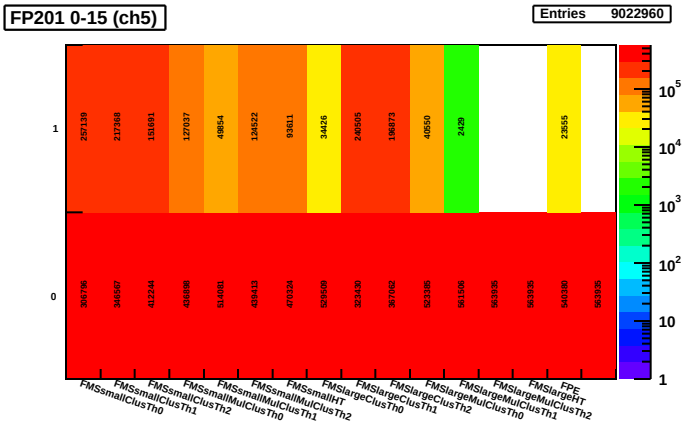

TF201 0-15 (ch0)
Entries 9022960
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

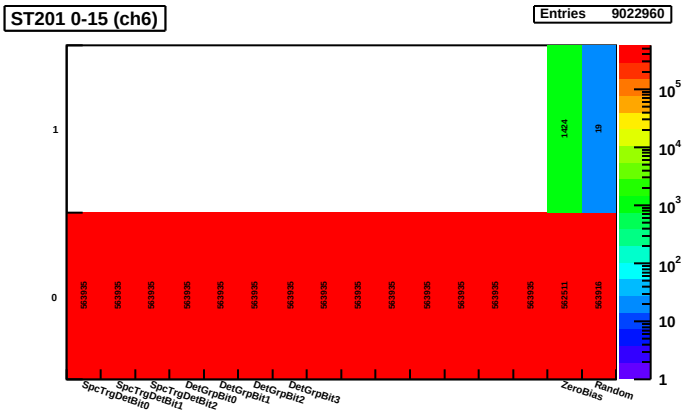

VT201 0-15 (ch1)
Entries 9022960
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

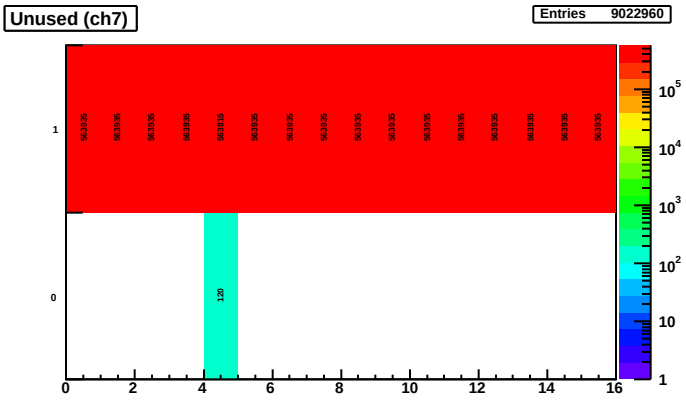

Unused (ch2)
Entries 9022960

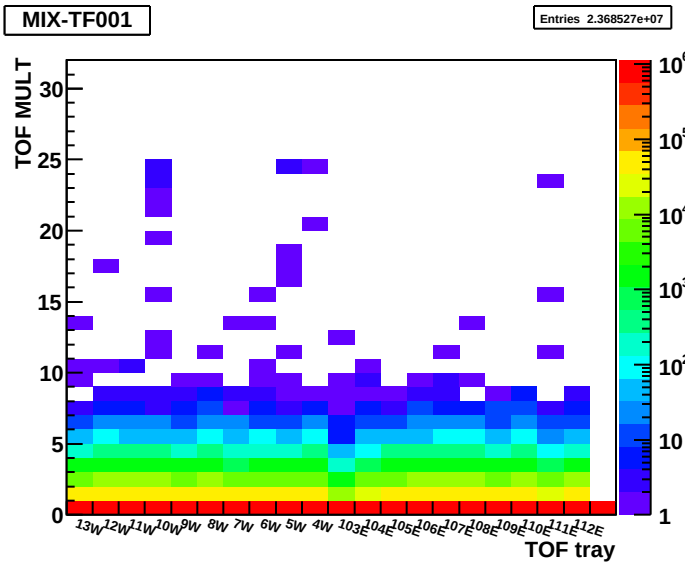

EM201 0-15 (ch3)
Entries 9022960
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

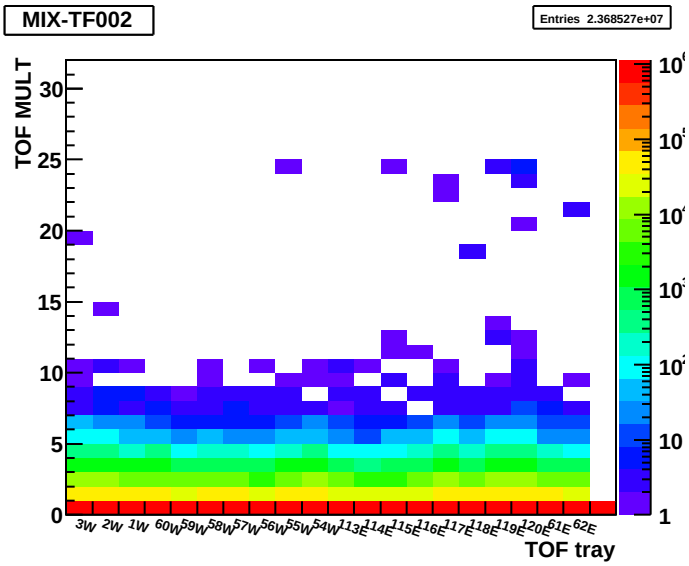

RAT board (ch4)
Entries 9022960
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

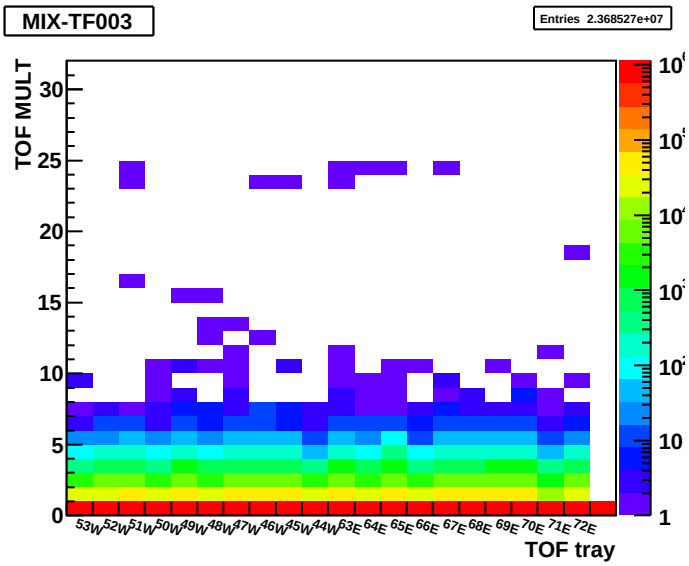

FP201 0-15 (ch5)
Entries 9022960
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

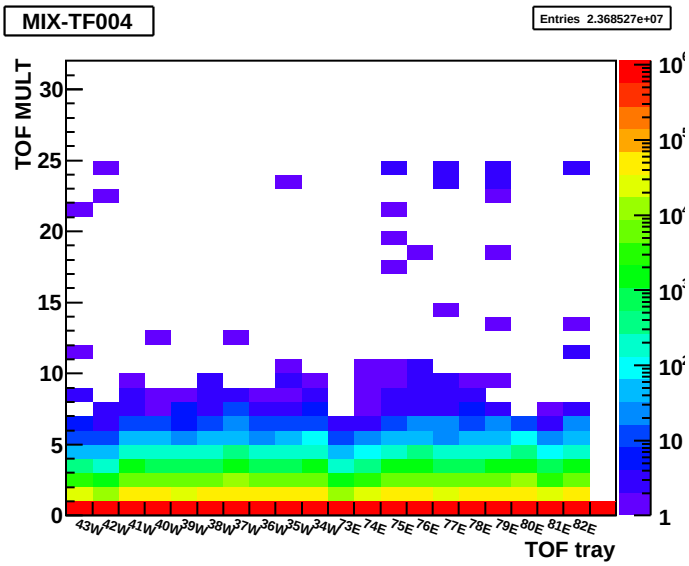

ST201 0-15 (ch6)
Entries 9022960
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

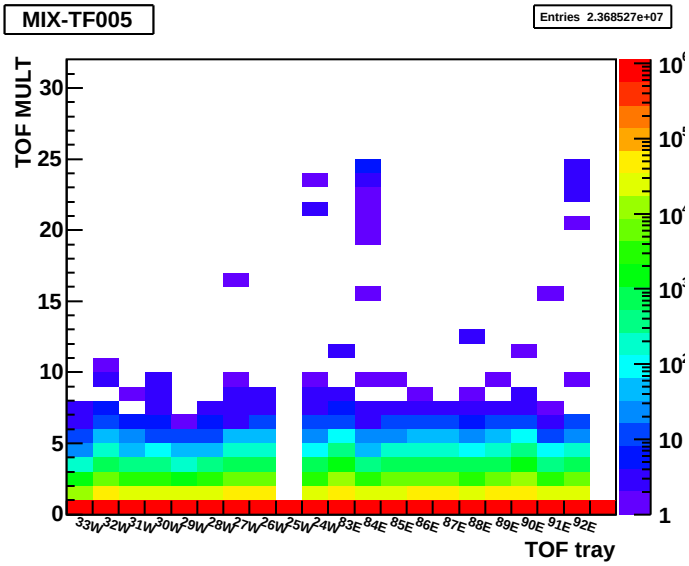

Unused (ch7)
Entries 9022960

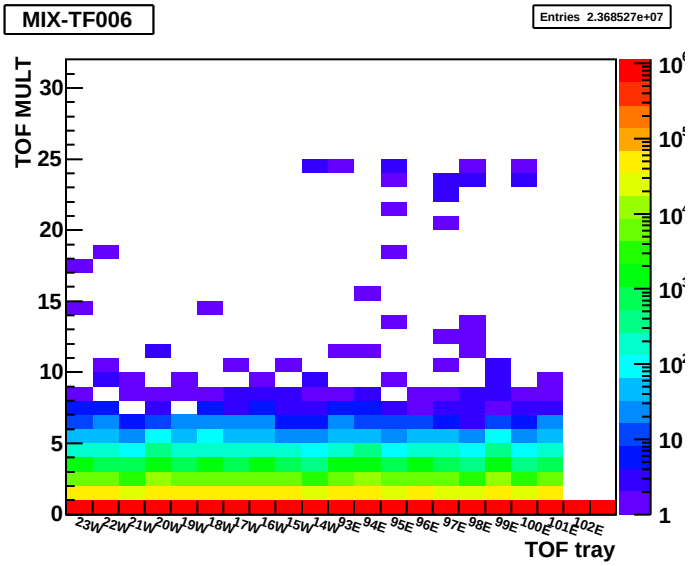

MIX-TF001
Entries 2.368527e+07
TOF MULT
TOF tray

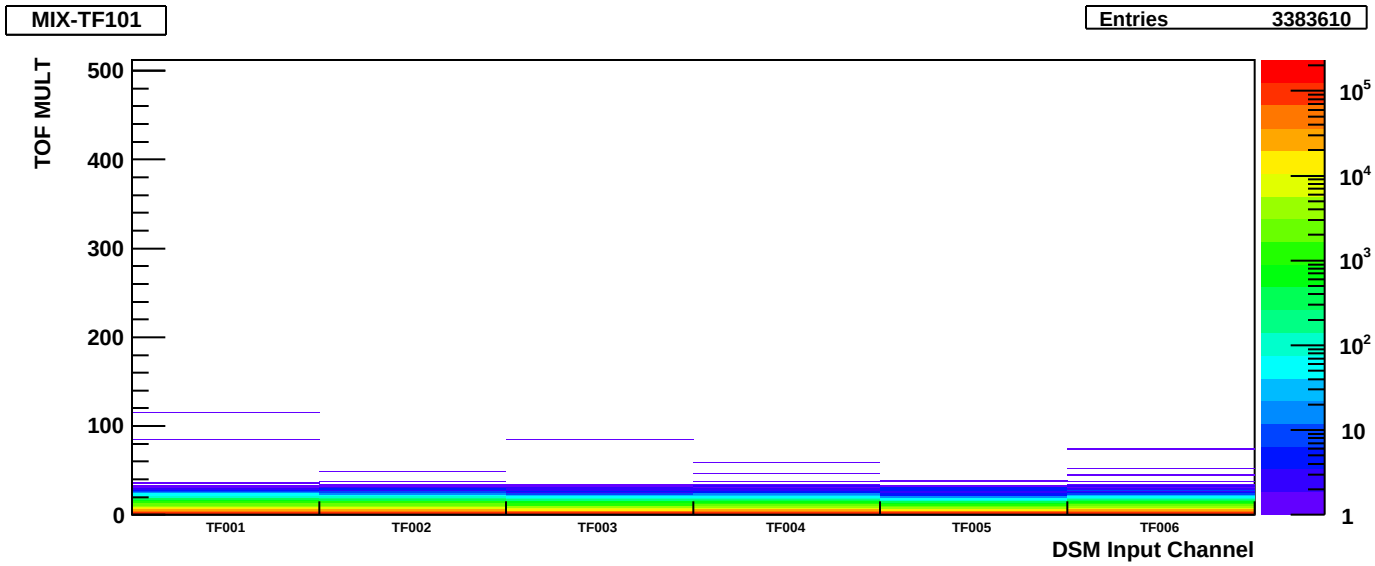

MIX-TF002
Entries 2.368527e+07
TOF MULT
TOF tray

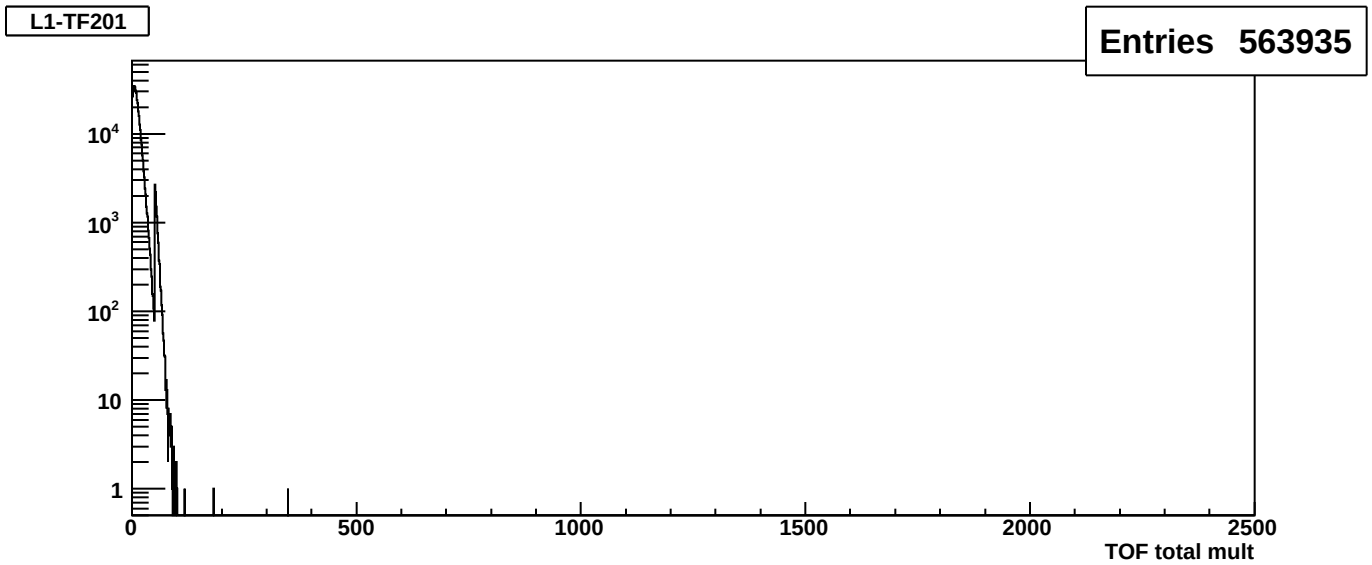

MIX-TF003
Entries 2.368527e+07
TOF MULT
TOF tray

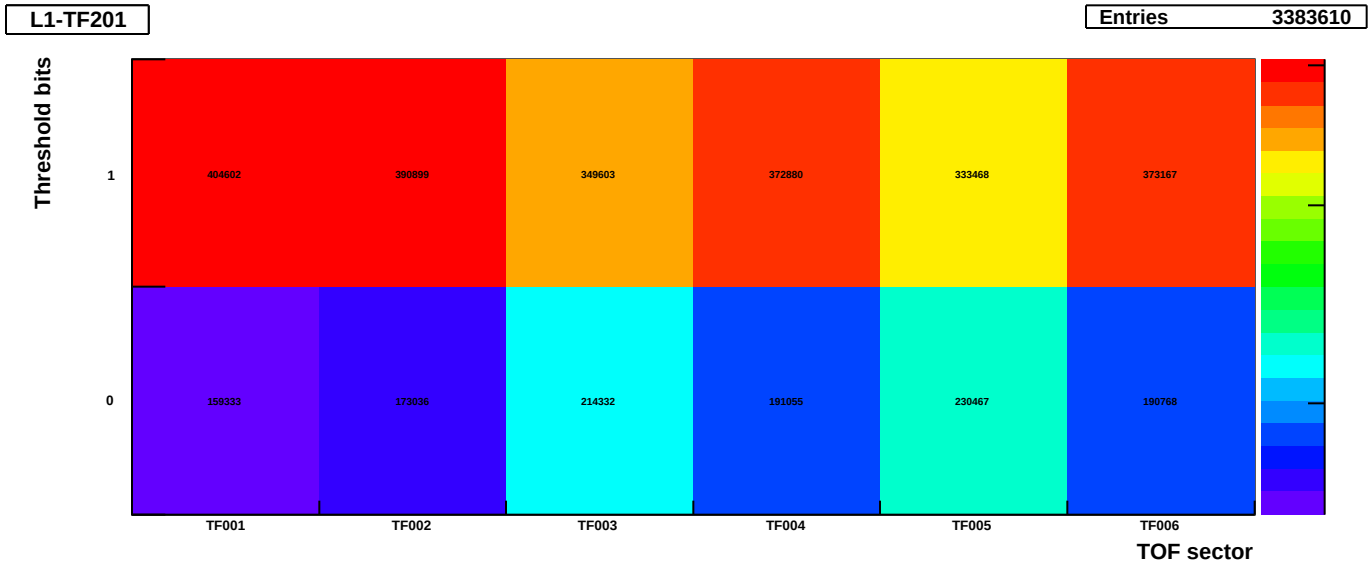

MIX-TF004
Entries 2.368527e+07
TOF MULT
TOF tray

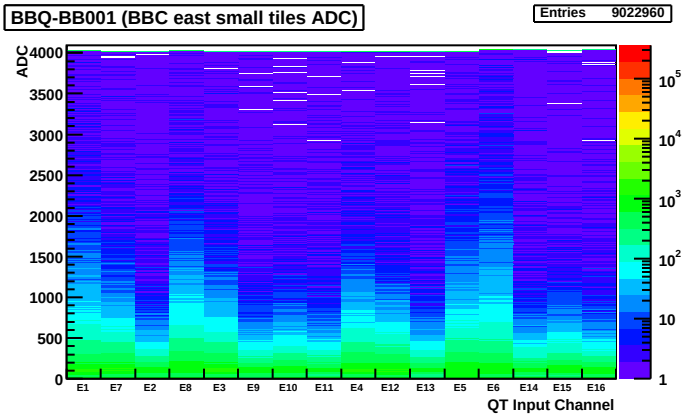

MIX-TF005
Entries 2.368527e+07
TOF MULT
TOF tray

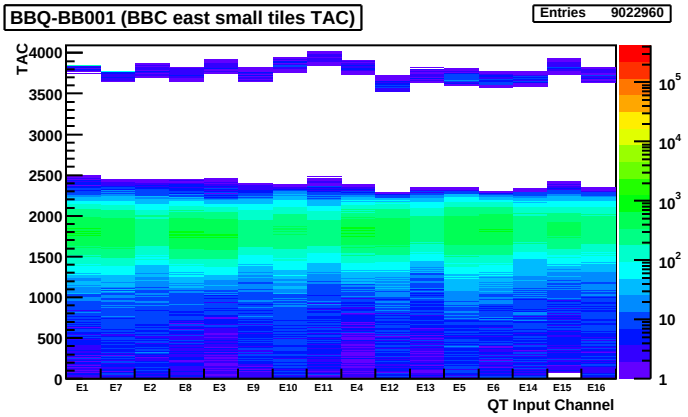

MIX-TF006
Entries 2.368527e+07
TOF MULT
TOF tray

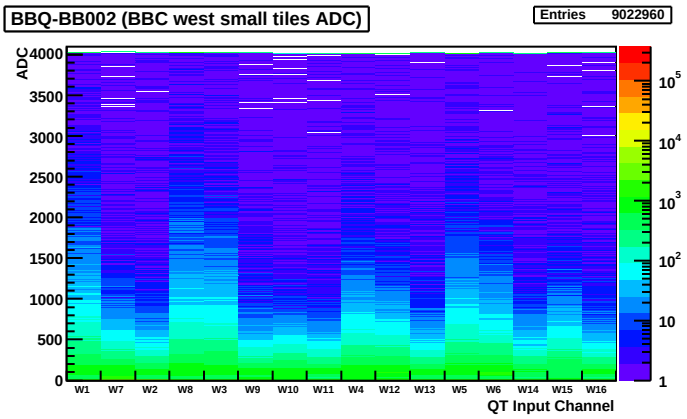

MIX-TF101
Entries 3383610
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

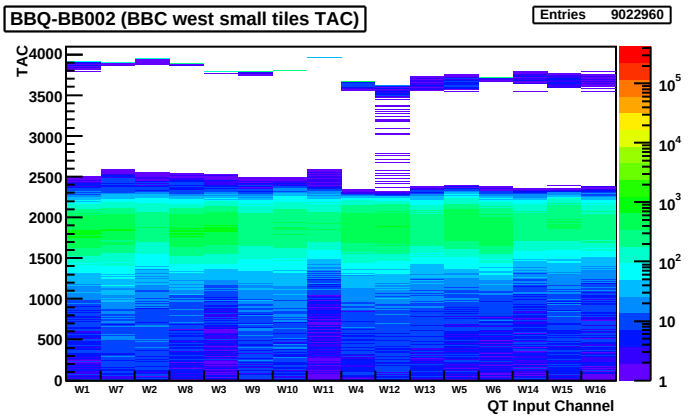

L1-TF201
Entries 563935
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

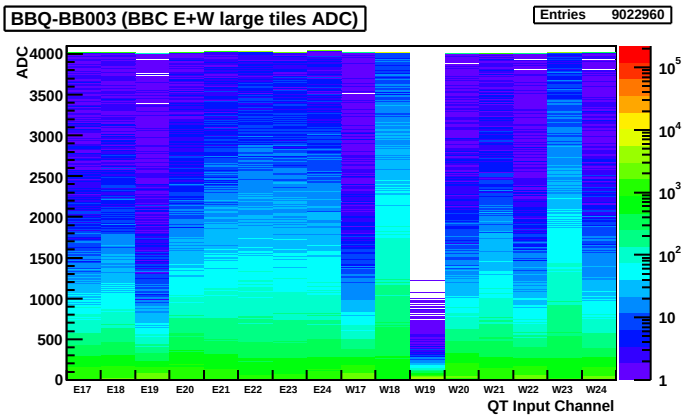

L1-TF201
Entries 3383610
Threshold bits
1
0
404602
390899
349603
372880
333468
373167
159333
173036
214332
191055
230467
190768
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

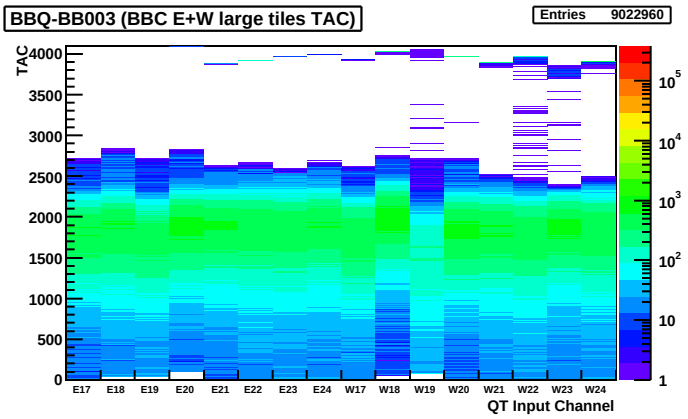

BBQ-BB001 (BBC east small tiles ADC)
Entries 9022960
ADC
QT Input Channel

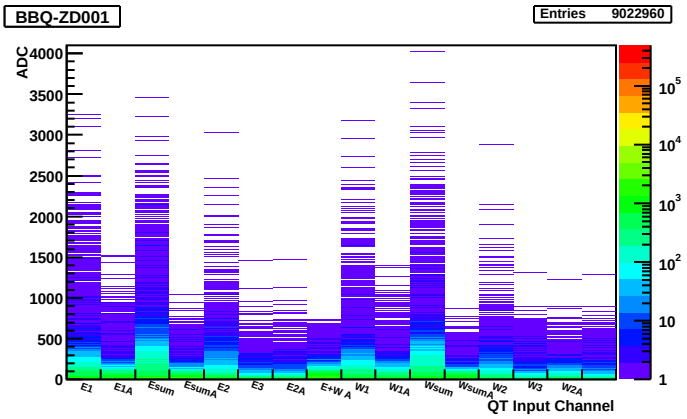

BBQ-BB001 (BBC east small tiles TAC)
Entries 9022960
TAC
QT Input Channel

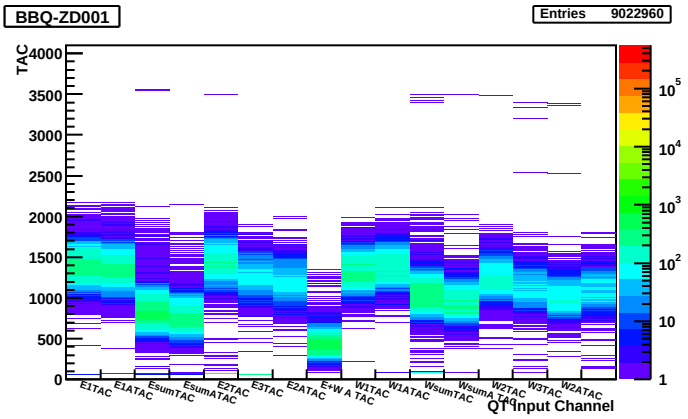

BBQ-BB002 (BBC west small tiles ADC)
Entries 9022960
ADC
QT Input Channel

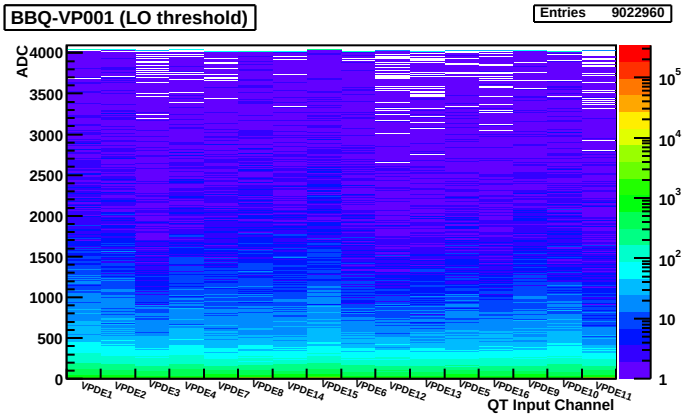

BBQ-BB002 (BBC west small tiles TAC)
Entries 9022960
TAC
QT Input Channel

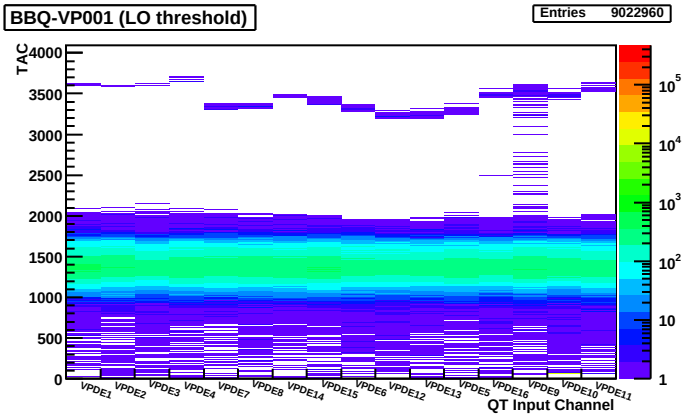

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9022960
ADC
QT Input Channel

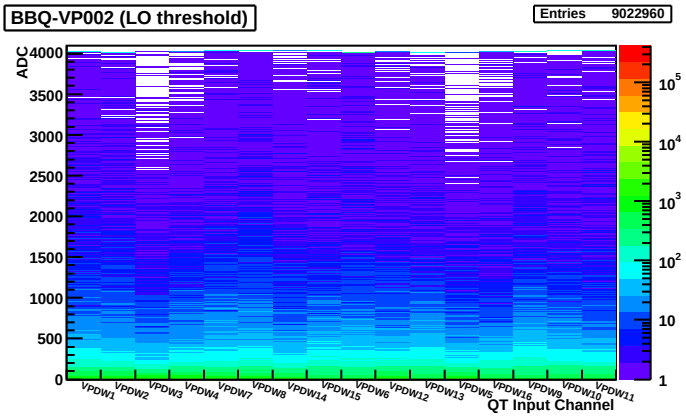

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9022960
TAC
QT Input Channel

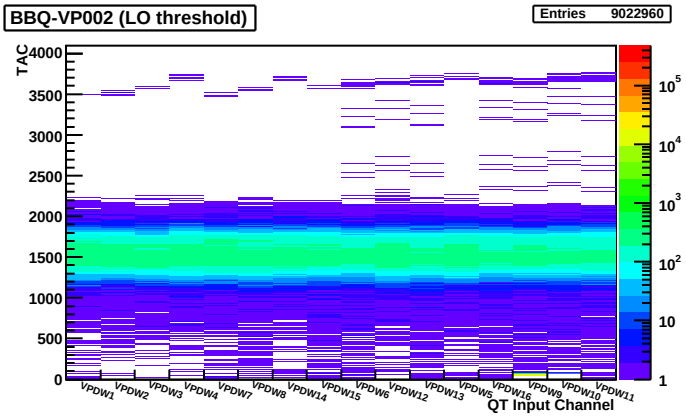

BBQ-ZD001
Entries 9022960
ADC
QT Input Channel

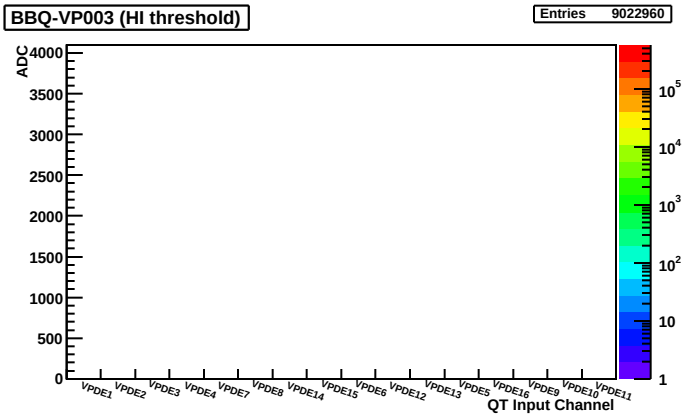

BBQ-ZD001
Entries 9022960
TAC
QT Input Channel

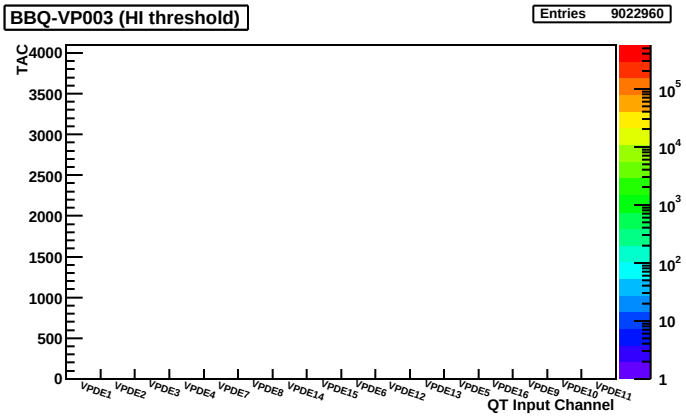

BBQ-VP001 (LO threshold)
Entries 9022960
ADC
QT Input Channel

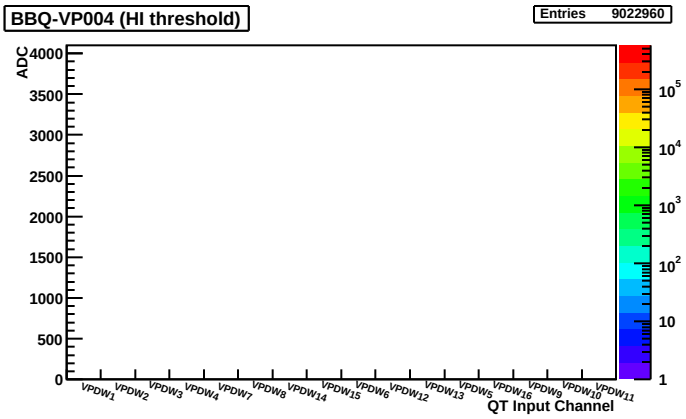

BBQ-VP001 (LO threshold)
Entries 9022960
TAC
QT Input Channel

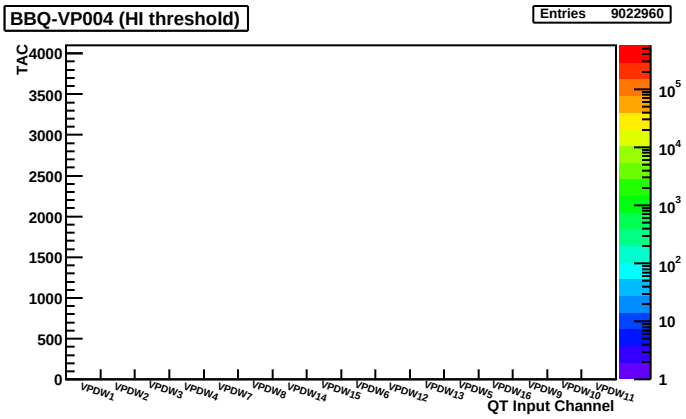

BBQ-VP002 (LO threshold)
Entries 9022960
ADC
QT Input Channel

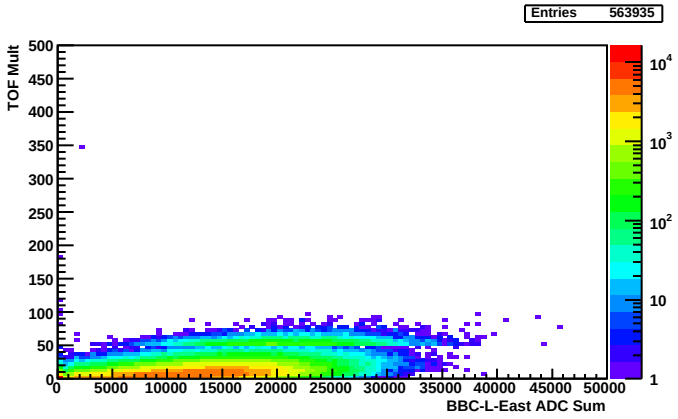

BBQ-VP002 (LO threshold)
Entries 9022960
TAC
QT Input Channel

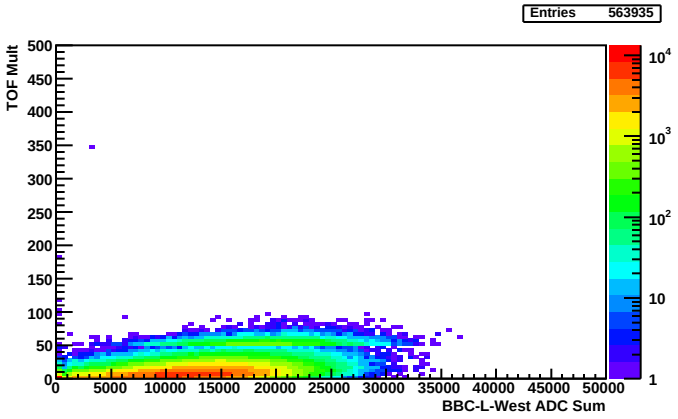

BBQ-VP003 (HI threshold)
Entries 9022960
ADC
QT Input Channel

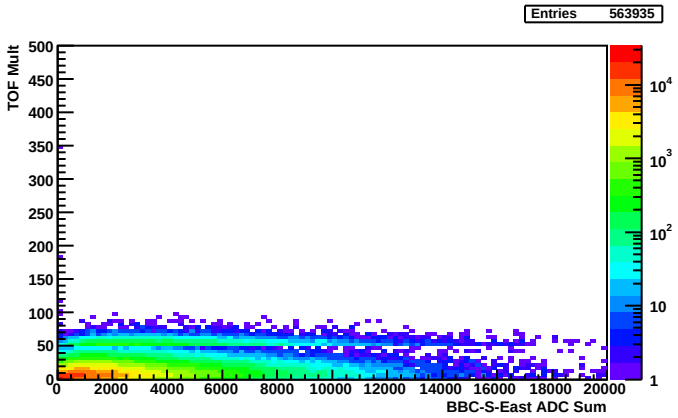

BBQ-VP003 (HI threshold)
Entries 9022960
TAC
QT Input Channel

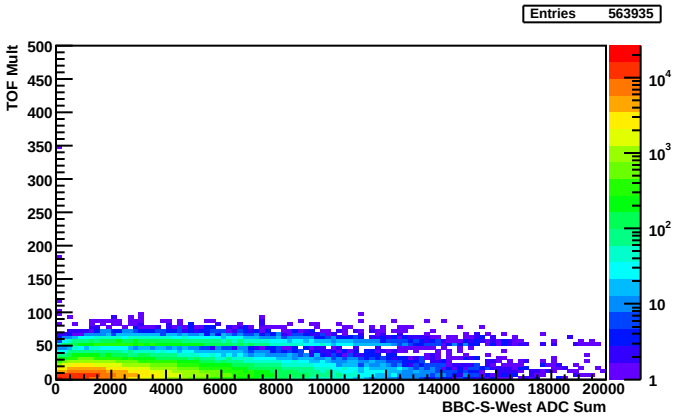

BBQ-VP004 (HI threshold)
Entries 9022960
ADC
QT Input Channel

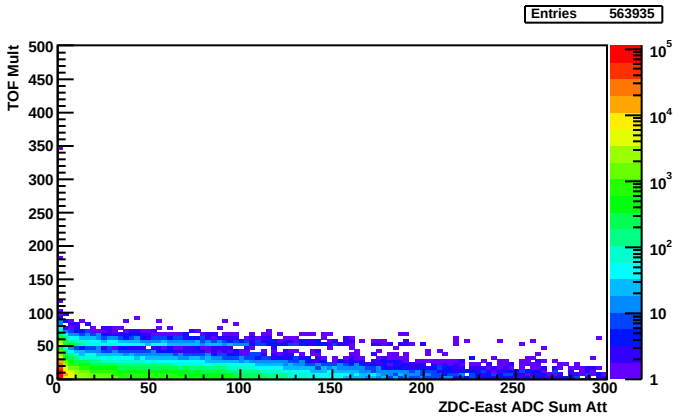

BBQ-VP004 (HI threshold)
Entries 9022960
TAC
QT Input Channel

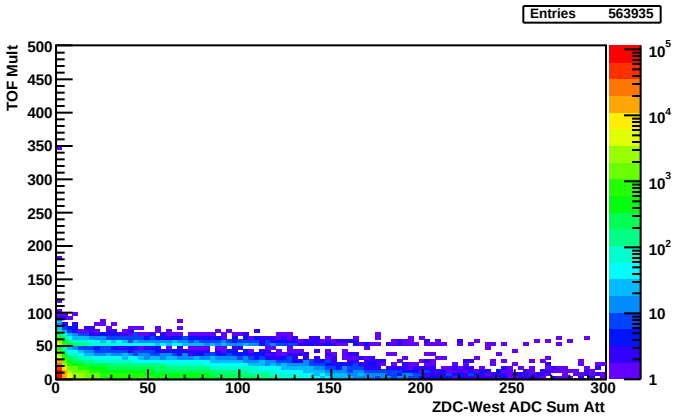

Entries 563935
TOF Mult
BBC-L-East ADC Sum

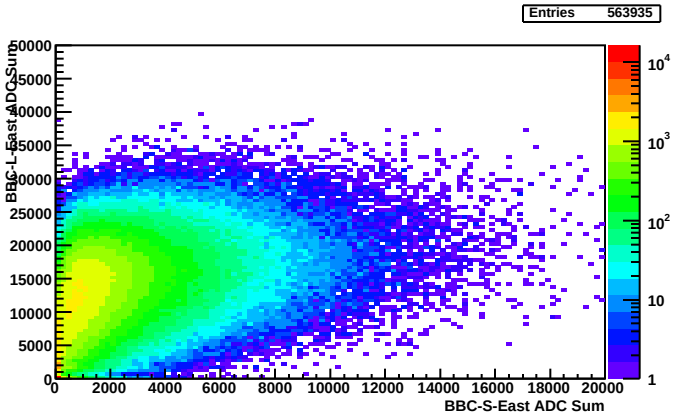

Entries 563935
TOF Mult
BBC-L-West ADC Sum

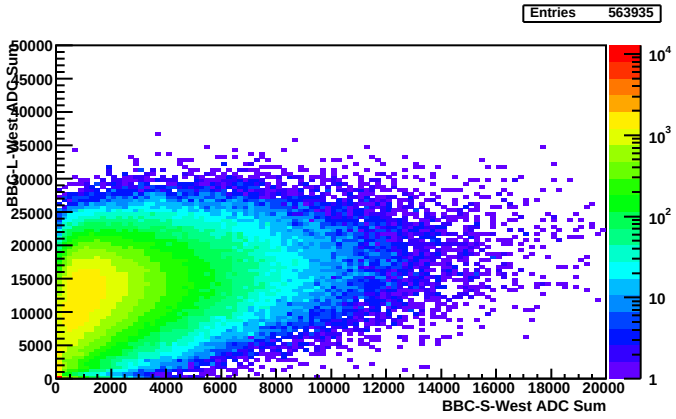

Entries 563935
TOF Mult
BBC-S-East ADC Sum

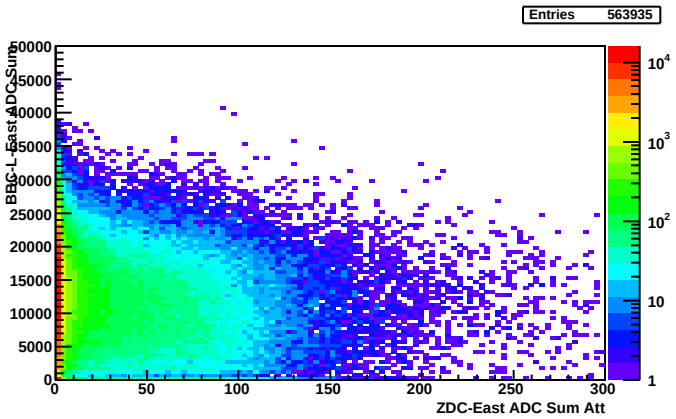

Entries 563935
TOF Mult
BBC-S-West ADC Sum

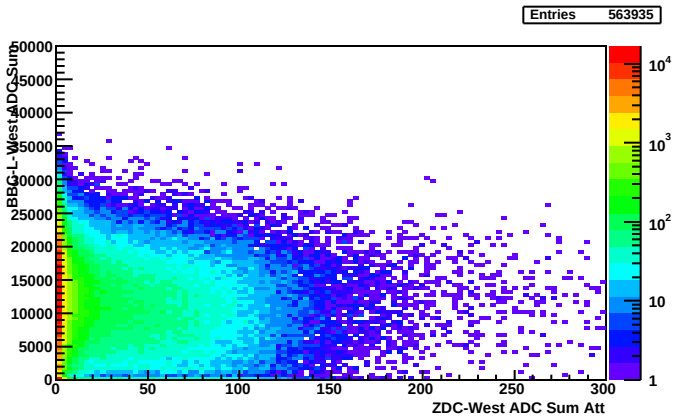

Entries 563935
TOF Mult
ZDC-East ADC Sum Att

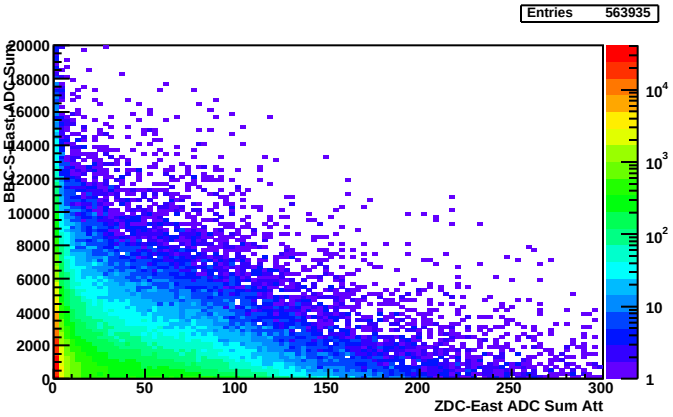

Entries 563935
TOF Mult
ZDC-West ADC Sum Att

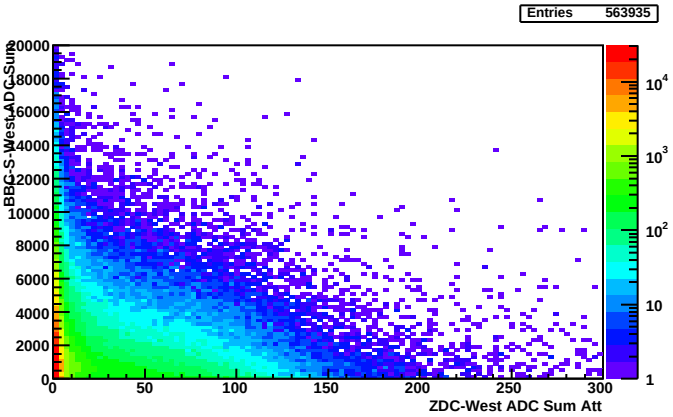

Entries 563935
BBC-L-East ADC Sum
BBC-S-East ADC Sum

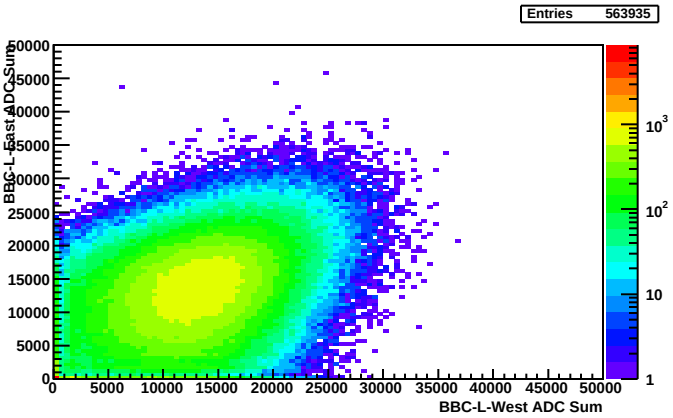

Entries 563935
BBC-L-West ADC Sum
BBC-S-West ADC Sum

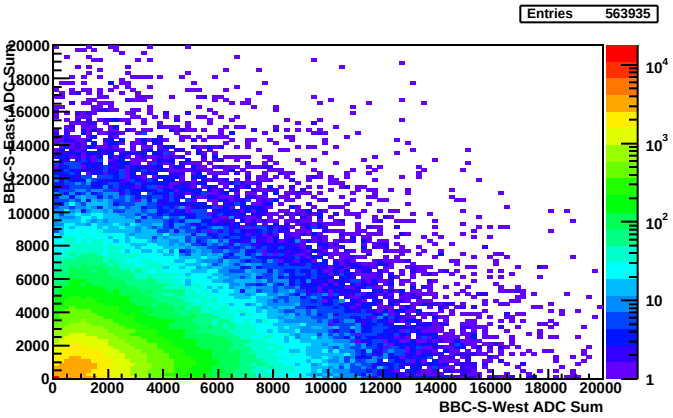

Entries 563935
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

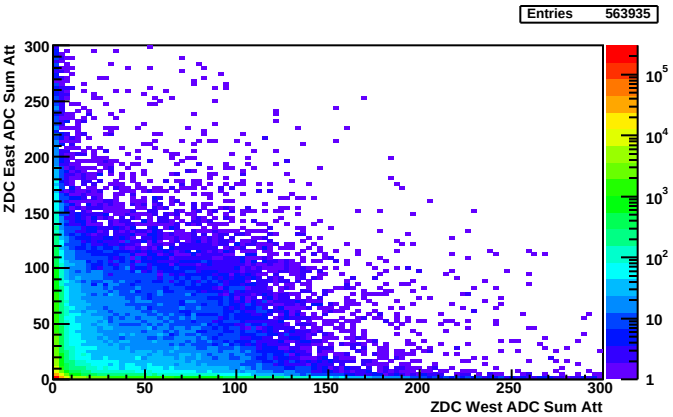

Entries 563935
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

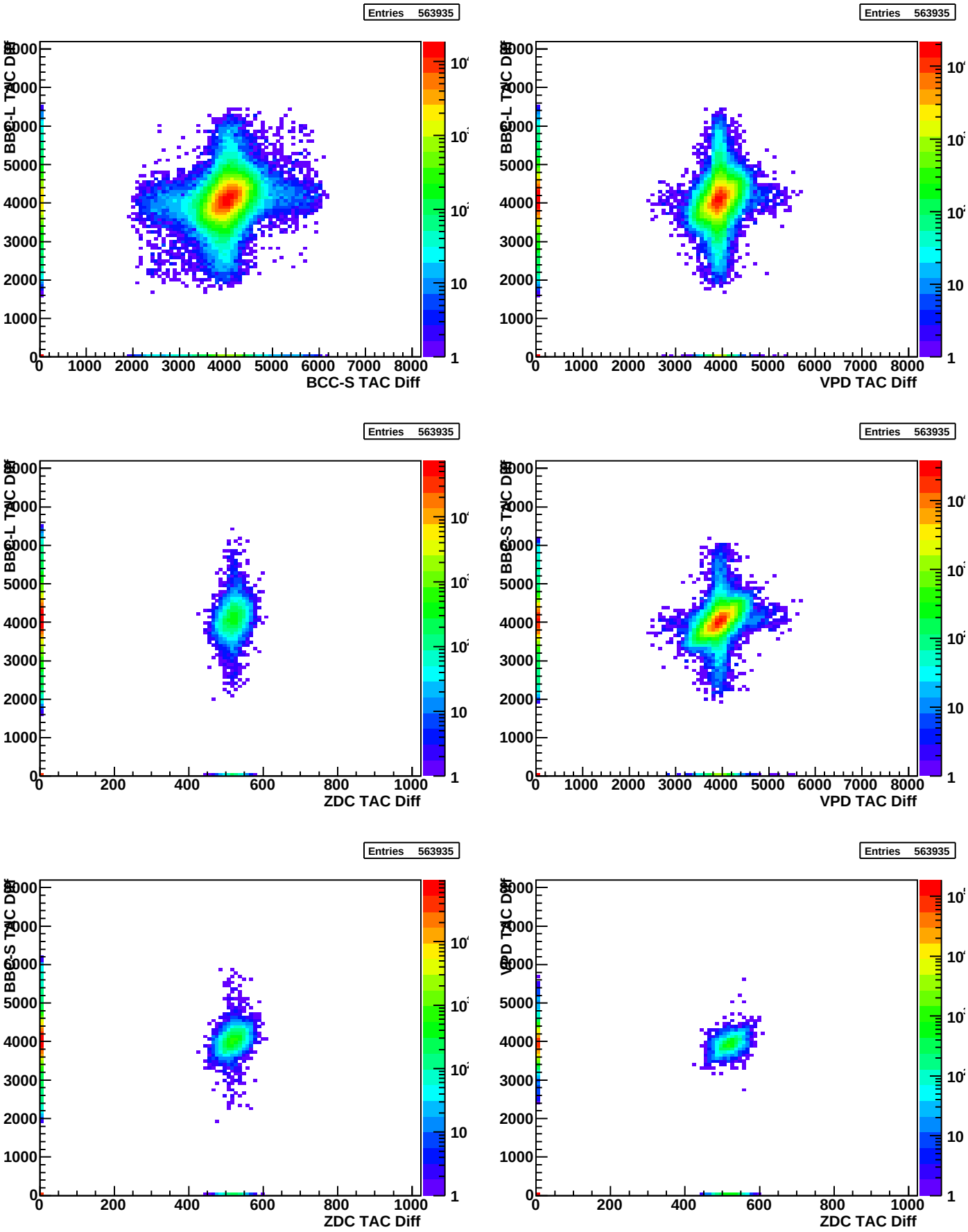

Entries 563935
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

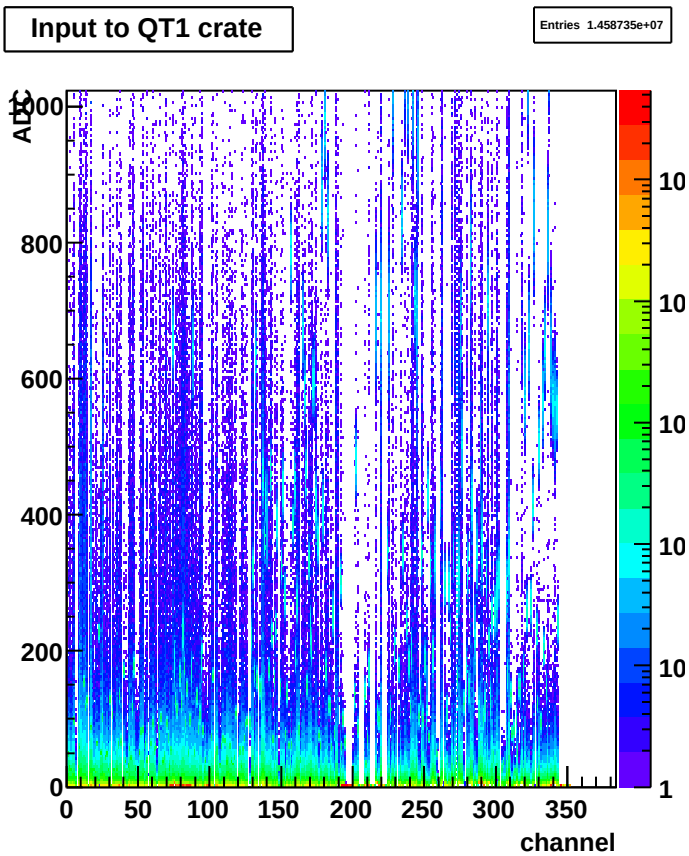

Entries 563935
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

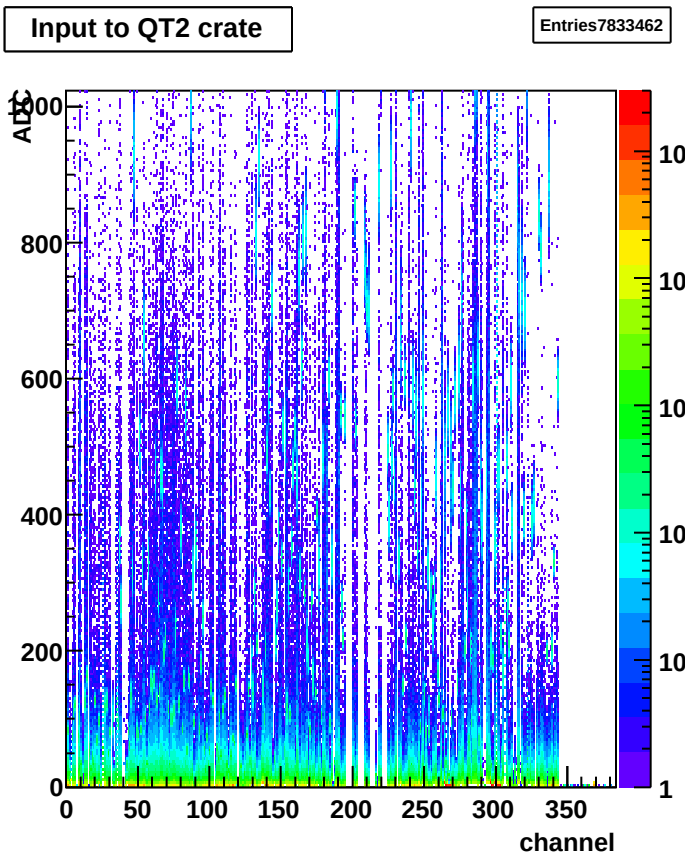

Entries 563935
BBC-L-East ADC Sum
BBC-L-West ADC Sum

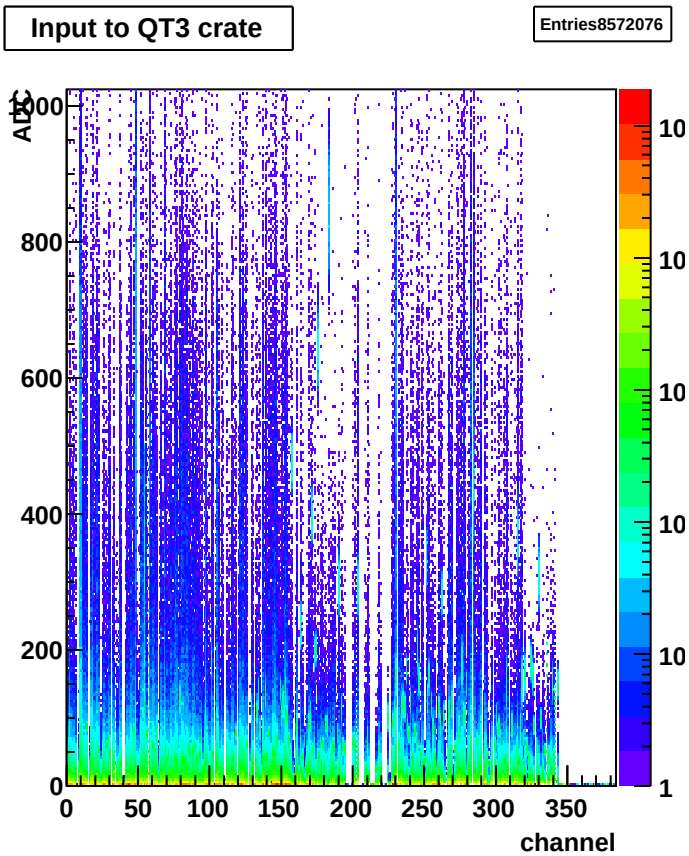

Entries 563935
BBC-S-East ADC Sum
BBC-S-West ADC Sum

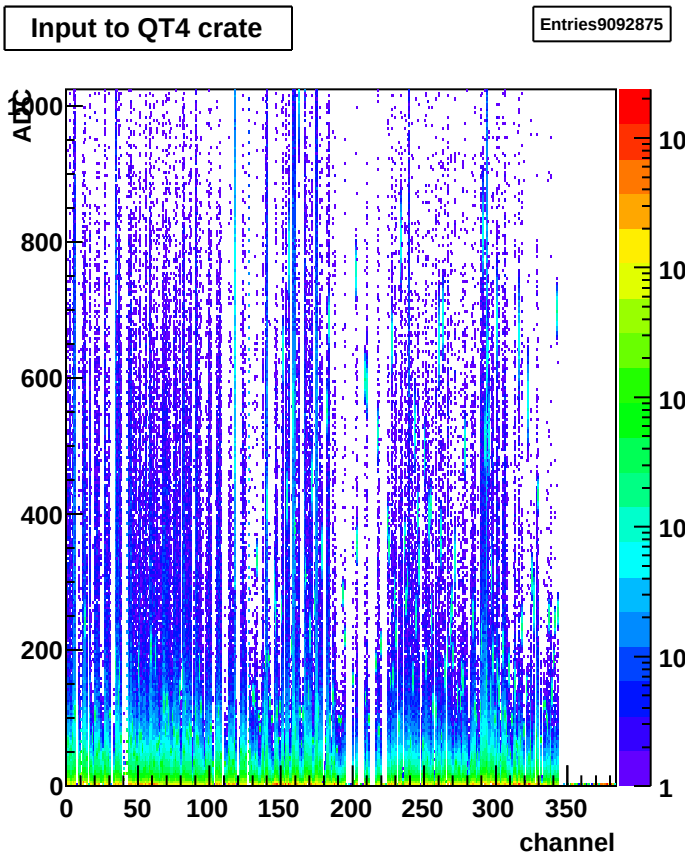

Entries 563935
ZDC East ADC Sum Att
ZDC West ADC Sum Att

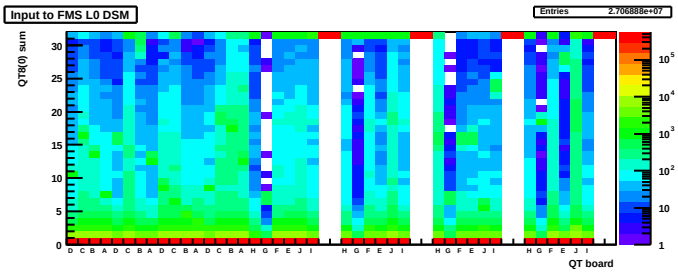

Entries 563935
BBC-L TAC Diff
BCC-S TAC Diff
Entries 563935
BBC-L TAC Diff
VPD TAC Diff
Entries 563935
BBC-L TAC Diff
ZDC TAC Diff
Entries 563935
BBC-S TAC Diff
VPD TAC Diff
Entries 563935
BBC-S TAC Diff
ZDC TAC Diff
Entries 563935
VPD TAC Diff
ZDC TAC Diff

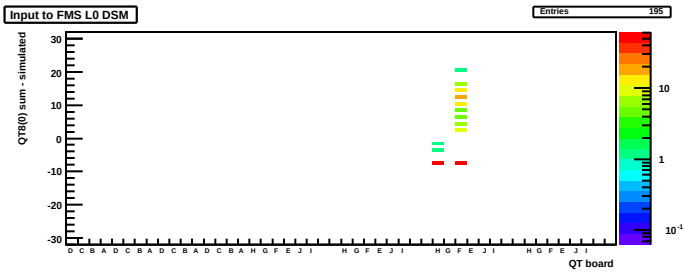

Input to QT1 crate
Entries 1.458735e+07
ADC
channel

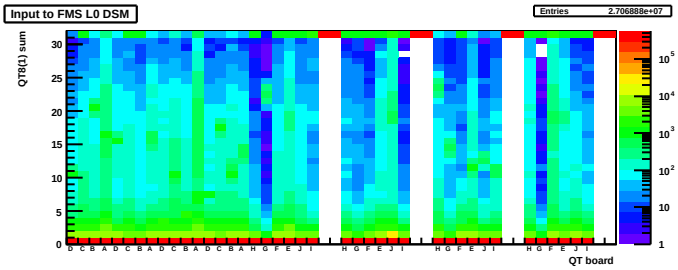

Input to QT2 crate
Entries7833462
ADC
channel

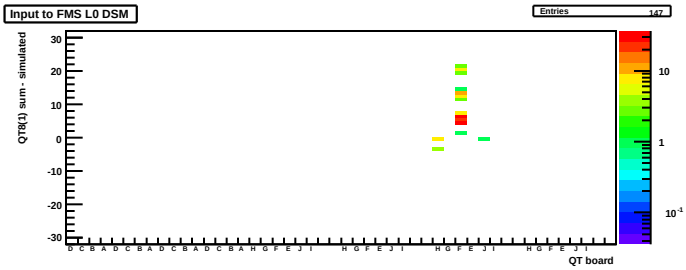

Input to QT3 crate
Entries8572076
ADC
channel

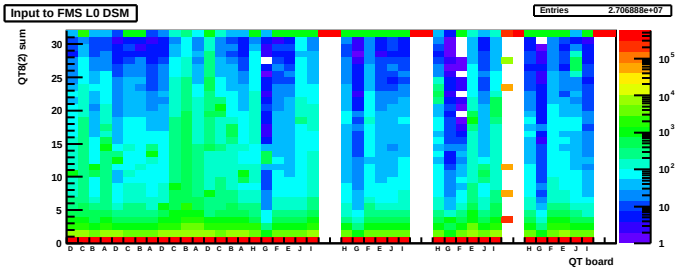

Input to QT4 crate
Entries9092875
ADC
channel

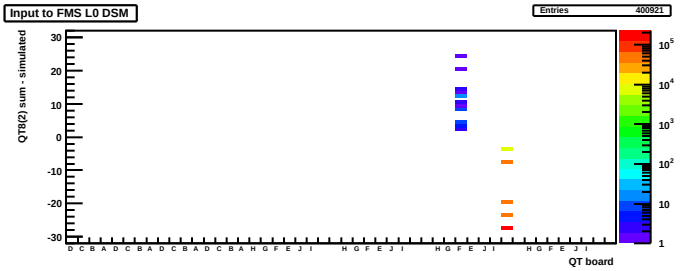

Input to FMS L0 DSM
Entries 2.706888e+07
QT8(0) sum
QT board

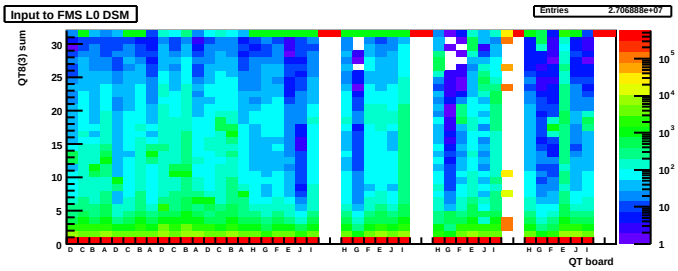

Input to FMS L0 DSM
Entries 195
QT8(0) sum - simulated
QT board

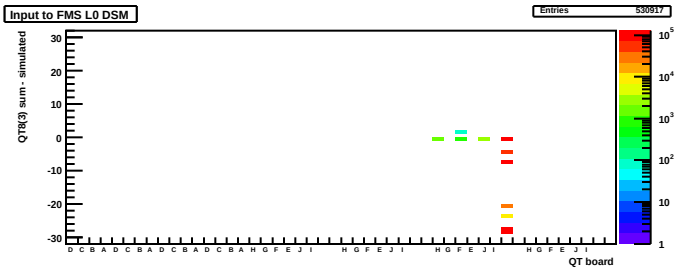

Input to FMS L0 DSM
Entries 2.706888e+07
QT8(1) sum
QT board

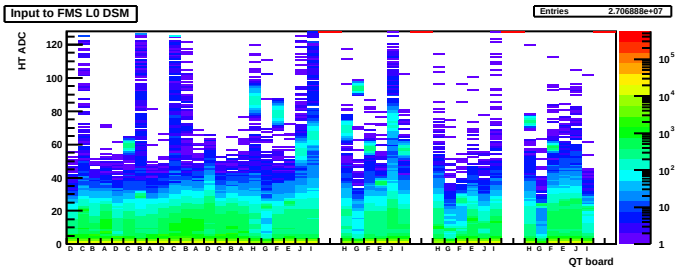

Input to FMS L0 DSM
Entries 147
QT8(1) sum - simulated
QT board

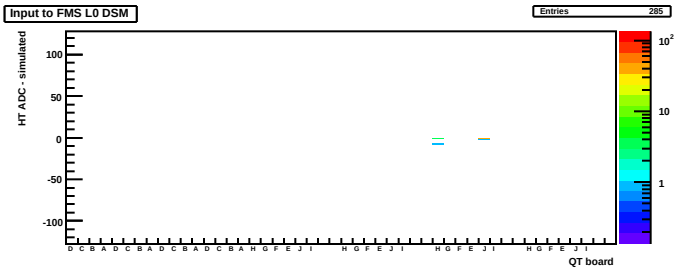

Input to FMS L0 DSM
Entries 2.706888e+07
QT8(2) sum
QT board

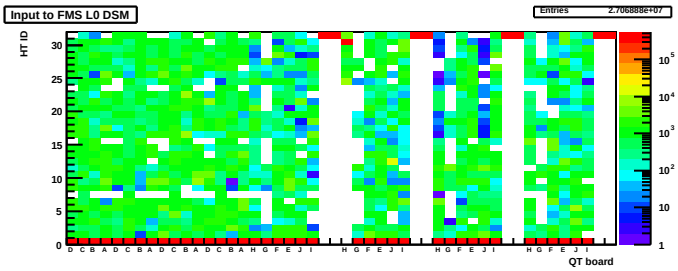

Input to FMS L0 DSM
Entries 400921
QT8(2) sum - simulated
QT board

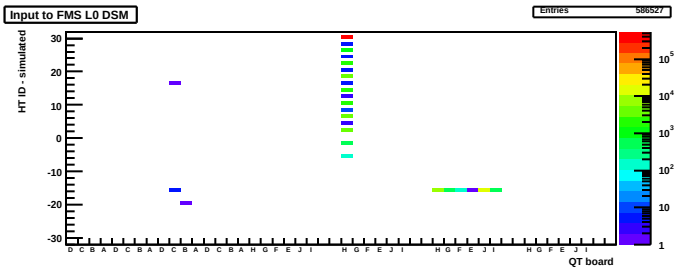

Input to FMS L0 DSM
Entries 2.706888e+07
QT8(3) sum
QT board

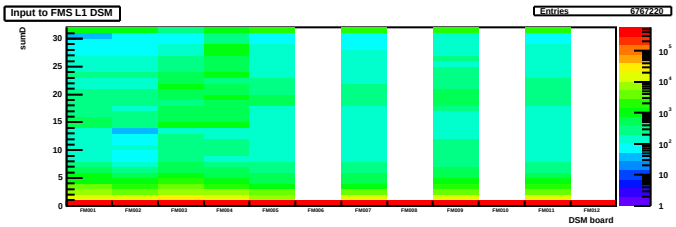

Input to FMS L0 DSM
Entries 530917
QT8(3) sum - simulated
QT board

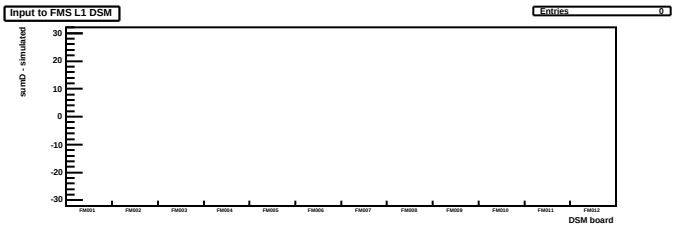

Input to FMS L0 DSM
Entries 2.706888e+07
HT ADC
QT board

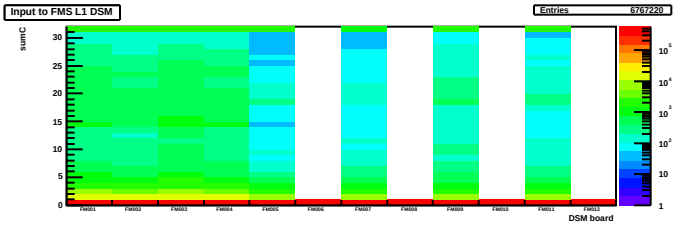

Input to FMS L0 DSM
Entries 285
HT ADC - simulated
QT board

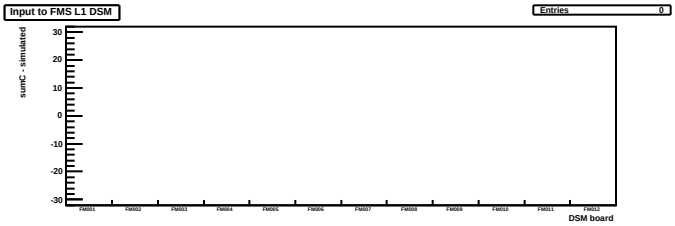

Input to FMS L0 DSM
Entries 2.706888e+07
HT ID
QT board

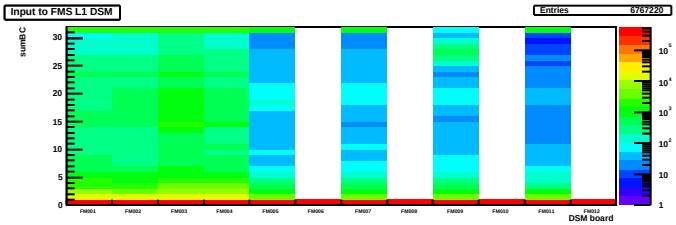

Input to FMS L0 DSM
Entries 586527
HT ID - simulated
QT board

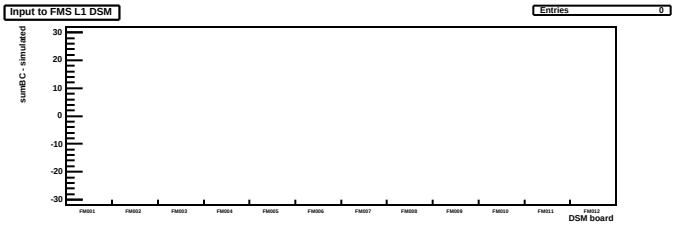

Input to FMS L1 DSM
Entries 6767220
sumD
DSM board

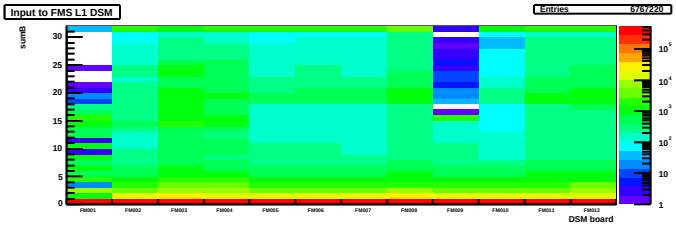

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

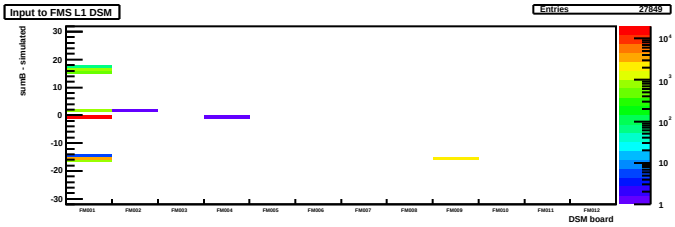

Input to FMS L1 DSM
Entries 6767220
sumC
DSM board

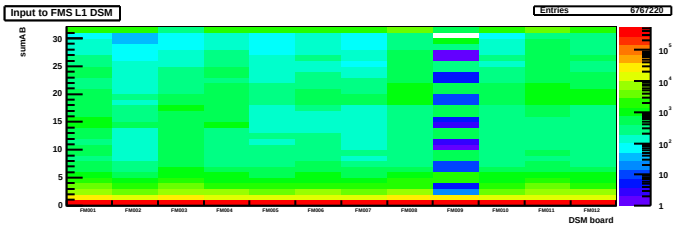

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

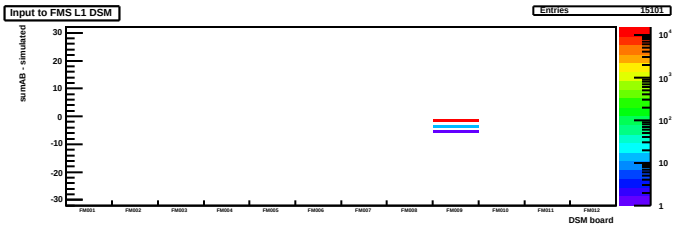

Input to FMS L1 DSM
Entries 6767220
sumBC
DSM board

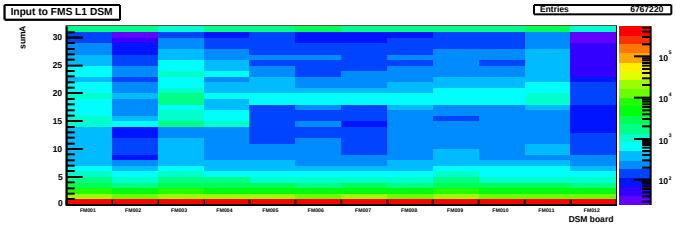

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

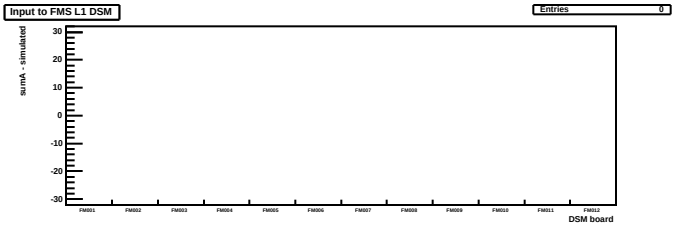

Input to FMS L1 DSM
Entries 6767220
sumB
DSM board

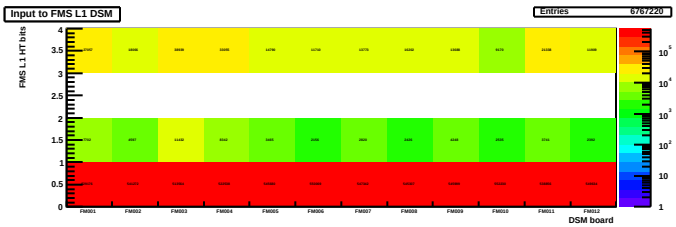

Input to FMS L1 DSM
Entries 27849
sumB - simulated
DSM board

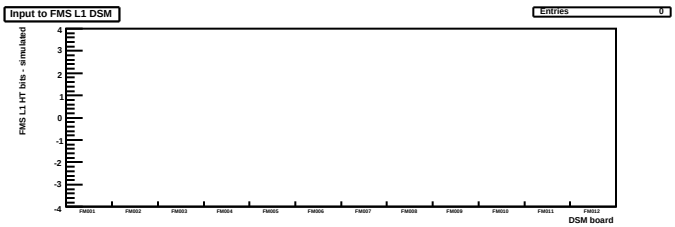

Input to FMS L1 DSM
Entries 6767220
sumAB
DSM board

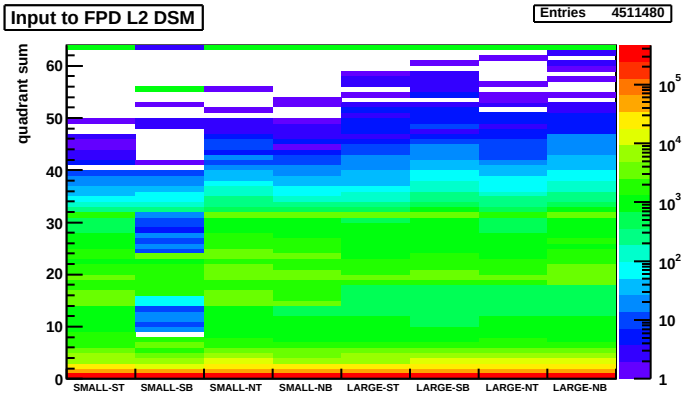

Input to FMS L1 DSM
Entries 15101
sumAB - simulated
DSM board

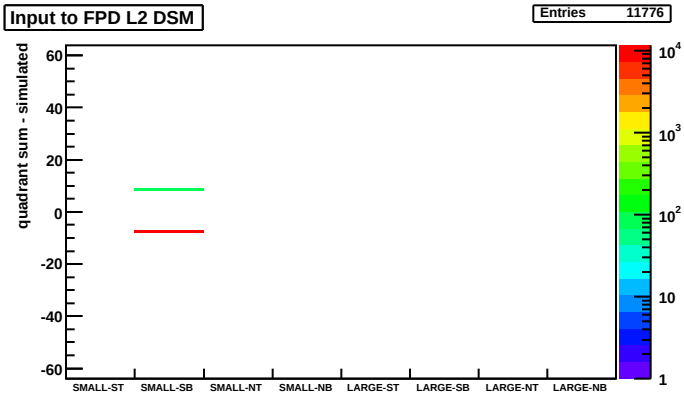

Input to FMS L1 DSM
Entries 6767220
sumA
DSM board

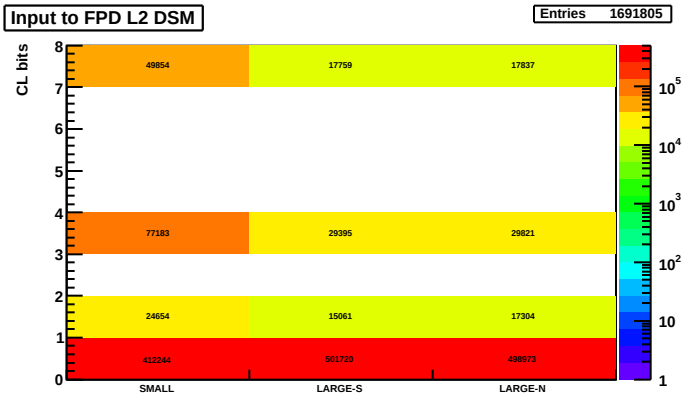

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

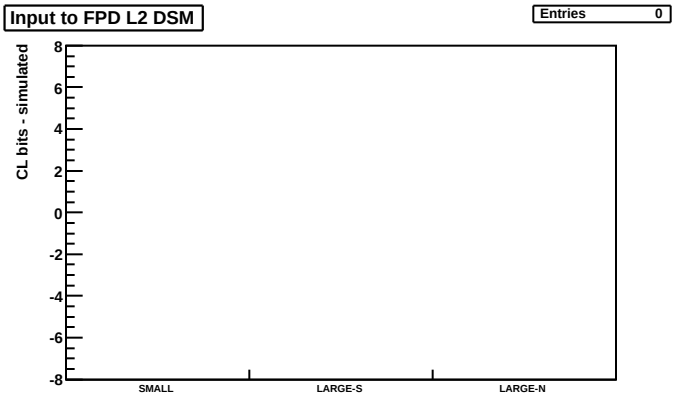

Input to FMS L1 DSM
Entries 6767220
FMS L1 HT bits
DSM board

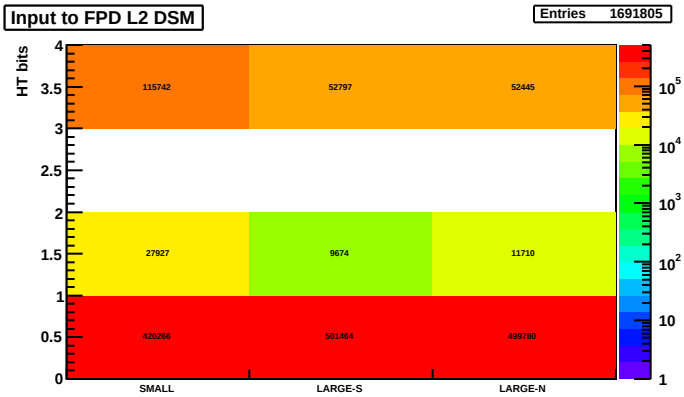

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

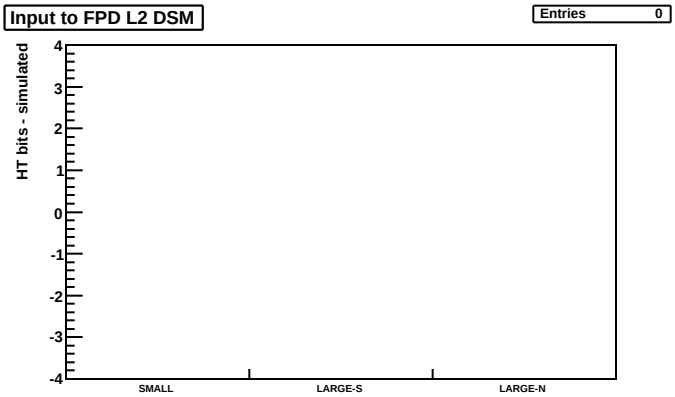

Input to FPD L2 DSM
Entries 4511480
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

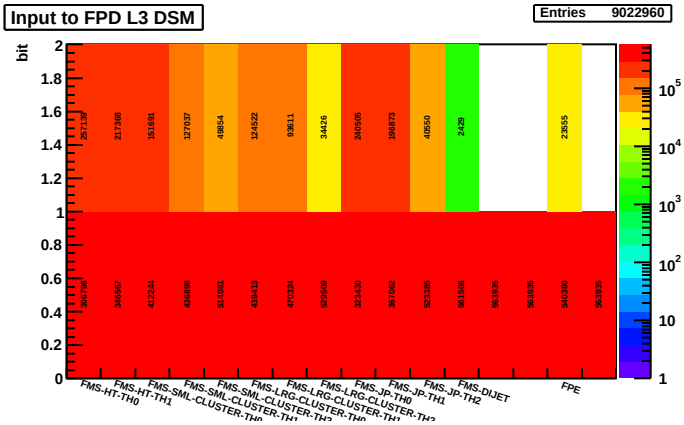

Input to FPD L2 DSM
Entries 11776
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

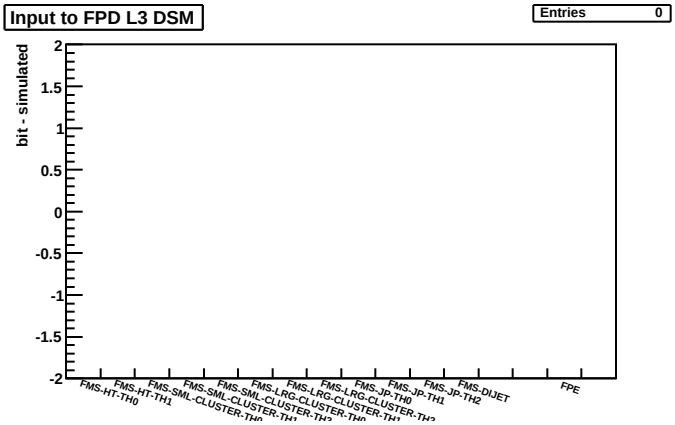

Input to FPD L2 DSM
Entries 1691805
CL bits
49854 17759 17837
77183 29395 29821
24654 15061 17304
412244 501720 498973
SMALL LARGE-S LARGE-N

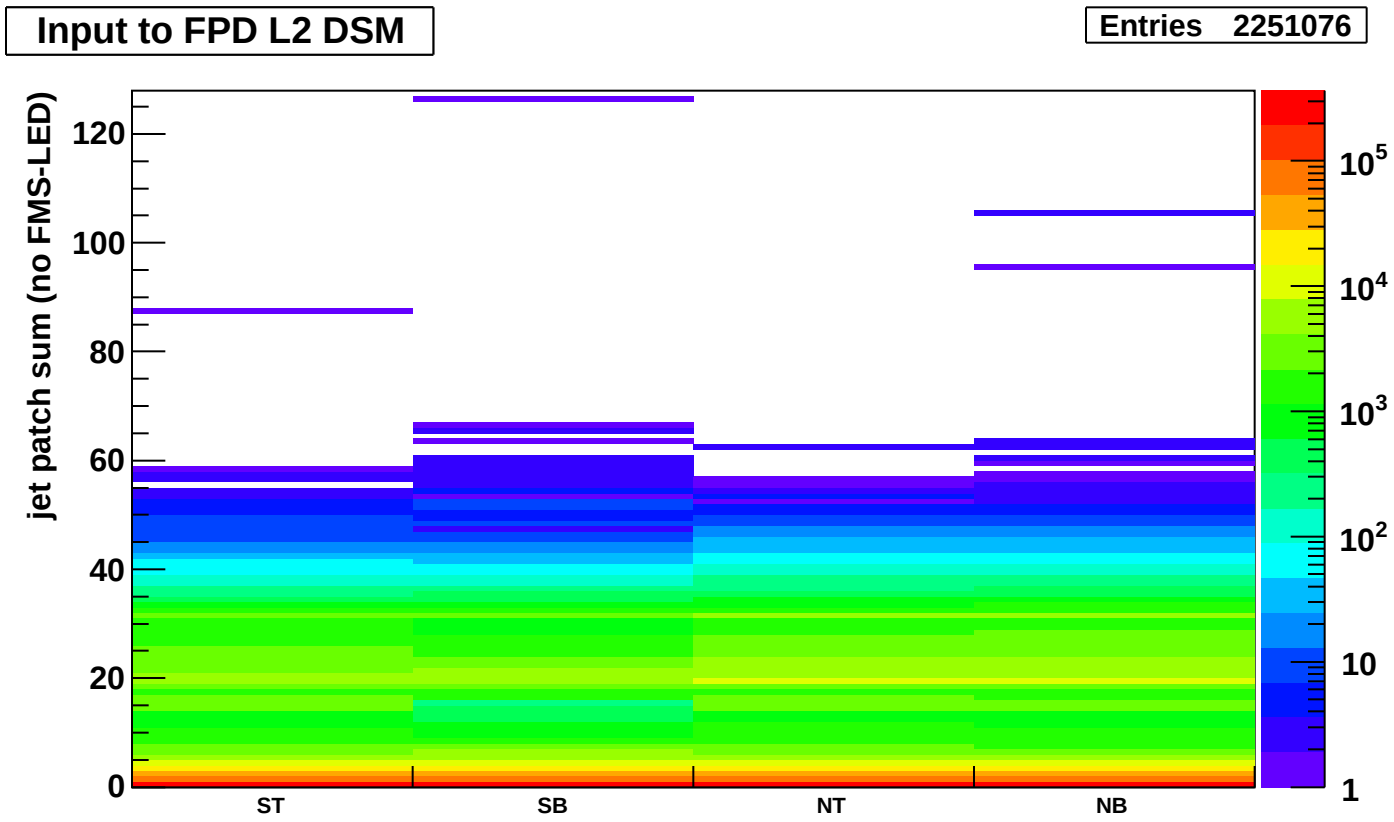

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL LARGE-S LARGE-N

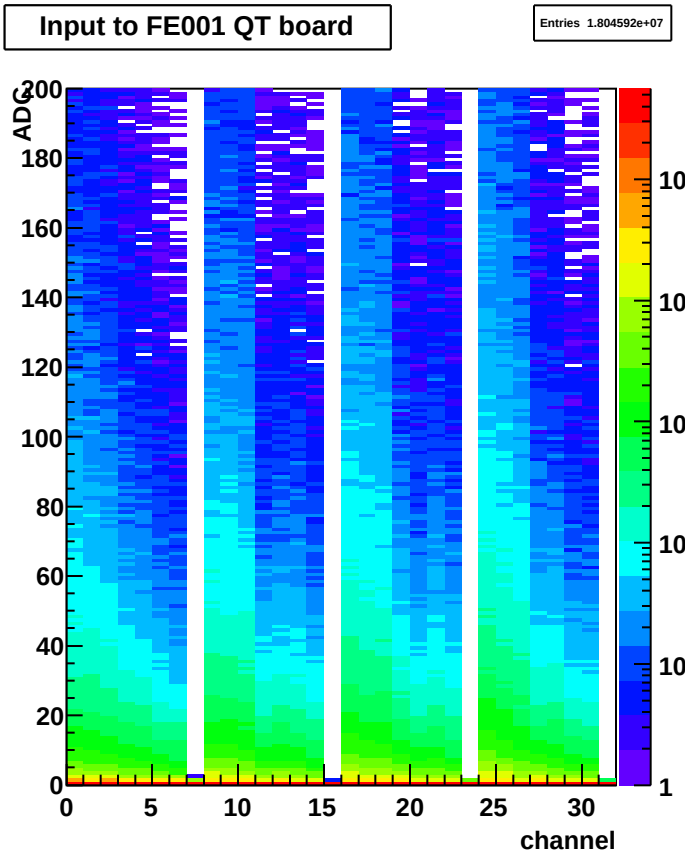

Input to FPD L2 DSM
Entries 1691805
HT bits
115742 52797 52445
27927 9674 11710
420266 501464 499780
SMALL LARGE-S LARGE-N

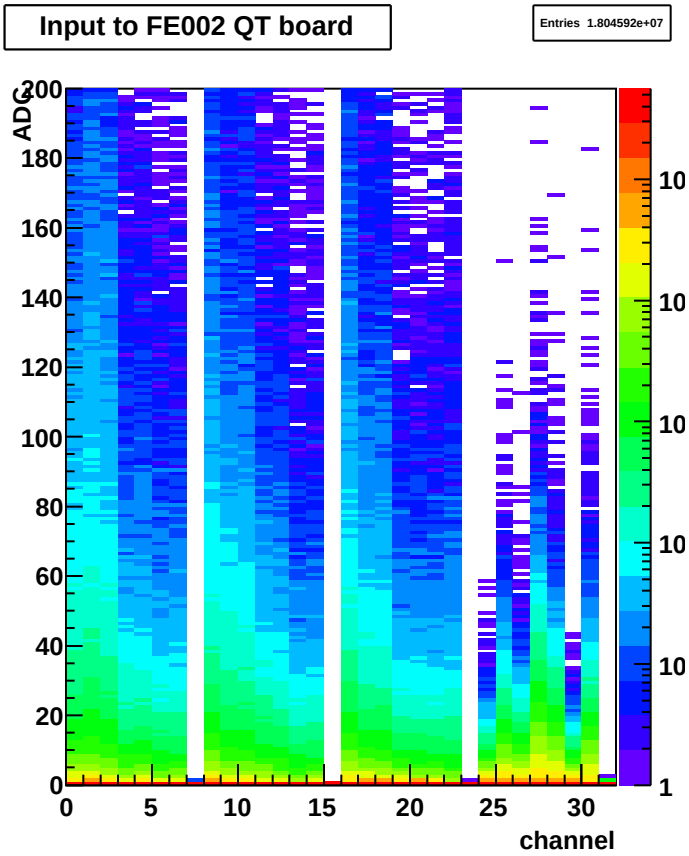

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

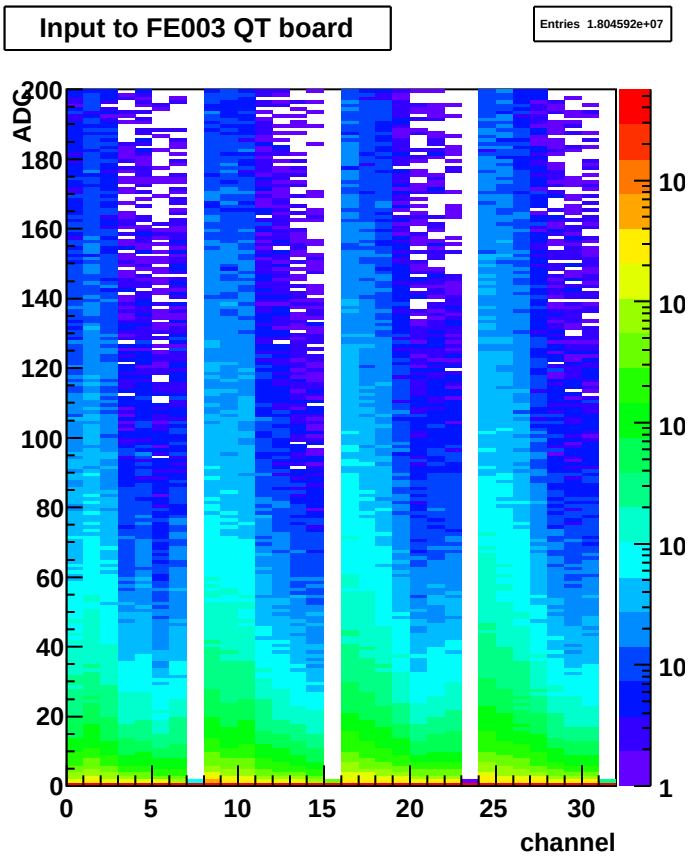

Input to FPD L3 DSM
Entries 9022960
bit
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

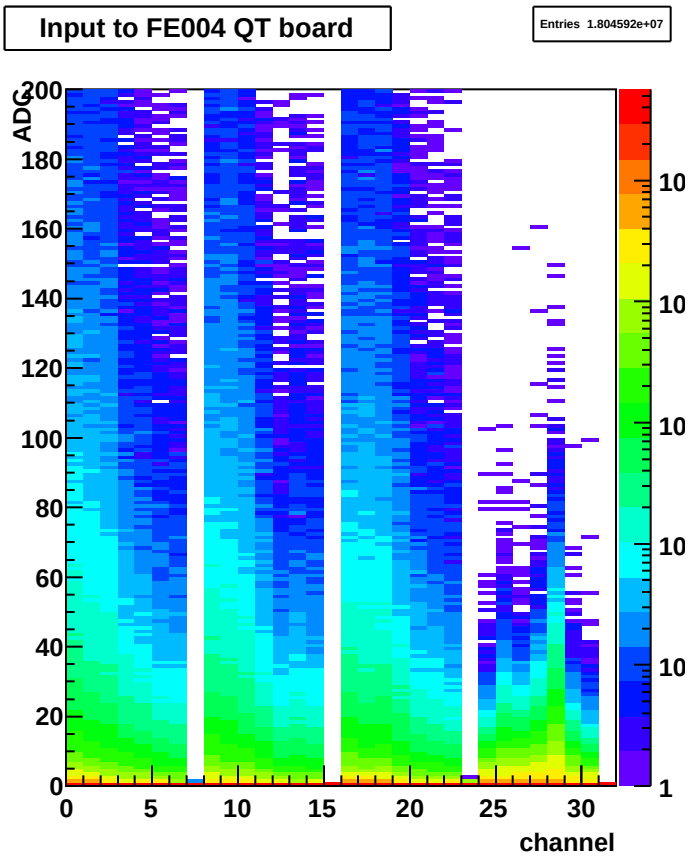

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

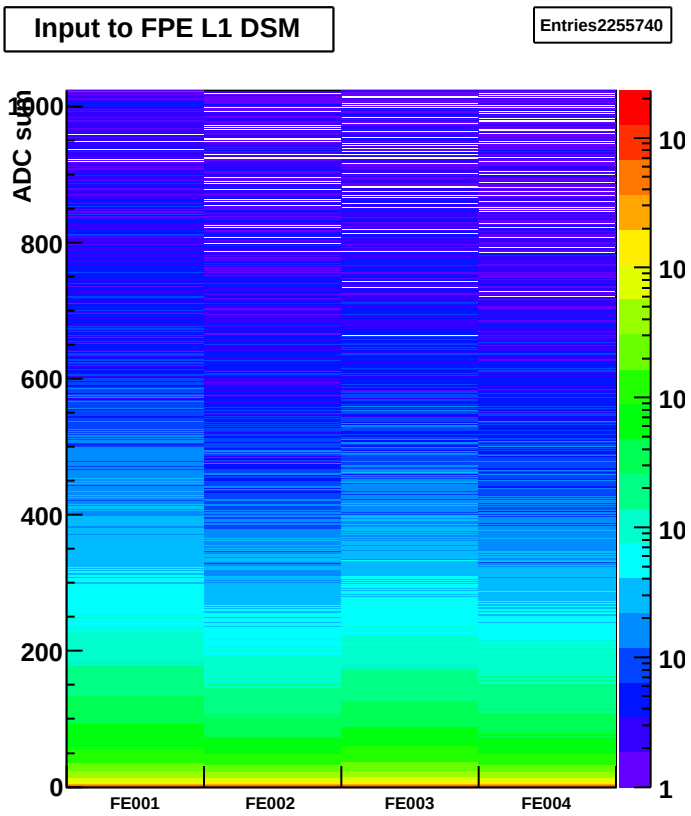

Input to FPD L2 DSM
Entries 2251076
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

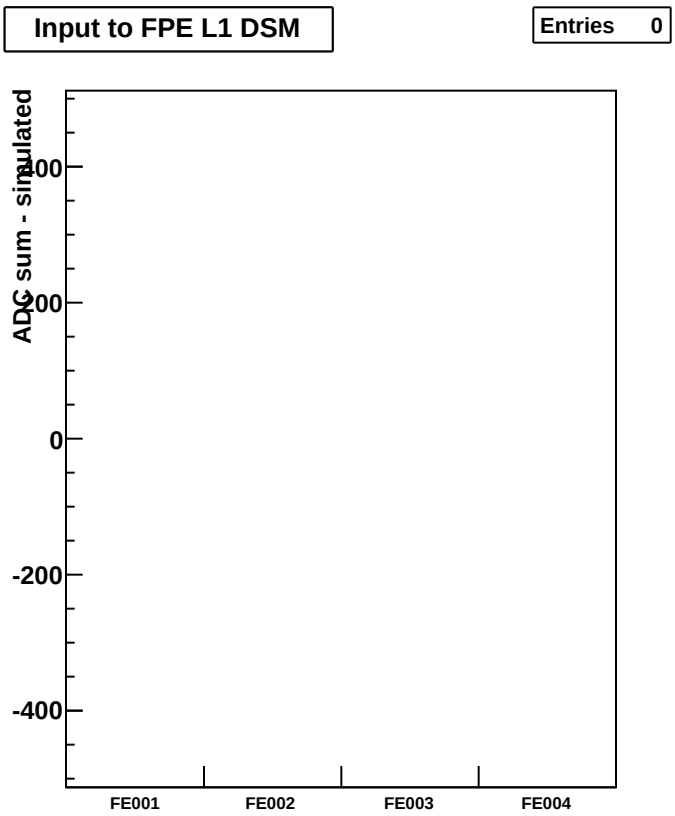

Input to FE001 QT board
Entries 1.804592e+07
ADC
channel

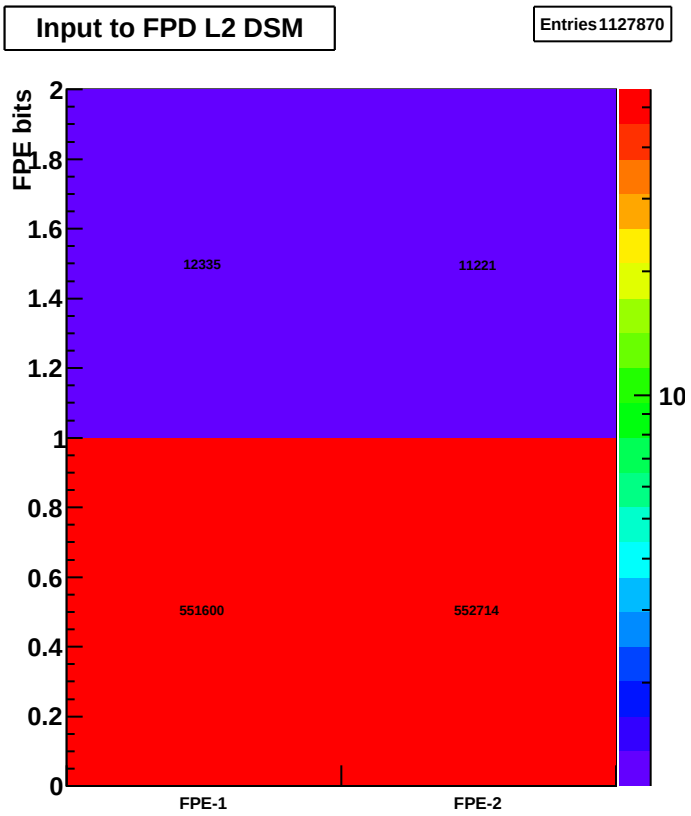

Input to FE002 QT board
Entries 1.804592e+07
ADC
channel

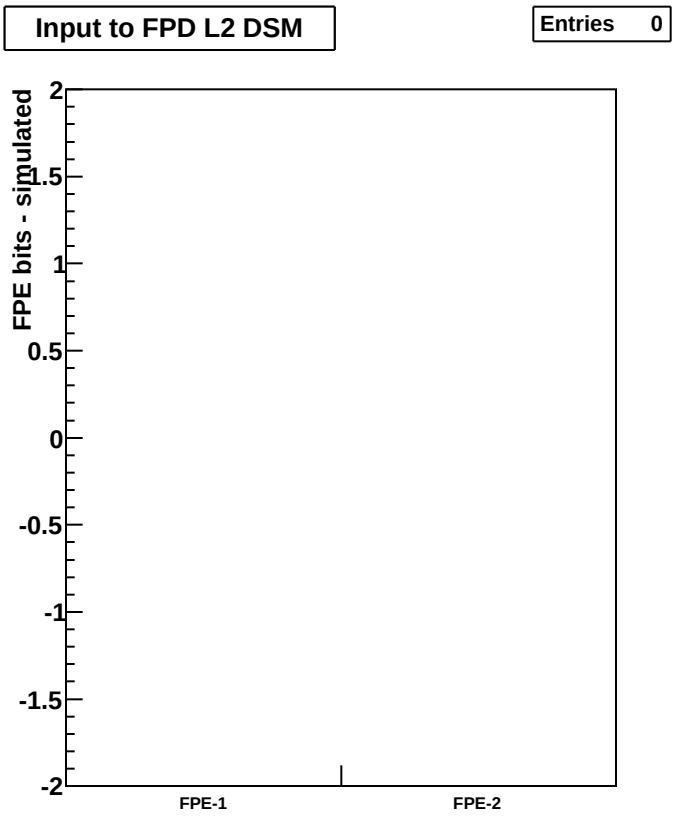

Input to FE003 QT board
Entries 1.804592e+07
ADC
channel


Input to FE004 QT board
Entries 1.804592e+07
ADC
channel


Input to FPE L1 DSM
Entries2255740
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1127870
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
12335
11221
551600
552714
10
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2