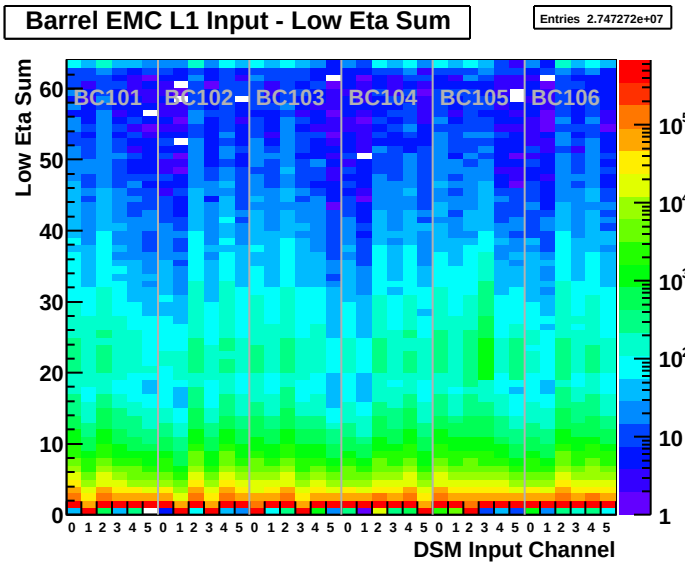
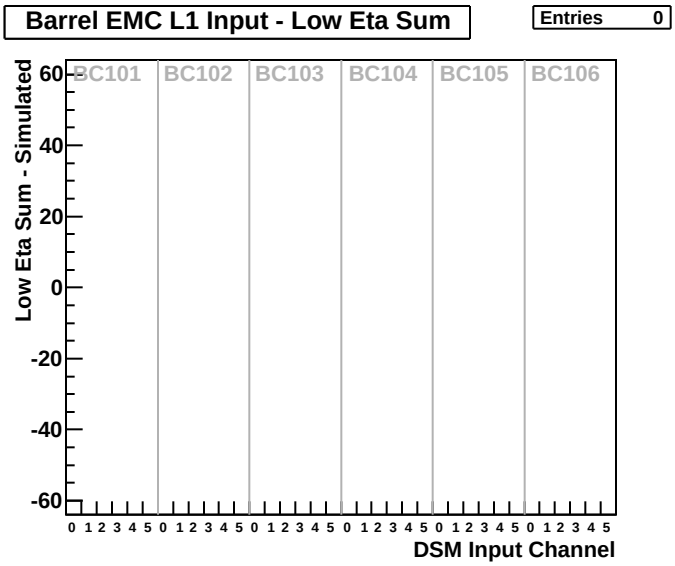
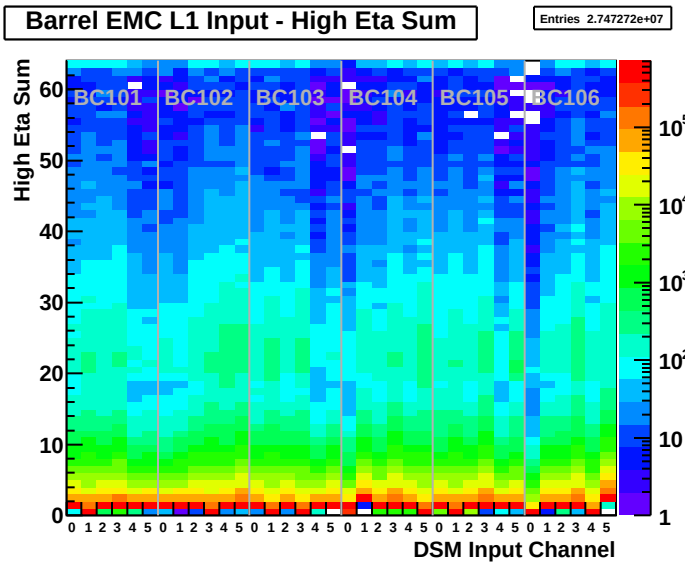
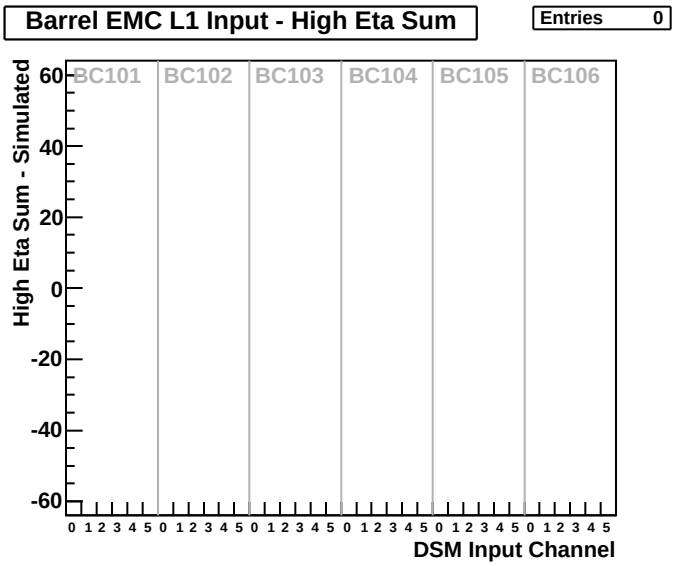
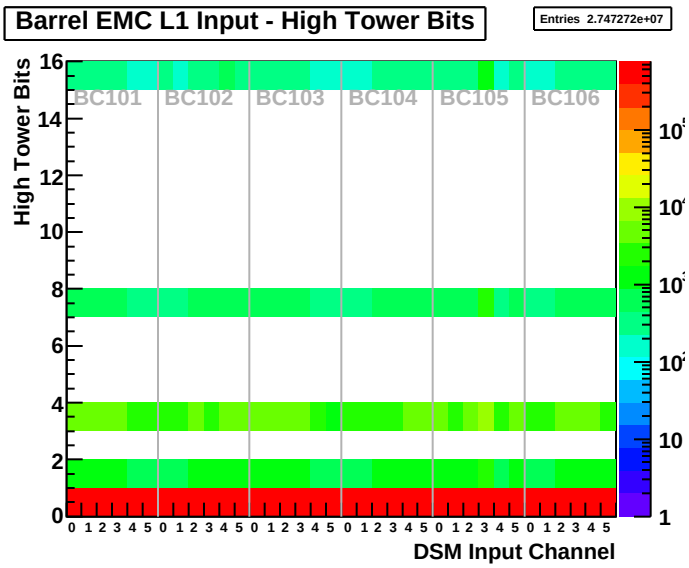
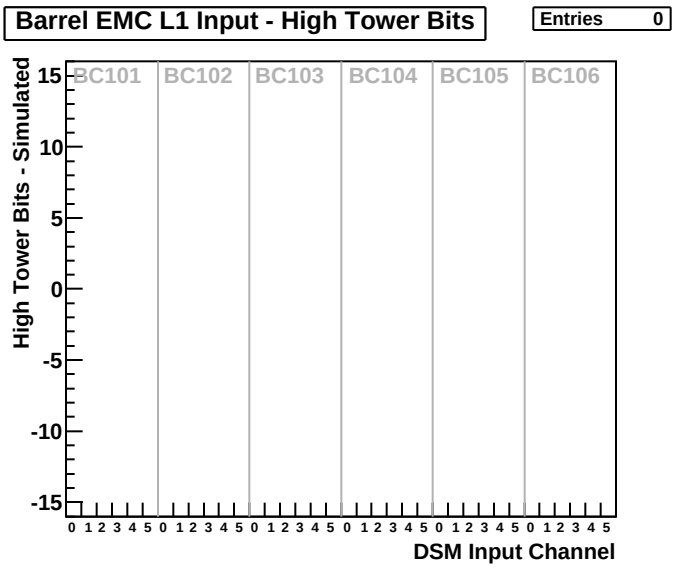

Barrel EMC L1 Input - Low Eta Sum
Entries 2.747272e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

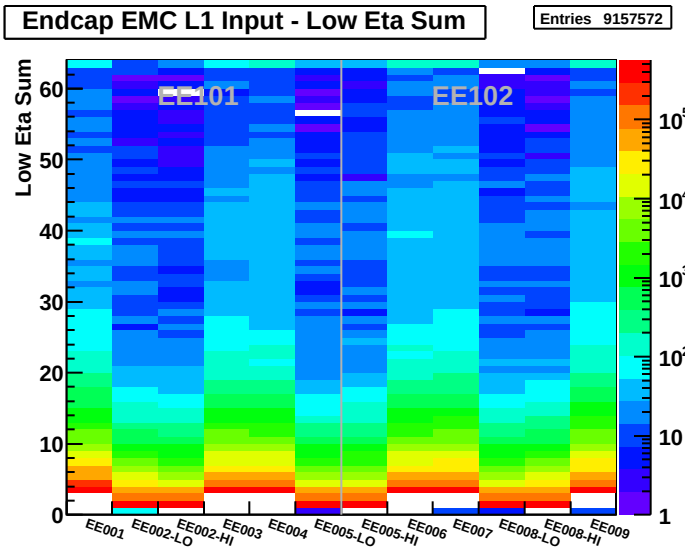

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

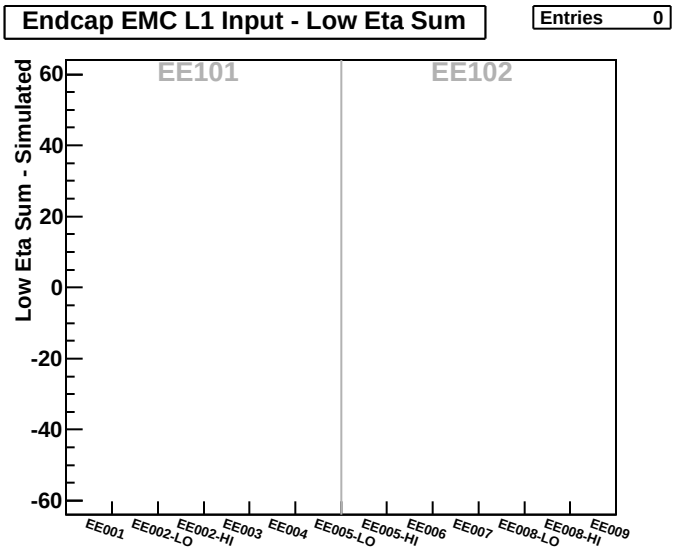

Barrel EMC L1 Input - High Eta Sum
Entries 2.747272e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

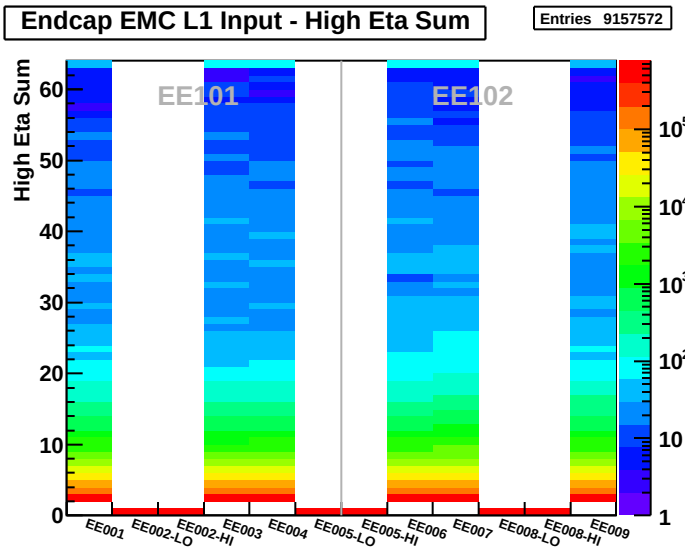

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

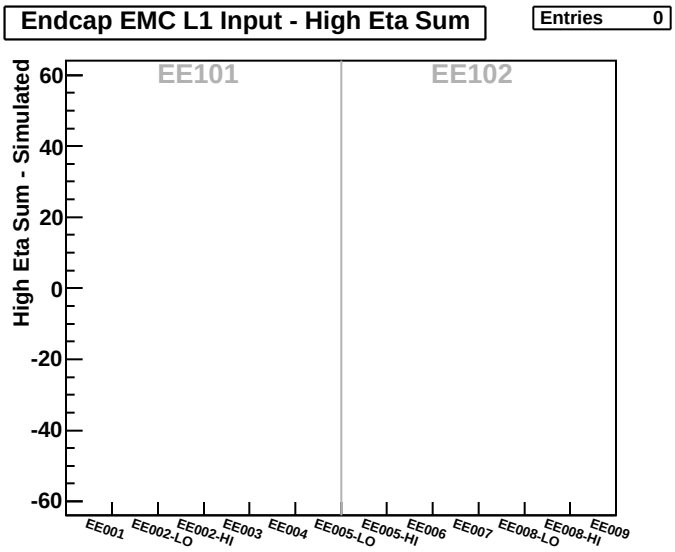

Barrel EMC L1 Input - High Tower Bits
Entries 2.747272e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

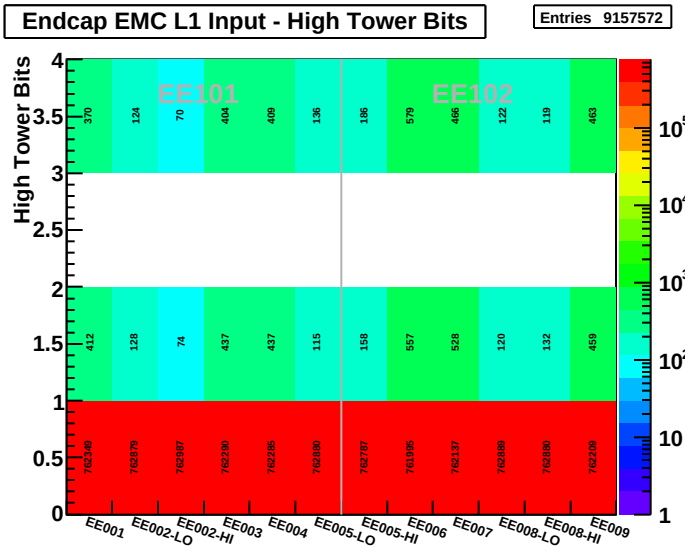

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

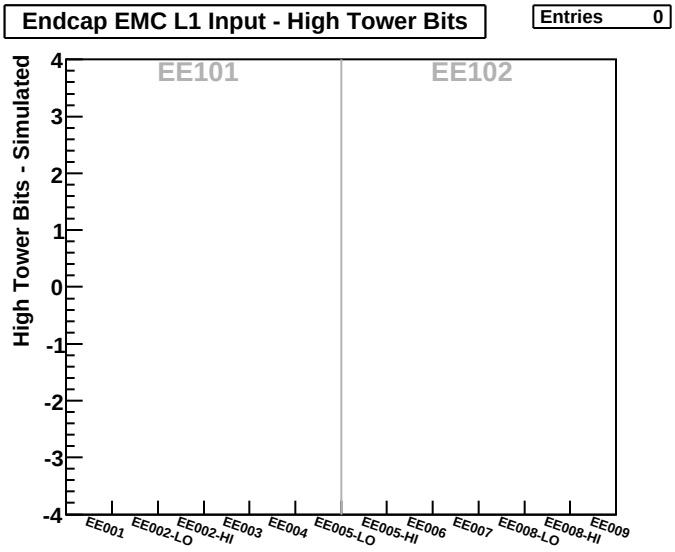

Endcap EMC L1 Input - Low Eta Sum
Entries 9157572
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

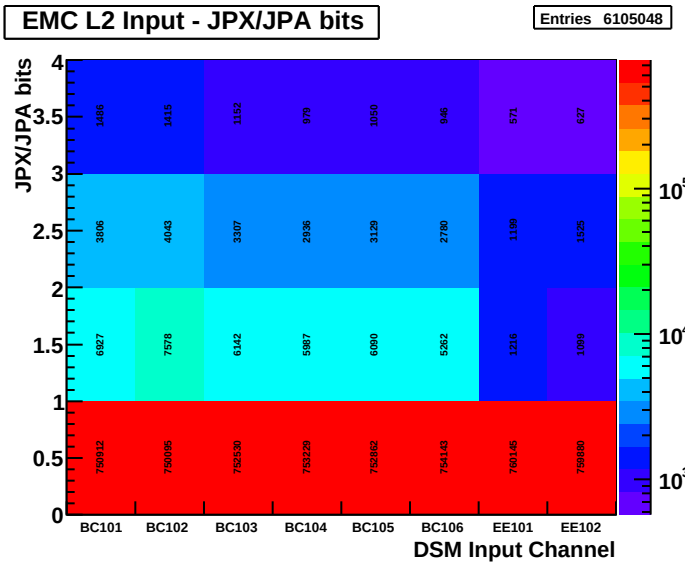

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

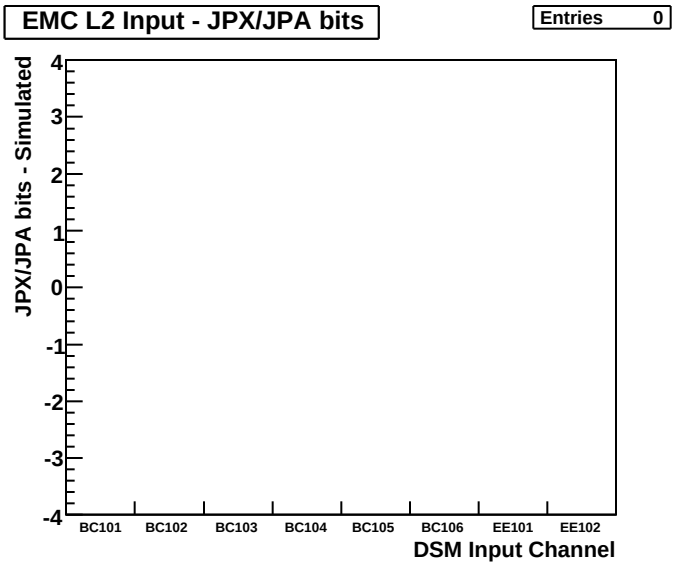

Endcap EMC L1 Input - High Eta Sum
Entries 9157572
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

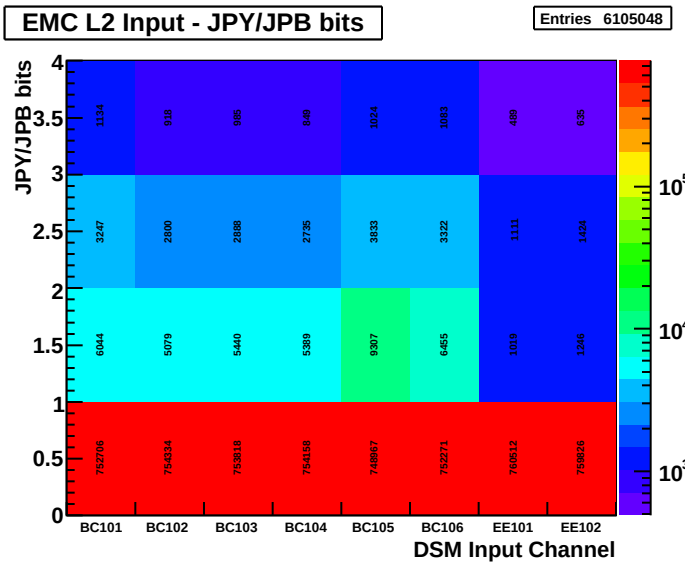

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

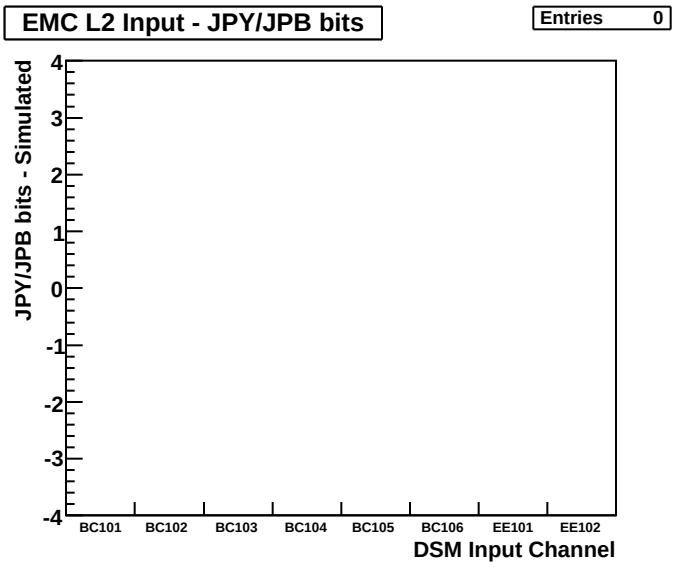

Endcap EMC L1 Input - High Tower Bits
Entries 9157572
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

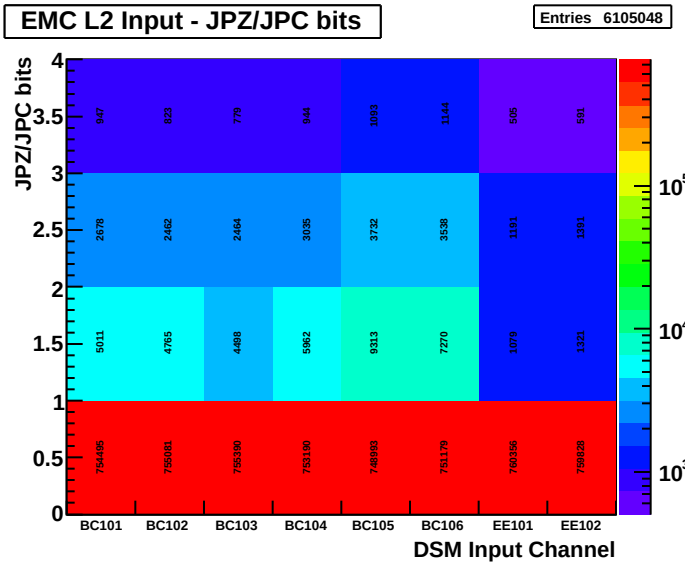

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

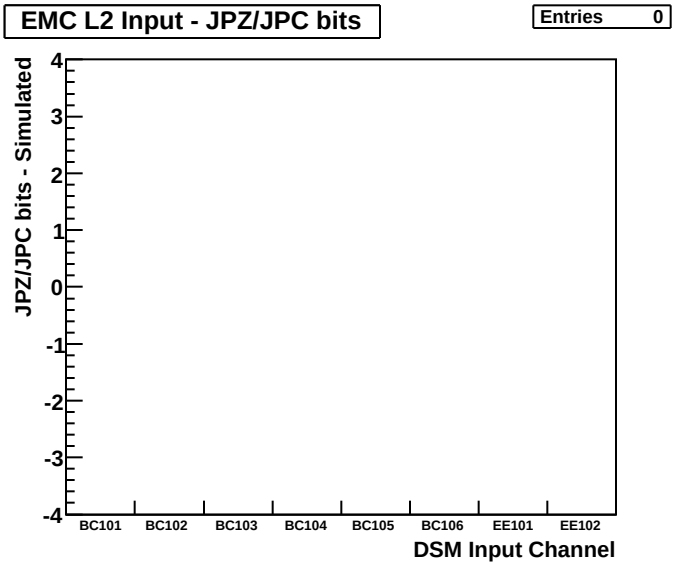

EMC L2 Input - JPX/JPA bits
Entries 6105048
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1486 1415 1152 979 1050 946 571 627
3806 4043 3307 2936 3129 2780 1199 1525
6927 7578 6142 5987 6090 5262 1216 1099
750912 750095 752530 753229 752862 754143 760145 759880

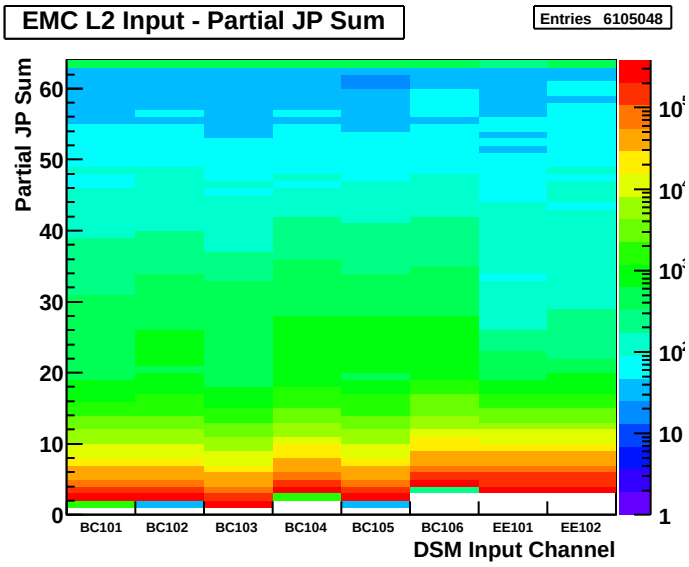

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

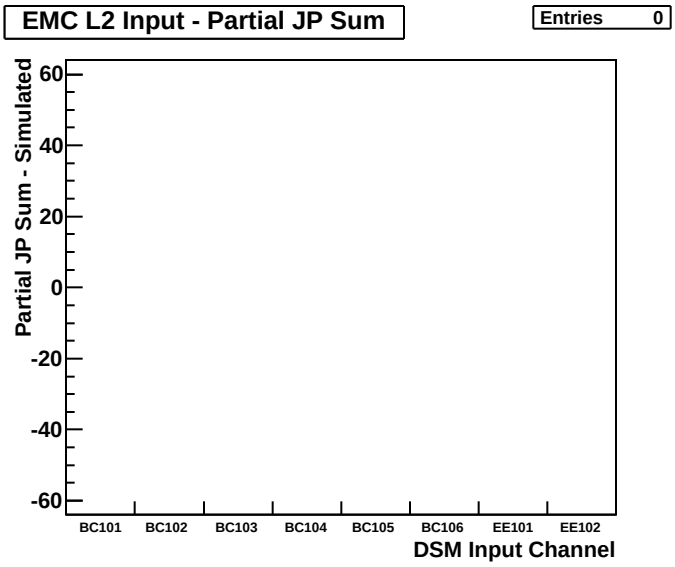

EMC L2 Input - JPY/JPB bits
Entries 6105048
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1134 918 905 849 1024 1093 489 635
3247 2800 2888 2735 3833 3322 1111 1424
6044 5079 5440 5389 9307 6455 1016 1246
752706 754334 753818 754158 749967 752271 760512 759826

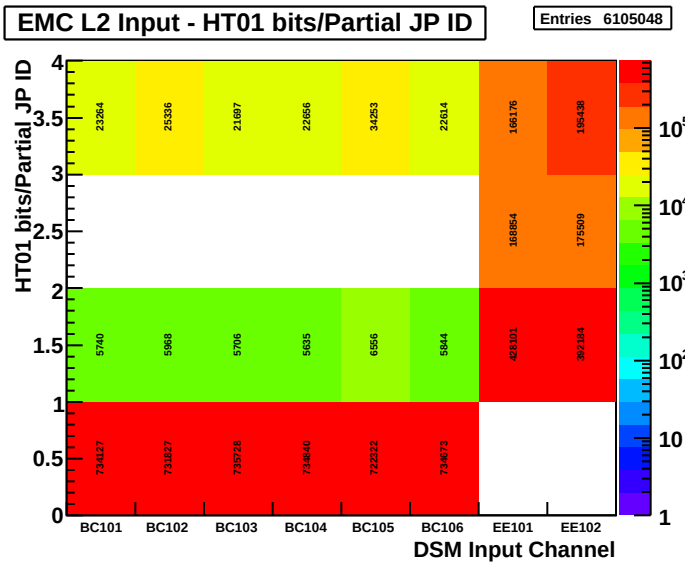

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

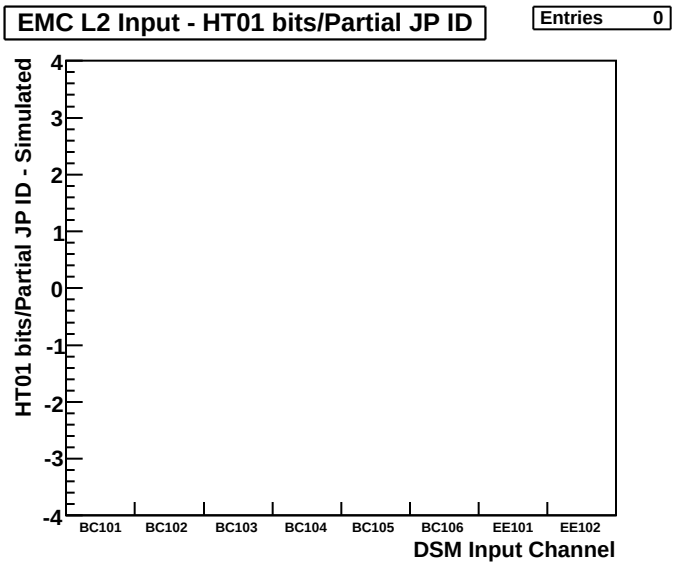

EMC L2 Input - JPZ/JPC bits
Entries 6105048
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
947 823 779 944 1003 1144 505 591
2678 2462 2464 3035 3732 3538 1191 1391
5011 4765 4498 5962 9313 7270 1079 1321
754495 755081 755390 753190 748993 751179 760356 759828

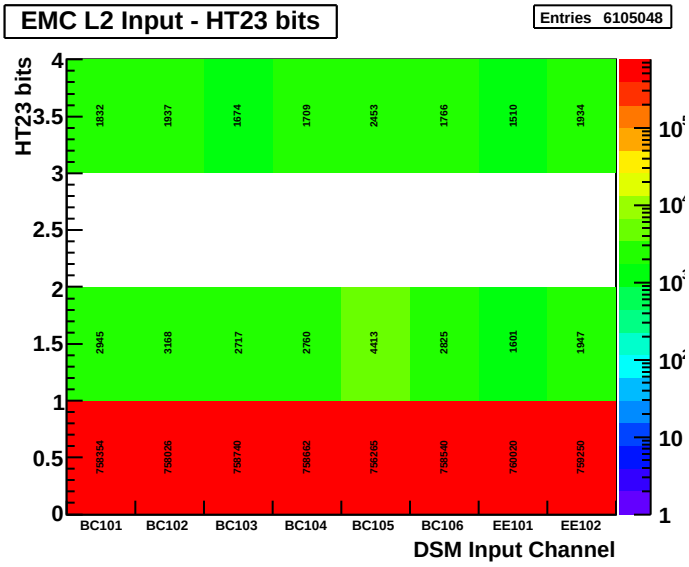

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

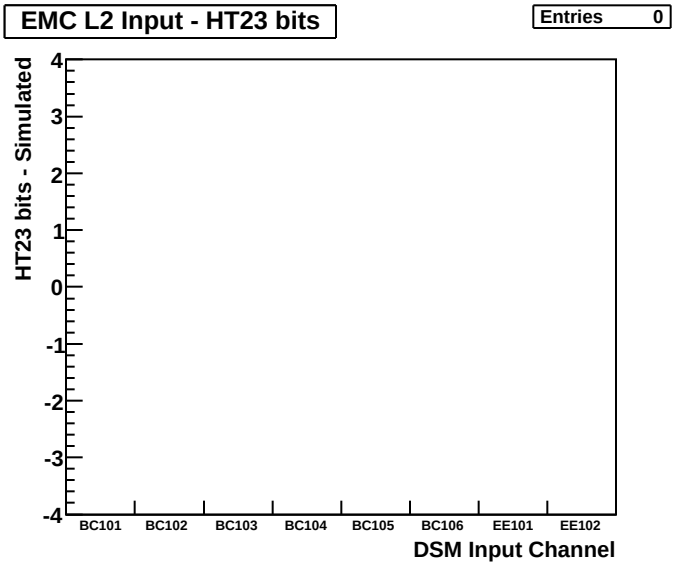

EMC L2 Input - Partial JP Sum
Entries 6105048
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

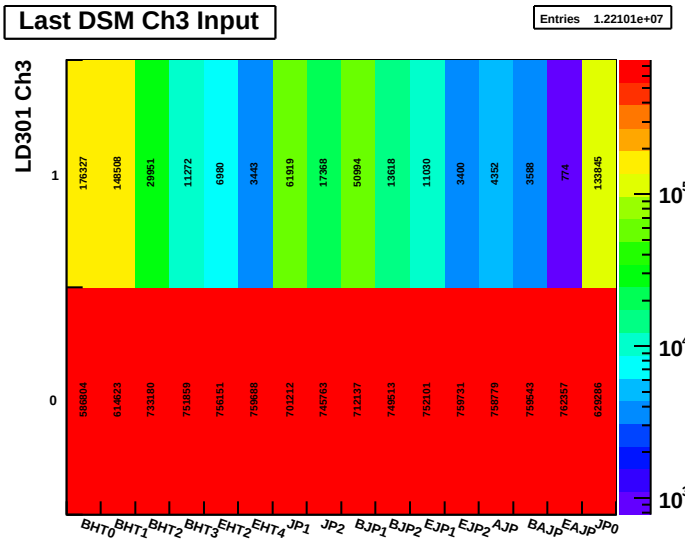

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

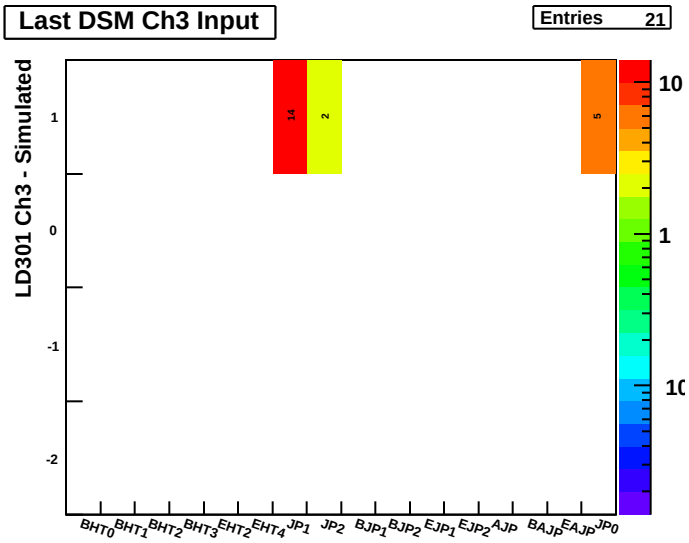

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6105048
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

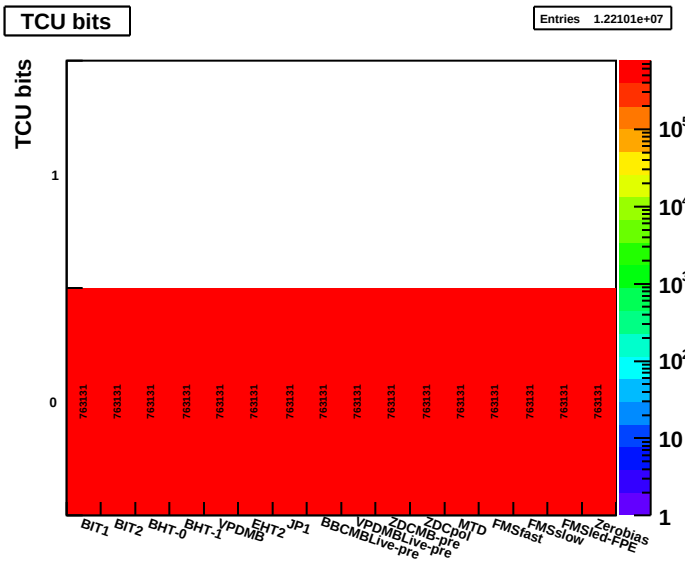

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

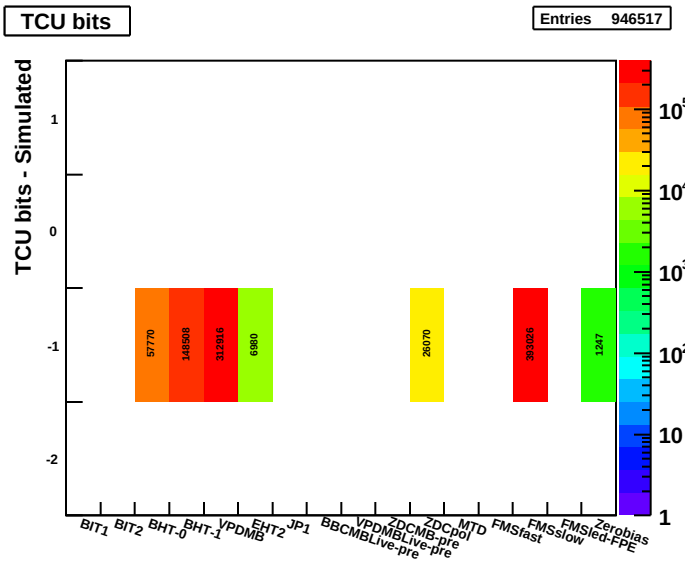

EMC L2 Input - HT23 bits
Entries 6105048
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

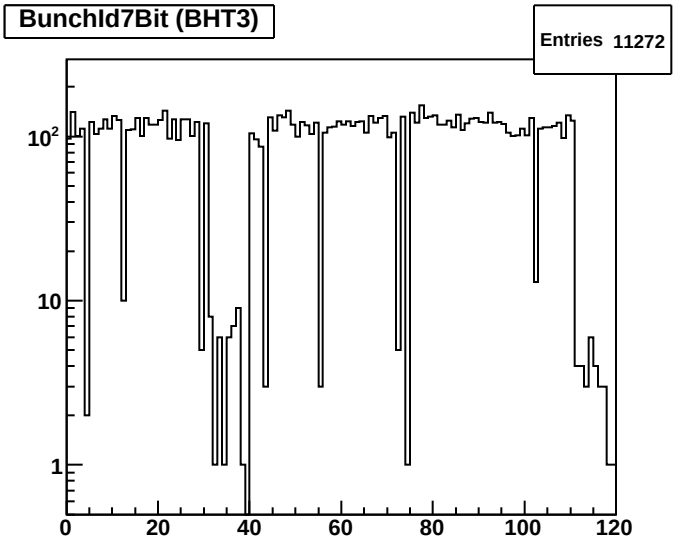

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

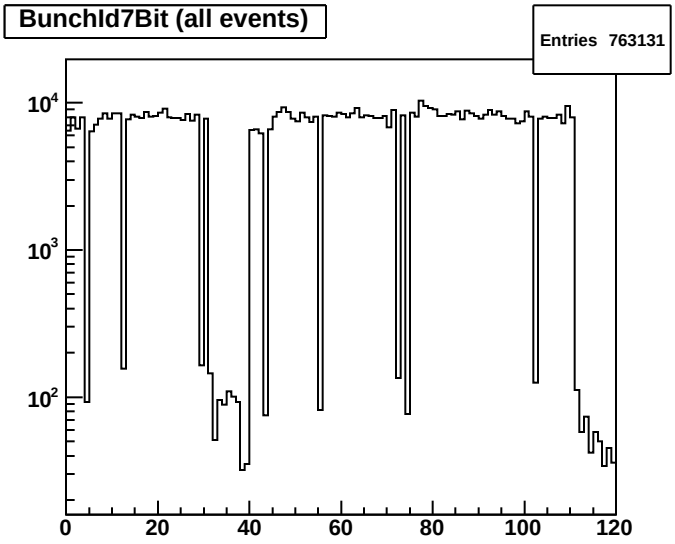

Last DSM Ch3 Input
Entries 1.22101e+07
LD301 Ch3
176327 148508 29951 11272 6980 3443 61919 17368 50994 13618 11030 3400 4352 3588 774 133845
586804 614623 733180 751859 756151 756688 701212 745763 712137 749513 752101 759731 758779 759543 762357 629286
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

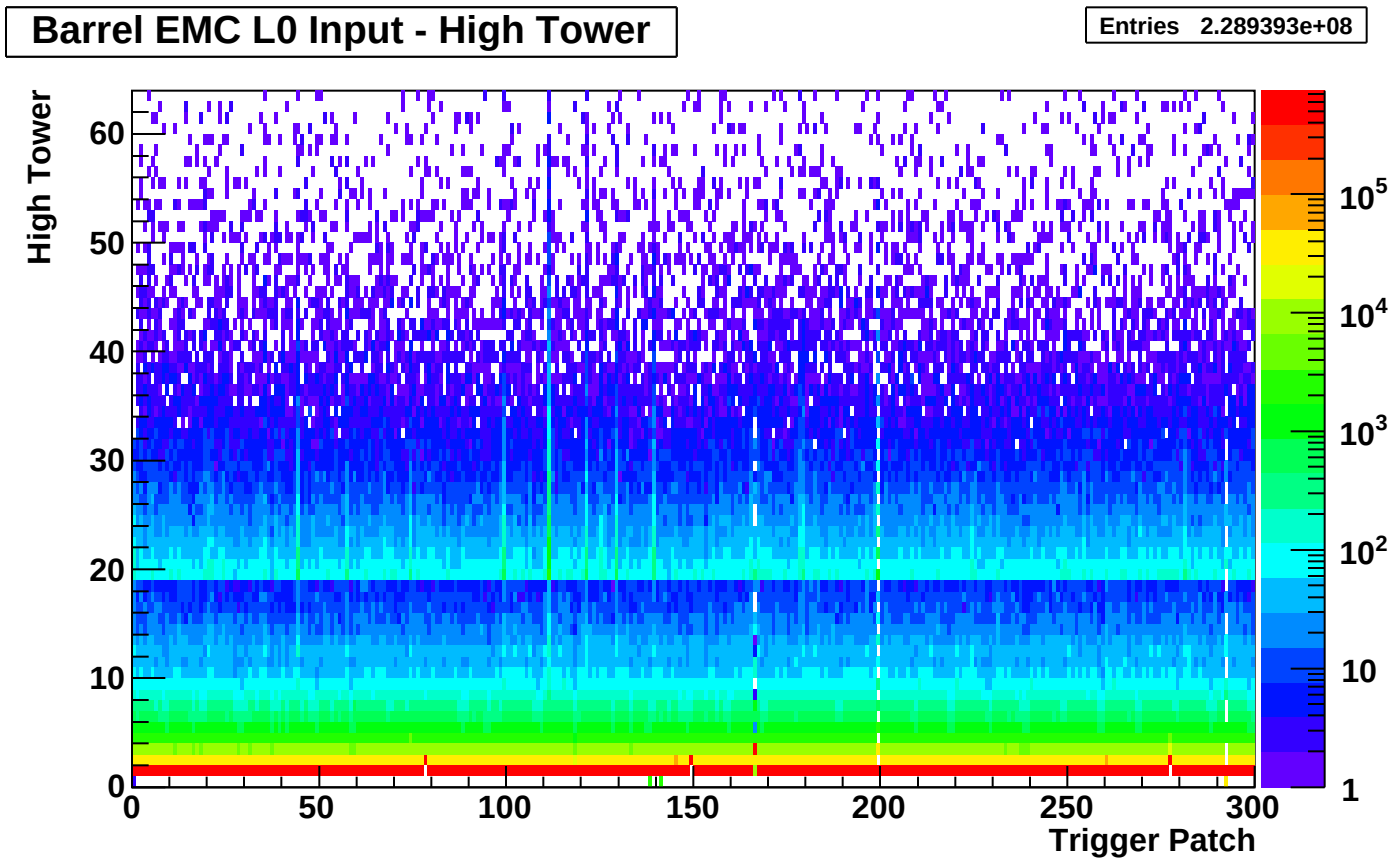

Last DSM Ch3 Input
Entries 21
LD301 Ch3 - Simulated
14 2 5
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

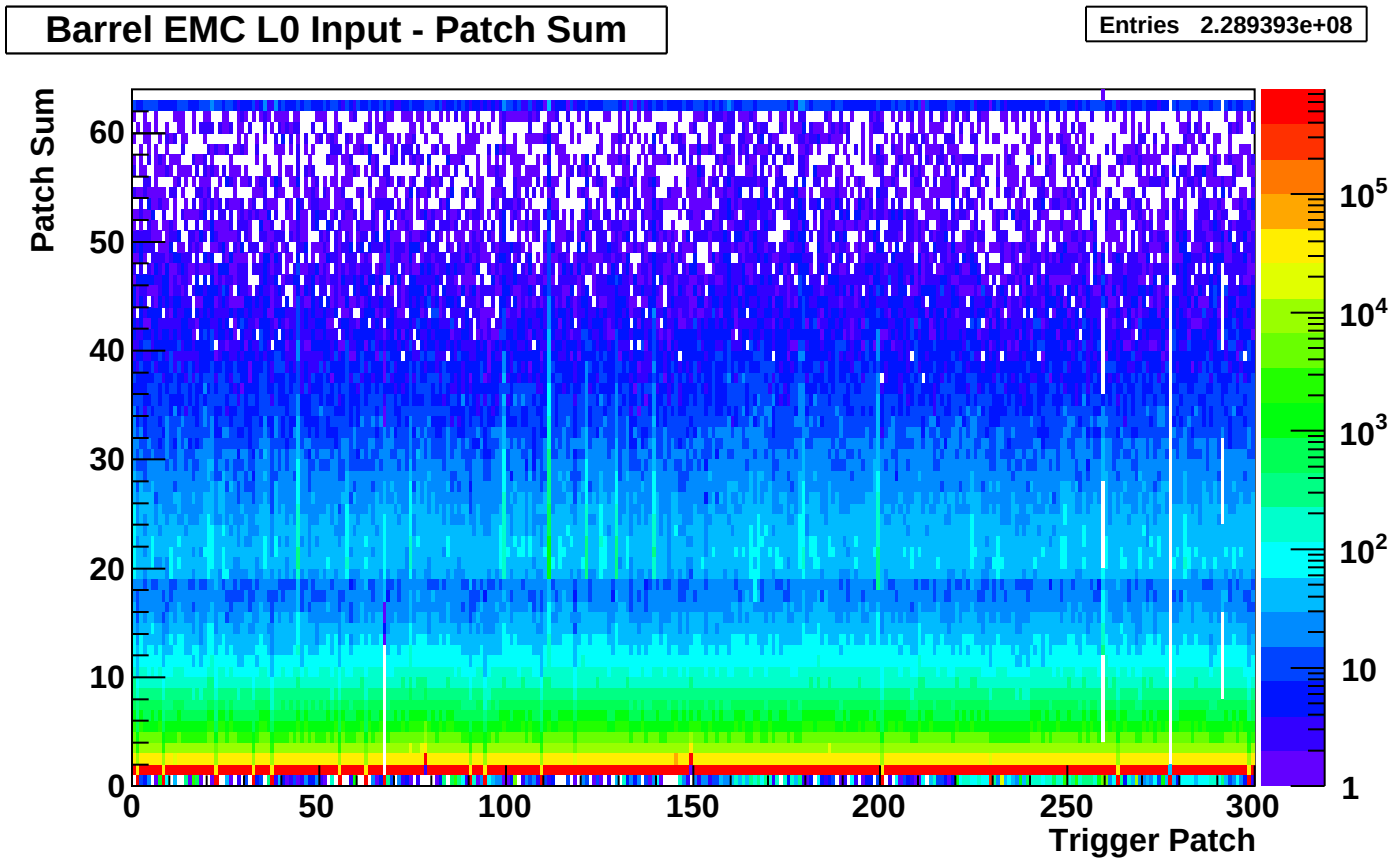

TCU bits
Entries 1.22101e+07
TCU bits
763131 763131 763131 763131 763131 763131 763131 763131 763131 763131 763131 763131 763131 763131 763131 763131
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

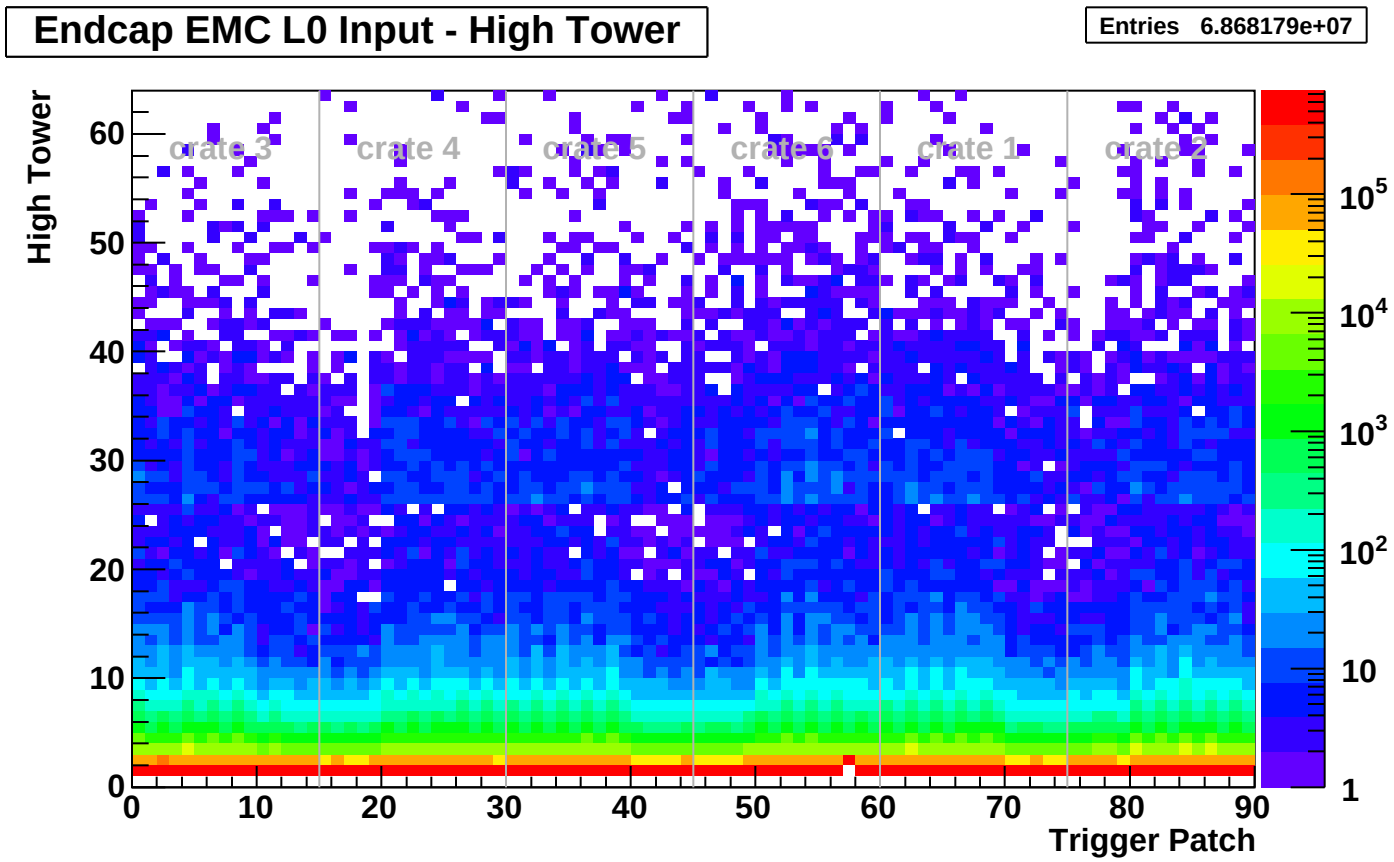

TCU bits
Entries 946517
TCU bits - Simulated
57770 148508 312916 6980 26070 393026 1247
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

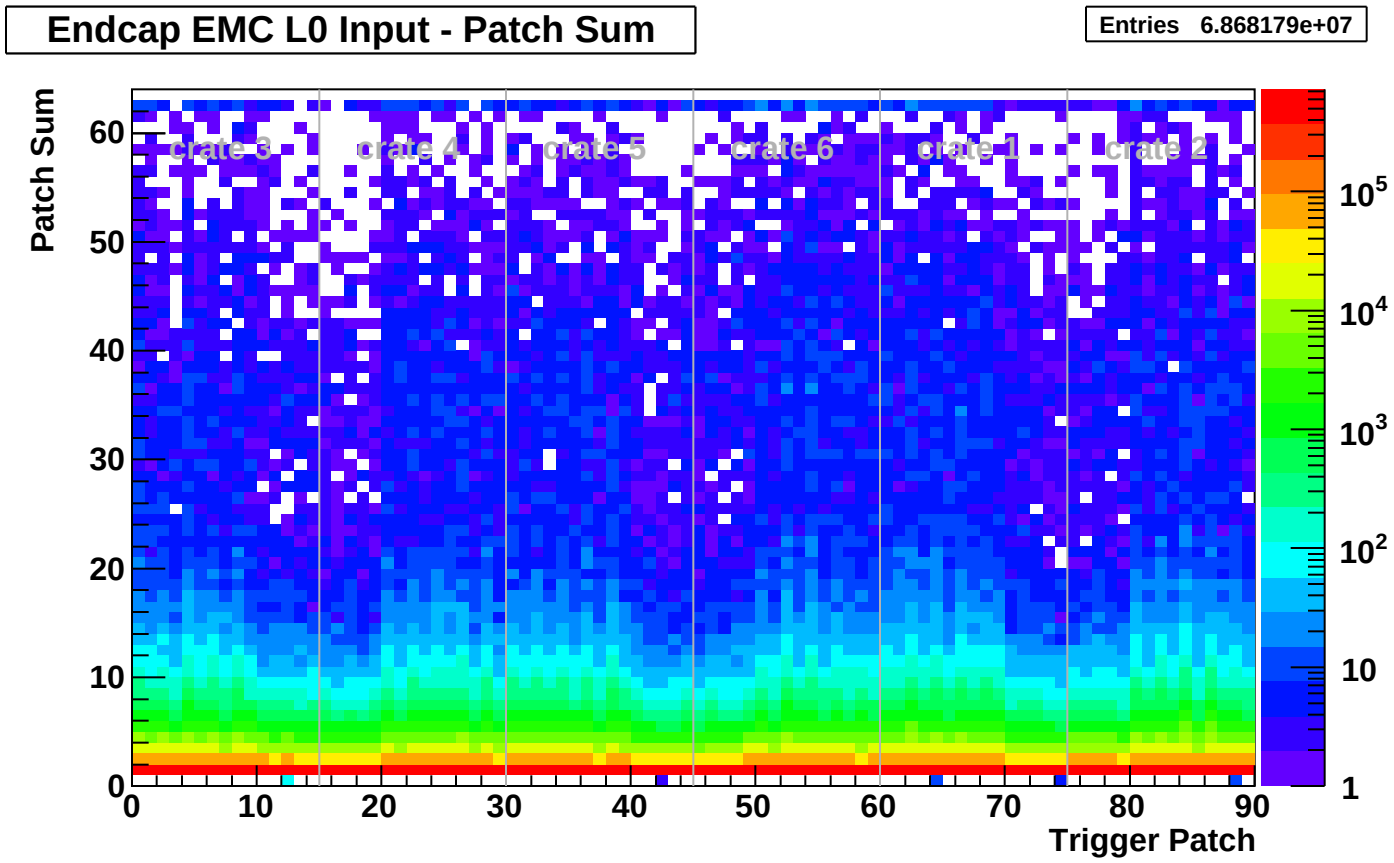

BunchId7Bit (BHT3)
Entries 11272

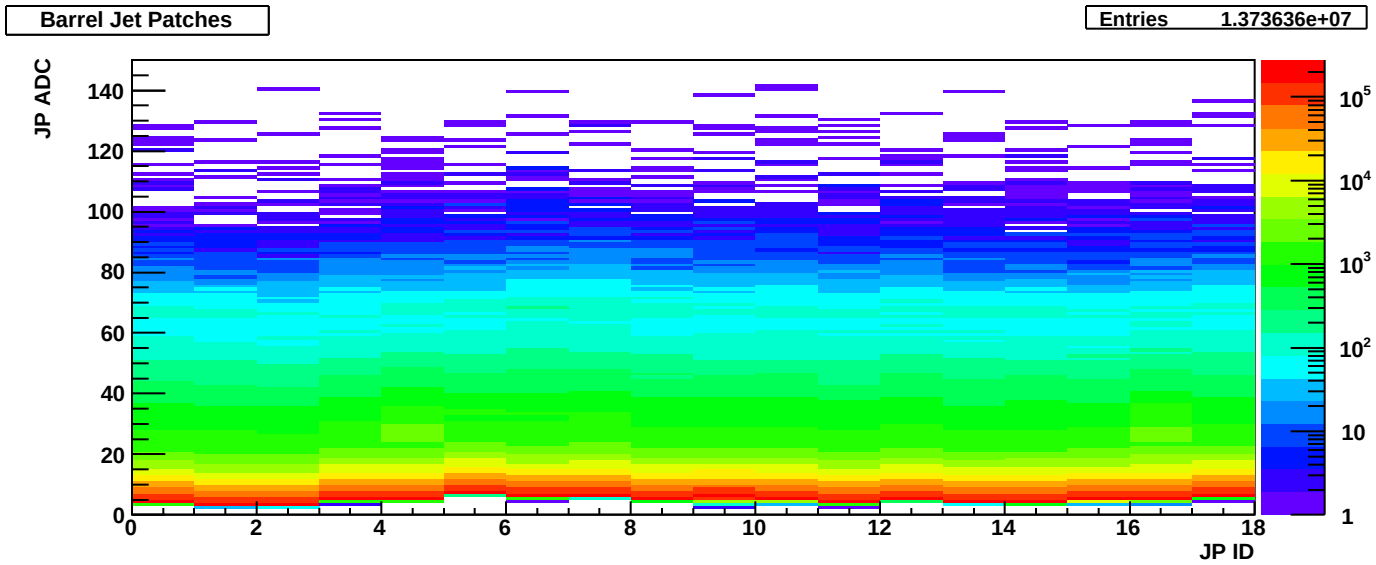

BunchId7Bit (all events)
Entries 763131

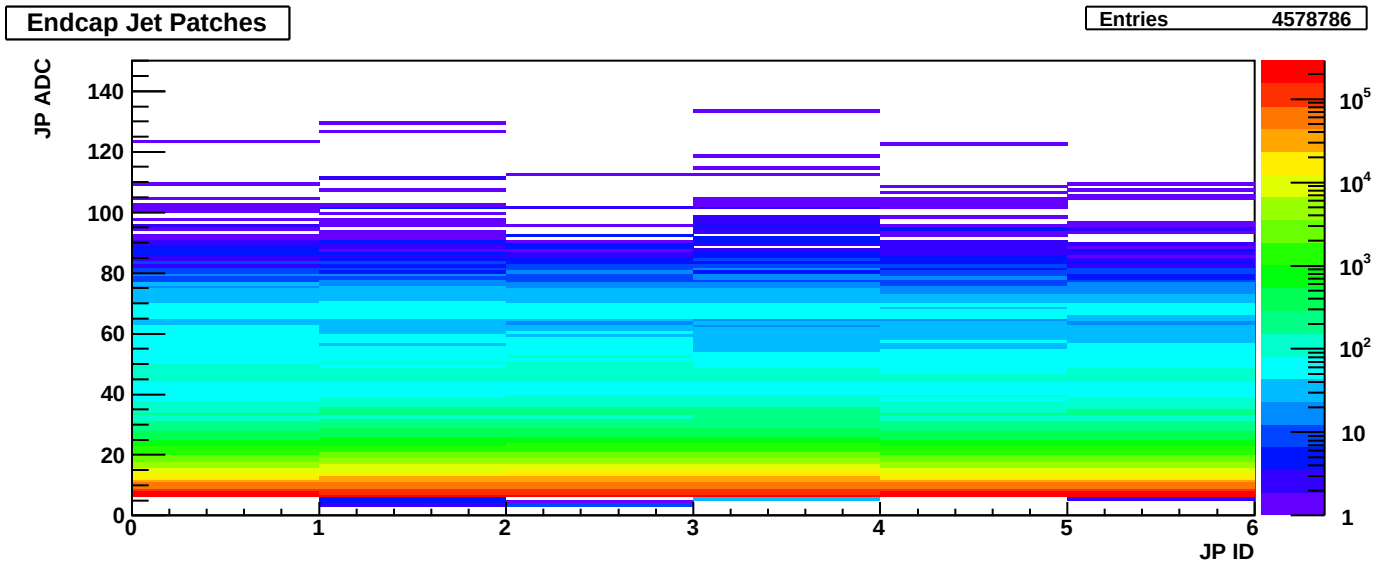

Barrel EMC L0 Input - High Tower
Entries 2.289393e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

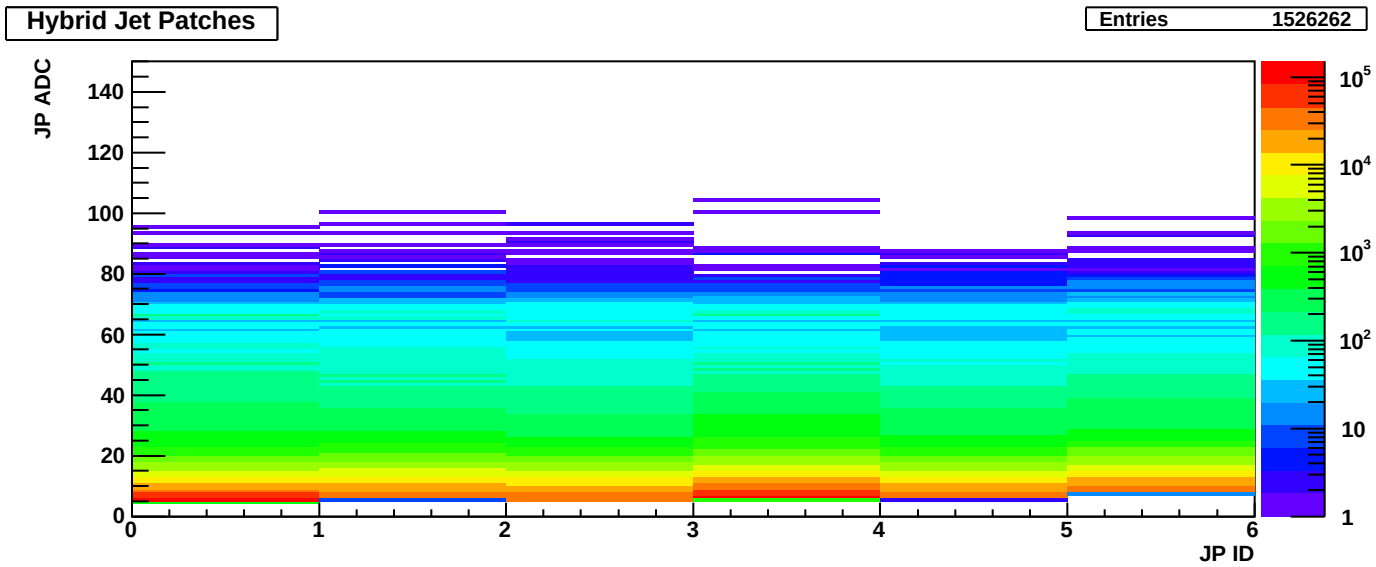

Barrel EMC L0 Input - Patch Sum
Entries 2.289393e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5


Endcap EMC L0 Input - High Tower
Entries 6.868179e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

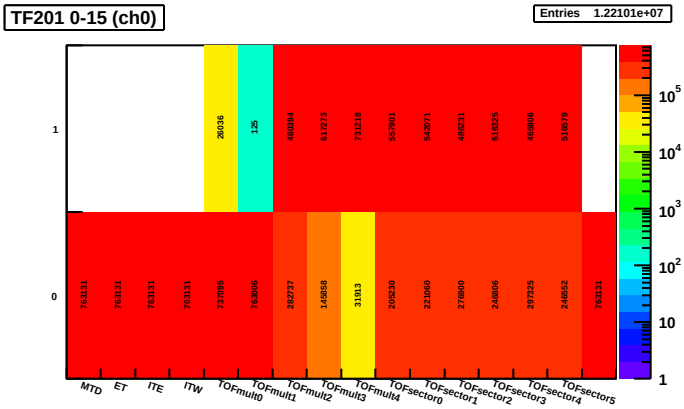

Endcap EMC L0 Input - Patch Sum
Entries 6.868179e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2


Barrel Jet Patches
Entries 1.373636e+07
JP ADC
JP ID

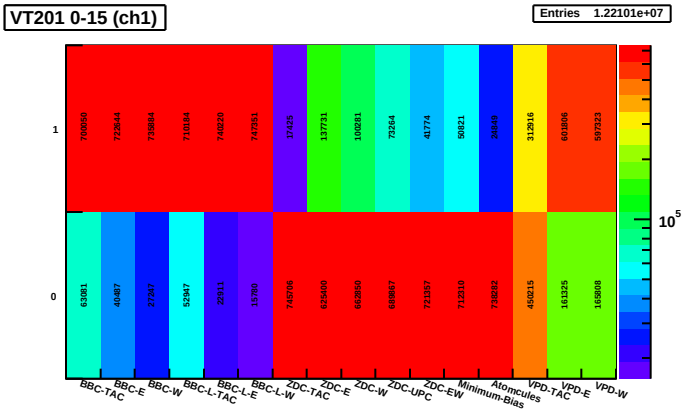

Endcap Jet Patches
Entries 4578786
JP ADC
JP ID


Hybrid Jet Patches
Entries 1526262
JP ADC
JP ID

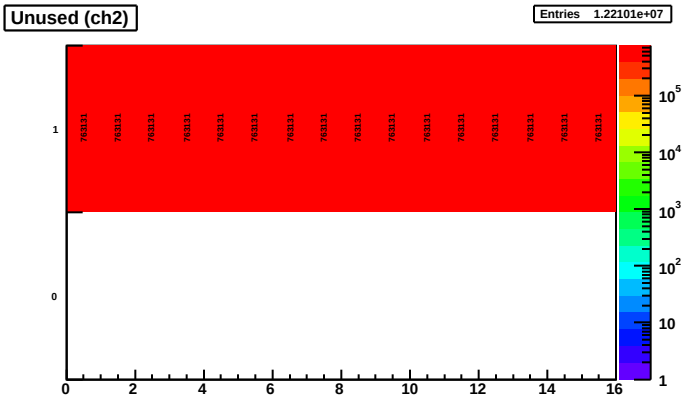
TF201 0-15 (ch0)



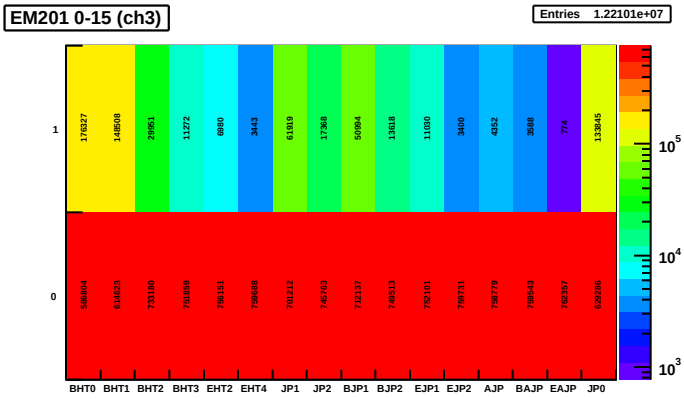
VT201 0-15 (ch1)



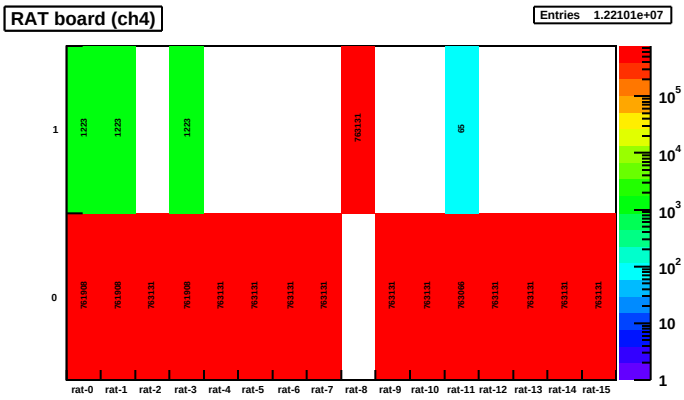
Unused (ch2)



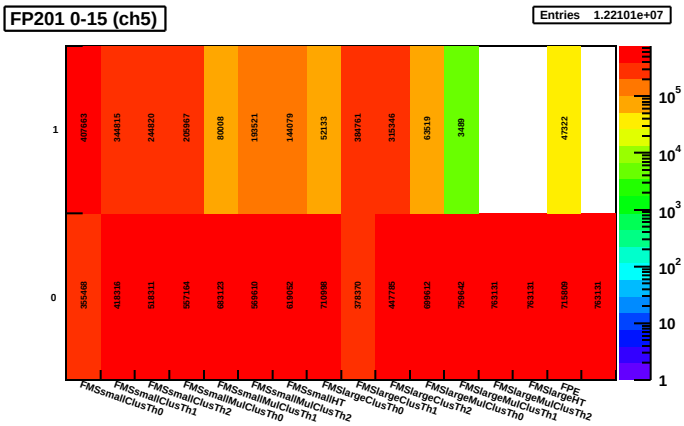
EM201 0-15 (ch3)



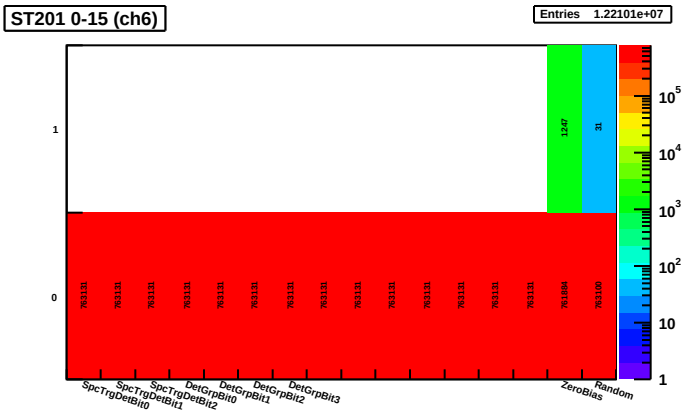
RAT board (ch4)



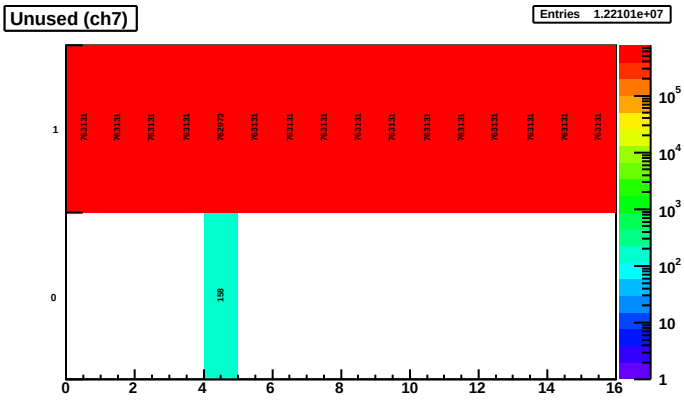
FP201 0-15 (ch5)

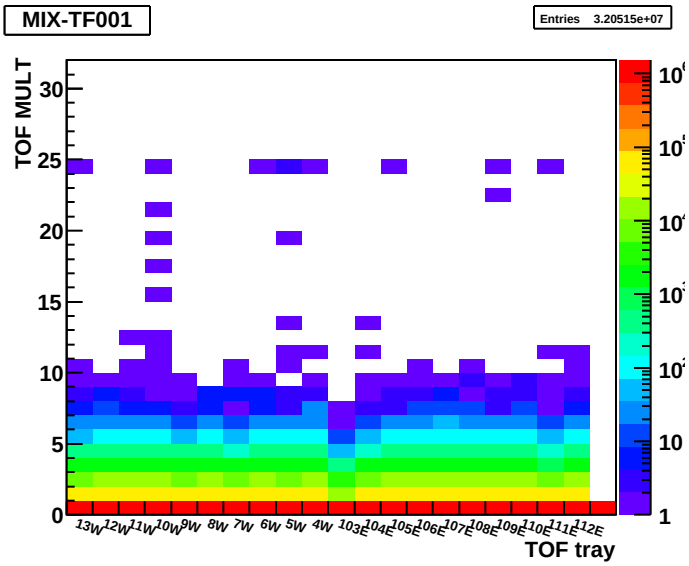


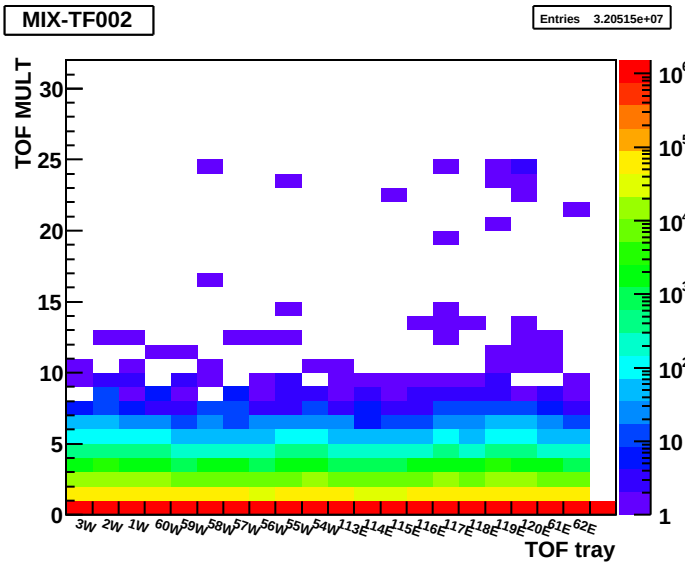
ST201 0-15 (ch6)

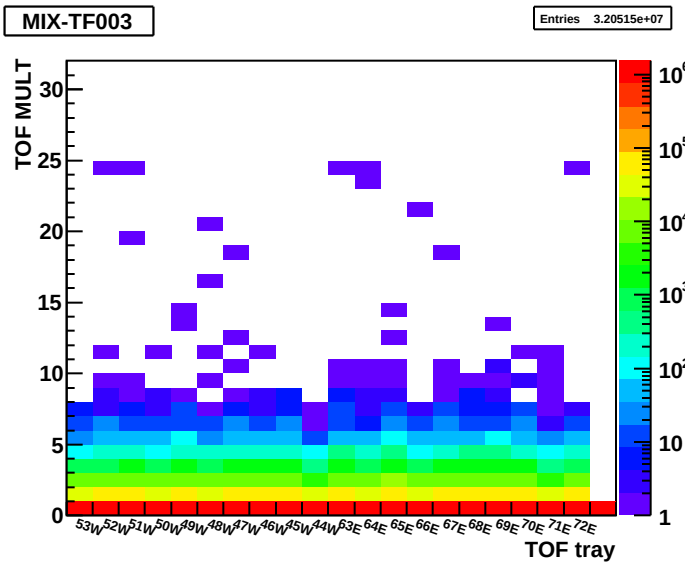


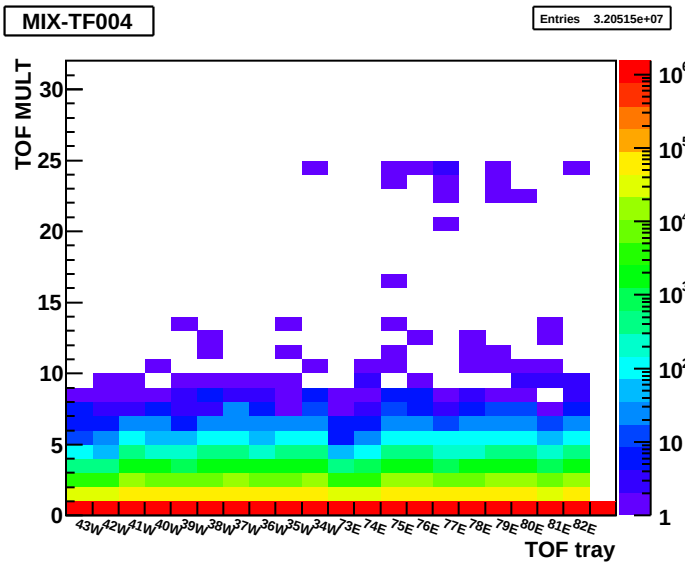
Unused (ch7)

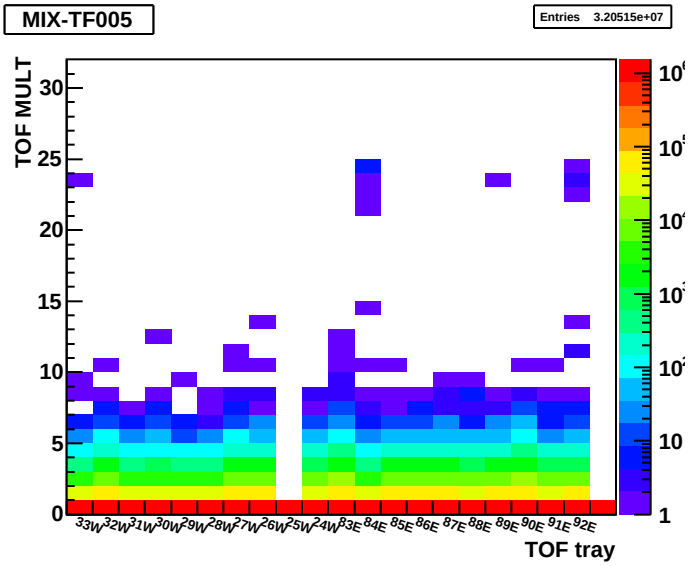


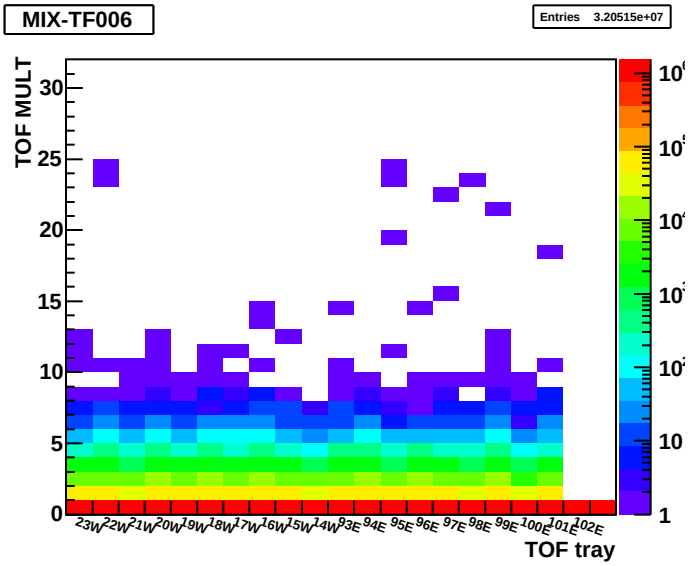

MIX-TF001
Entries 3.20515e+07
TOF MULT
TOF tray

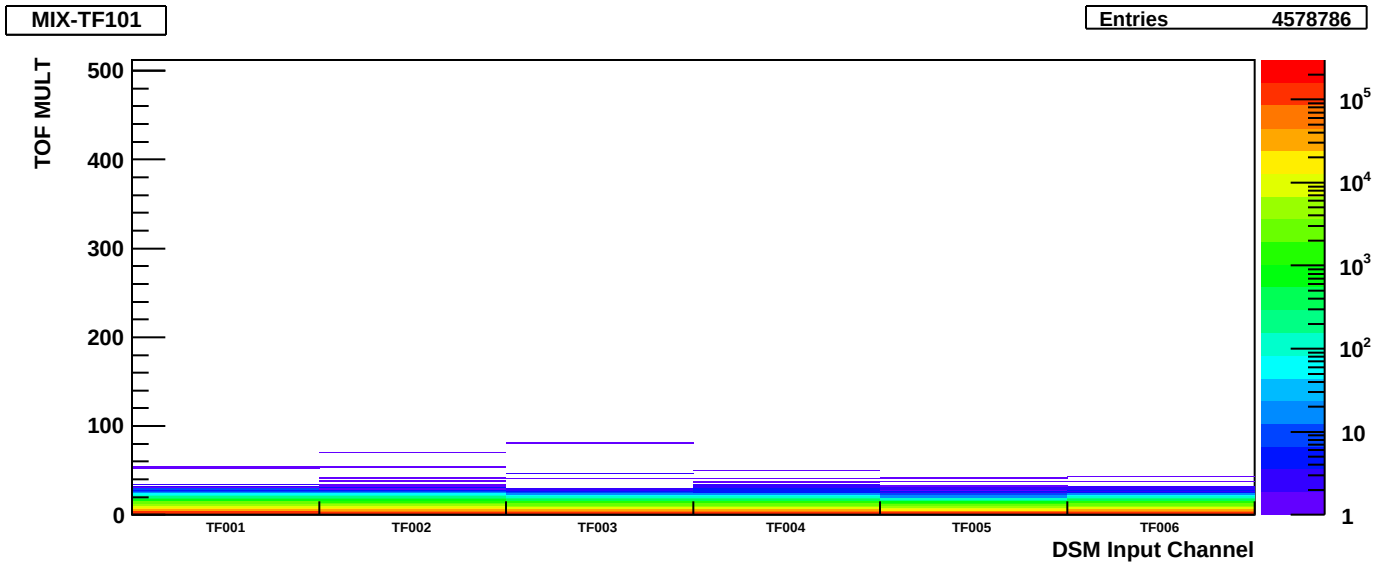

MIX-TF002
Entries 3.20515e+07
TOF MULT
TOF tray

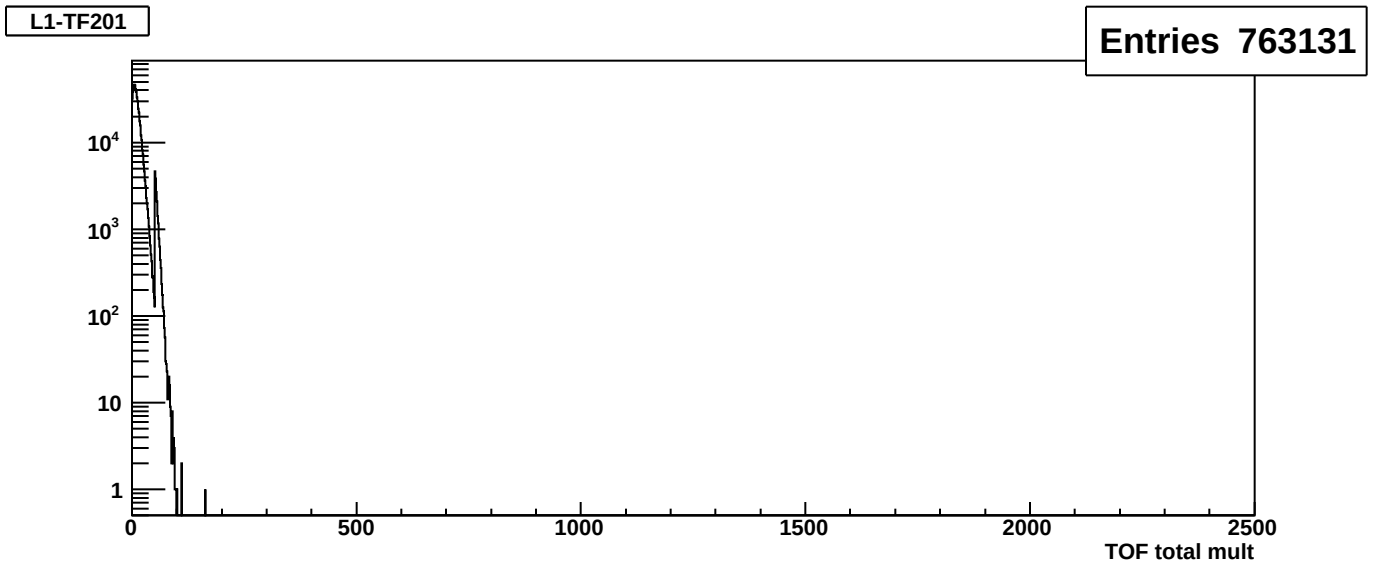

MIX-TF003
Entries 3.20515e+07
TOF MULT
TOF tray

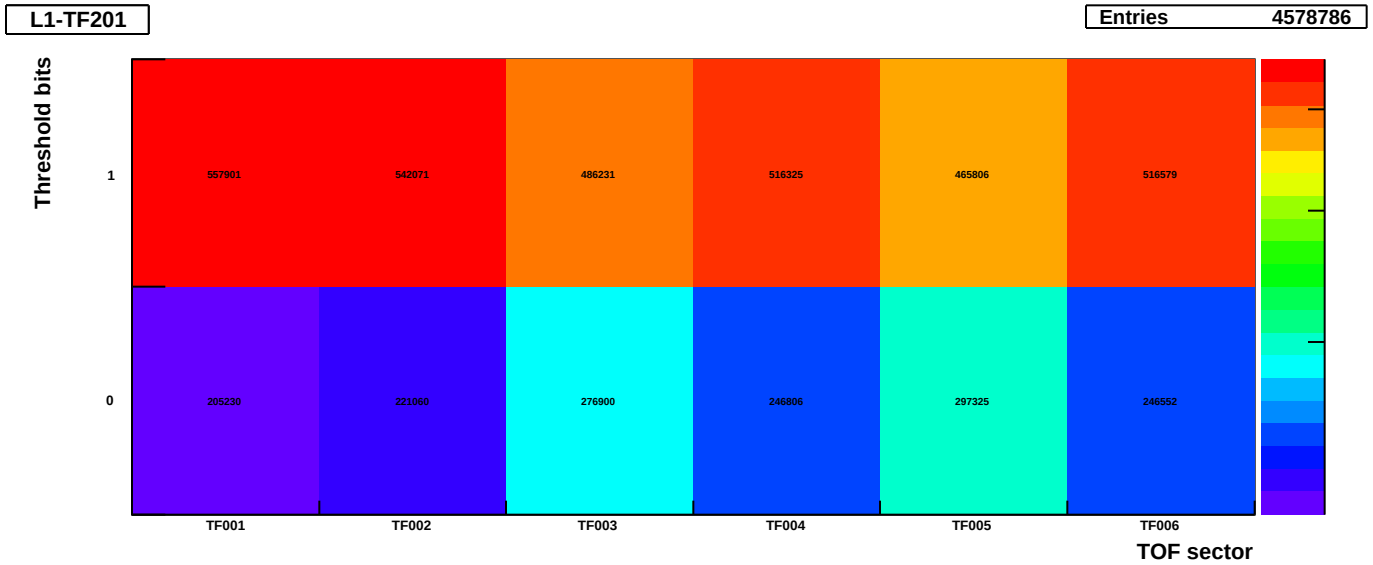

MIX-TF004
Entries 3.20515e+07
TOF MULT
TOF tray

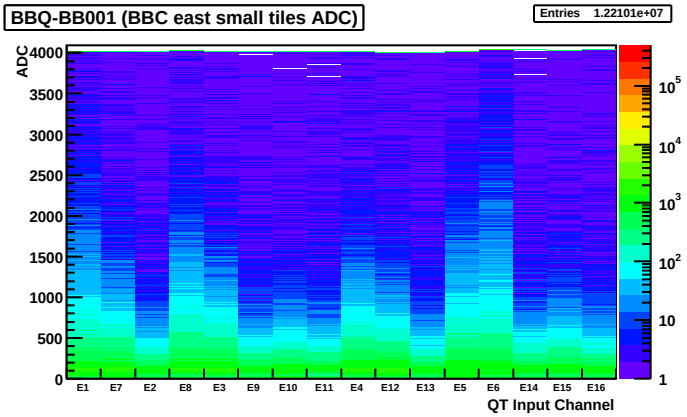

MIX-TF005
Entries 3.20515e+07
TOF MULT
TOF tray

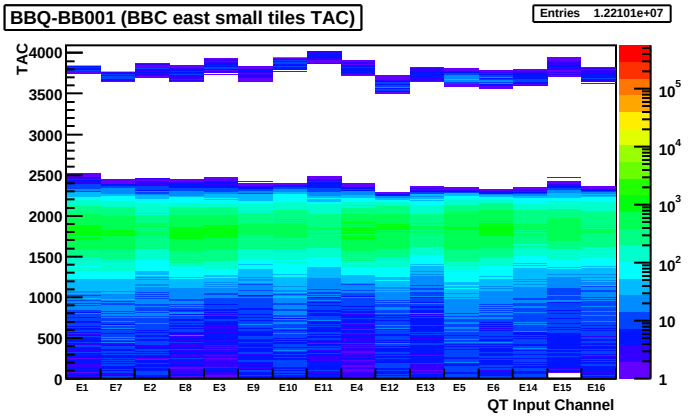

MIX-TF006
Entries 3.20515e+07
TOF MULT
TOF tray

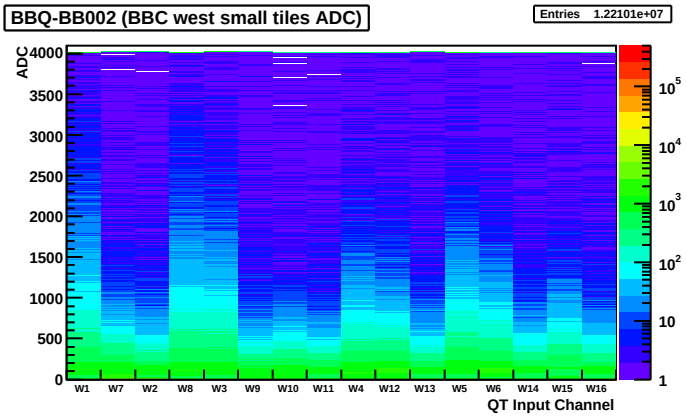

MIX-TF101
Entries 4578786
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

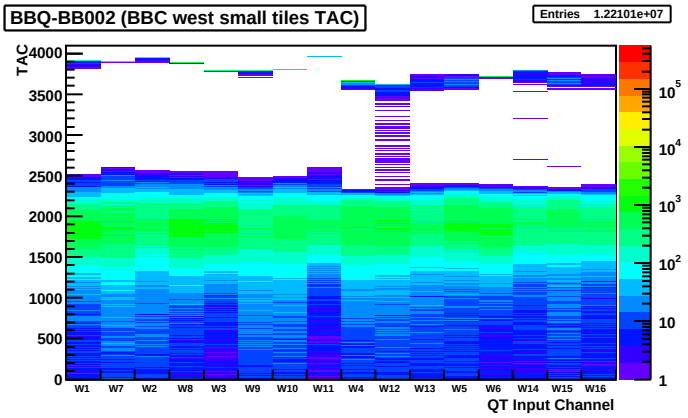

L1-TF201
Entries 763131
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

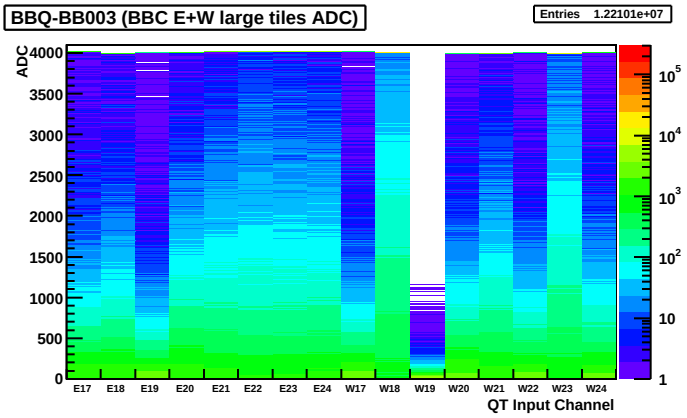

L1-TF201
Entries 4578786
Threshold bits
1
0
557901
542071
486231
516325
465806
516579
205230
221060
276900
246806
297325
246552
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

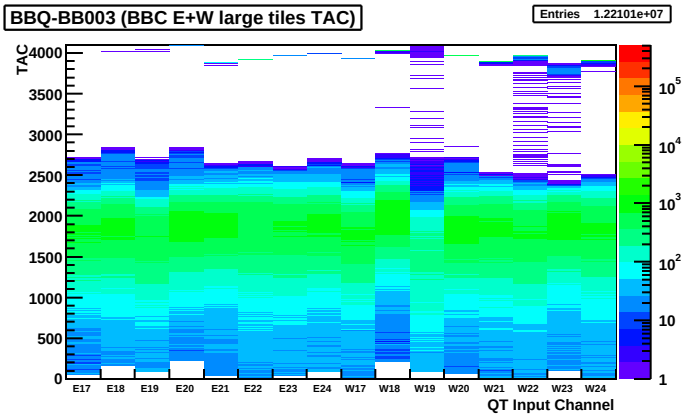

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.22101e+07
ADC
QT Input Channel

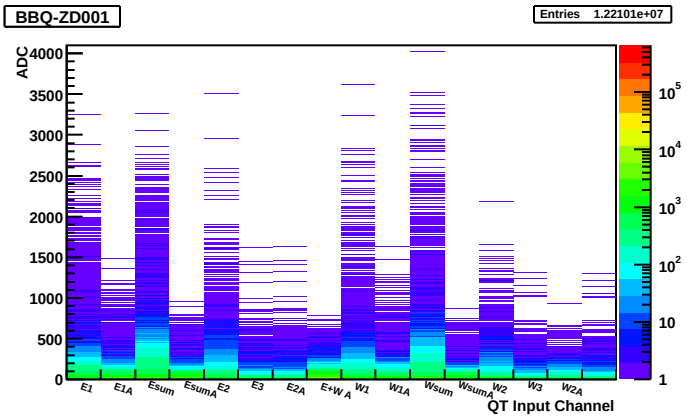

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.22101e+07
TAC
QT Input Channel

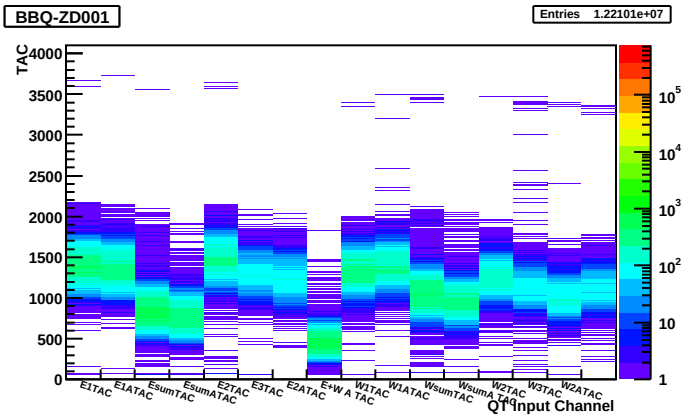

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.22101e+07
ADC
QT Input Channel

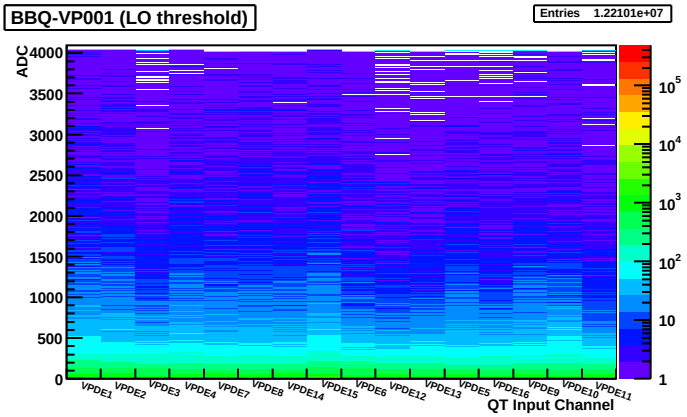

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.22101e+07
TAC
QT Input Channel

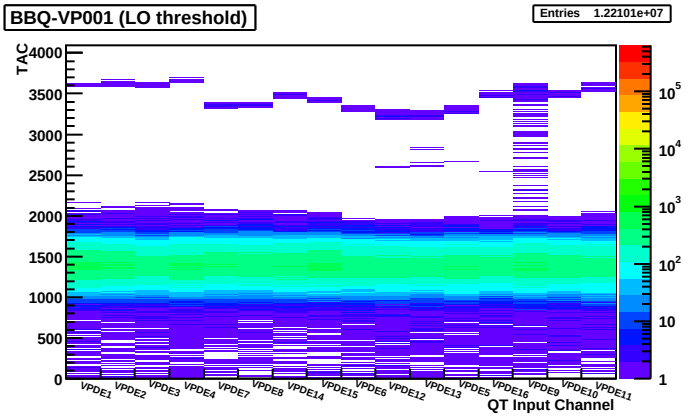

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.22101e+07
ADC
QT Input Channel

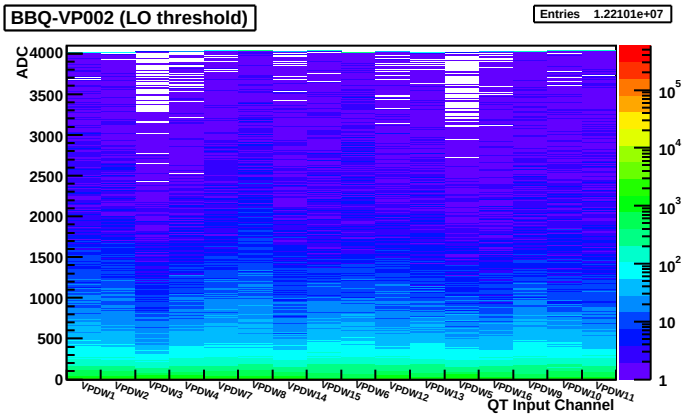

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.22101e+07
TAC
QT Input Channel

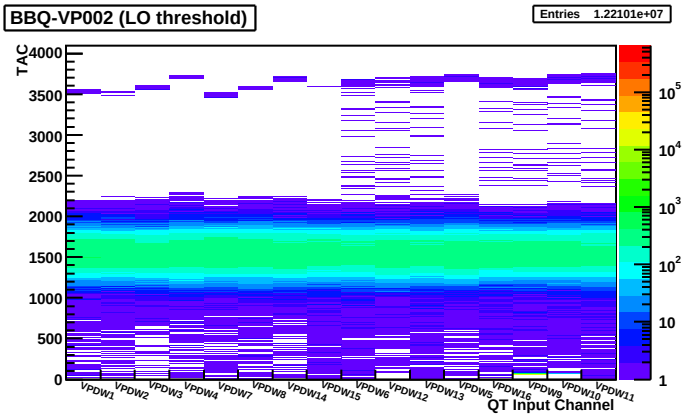

BBQ-ZD001
Entries 1.22101e+07
ADC
QT Input Channel

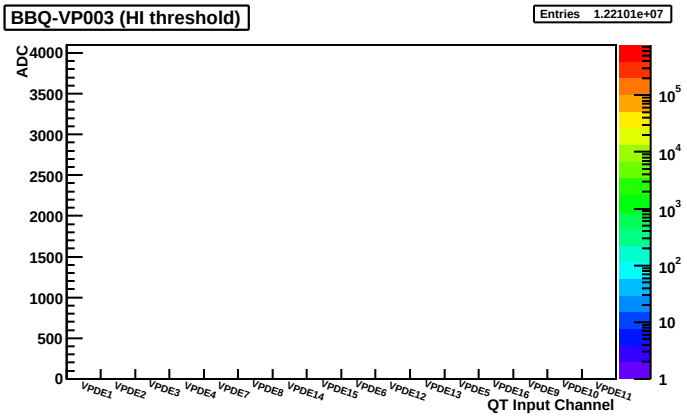

BBQ-ZD001
Entries 1.22101e+07
TAC
QT Input Channel

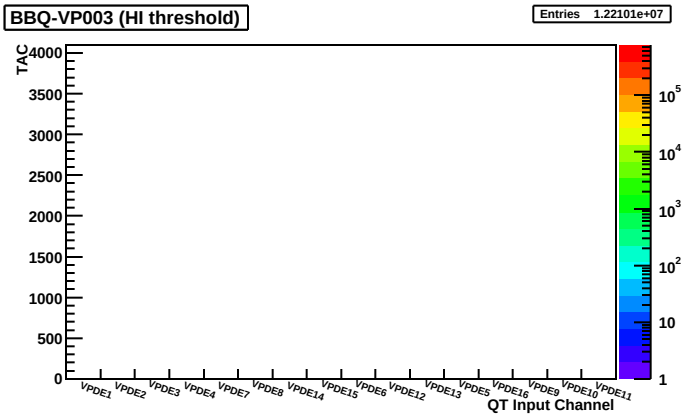

BBQ-VP001 (LO threshold)
Entries 1.22101e+07
ADC
QT Input Channel

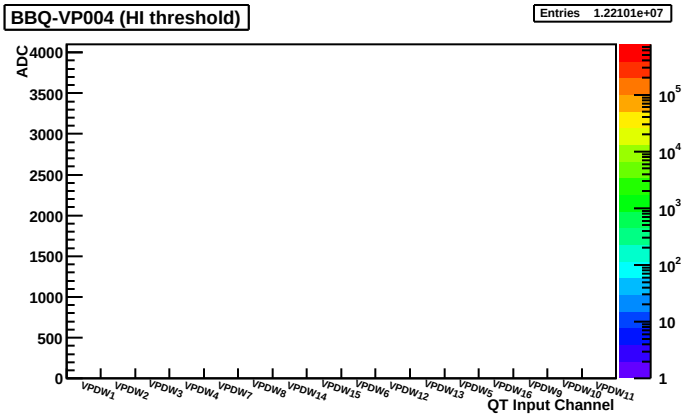

BBQ-VP001 (LO threshold)
Entries 1.22101e+07
TAC
QT Input Channel

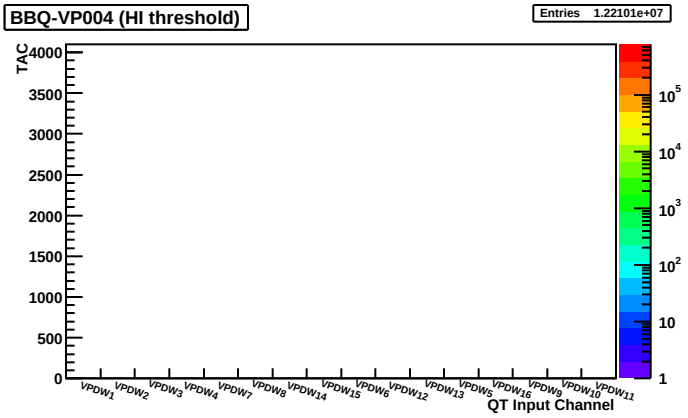

BBQ-VP002 (LO threshold)
Entries 1.22101e+07
ADC
QT Input Channel

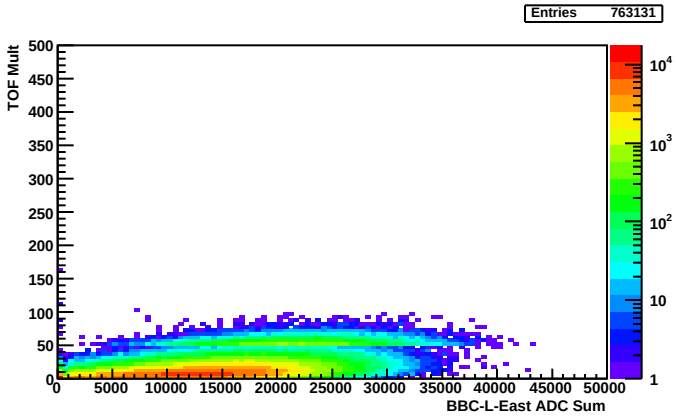

BBQ-VP002 (LO threshold)
Entries 1.22101e+07
TAC
QT Input Channel

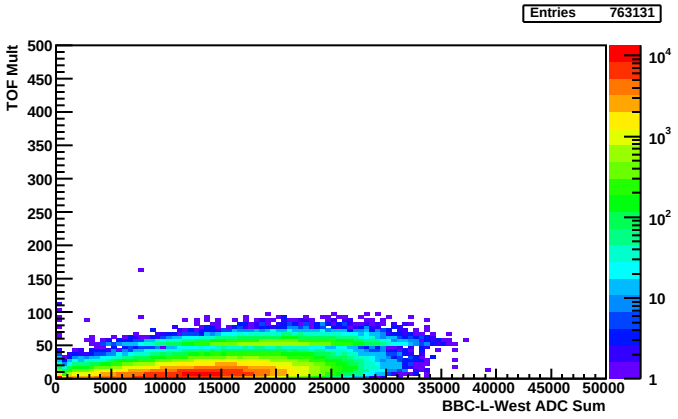

BBQ-VP003 (HI threshold)
Entries 1.22101e+07
ADC
QT Input Channel

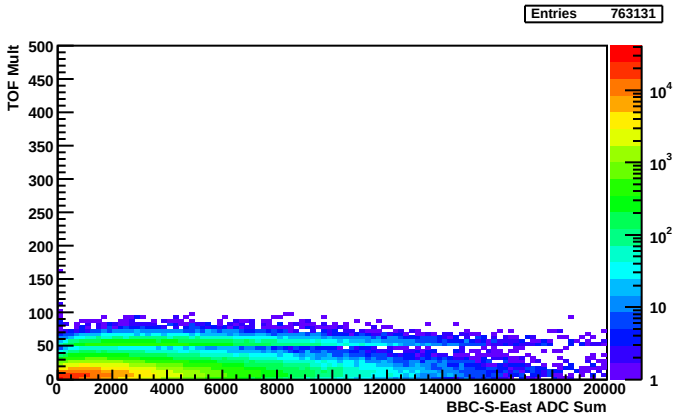

BBQ-VP003 (HI threshold)
Entries 1.22101e+07
TAC
QT Input Channel

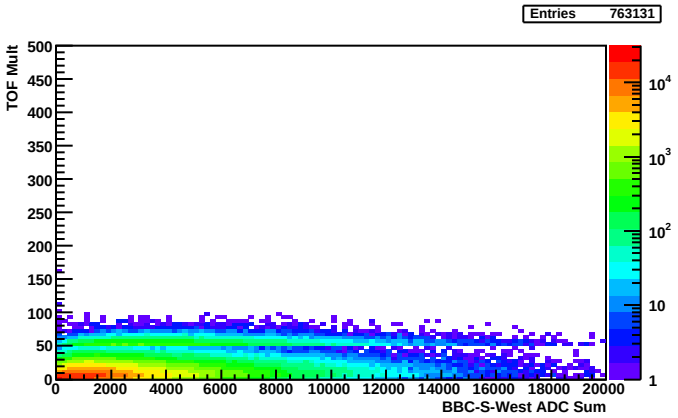

BBQ-VP004 (HI threshold)
Entries 1.22101e+07
ADC
QT Input Channel

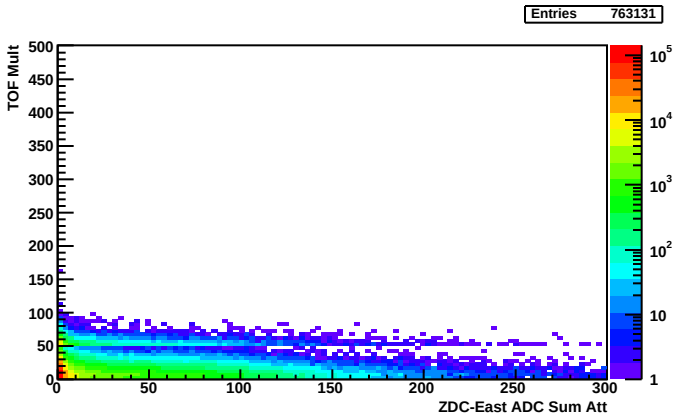

BBQ-VP004 (HI threshold)
Entries 1.22101e+07
TAC
QT Input Channel

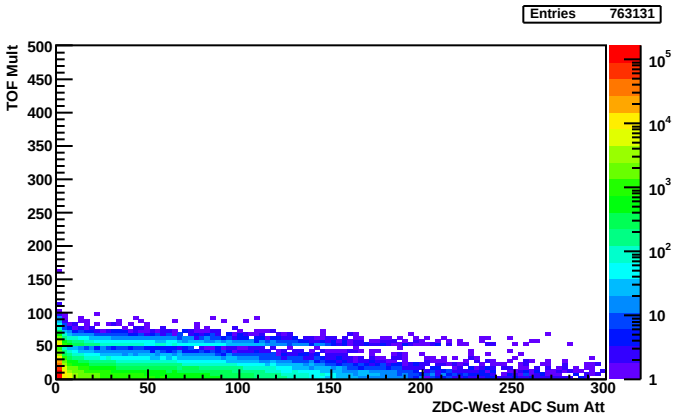

Entries 763131
TOF Mult
BBC-L-East ADC Sum

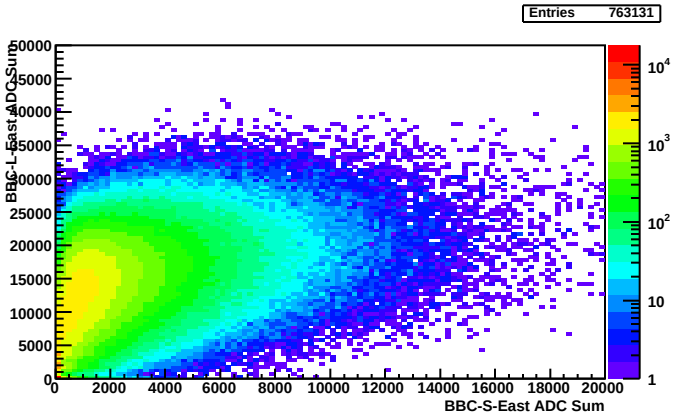

Entries 763131
TOF Mult
BBC-L-West ADC Sum

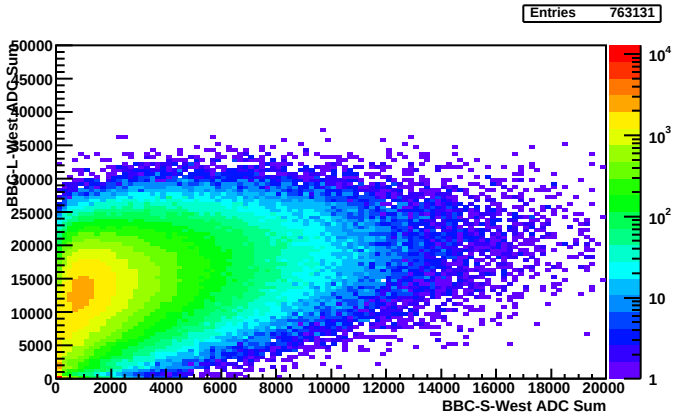

Entries 763131
TOF Mult
BBC-S-East ADC Sum

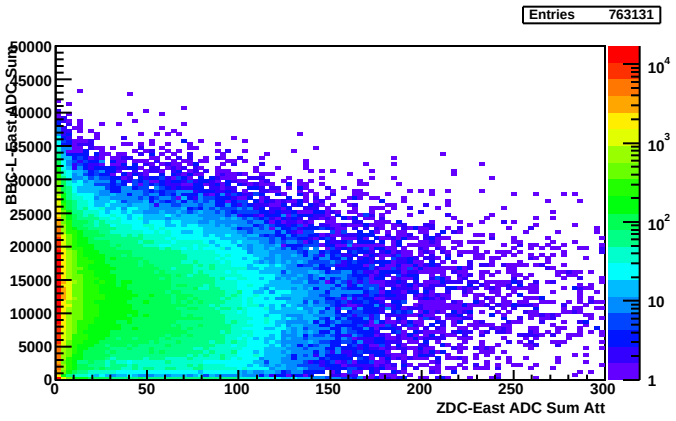

Entries 763131
TOF Mult
BBC-S-West ADC Sum

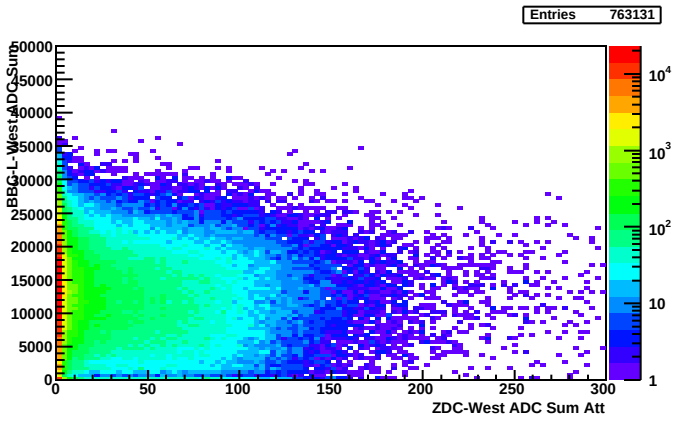

Entries 763131
TOF Mult
ZDC-East ADC Sum Att

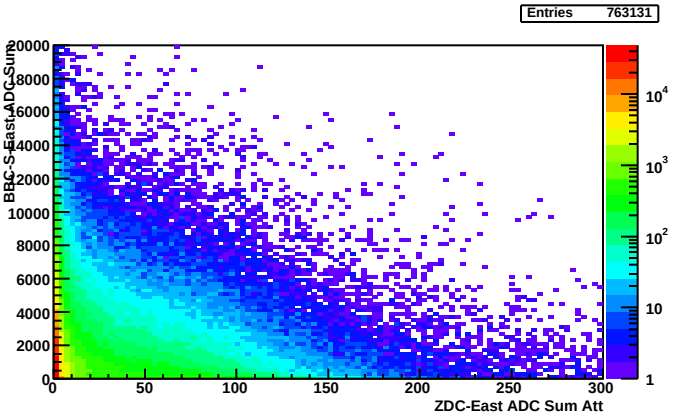

Entries 763131
TOF Mult
ZDC-West ADC Sum Att

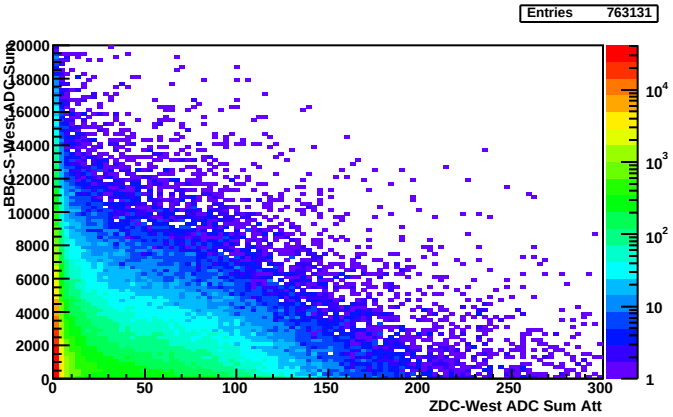

Entries 763131
BBC-L-East ADC Sum
BBC-S-East ADC Sum

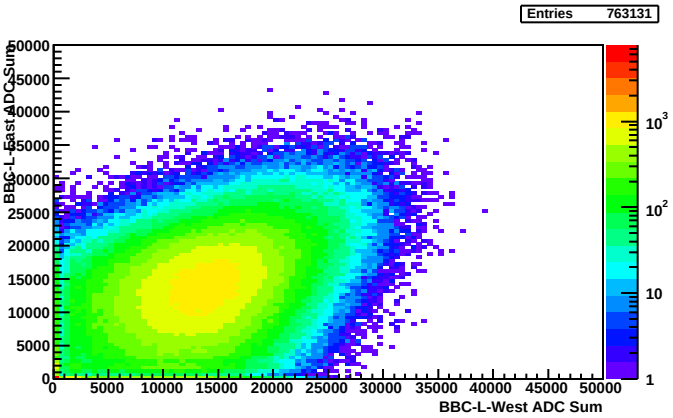

Entries 763131
BBC-L-West ADC Sum
BBC-S-West ADC Sum

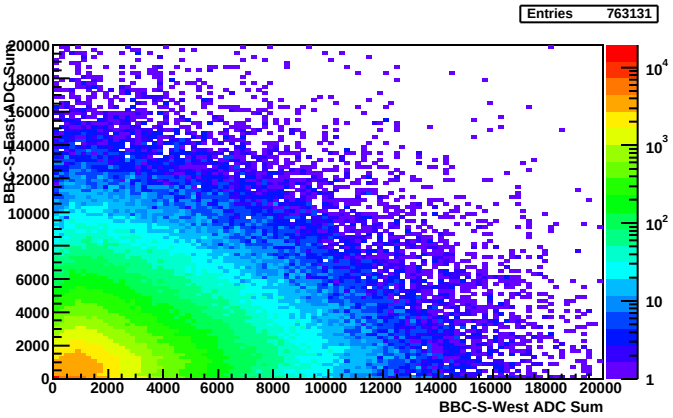

Entries 763131
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

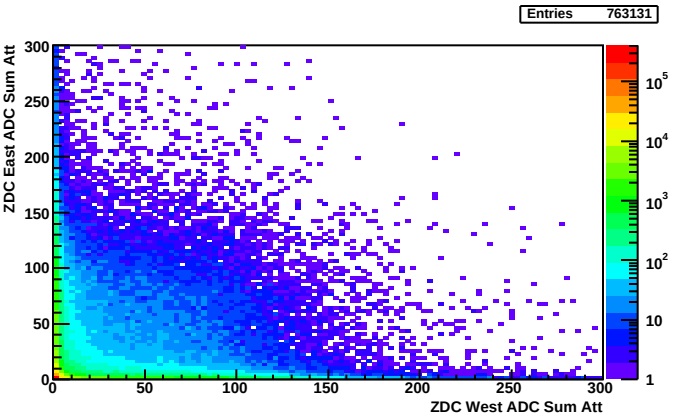

Entries 763131
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

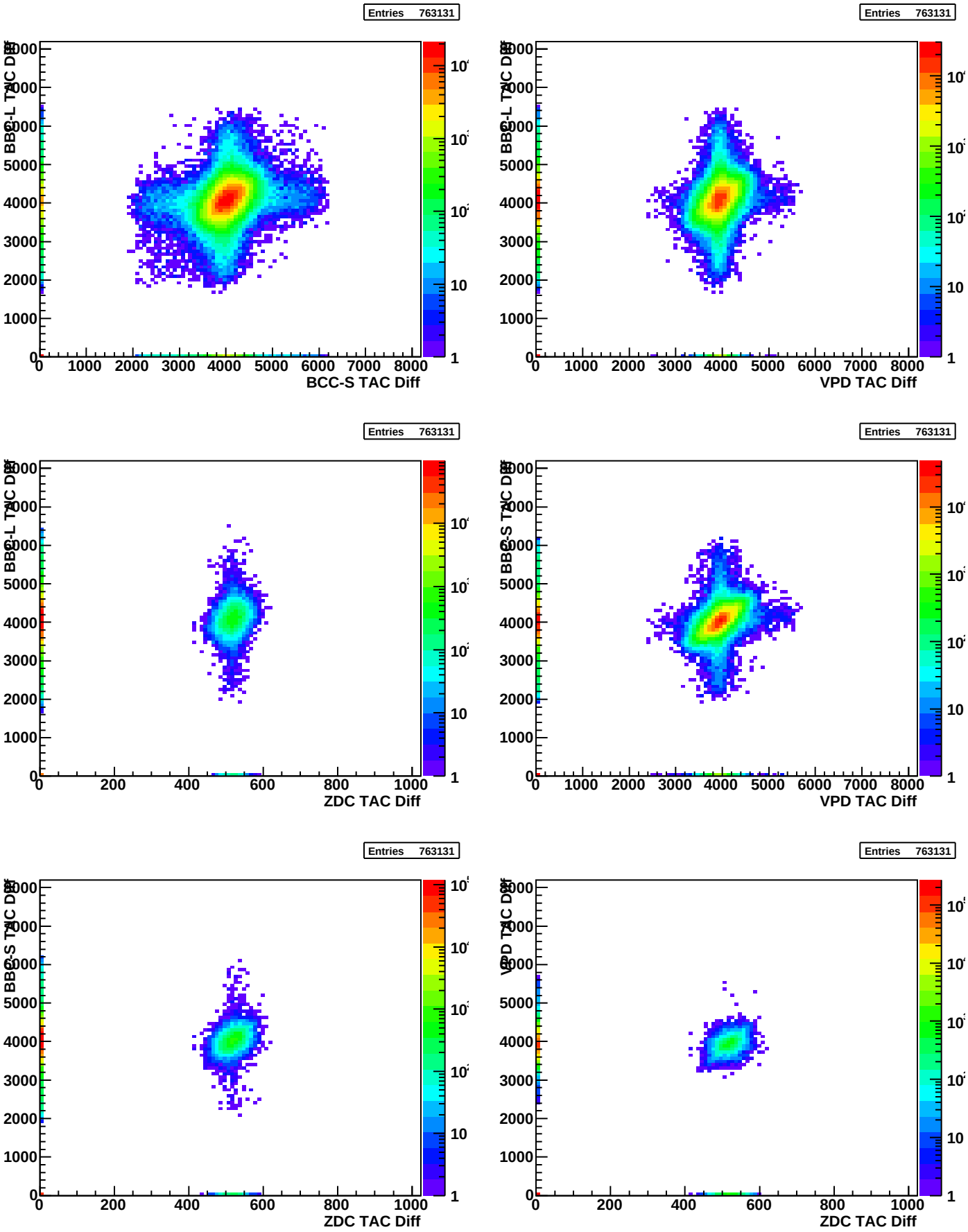

Entries 763131
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

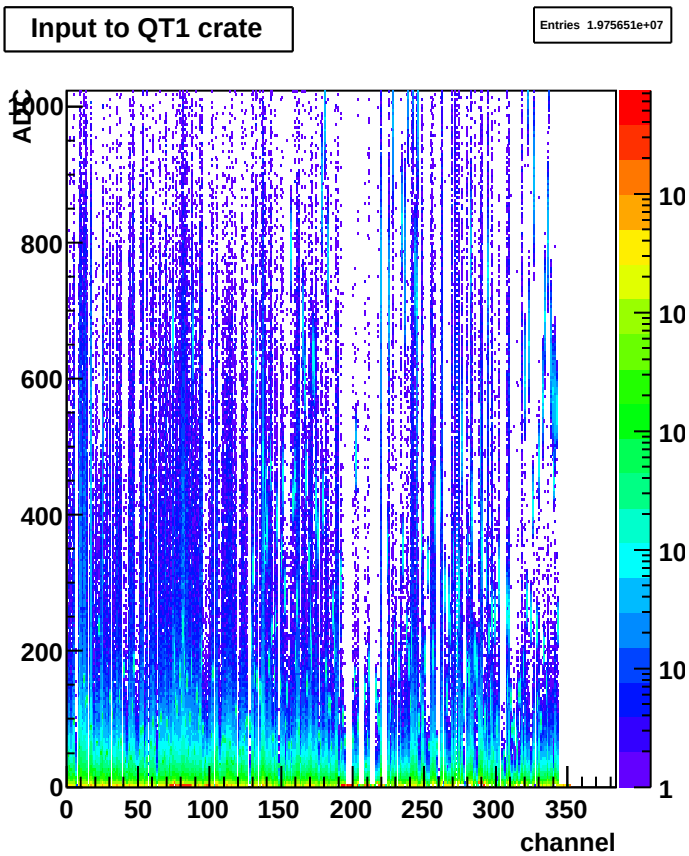

Entries 763131
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

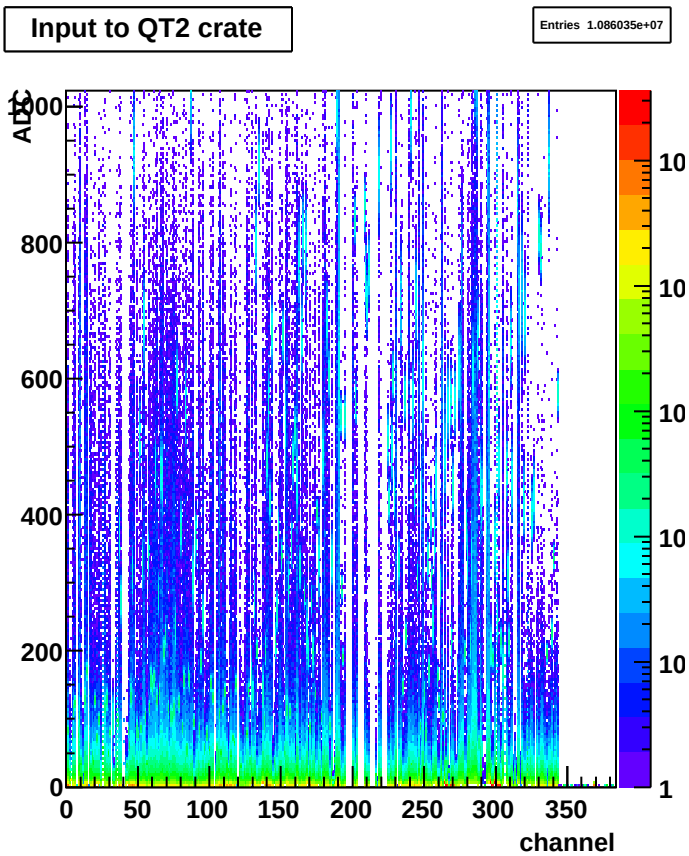

Entries 763131
BBC-L-East ADC Sum
BBC-L-West ADC Sum

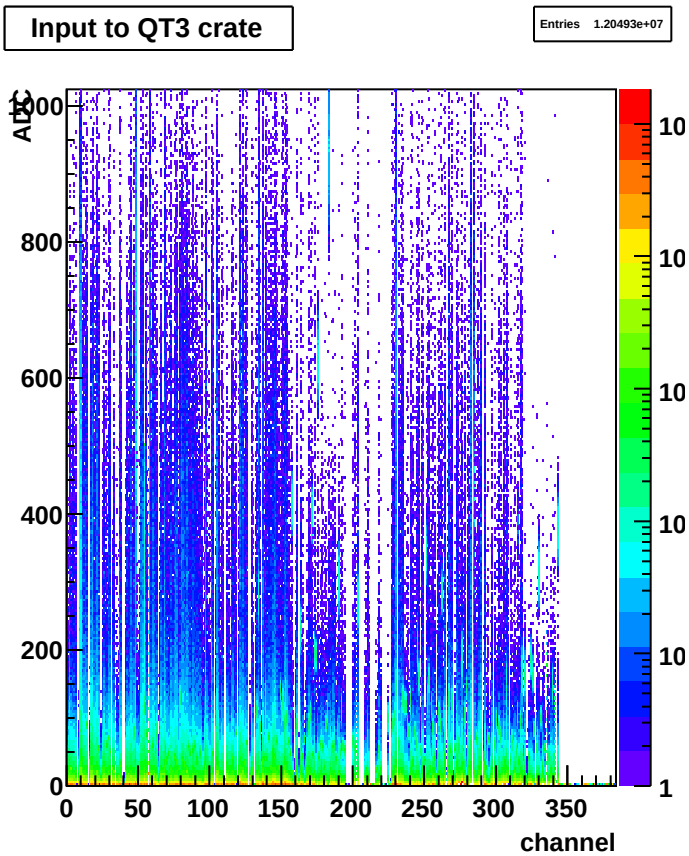

Entries 763131
BBC-S-East ADC Sum
BBC-S-West ADC Sum

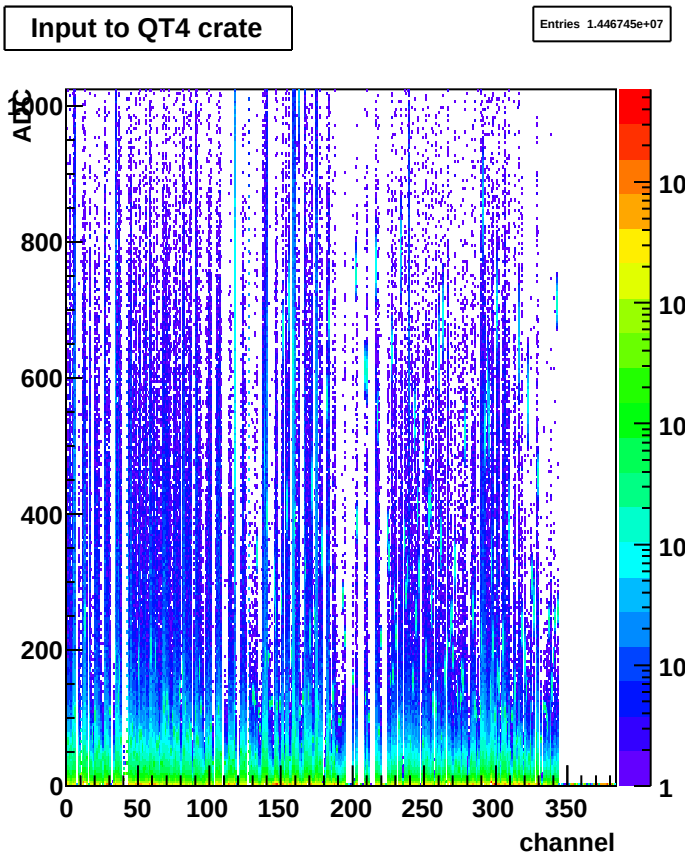

Entries 763131
ZDC East ADC Sum Att
ZDC West ADC Sum Att

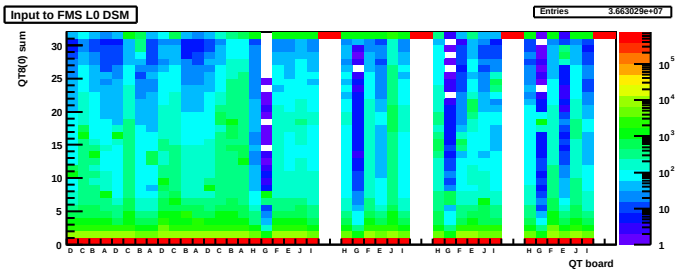

Entries 763131
BBC-L TAC Diff
BCC-S TAC Diff
Entries 763131
BBC-L TAC Diff
VPD TAC Diff
Entries 763131
BBC-L TAC Diff
ZDC TAC Diff
Entries 763131
BBC-S TAC Diff
VPD TAC Diff
Entries 763131
BBC-S TAC Diff
ZDC TAC Diff
Entries 763131
VPD TAC Diff
ZDC TAC Diff

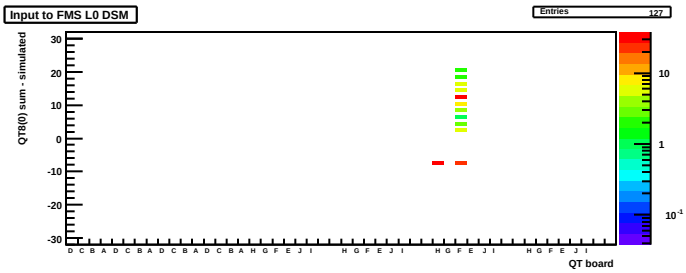

Input to QT1 crate
Entries 1.975651e+07
ADC
channel

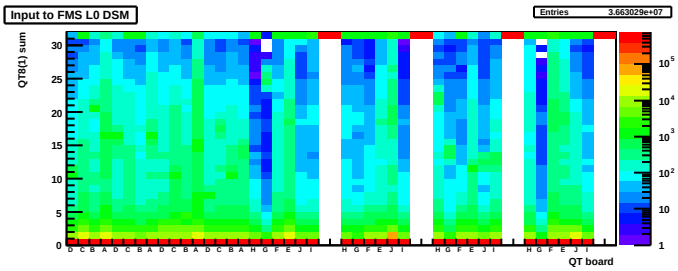

Input to QT2 crate
Entries 1.086035e+07
ADC
channel

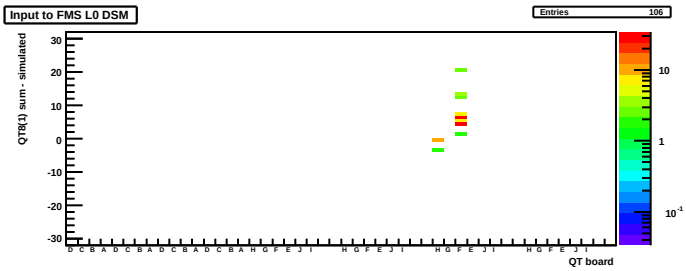

Input to QT3 crate
Entries 1.20493e+07
ADC
channel

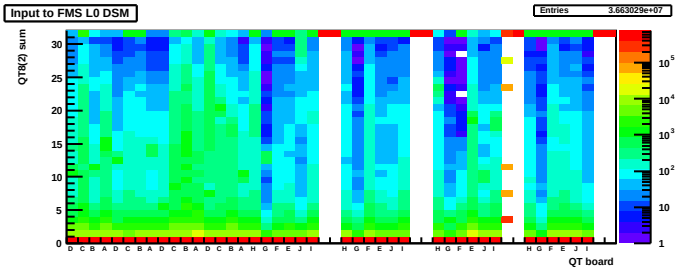

Input to QT4 crate
Entries 1.446745e+07
ADC
channel

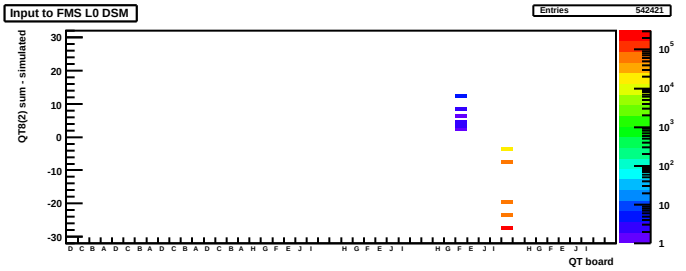

Input to FMS L0 DSM
Entries 3.663029e+07
QT8(0) sum
QT board

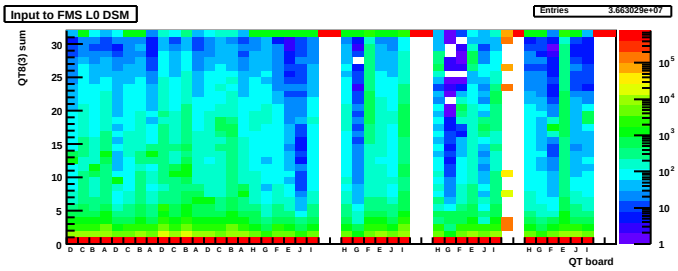

Input to FMS L0 DSM
Entries 127
QT8(0) sum - simulated
QT board

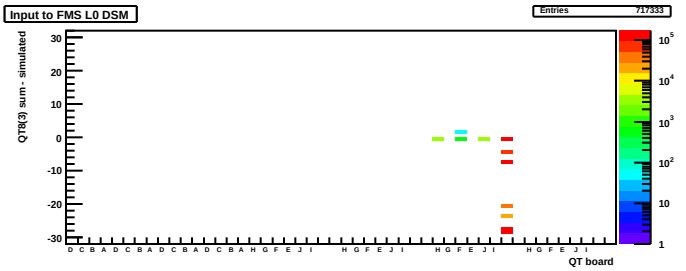

Input to FMS L0 DSM
Entries 3.663029e+07
QT8(1) sum
QT board

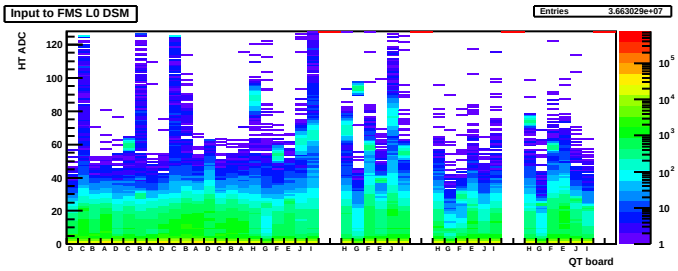

Input to FMS L0 DSM
Entries 106
QT8(1) sum - simulated
QT board

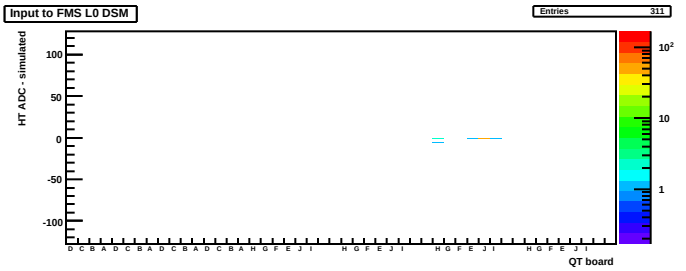

Input to FMS L0 DSM
Entries 3.663029e+07
QT8(2) sum
QT board

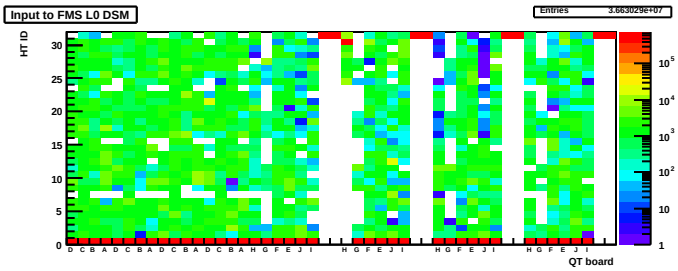

Input to FMS L0 DSM
Entries 542421
QT8(2) sum - simulated
QT board

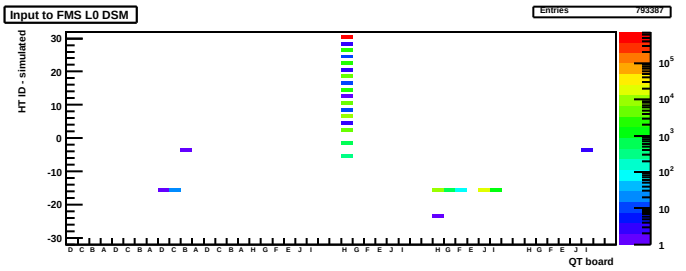

Input to FMS L0 DSM
Entries 3.663029e+07
QT8(3) sum
QT board

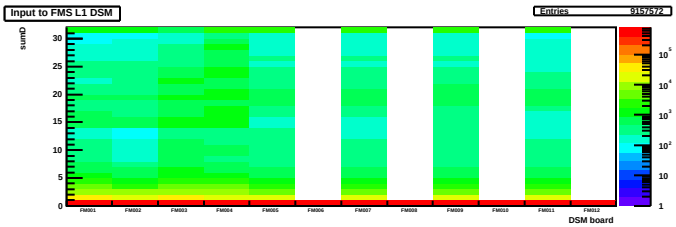

Input to FMS L0 DSM
Entries 717333
QT8(3) sum - simulated
QT board

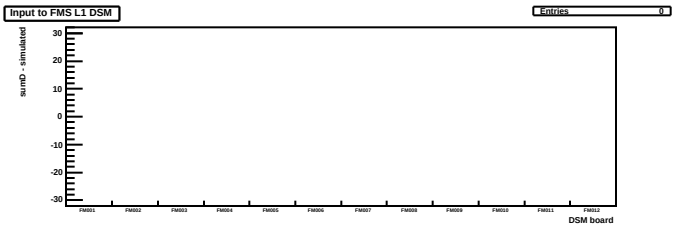

Input to FMS L0 DSM
Entries 3.663029e+07
HT ADC
QT board

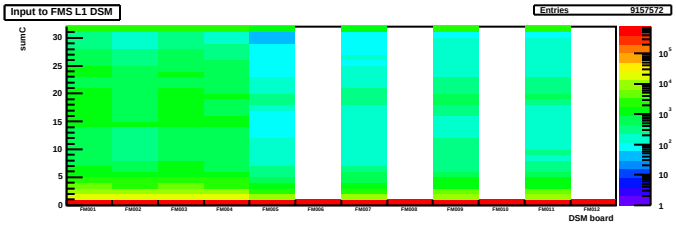

Input to FMS L0 DSM
Entries 311
HT ADC - simulated
QT board

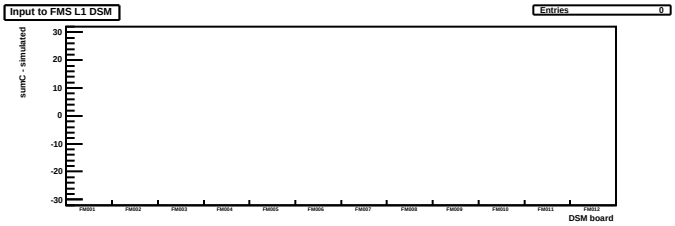

Input to FMS L0 DSM
Entries 3.663029e+07
HT ID
QT board

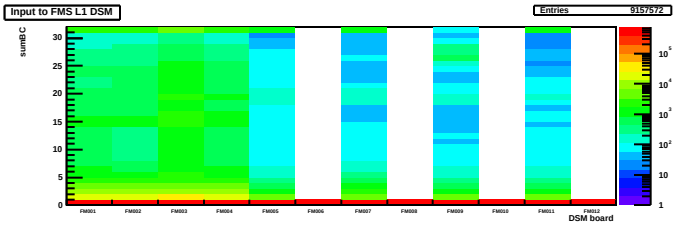

Input to FMS L0 DSM
Entries 793387
HT ID - simulated
QT board

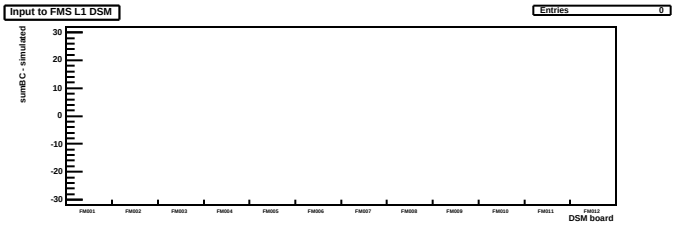

Input to FMS L1 DSM
Entries 9157572
sumD
DSM board

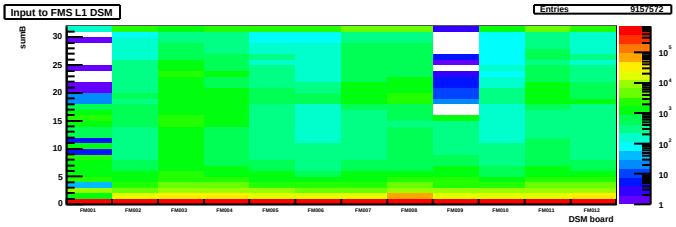

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

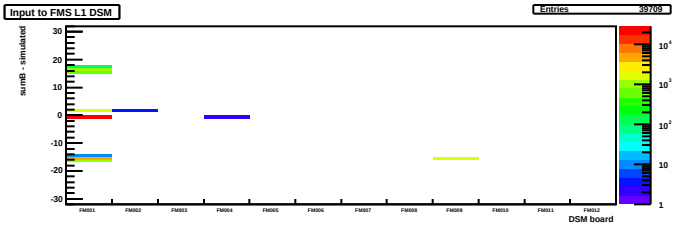

Input to FMS L1 DSM
Entries 9157572
sumC
DSM board

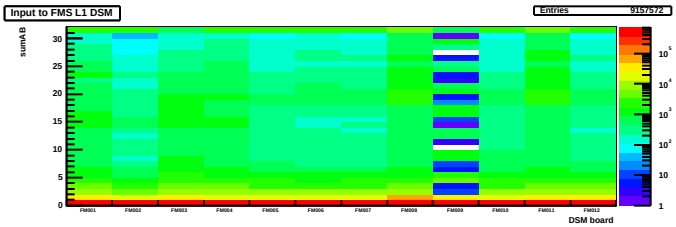

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

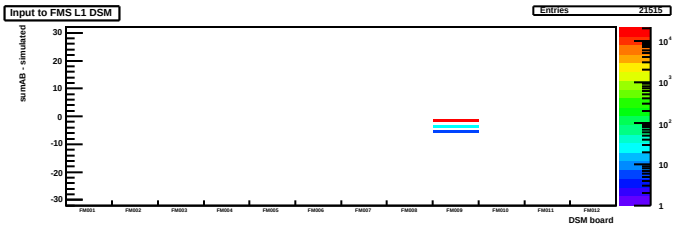

Input to FMS L1 DSM
Entries 9157572
sumBC
DSM board

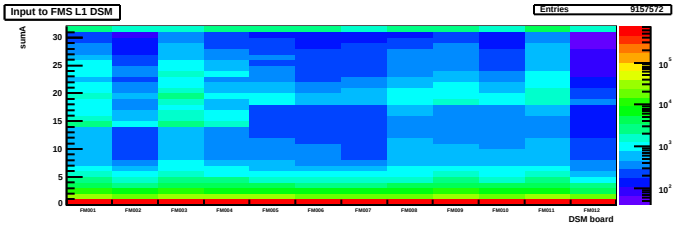

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

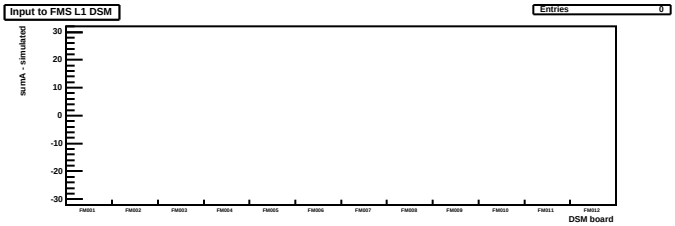

Input to FMS L1 DSM
Entries 9157572
sumB
DSM board

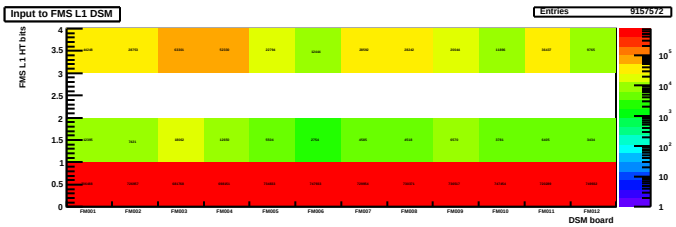

Input to FMS L1 DSM
Entries 39709
sumB - simulated
DSM board

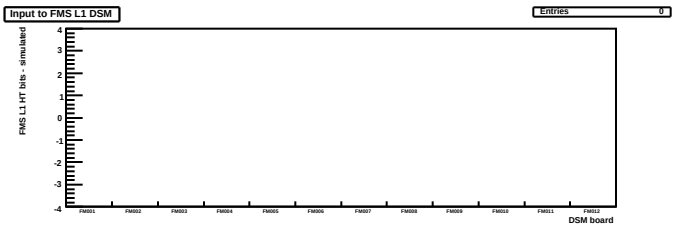

Input to FMS L1 DSM
Entries 9157572
sumAB
DSM board

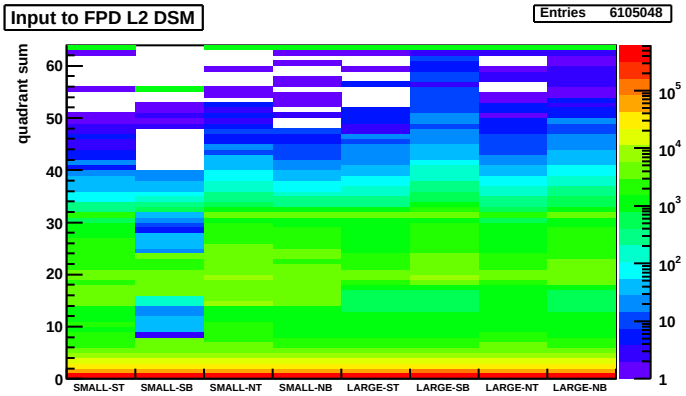

Input to FMS L1 DSM
Entries 21515
sumAB - simulated
DSM board

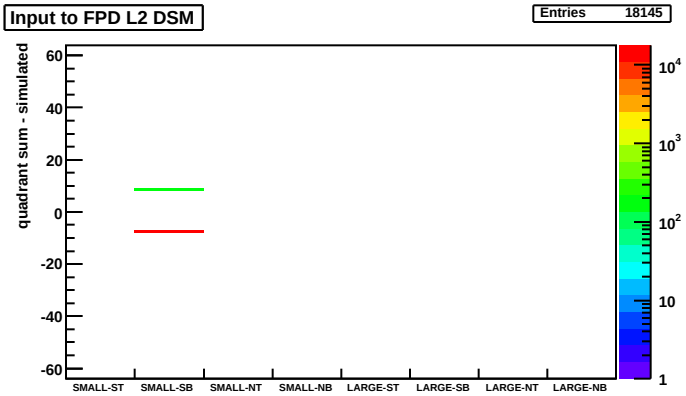

Input to FMS L1 DSM
Entries 9157572
sumA
DSM board

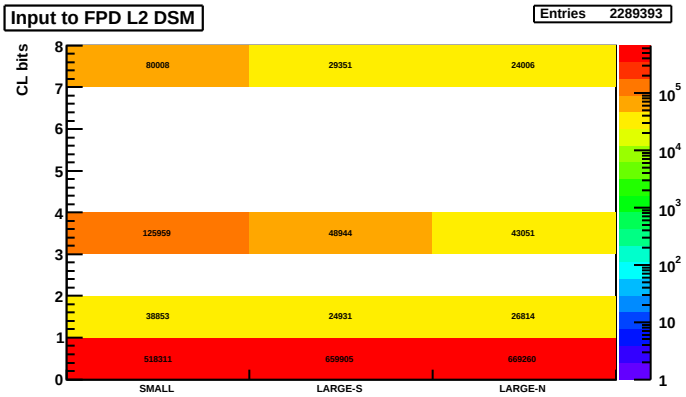

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

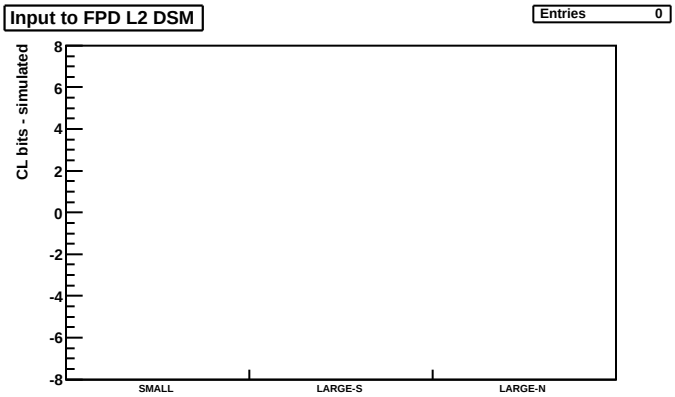

Input to FMS L1 DSM
Entries 9157572
FMS L1 HT bits
DSM board

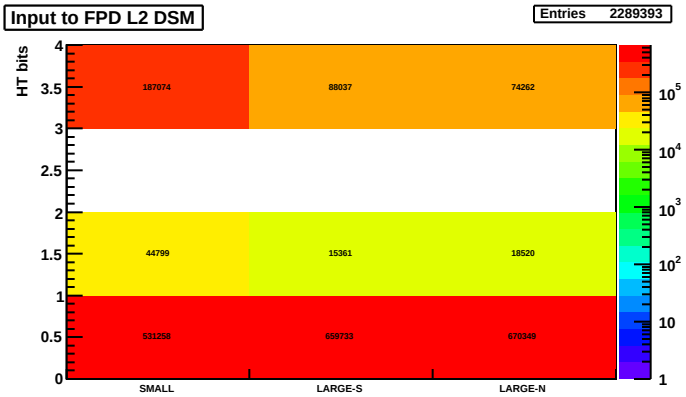

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

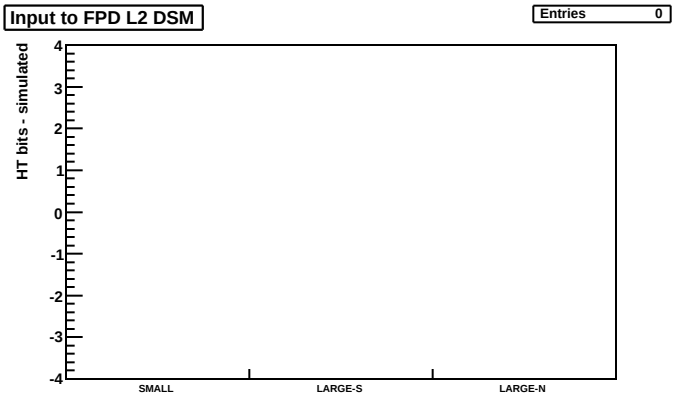

Input to FPD L2 DSM
Entries 6105048
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

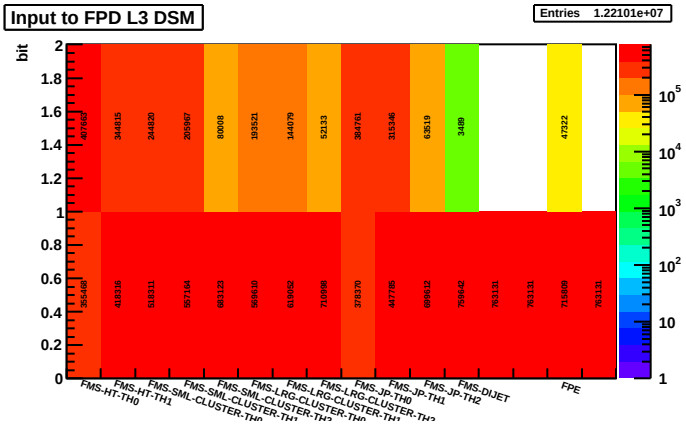

Input to FPD L2 DSM
Entries 18145
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

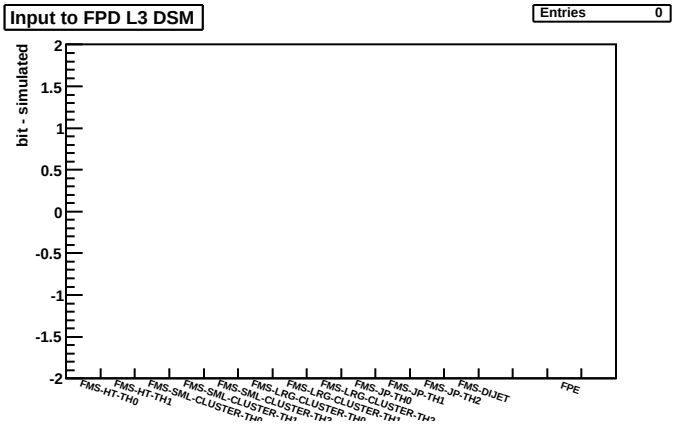

Input to FPD L2 DSM
Entries 2289393
CL bits
80008 29351 24006
125959 48944 43051
38853 24931 26814
518311 659905 669260
SMALL LARGE-S LARGE-N

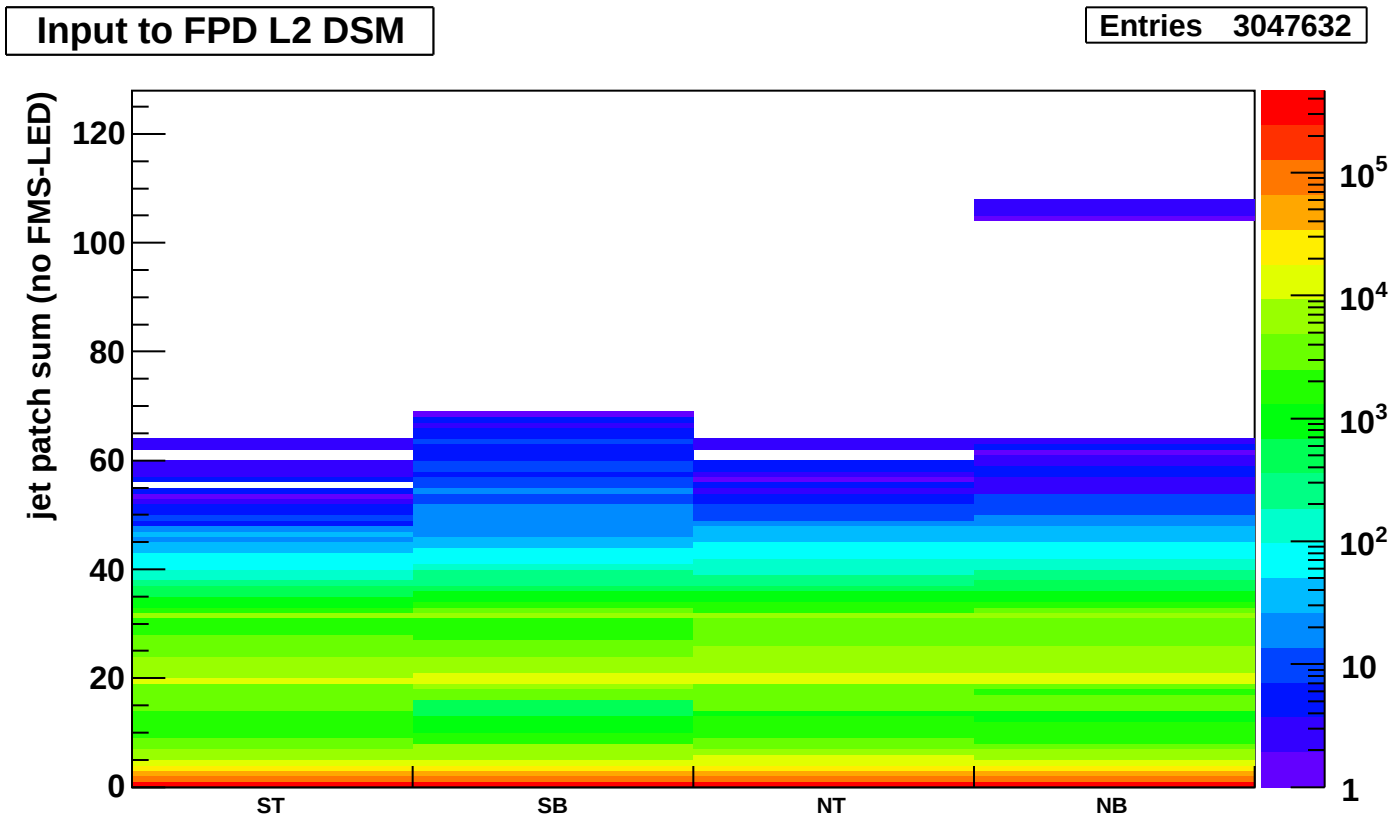

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL LARGE-S LARGE-N

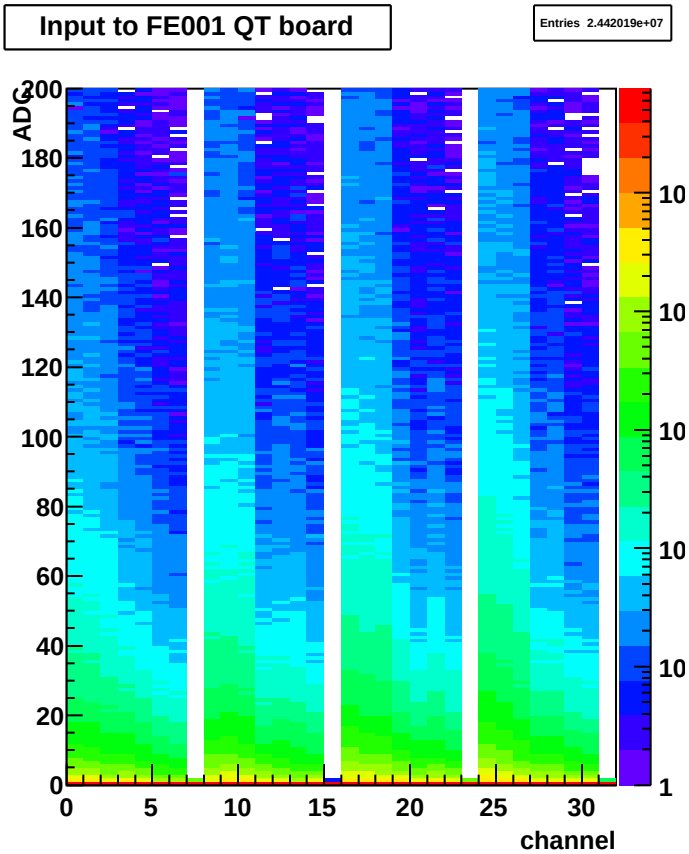

Input to FPD L2 DSM
Entries 2289393
HT bits
187074 88037 74262
44799 15361 18520
531258 659733 670349
SMALL LARGE-S LARGE-N

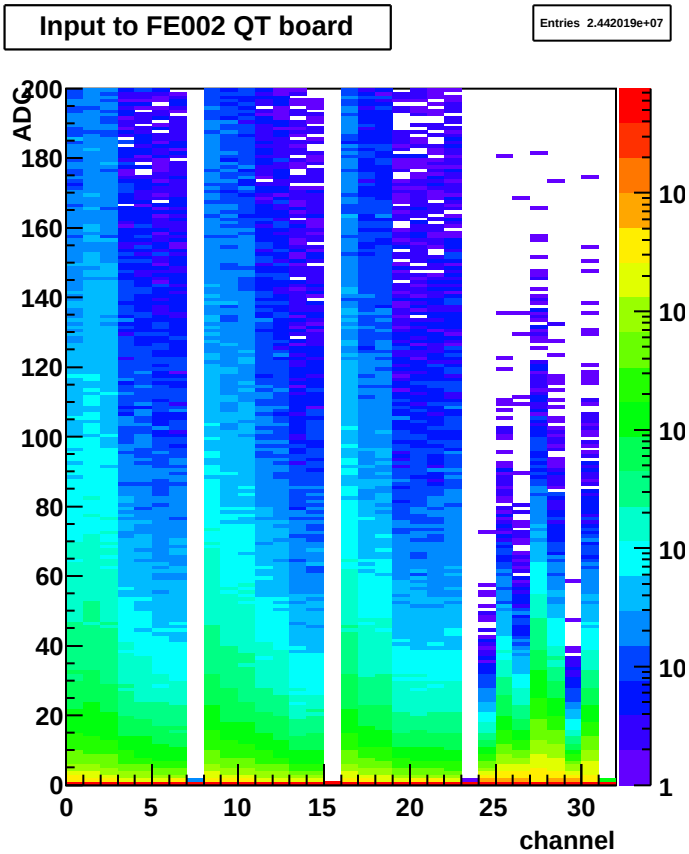

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

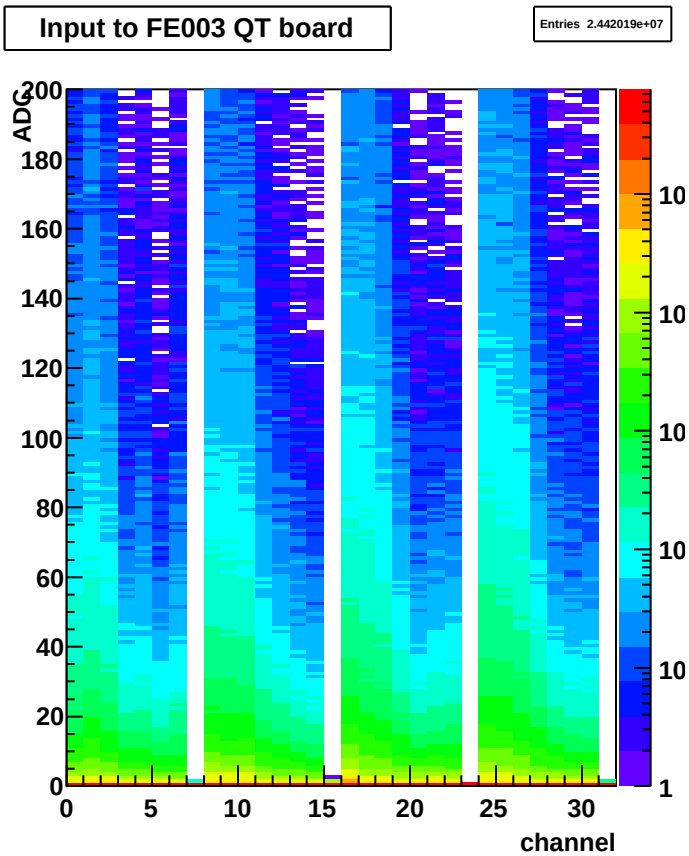

Input to FPD L3 DSM
Entries 1.22101e+07
bit
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

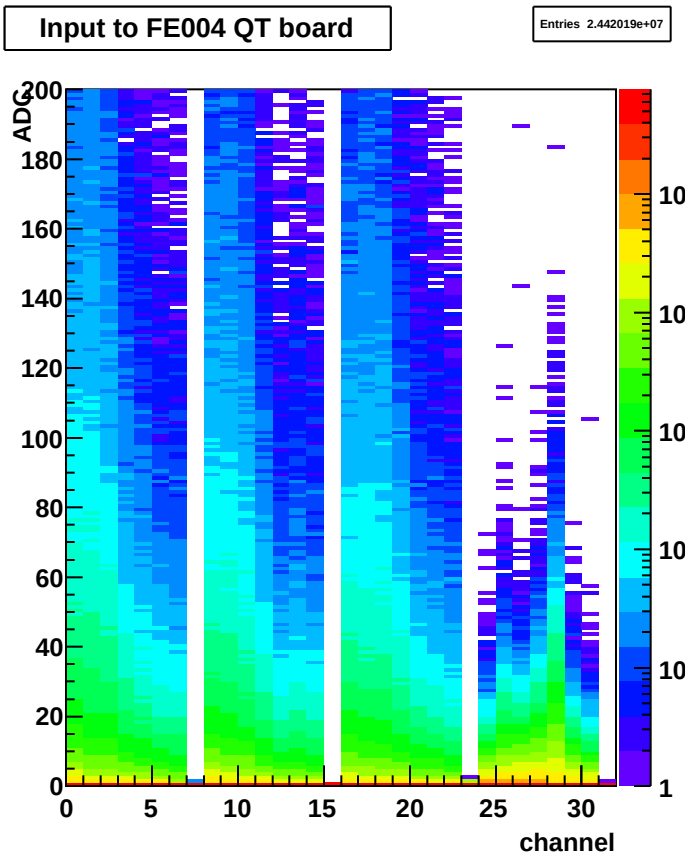

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

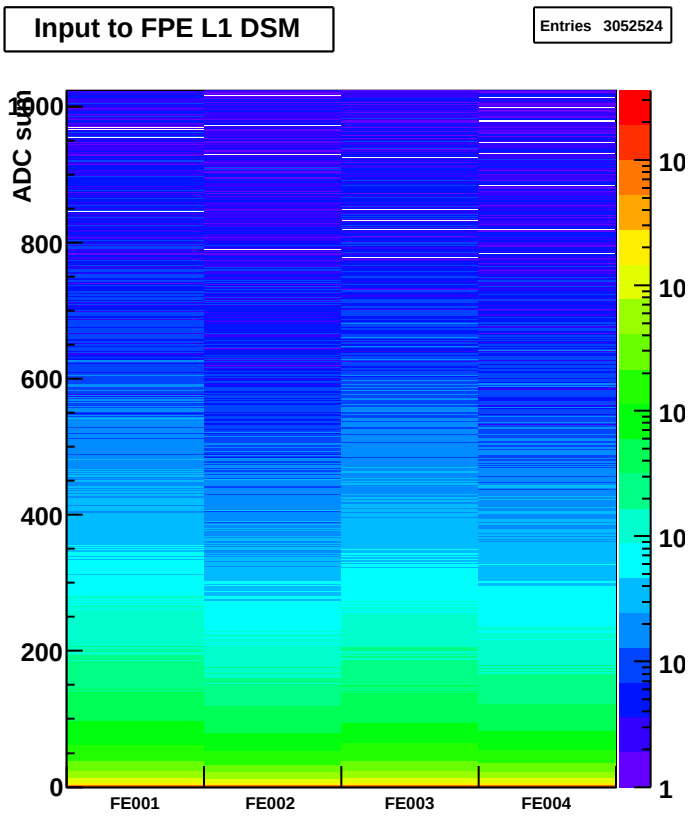

Input to FPD L2 DSM
Entries 3047632
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

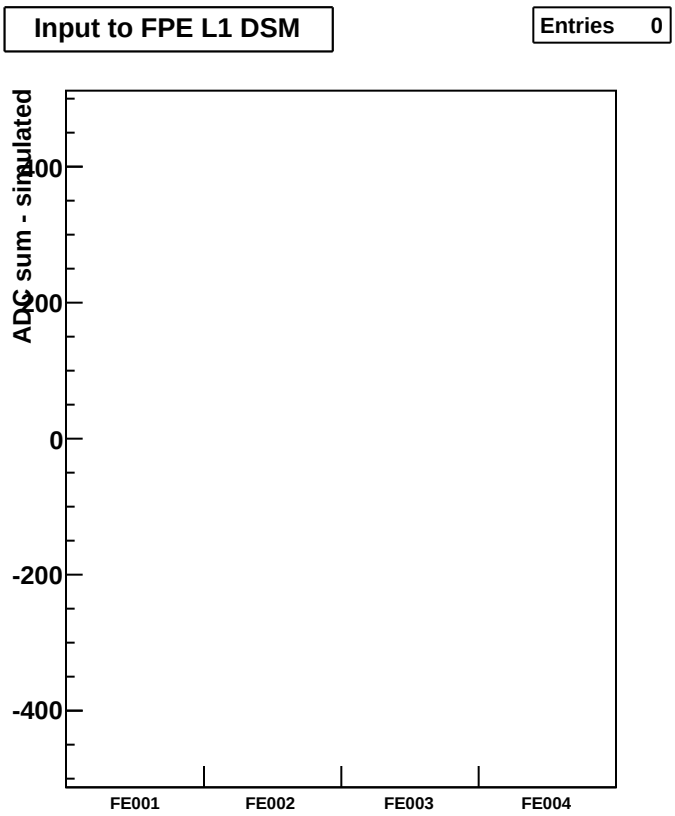

Input to FE001 QT board
Entries 2.442019e+07
ADC
channel

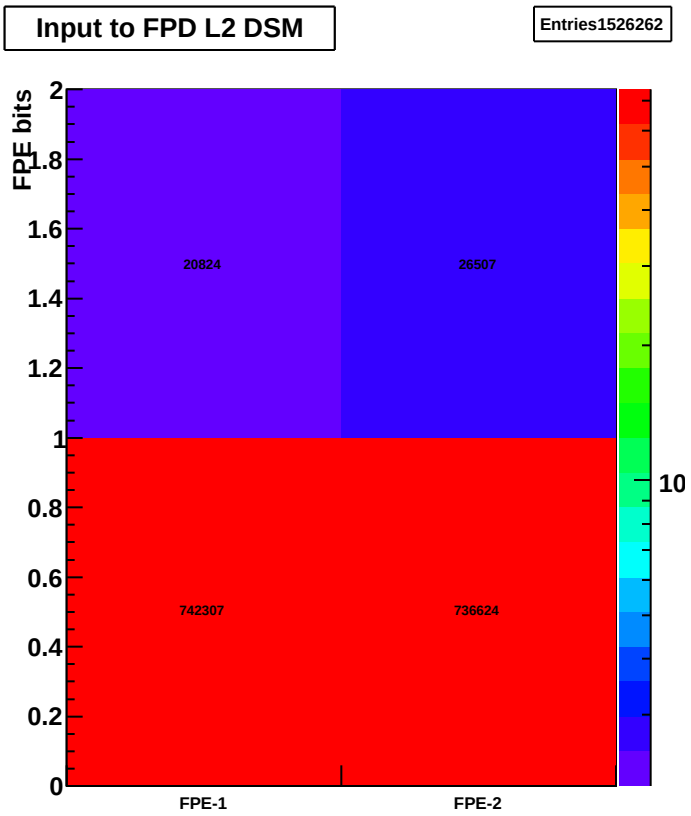

Input to FE002 QT board
Entries 2.442019e+07
ADC
channel

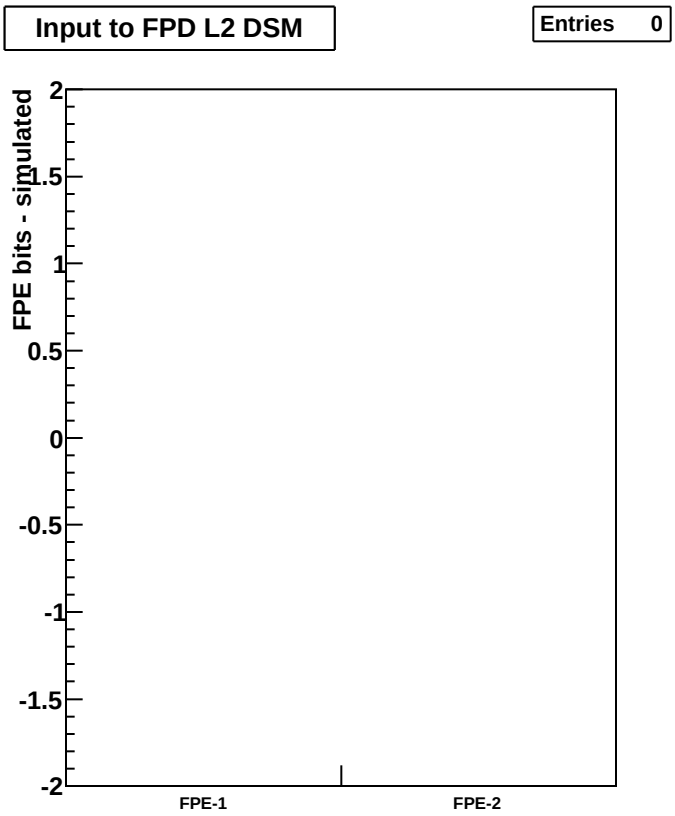

Input to FE003 QT board
Entries 2.442019e+07
ADC
channel


Input to FE004 QT board
Entries 2.442019e+07
ADC
channel


Input to FPE L1 DSM
Entries 3052524
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1526262
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
20824
26507
742307
736624
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2