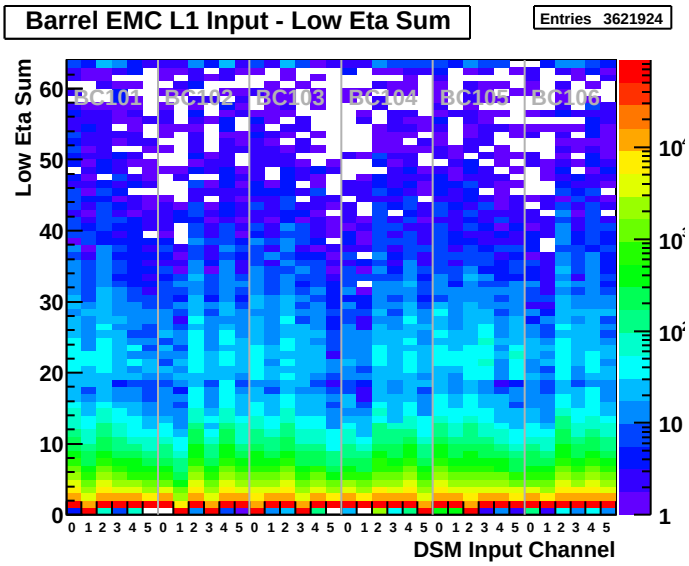
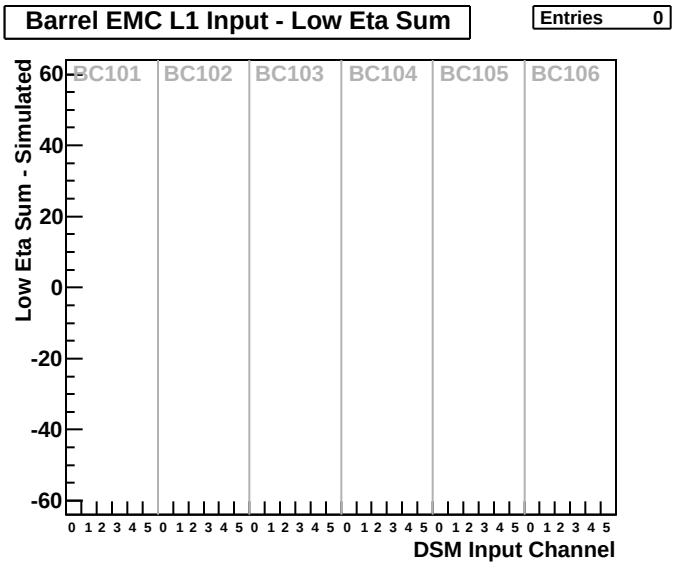
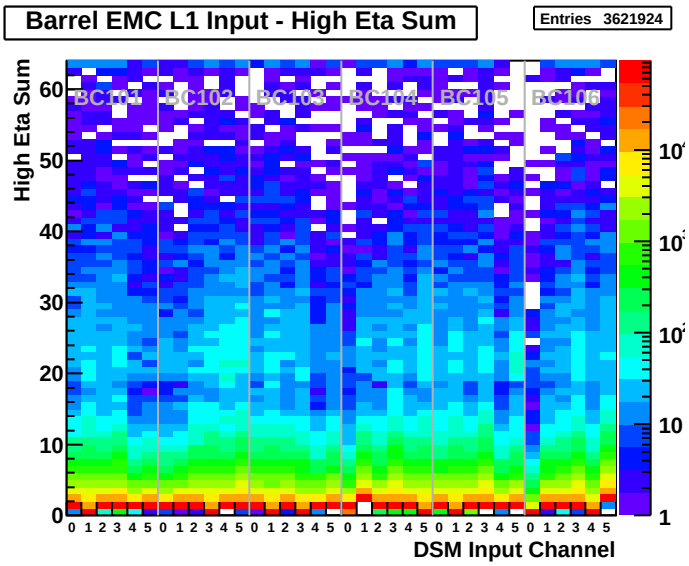
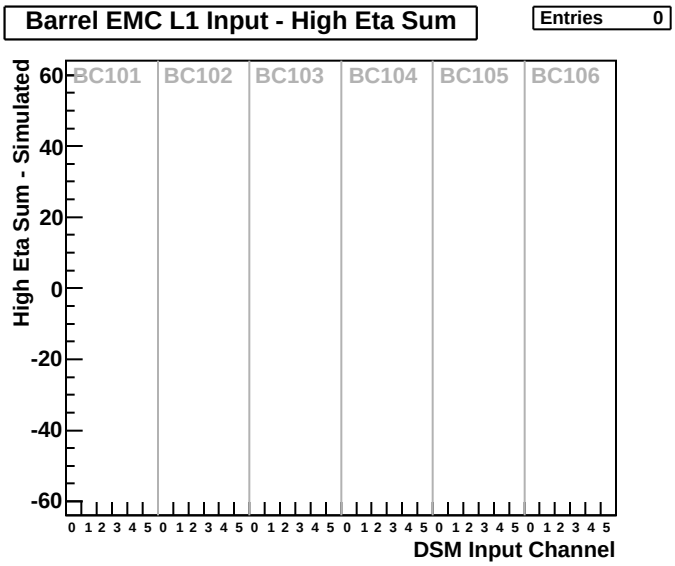
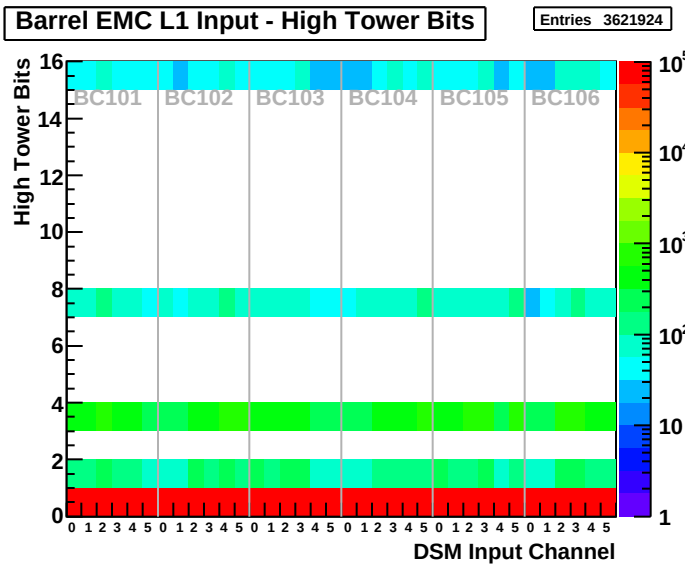
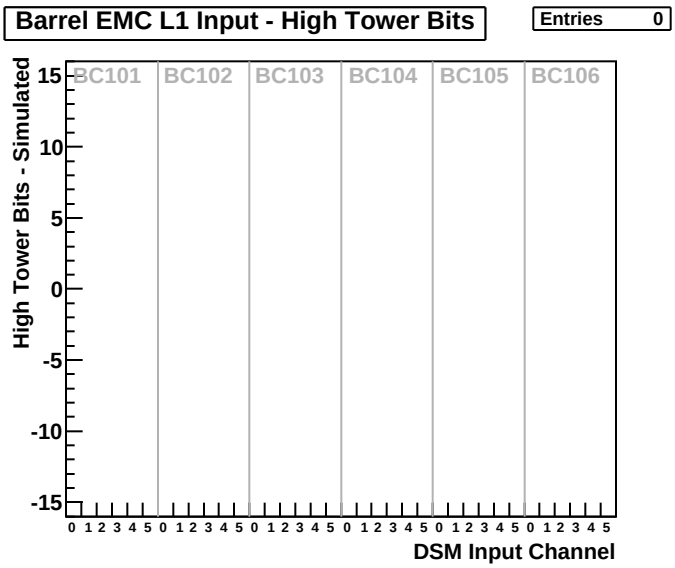

Barrel EMC L1 Input - Low Eta Sum
Entries 3621924
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

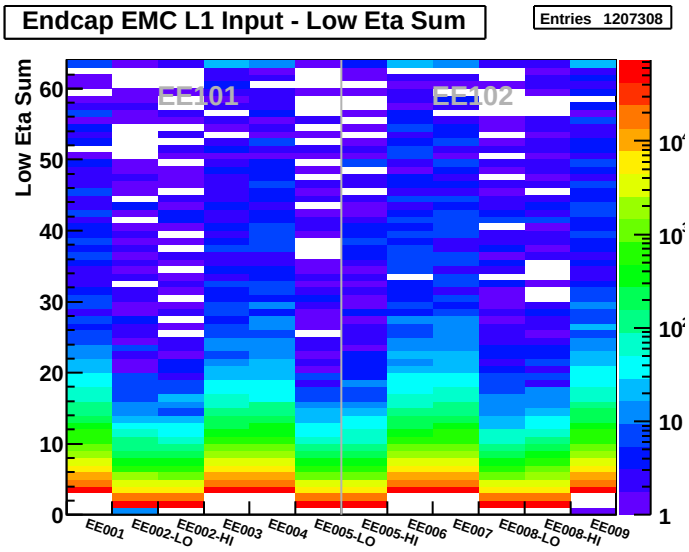

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

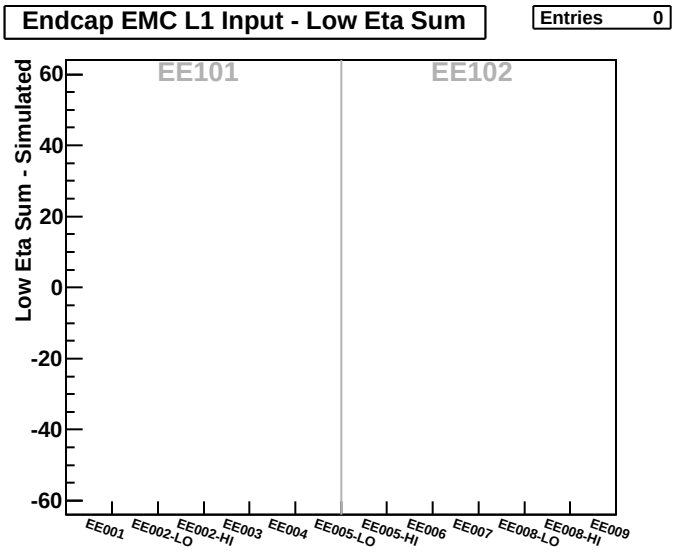

Barrel EMC L1 Input - High Eta Sum
Entries 3621924
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

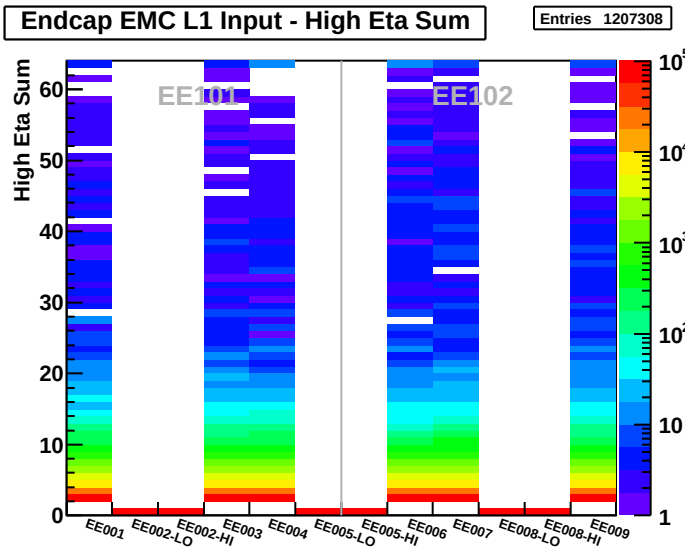

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

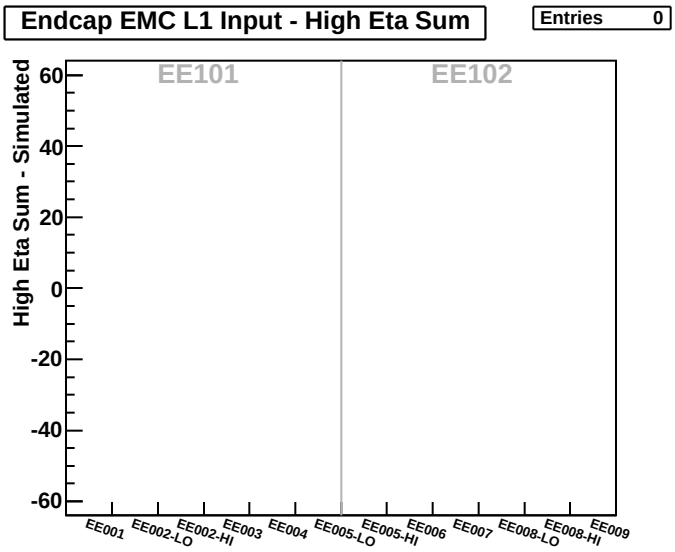

Barrel EMC L1 Input - High Tower Bits
Entries 3621924
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

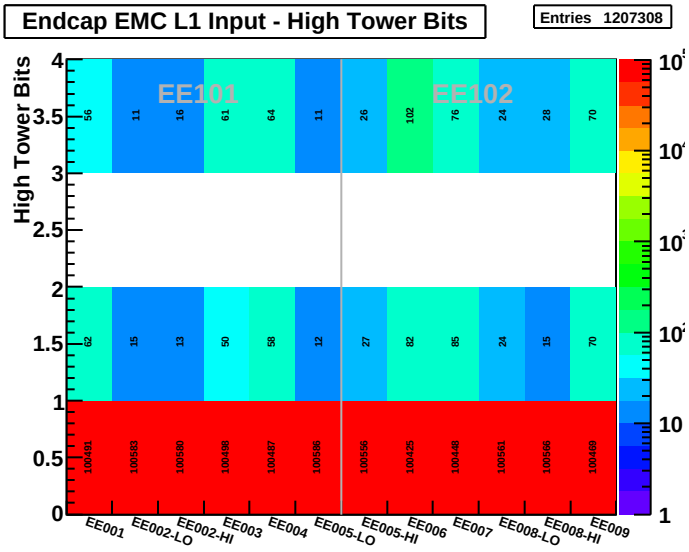

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

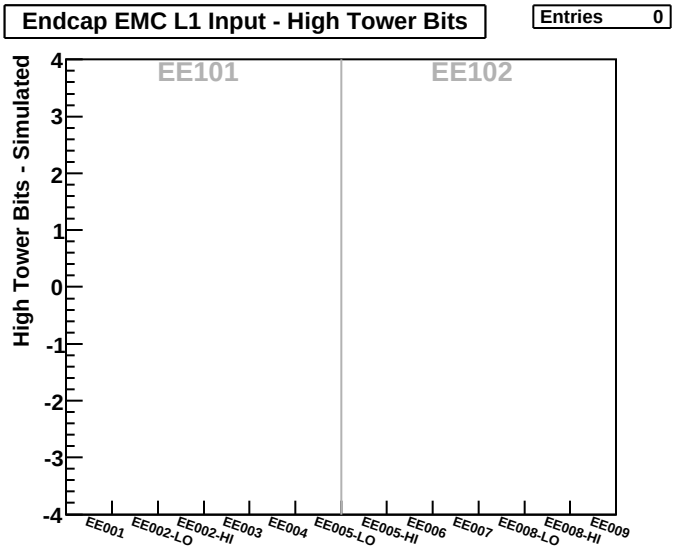

Endcap EMC L1 Input - Low Eta Sum
Entries 1207308
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

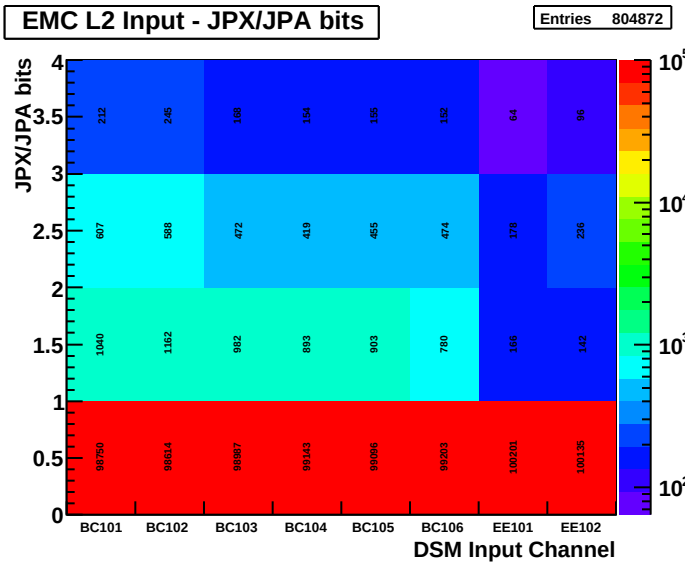

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

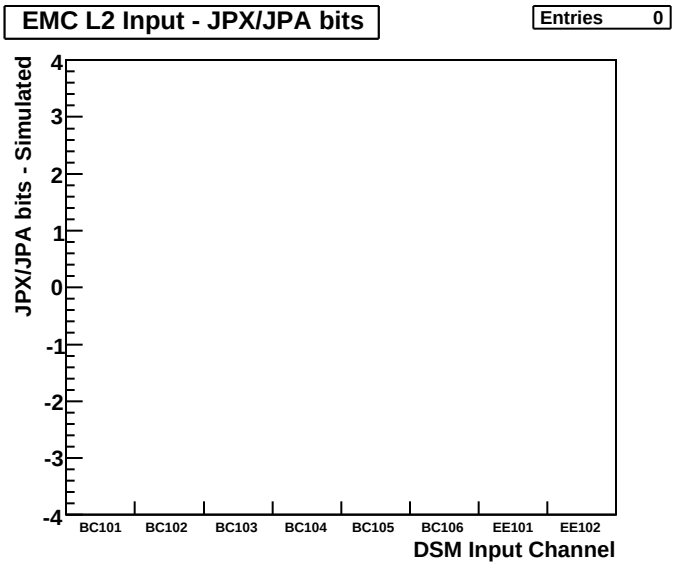

Endcap EMC L1 Input - High Eta Sum
Entries 1207308
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

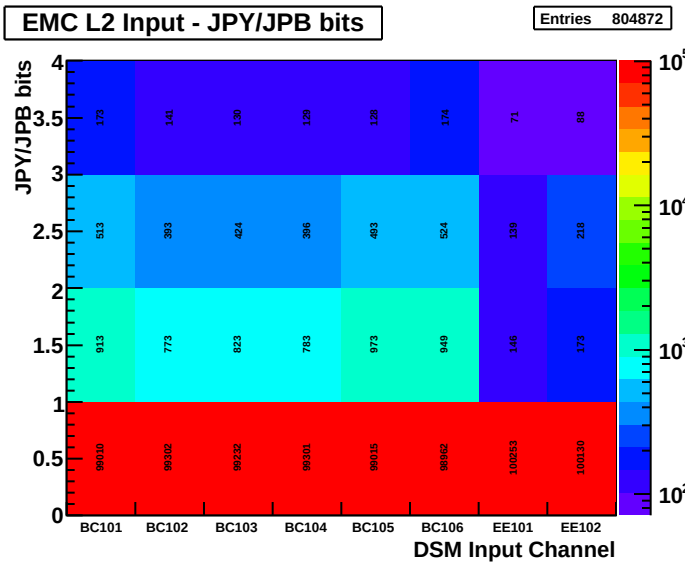

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

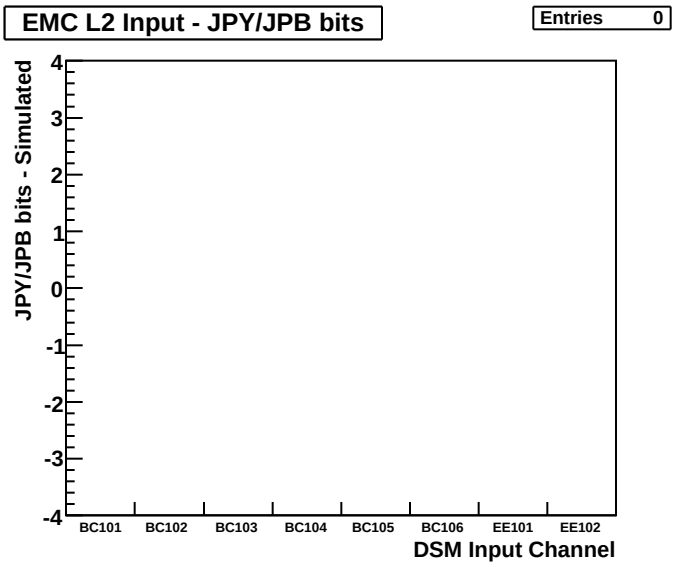

Endcap EMC L1 Input - High Tower Bits
Entries 1207308
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

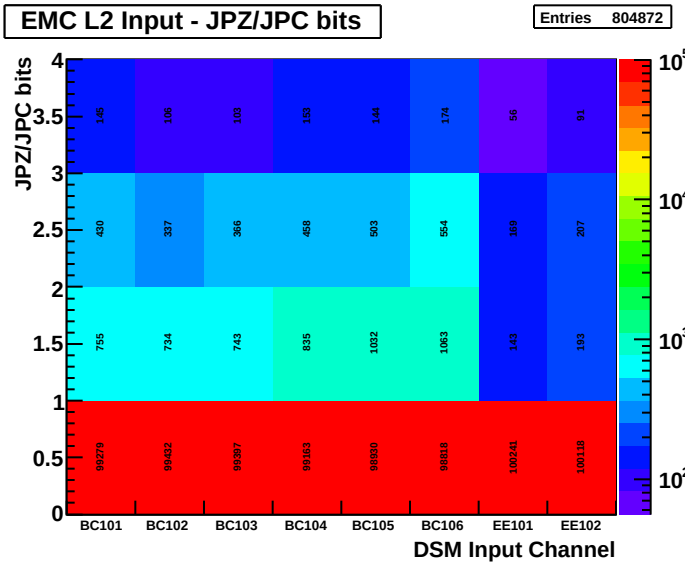

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

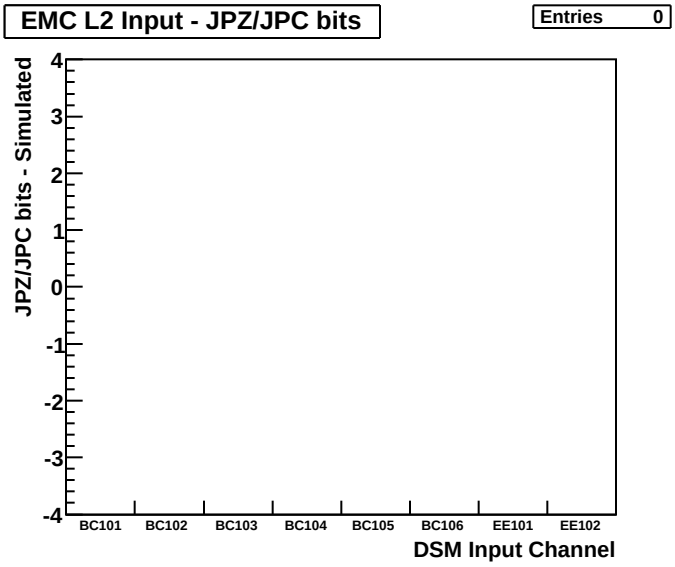

EMC L2 Input - JPX/JPA bits
Entries 804872
JPX/JPA bits
212 245 168 154 155 152 64 96
607 588 472 419 455 474 178 236
1040 1162 982 893 903 780 166 142
99750 99614 99887 99143 99096 99203 100201 100135
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

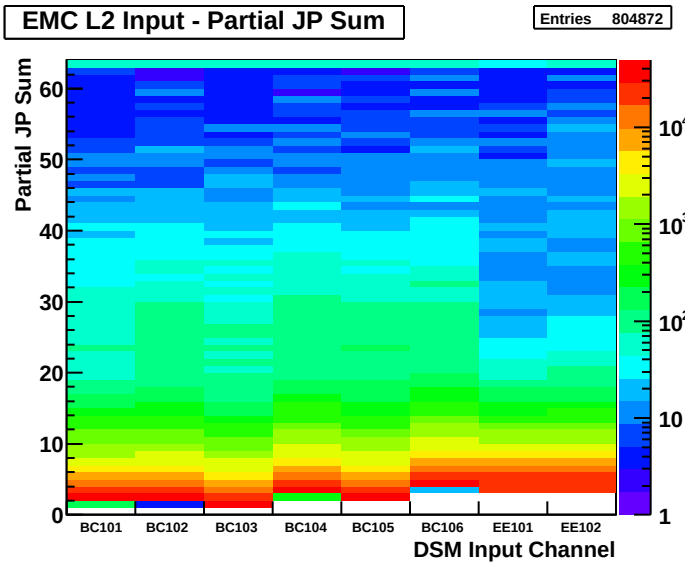

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

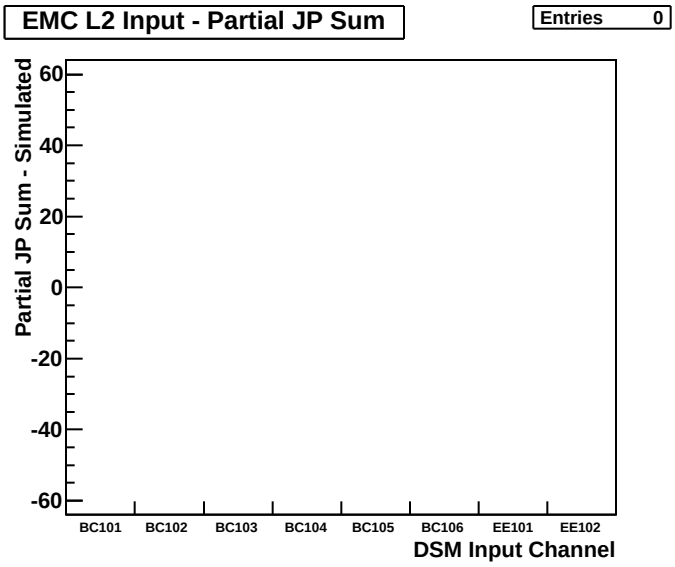

EMC L2 Input - JPY/JPB bits
Entries 804872
JPY/JPB bits
178 141 130 128 128 174 71 88
513 393 424 396 493 524 139 218
913 773 823 783 973 949 146 173
99010 99302 99232 99301 99015 98962 100253 100130
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

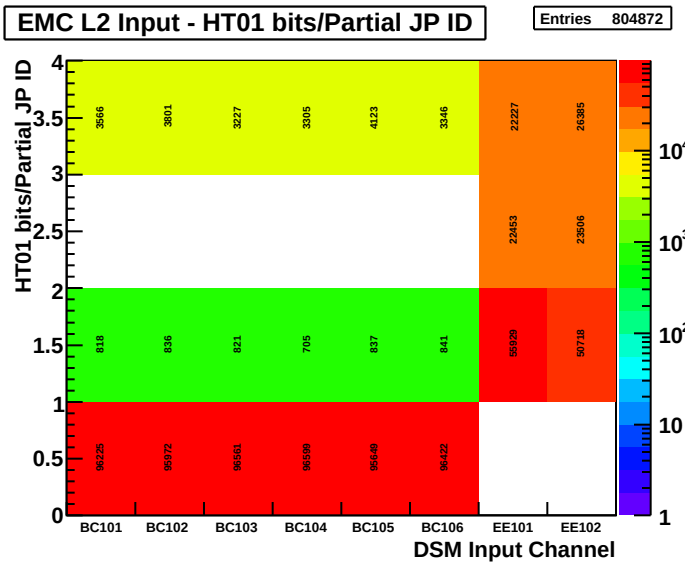

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

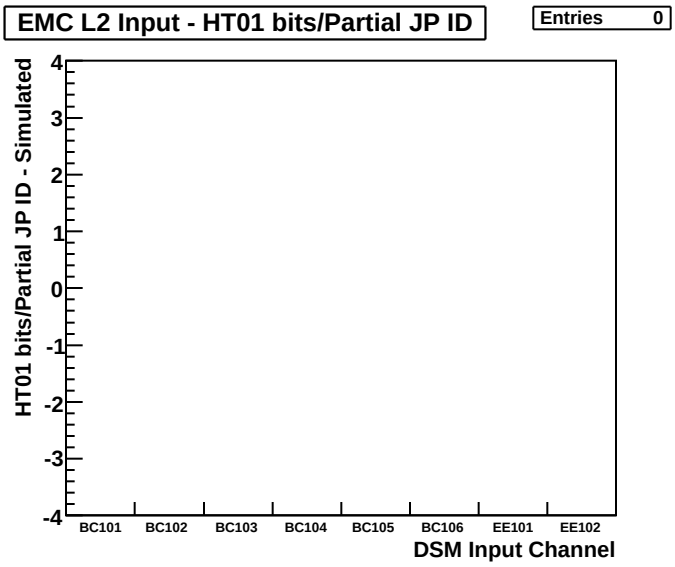

EMC L2 Input - JPZ/JPC bits
Entries 804872
JPZ/JPC bits
145 106 103 153 144 174 56 91
430 337 366 458 503 554 169 207
755 734 743 835 1032 1063 143 193
99279 99432 99397 99163 98930 98818 100241 100118
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

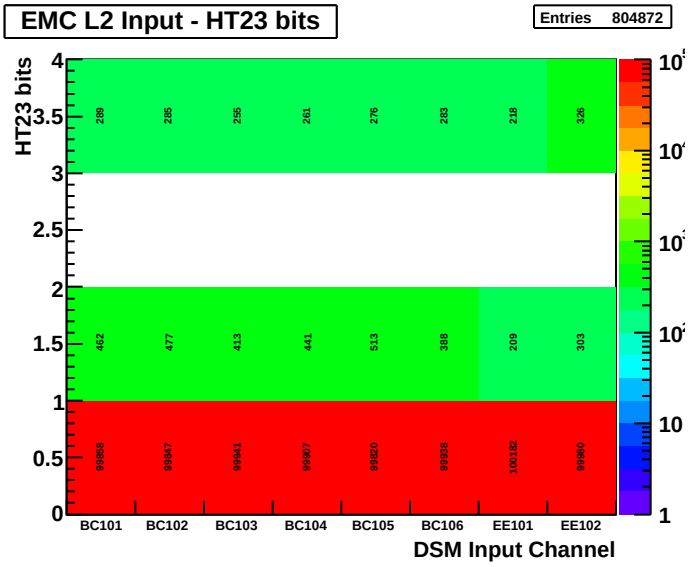

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

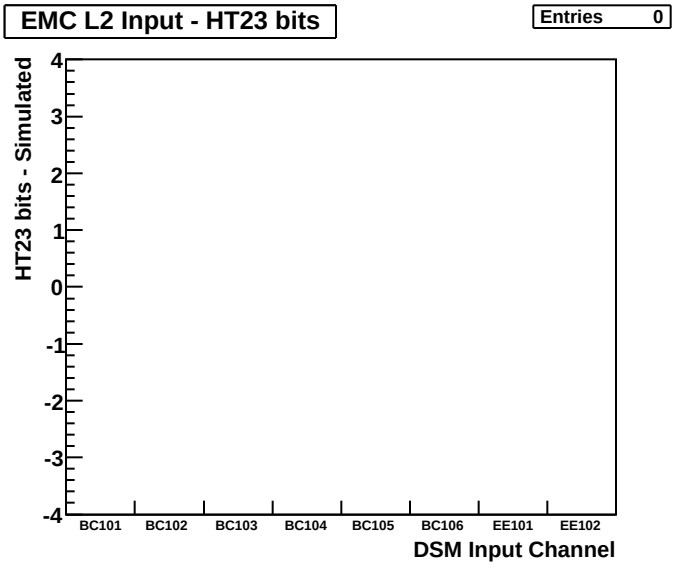

EMC L2 Input - Partial JP Sum
Entries 804872
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

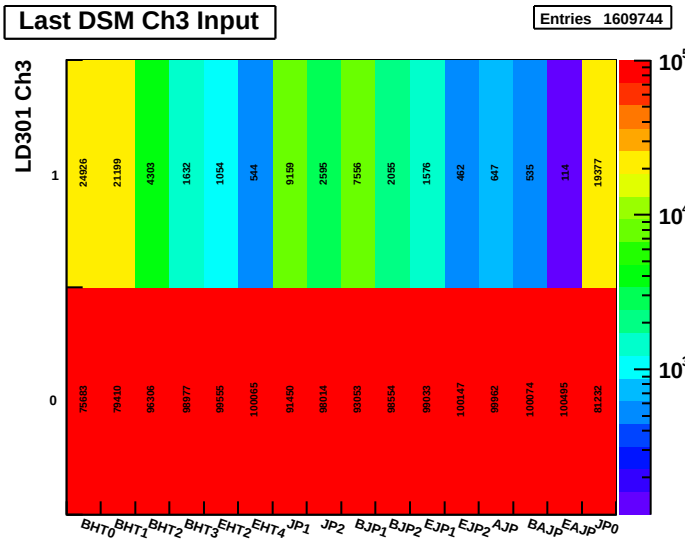

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

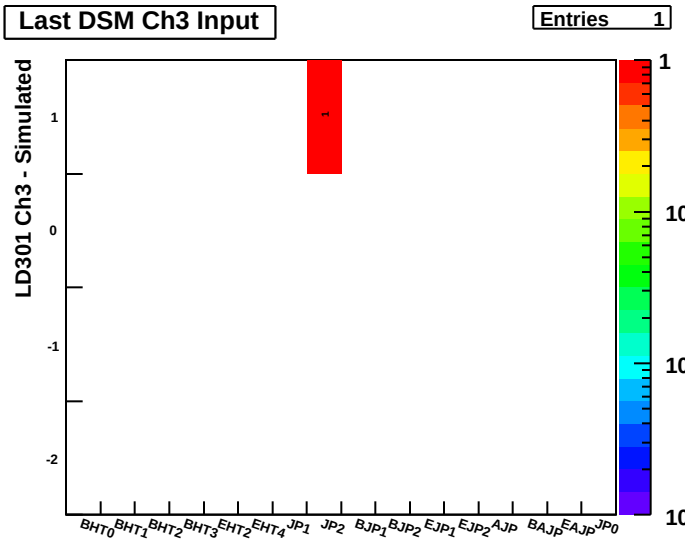

EMC L2 Input - HT01 bits/Partial JP ID
Entries 804872
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

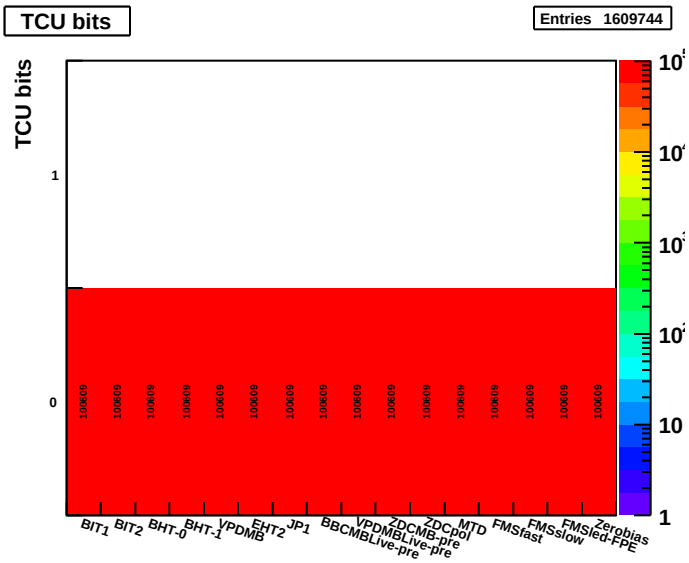

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

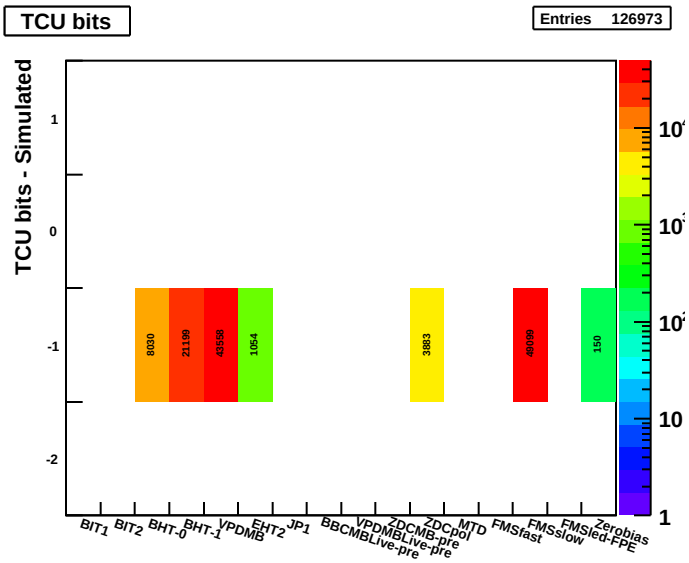

EMC L2 Input - HT23 bits
Entries 804872
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

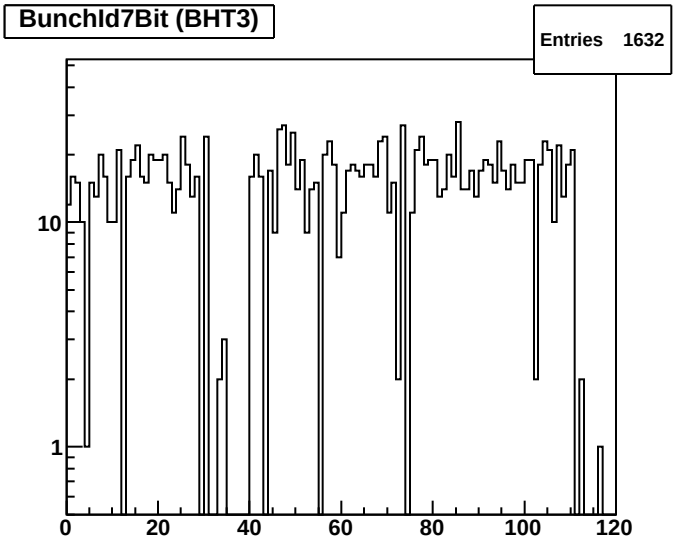

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

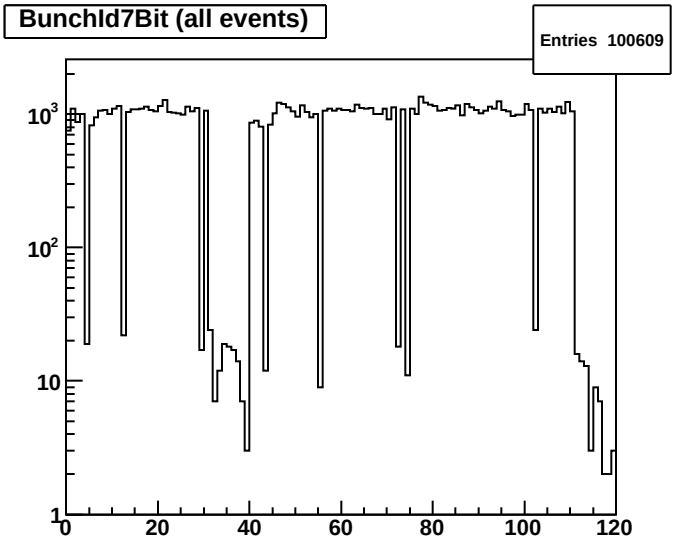

Last DSM Ch3 Input
Entries 1609744
LD301 Ch3
24926 21199 4303 1632 1054 544 9159 2595 7556 2055 1576 462 647 535 114 19377
75683 79410 96306 98977 99555 100065 91450 98014 93053 98554 99033 100147 99962 100074 100495 81232
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

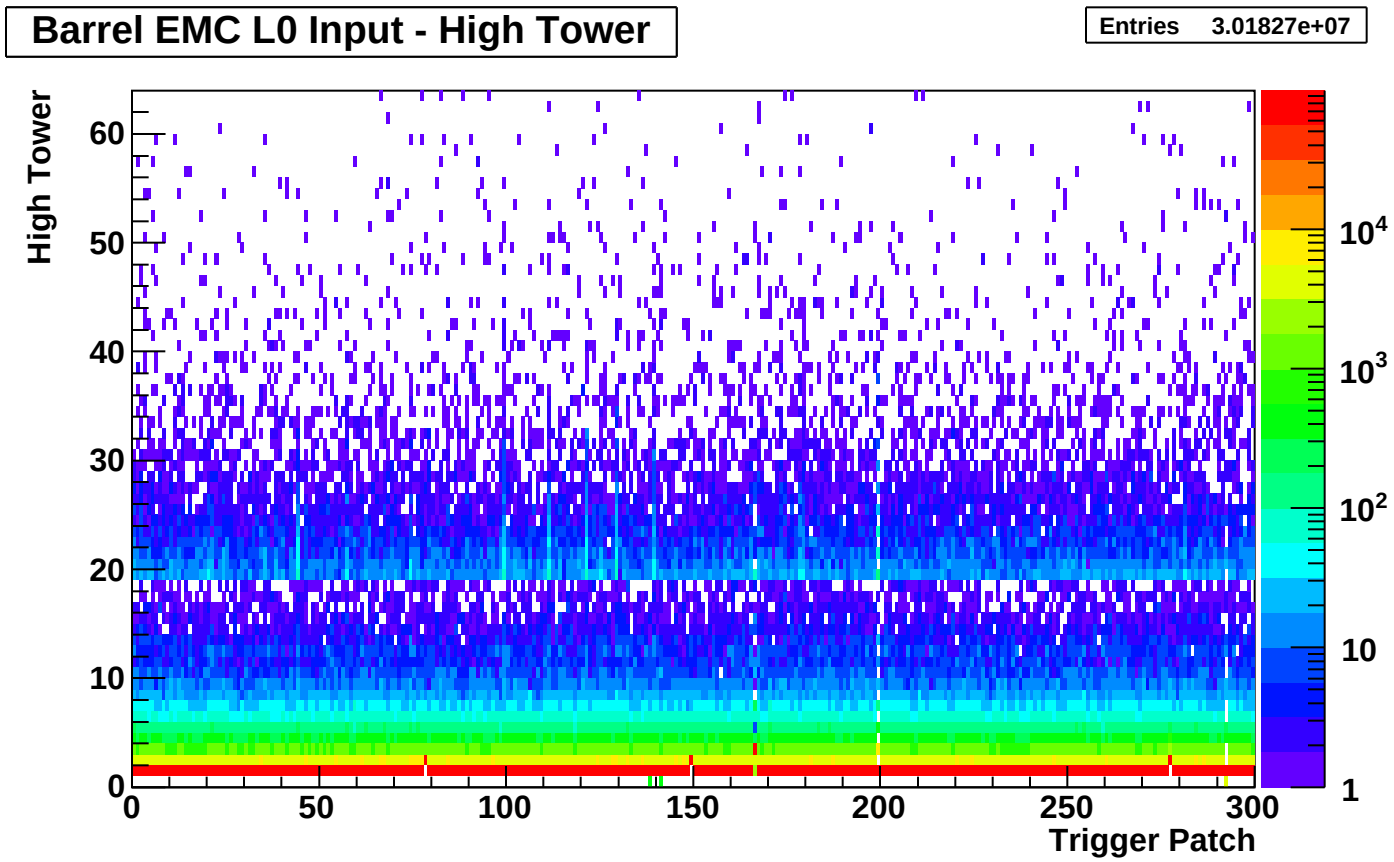

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

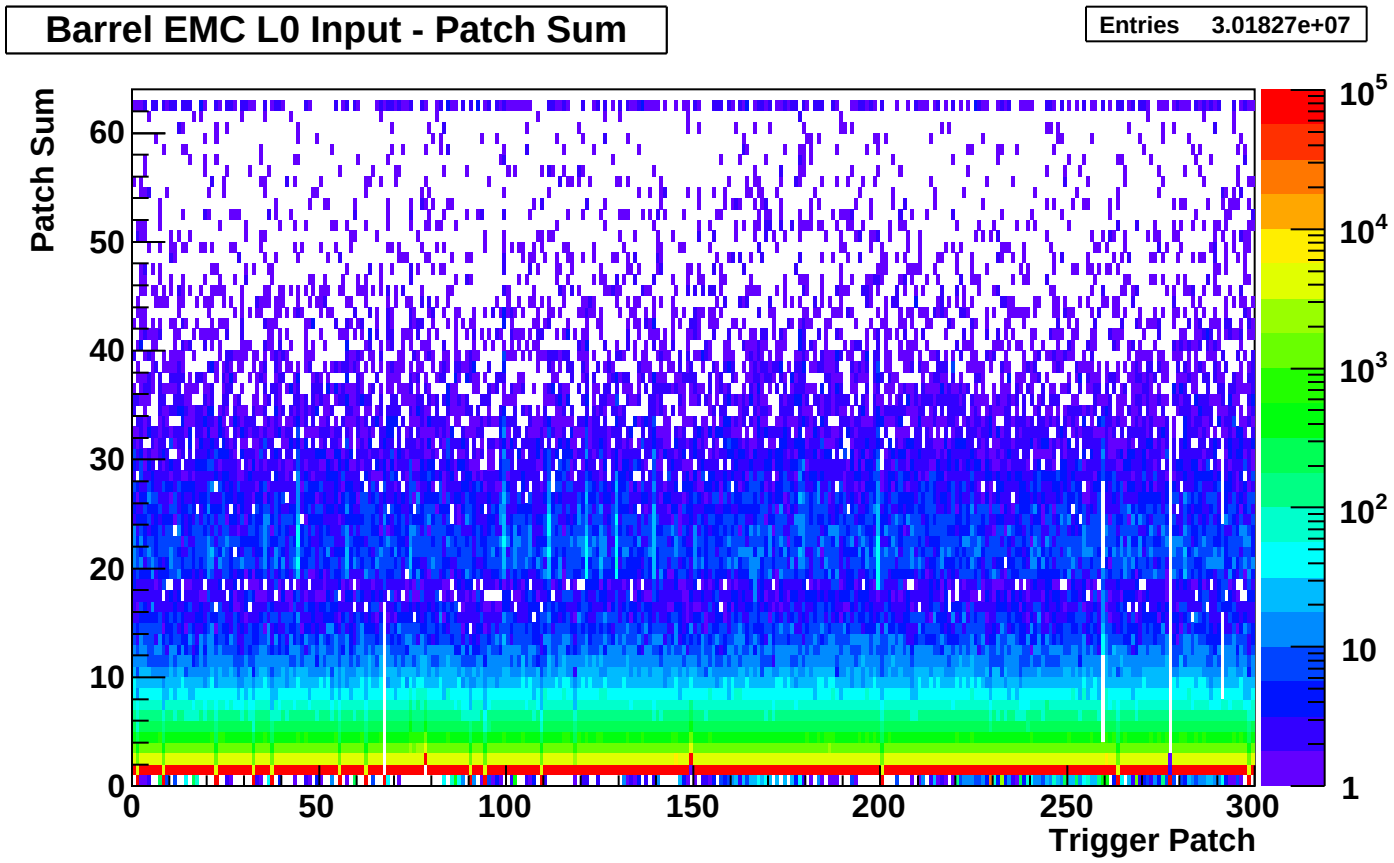

TCU bits
Entries 1609744
TCU bits
100609
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

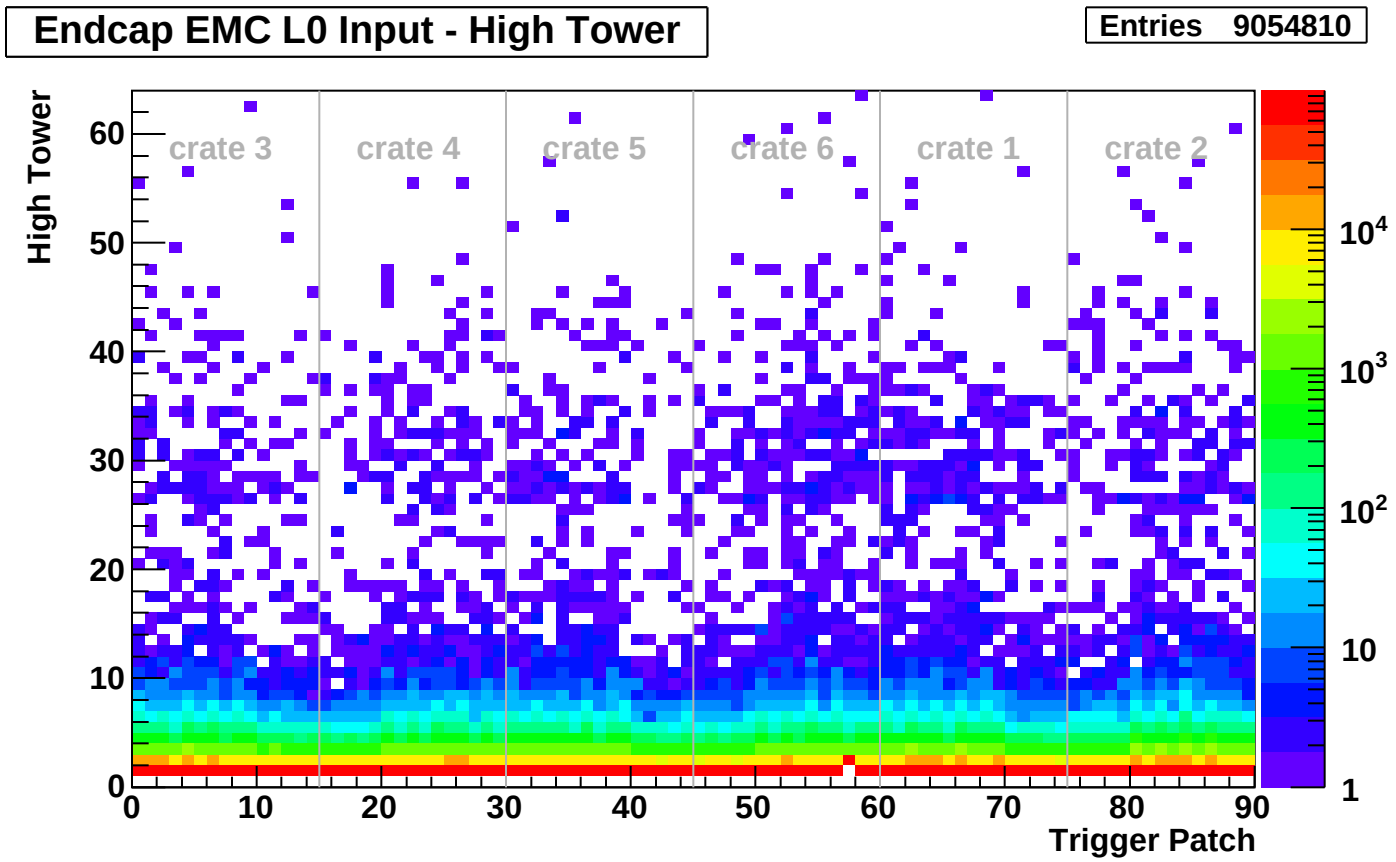

TCU bits
Entries 126973
TCU bits - Simulated
8030 21199 43558 1054 3883 49099 150
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

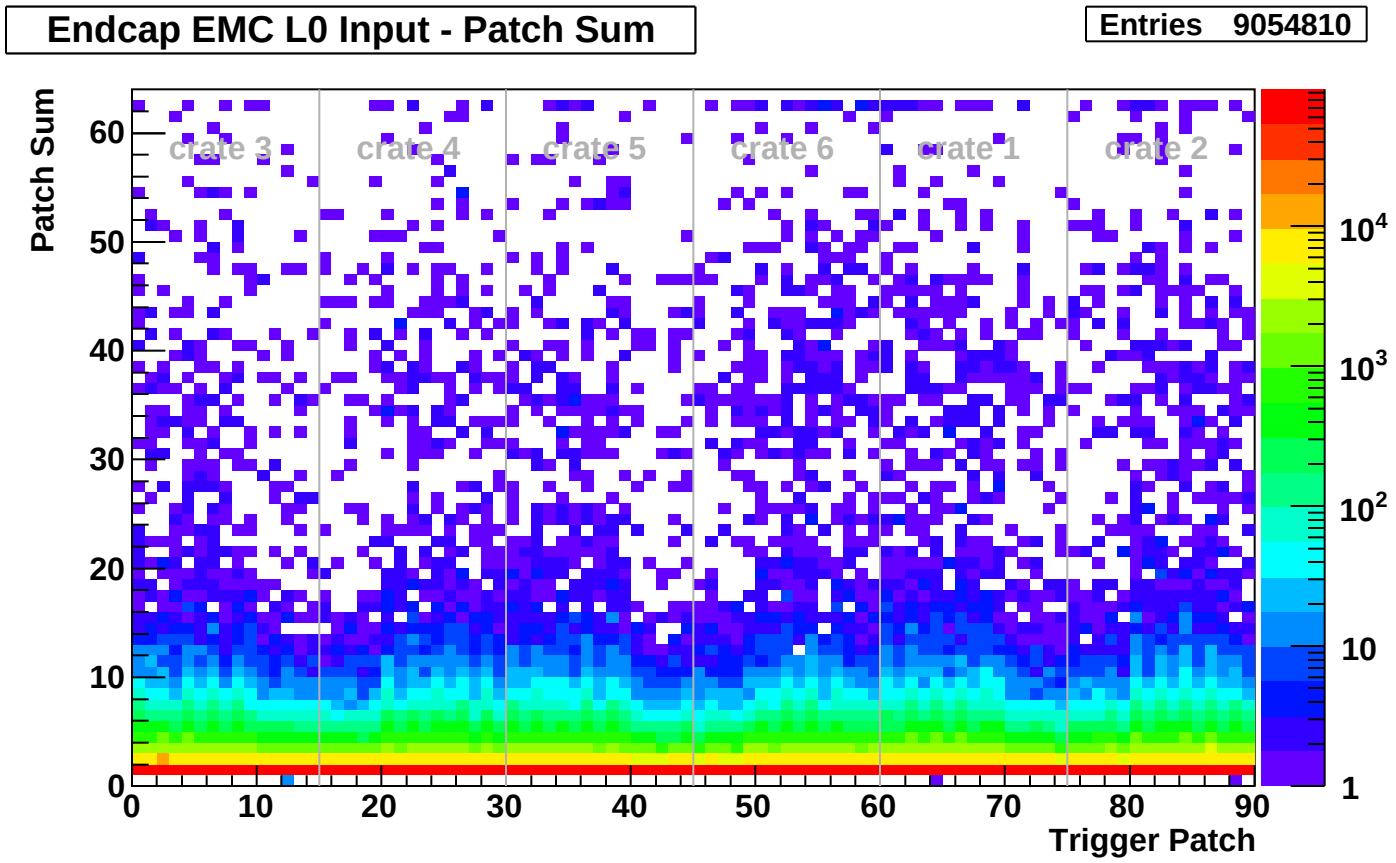

BunchId7Bit (BHT3)
Entries 1632

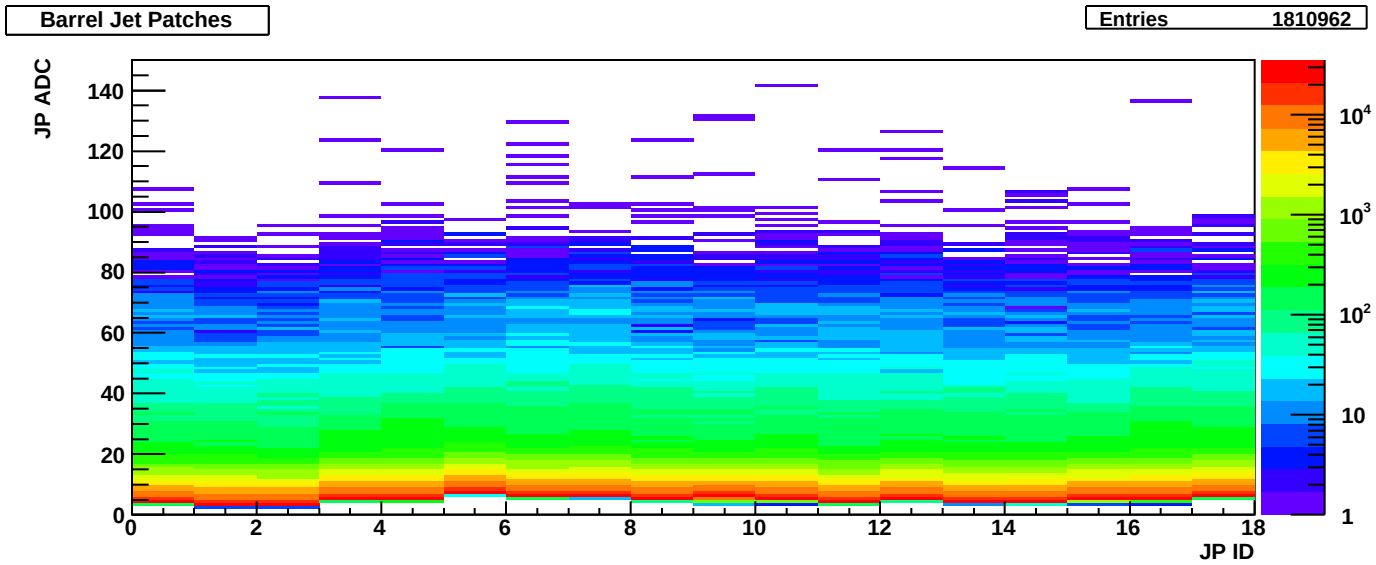

BunchId7Bit (all events)
Entries 100609

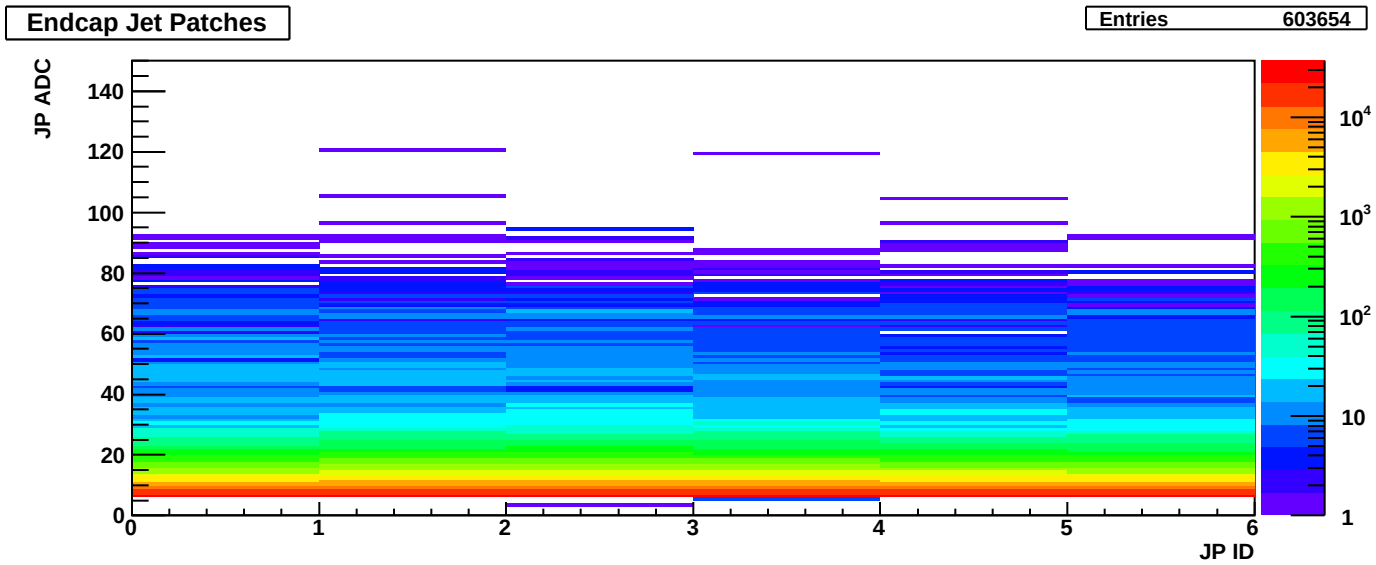

Barrel EMC L0 Input - High Tower
Entries 3.01827e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

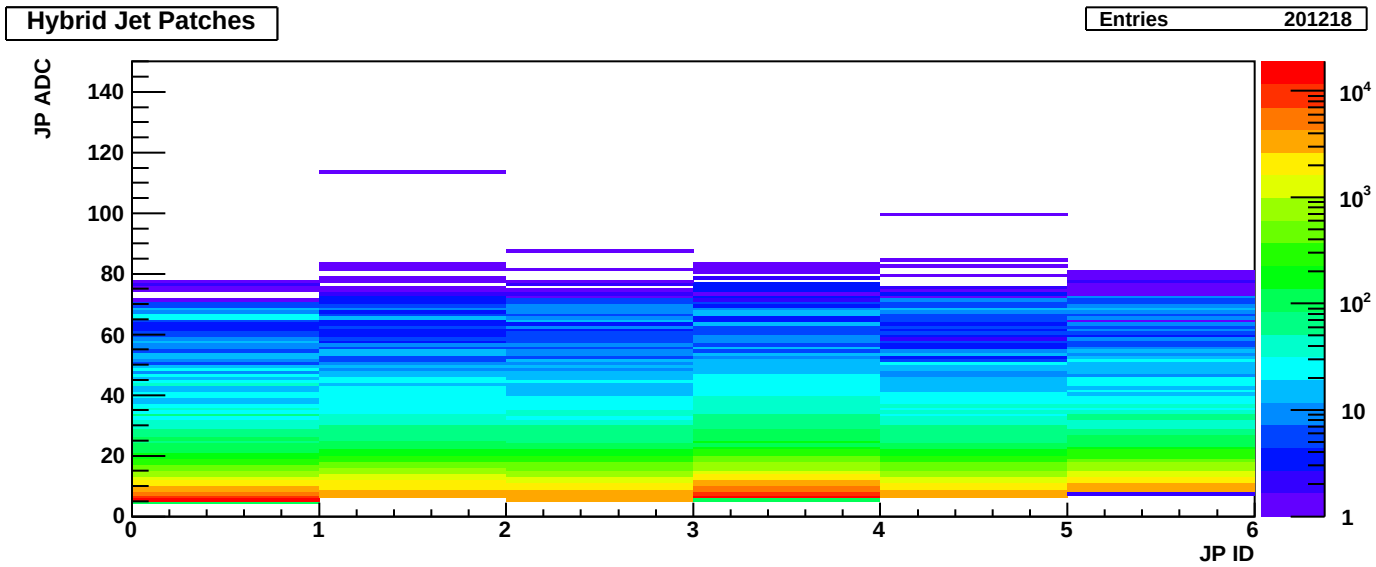

Barrel EMC L0 Input - Patch Sum
Entries 3.01827e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

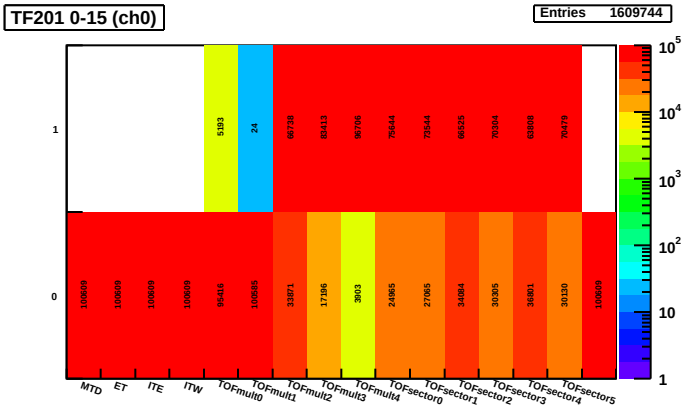

Endcap EMC L0 Input - High Tower
Entries 9054810
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

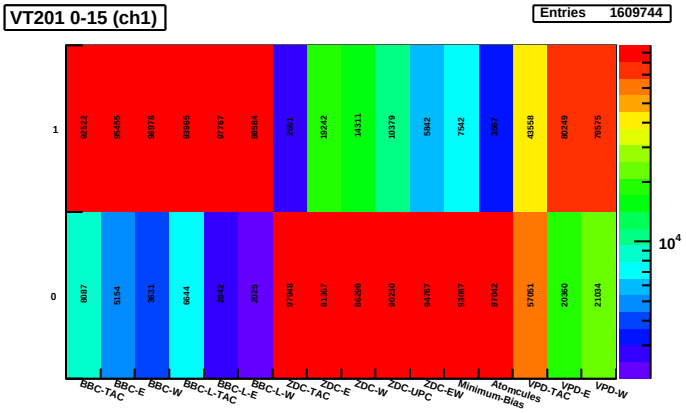

Endcap EMC L0 Input - Patch Sum
Entries 9054810
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

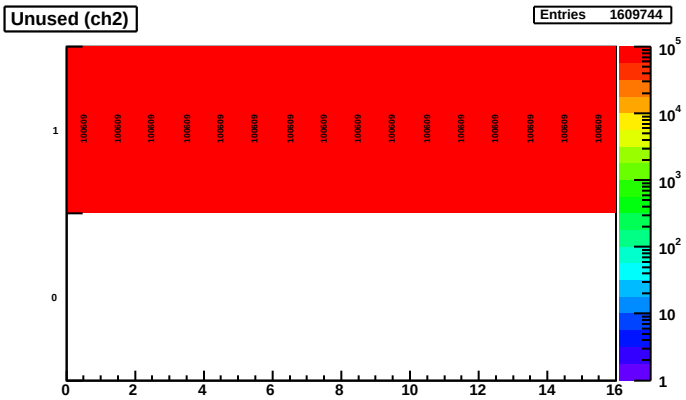

Barrel Jet Patches
Entries 1810962
JP ADC
JP ID

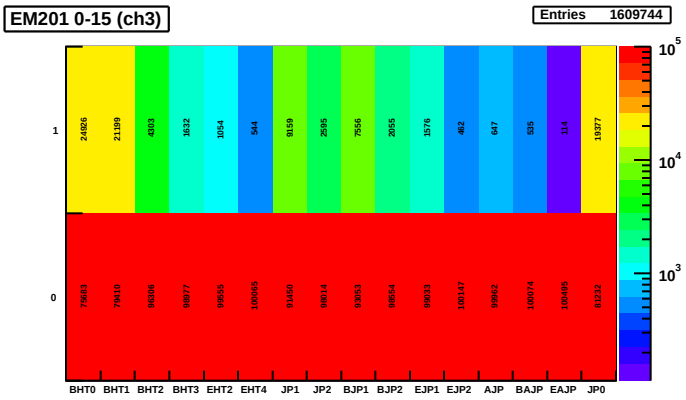

Endcap Jet Patches
Entries 603654
JP ADC
JP ID

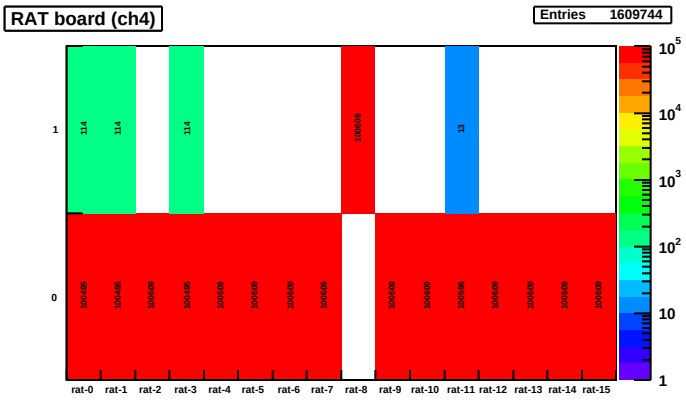

Hybrid Jet Patches
Entries 201218
JP ADC
JP ID

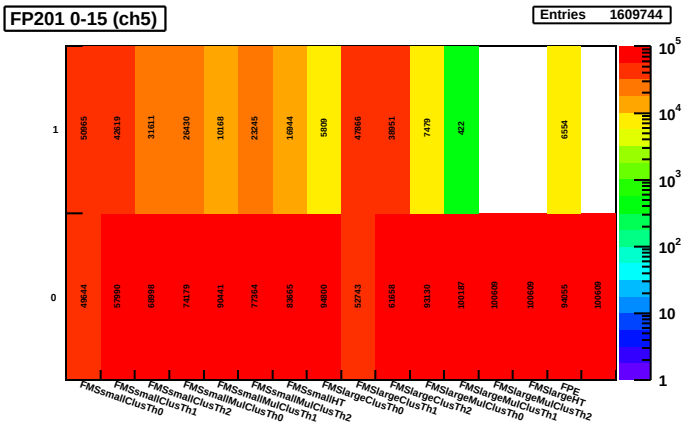

TF201 0-15 (ch0)
Entries 1609744
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

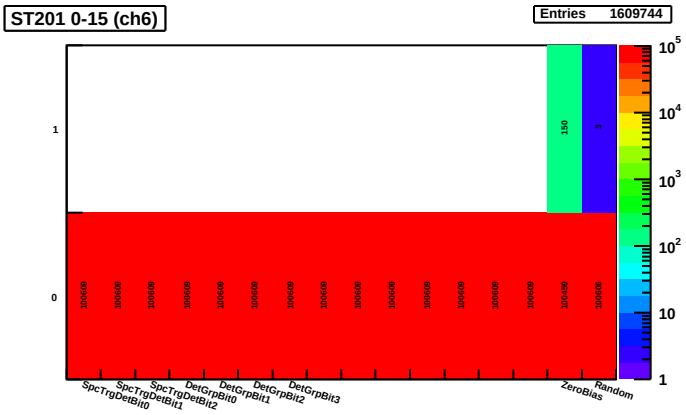

VT201 0-15 (ch1)
Entries 1609744
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

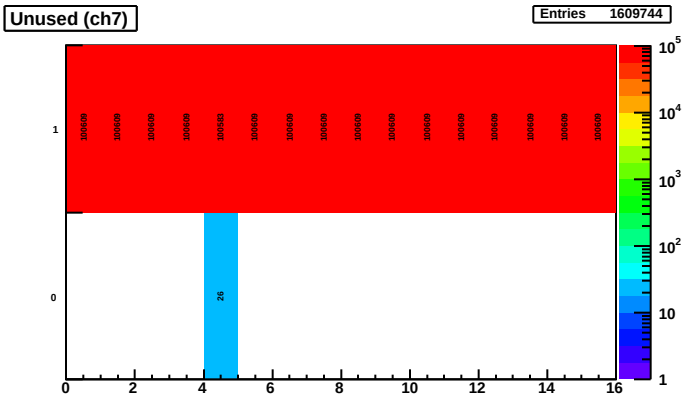

Unused (ch2)
Entries 1609744

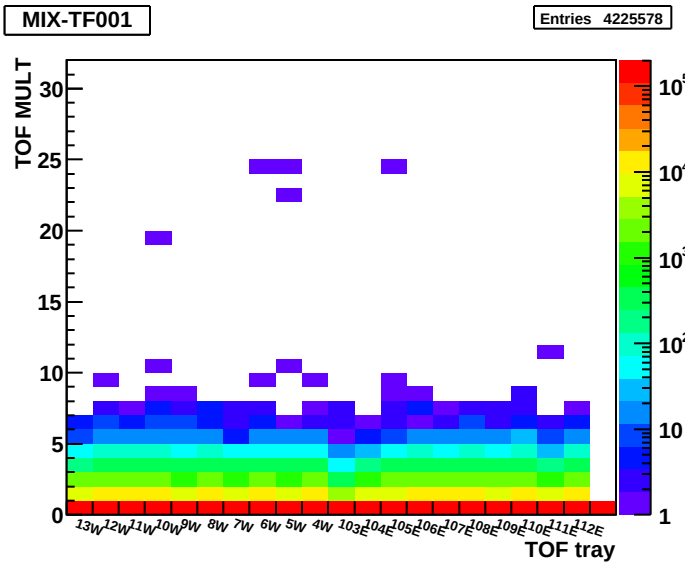

EM201 0-15 (ch3)
Entries 1609744
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

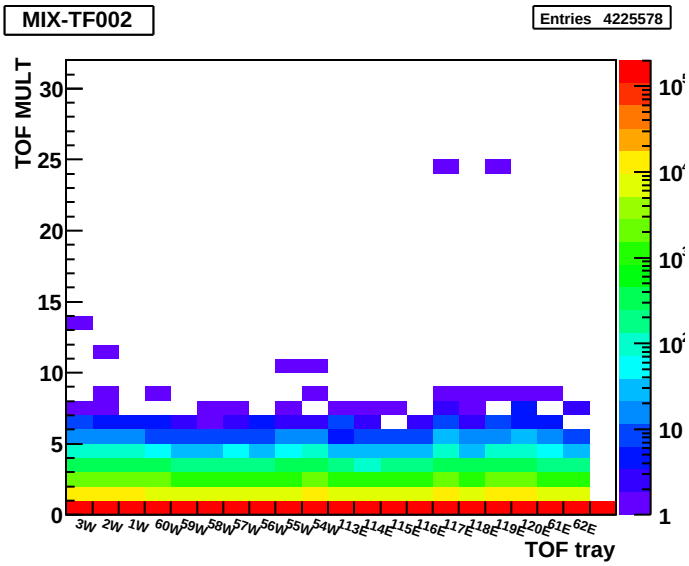

RAT board (ch4)
Entries 1609744
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

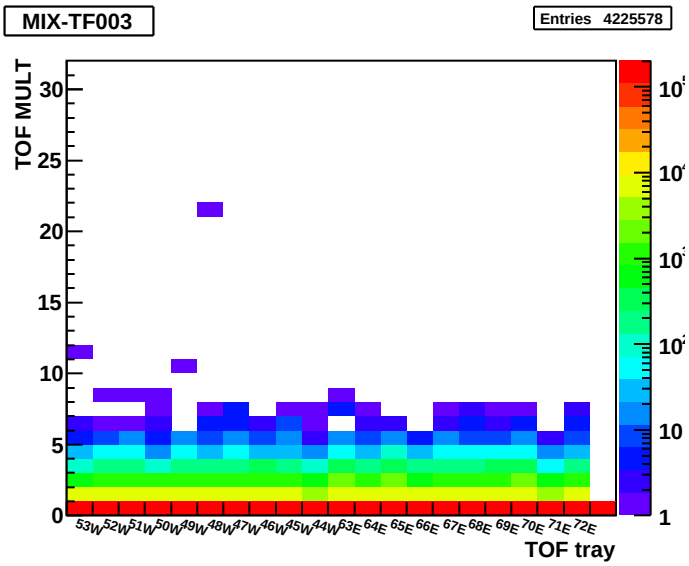

FP201 0-15 (ch5)
Entries 1609744
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

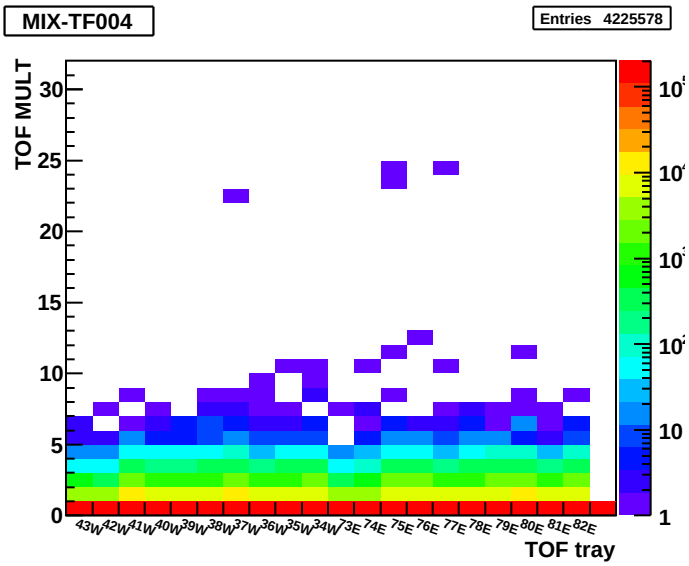

ST201 0-15 (ch6)
Entries 1609744
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

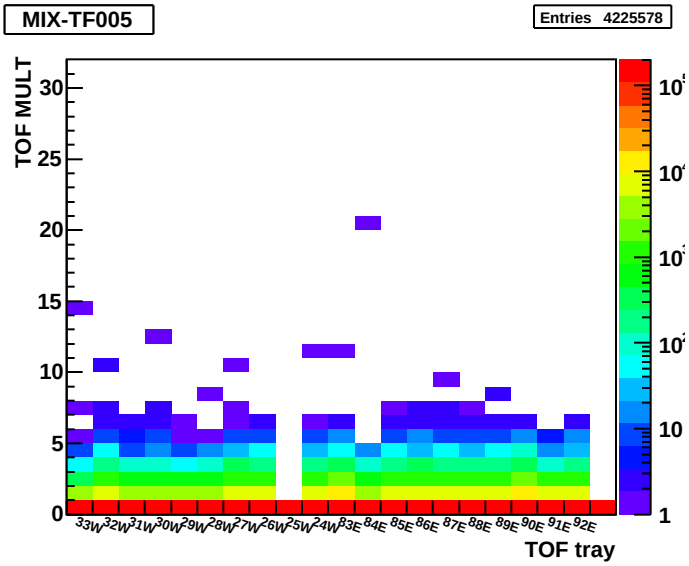

Unused (ch7)
Entries 1609744

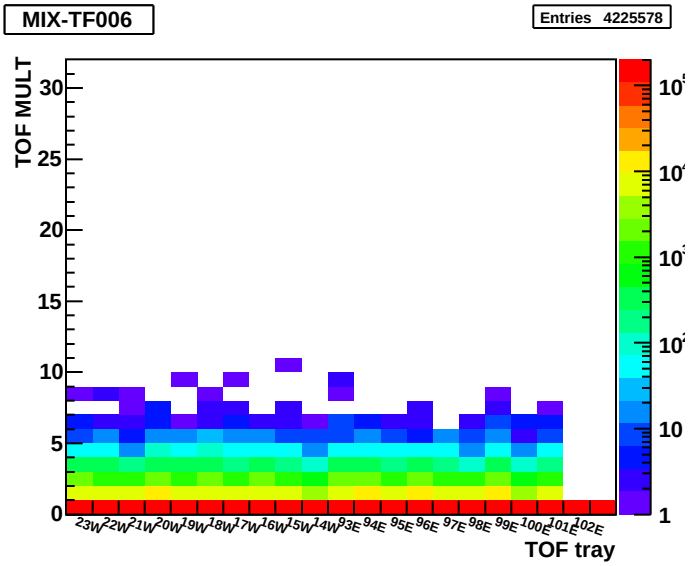

MIX-TF001
Entries 4225578
TOF MULT
TOF tray

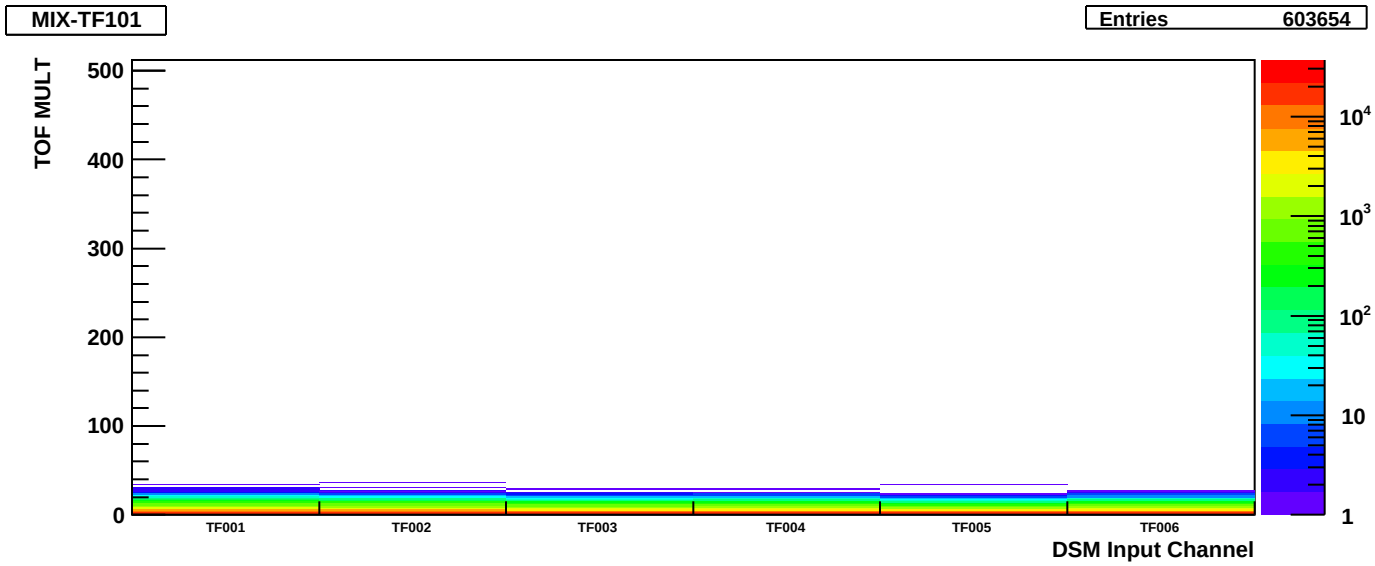

MIX-TF002
Entries 4225578
TOF MULT
TOF tray

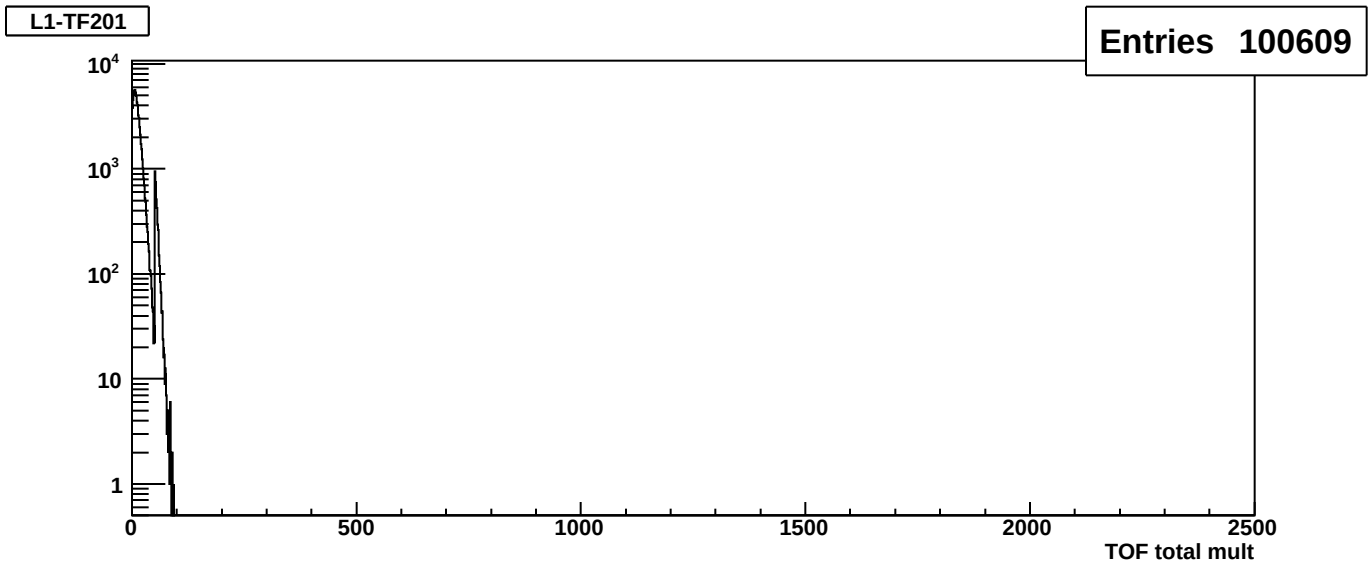

MIX-TF003
Entries 4225578
TOF MULT
TOF tray

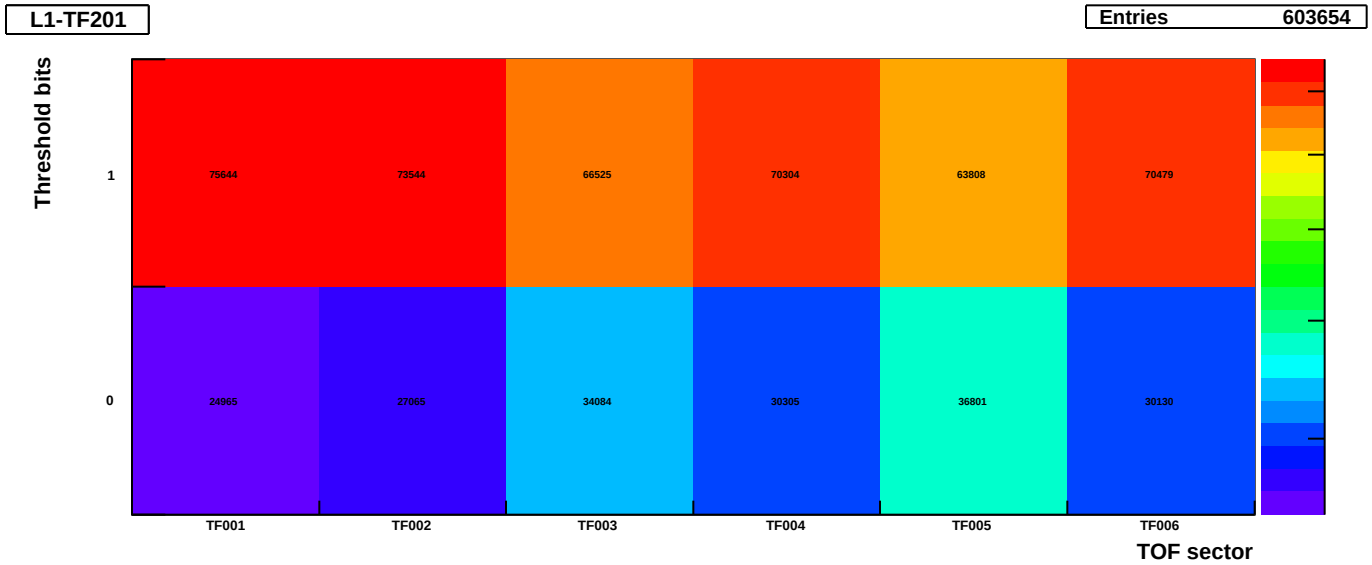

MIX-TF004
Entries 4225578
TOF MULT
TOF tray

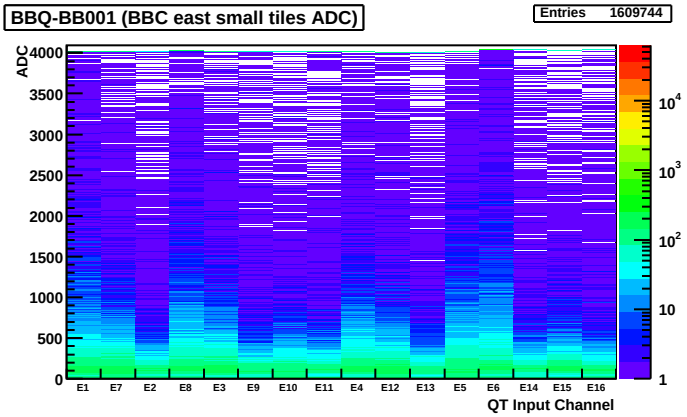

MIX-TF005
Entries 4225578
TOF MULT
TOF tray

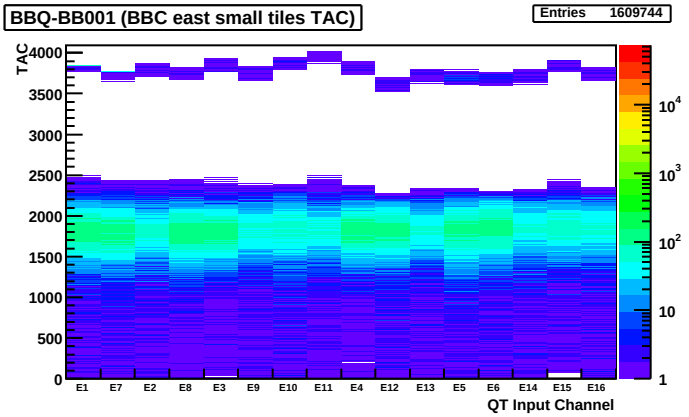

MIX-TF006
Entries 4225578
TOF MULT
TOF tray

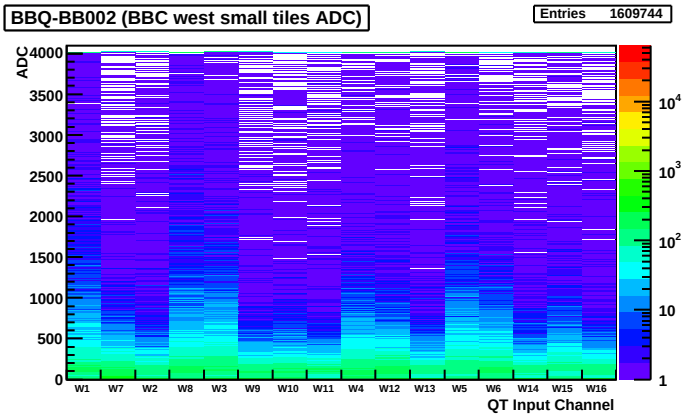

MIX-TF101
Entries 603654
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

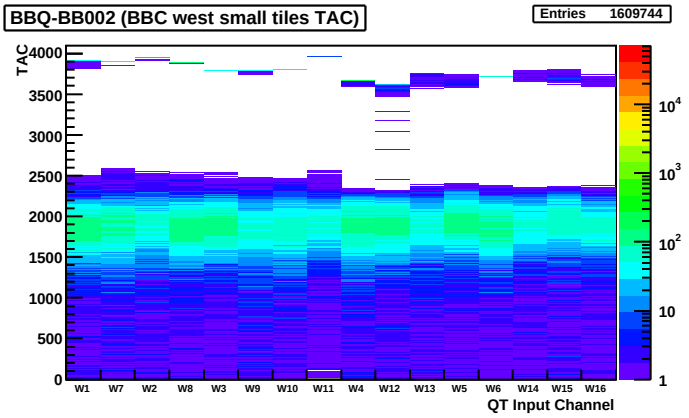

L1-TF201
Entries 100609
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

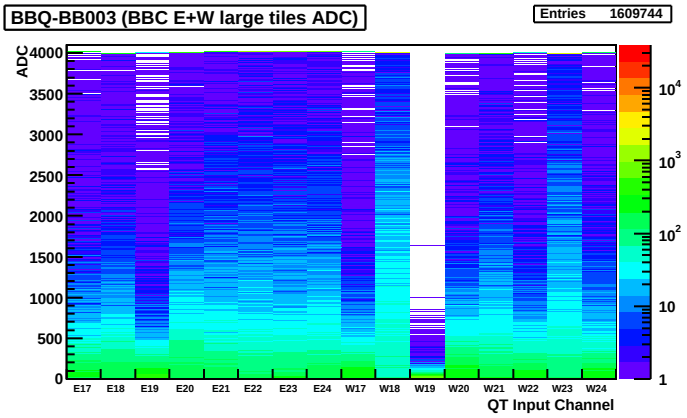

L1-TF201
Entries 603654
Threshold bits
1
0
75644
73544
66525
70304
63808
70479
24965
27065
34084
30305
36801
30130
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

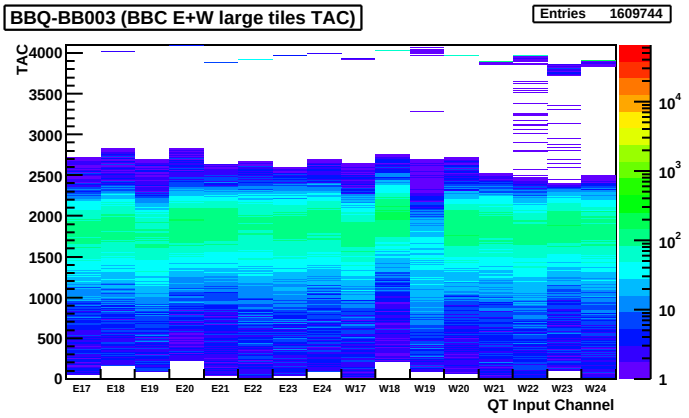

BBQ-BB001 (BBC east small tiles ADC)
Entries 1609744
ADC
QT Input Channel

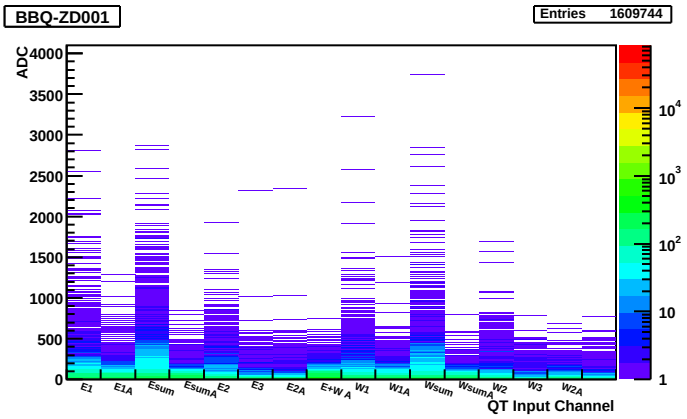

BBQ-BB001 (BBC east small tiles TAC)
Entries 1609744
TAC
QT Input Channel

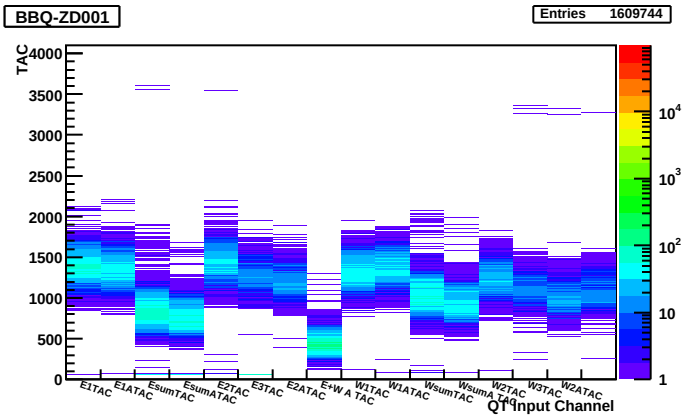

BBQ-BB002 (BBC west small tiles ADC)
Entries 1609744
ADC
QT Input Channel

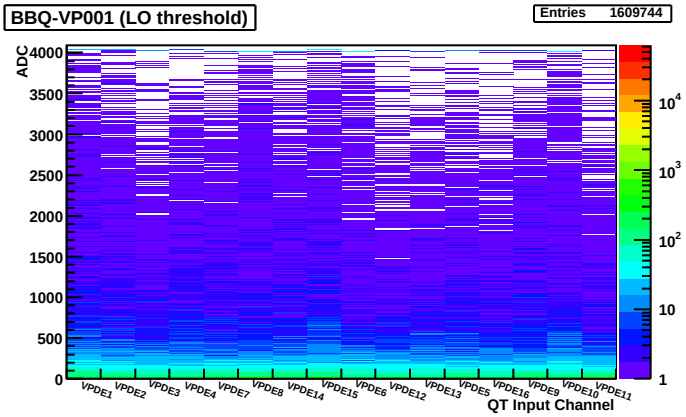

BBQ-BB002 (BBC west small tiles TAC)
Entries 1609744
TAC
QT Input Channel

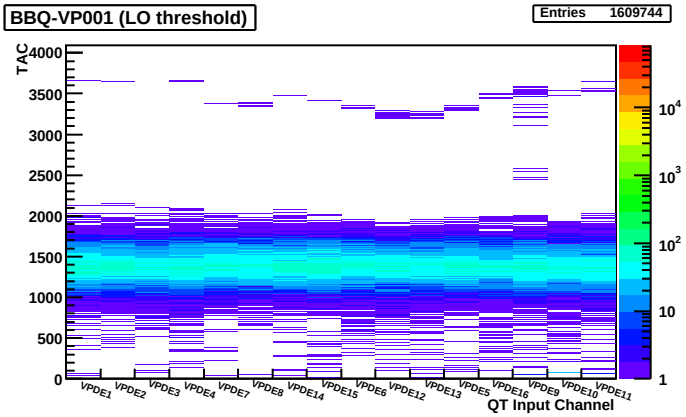

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1609744
ADC
QT Input Channel

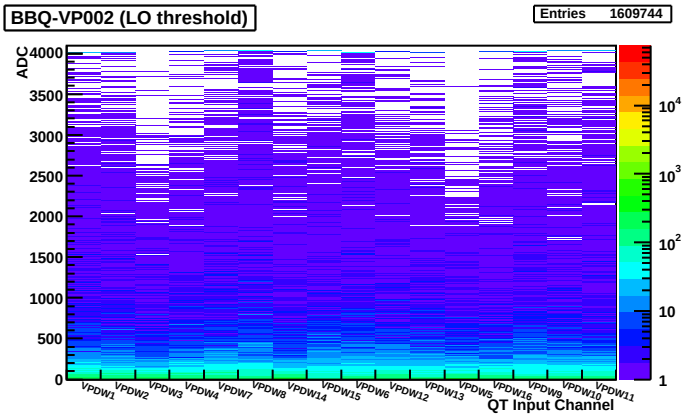

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1609744
TAC
QT Input Channel

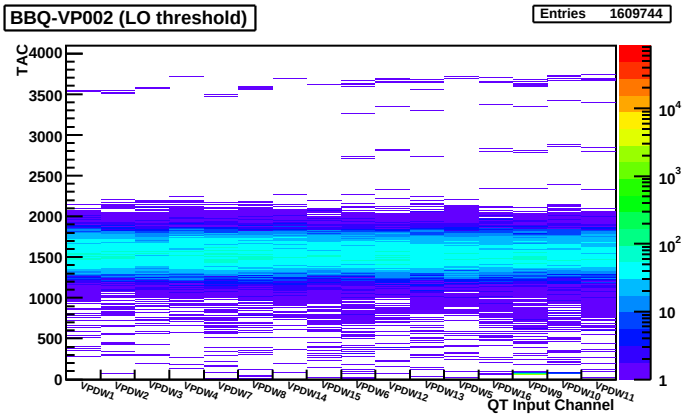

BBQ-ZD001
Entries 1609744
ADC
QT Input Channel

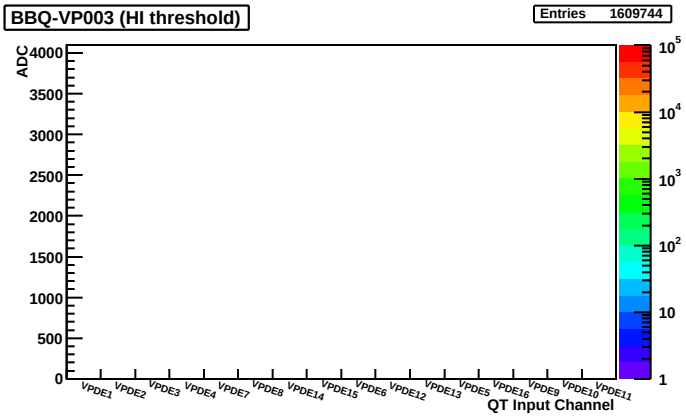

BBQ-ZD001
Entries 1609744
TAC
QT Input Channel

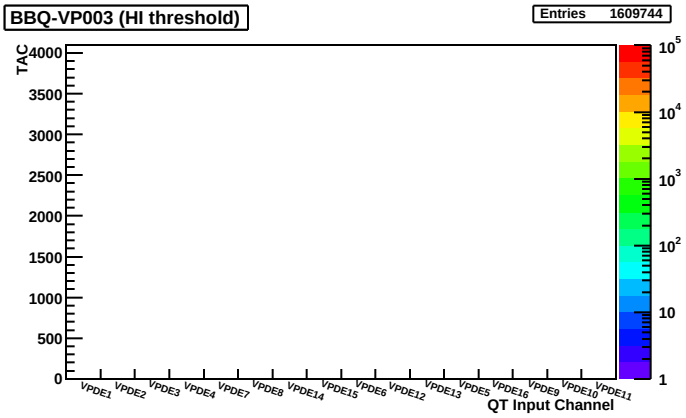

BBQ-VP001 (LO threshold)
Entries 1609744
ADC
QT Input Channel

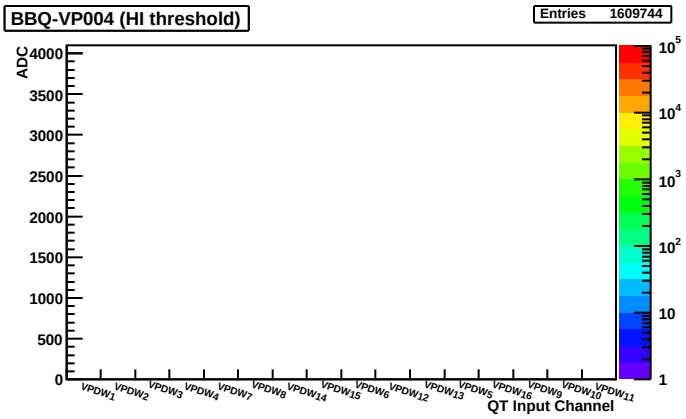

BBQ-VP001 (LO threshold)
Entries 1609744
TAC
QT Input Channel

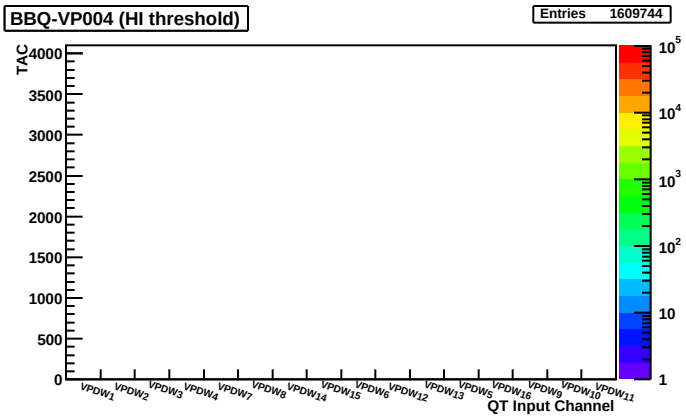

BBQ-VP002 (LO threshold)
Entries 1609744
ADC
QT Input Channel

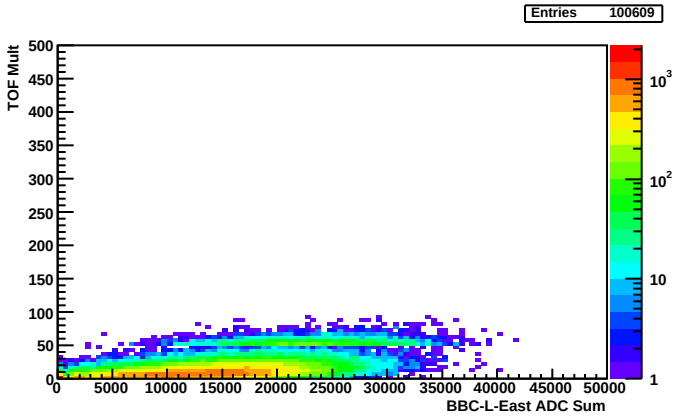

BBQ-VP002 (LO threshold)
Entries 1609744
TAC
QT Input Channel

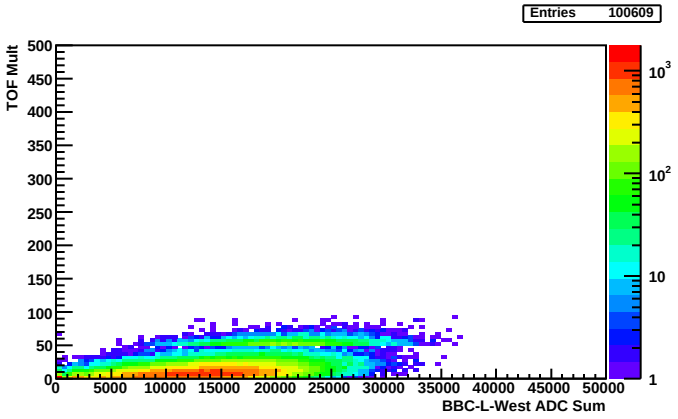

BBQ-VP003 (HI threshold)
Entries 1609744
ADC
QT Input Channel

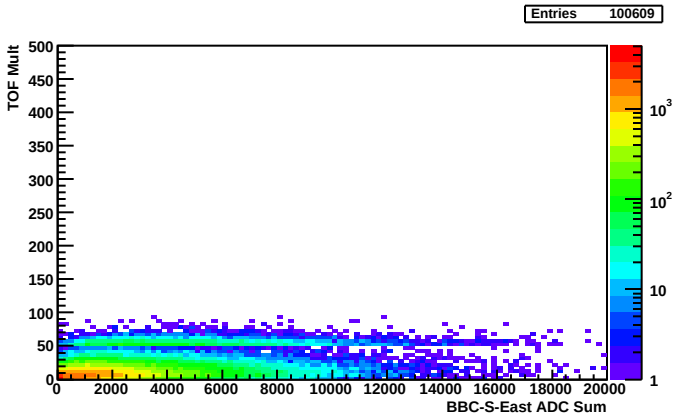

BBQ-VP003 (HI threshold)
Entries 1609744
TAC
QT Input Channel

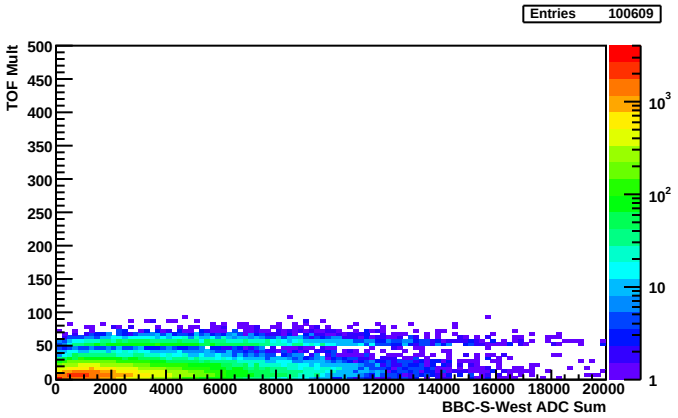

BBQ-VP004 (HI threshold)
Entries 1609744
ADC
QT Input Channel

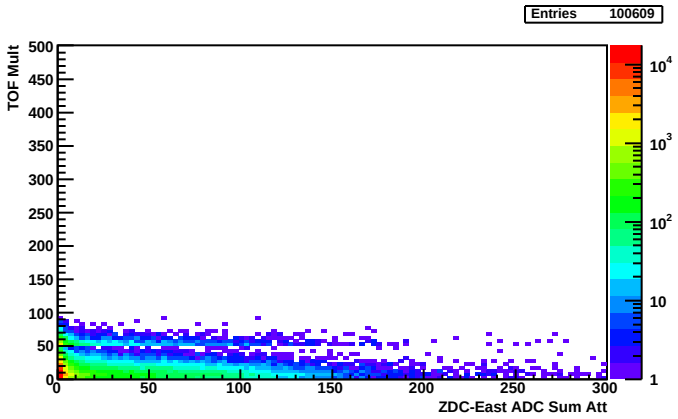

BBQ-VP004 (HI threshold)
Entries 1609744
TAC
QT Input Channel

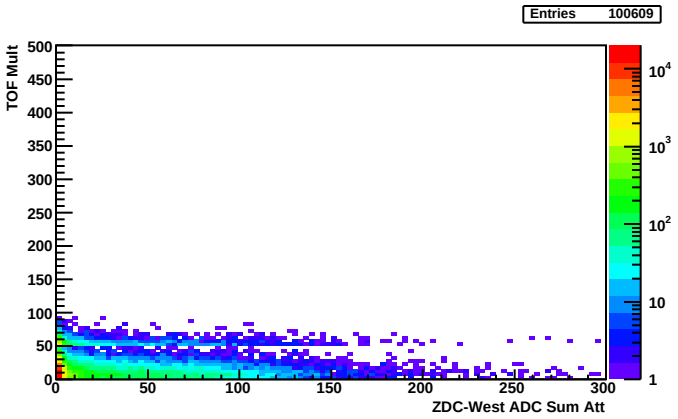

Entries 100609
TOF Mult
BBC-L-East ADC Sum

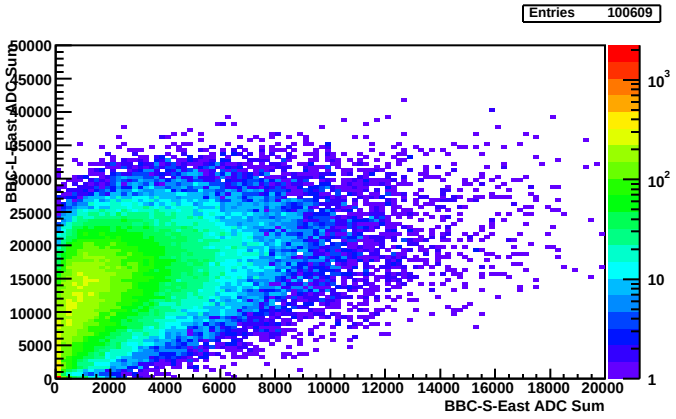

Entries 100609
TOF Mult
BBC-L-West ADC Sum

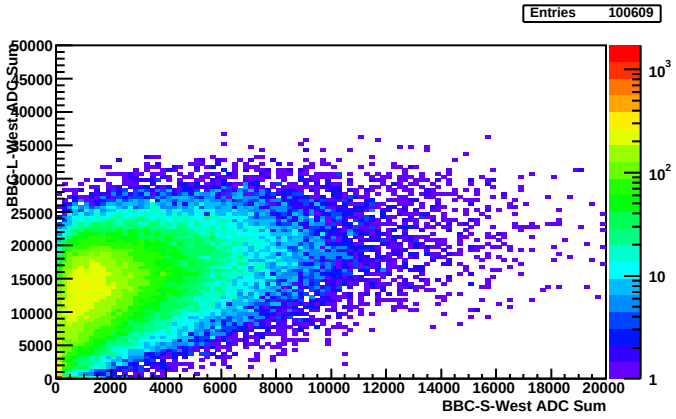

Entries 100609
TOF Mult
BBC-S-East ADC Sum

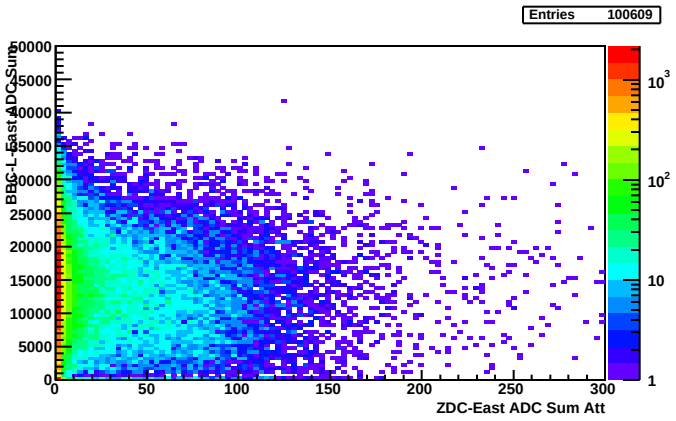

Entries 100609
TOF Mult
BBC-S-West ADC Sum

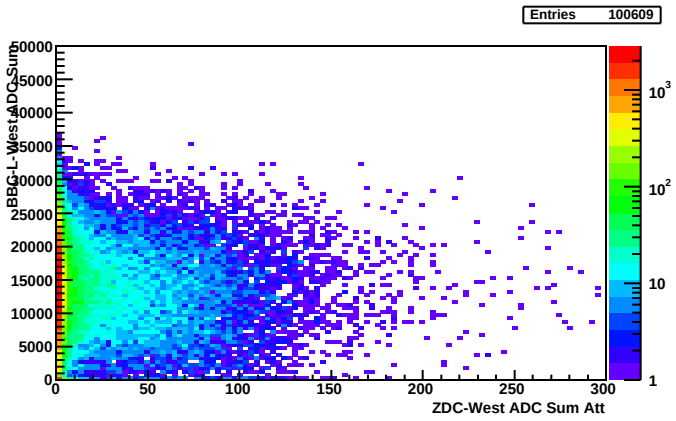

Entries 100609
TOF Mult
ZDC-East ADC Sum Att

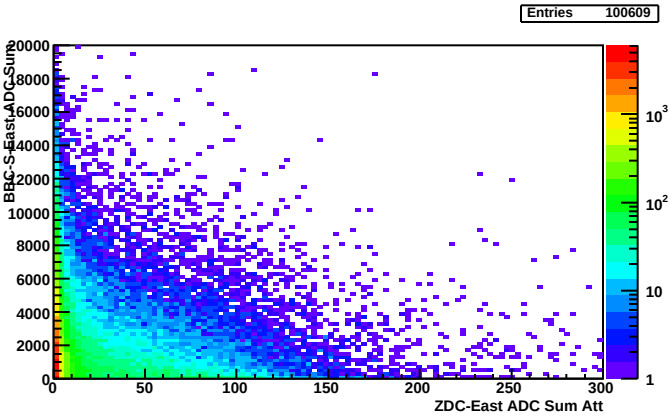

Entries 100609
TOF Mult
ZDC-West ADC Sum Att

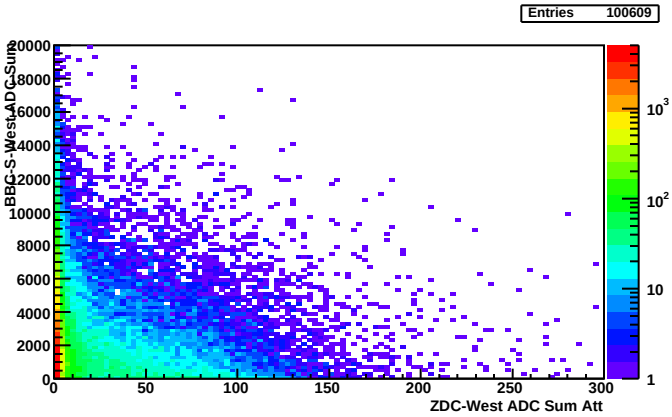

Entries 100609
BBC-L-East ADC Sum
BBC-S-East ADC Sum

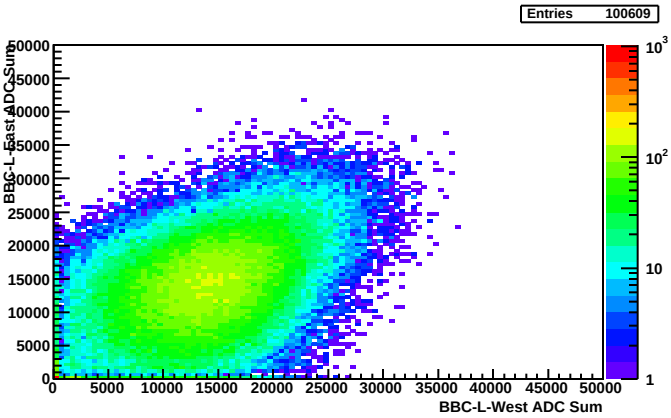

Entries 100609
BBC-L-West ADC Sum
BBC-S-West ADC Sum

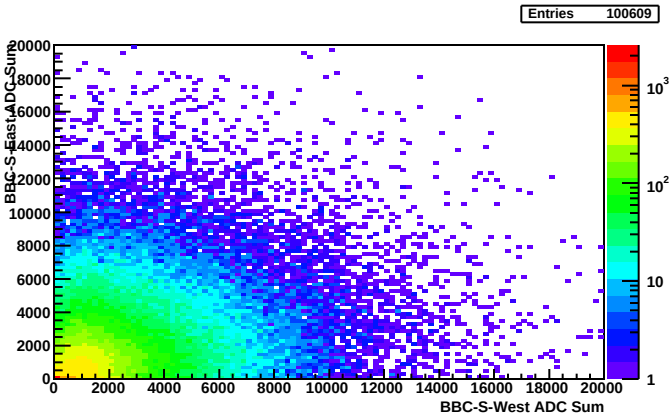

Entries 100609
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

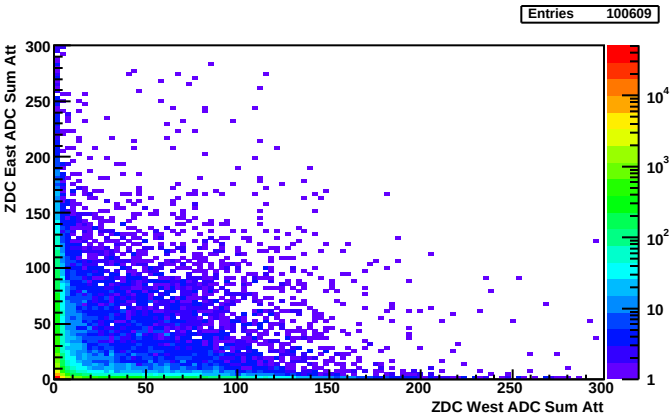

Entries 100609
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

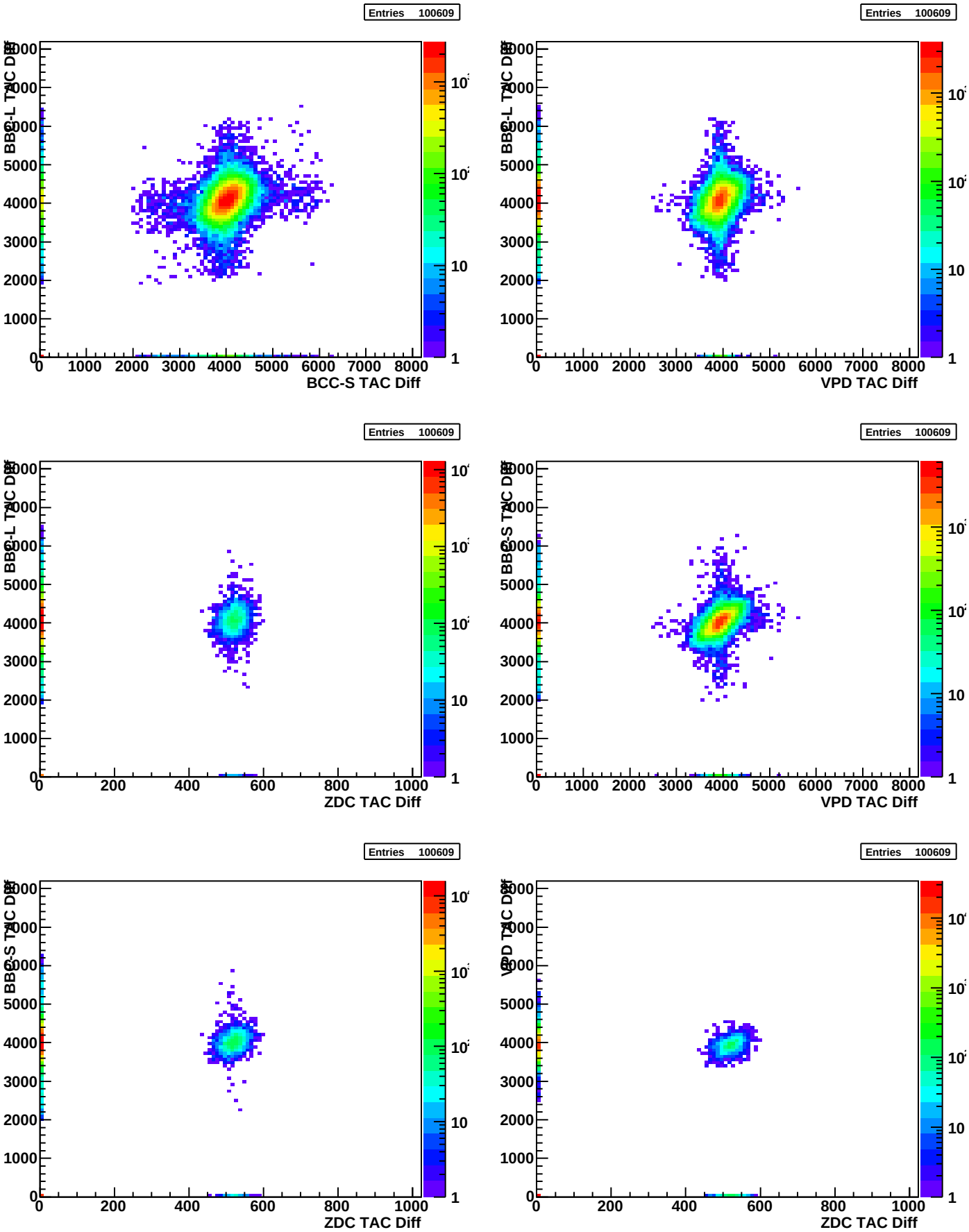

Entries 100609
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

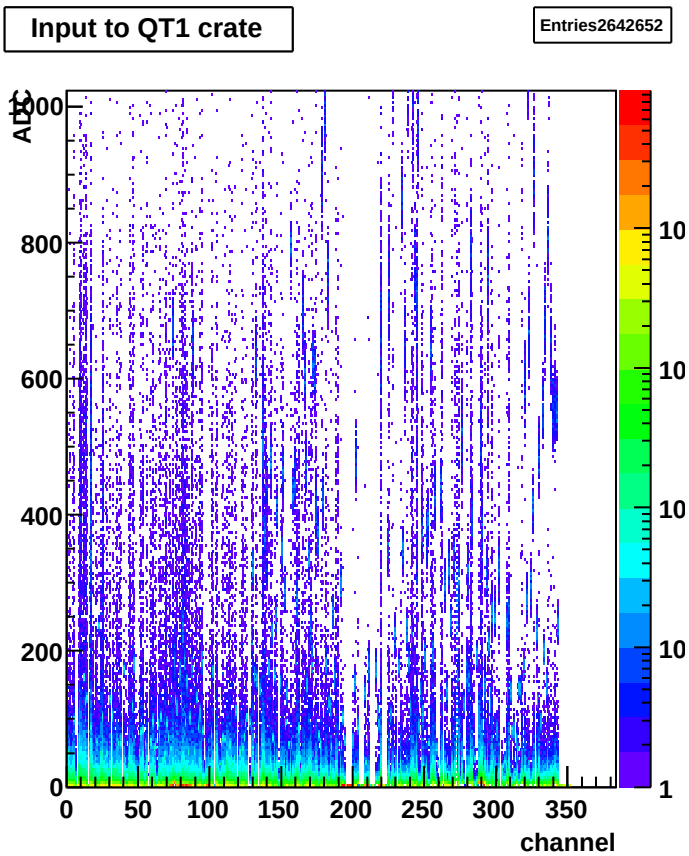

Entries 100609
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

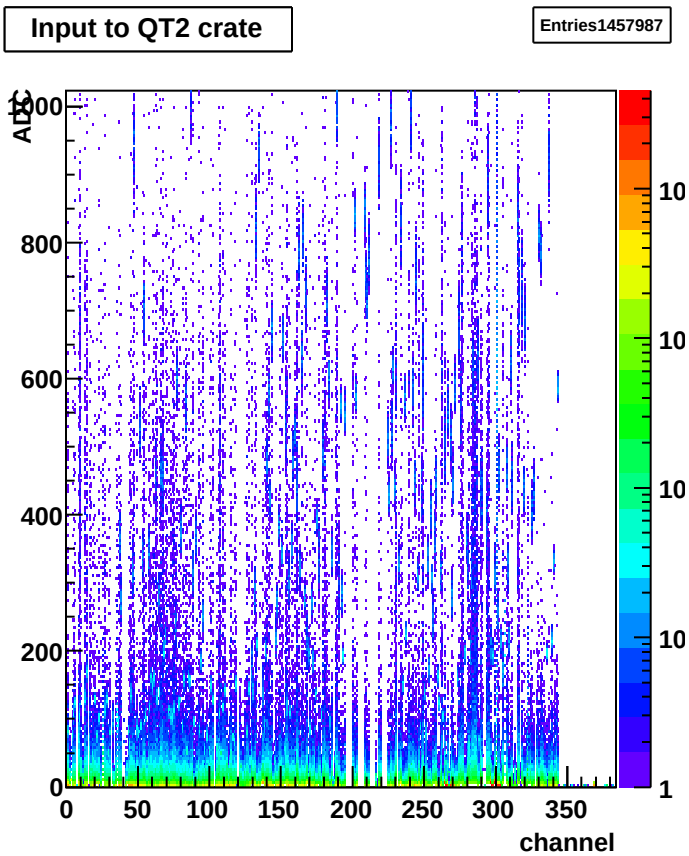

Entries 100609
BBC-L-East ADC Sum
BBC-L-West ADC Sum

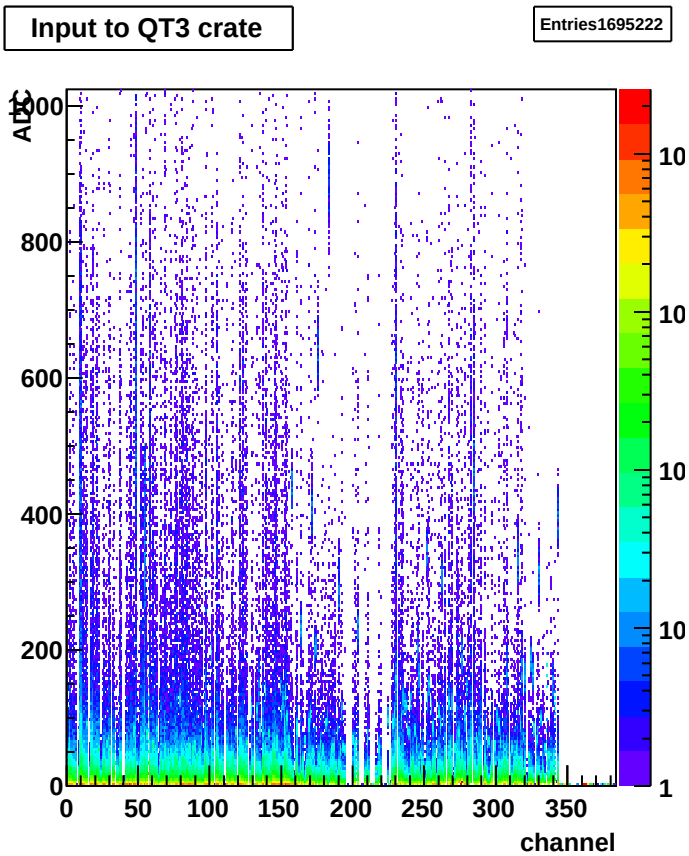

Entries 100609
BBC-S-East ADC Sum
BBC-S-West ADC Sum

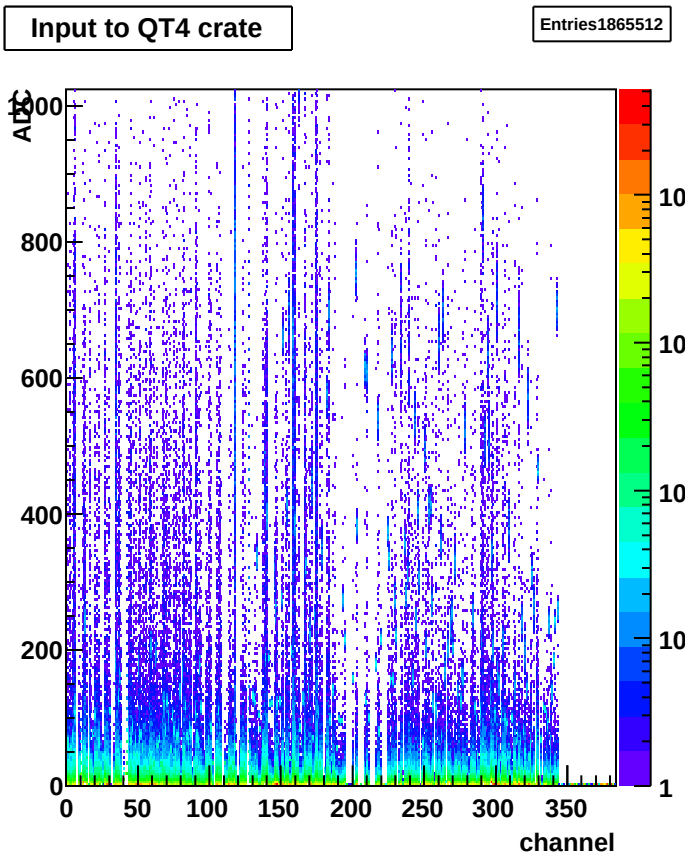

Entries 100609
ZDC East ADC Sum Att
ZDC West ADC Sum Att

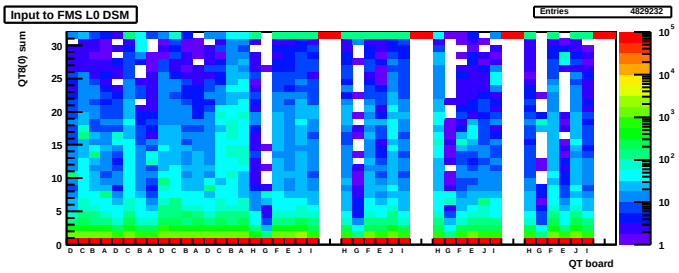

Entries 100609
BBC-L TAC Diff
BCC-S TAC Diff
Entries 100609
BBC-L TAC Diff
VPD TAC Diff
Entries 100609
BBC-L TAC Diff
ZDC TAC Diff
Entries 100609
BBC-S TAC Diff
VPD TAC Diff
Entries 100609
BBC-S TAC Diff
ZDC TAC Diff
Entries 100609
VPD TAC Diff
ZDC TAC Diff

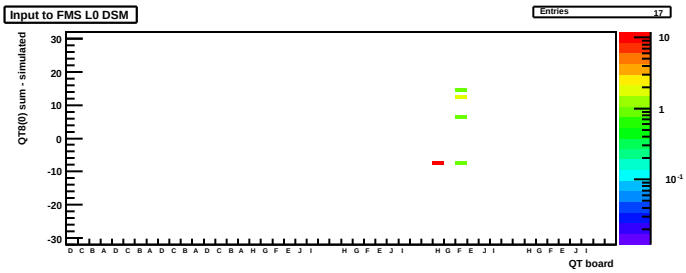

Input to QT1 crate
Entries2642652
ADC
channel

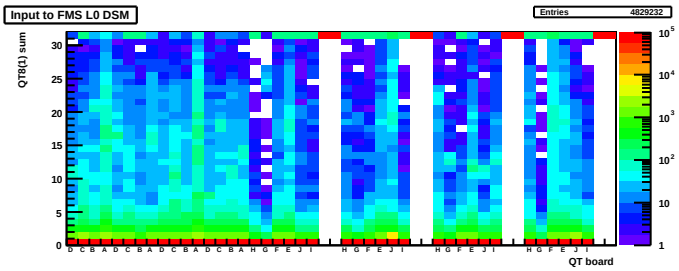

Input to QT2 crate
Entries1457987
ADC
channel

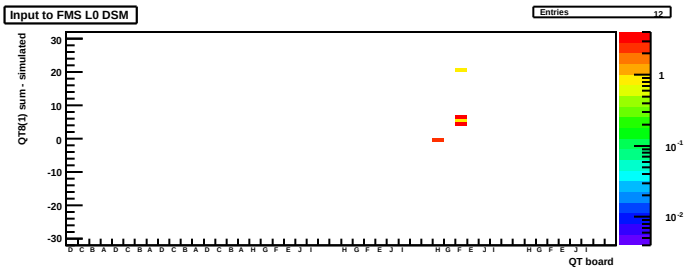

Input to QT3 crate
Entries1695222
ADC
channel

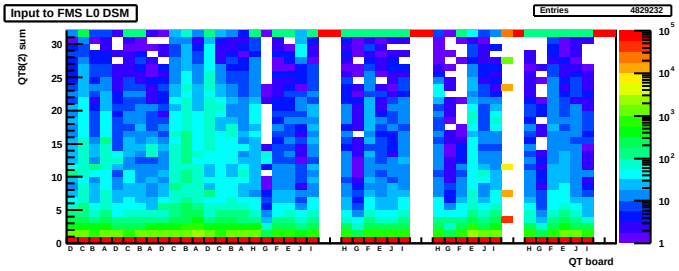

Input to QT4 crate
Entries1865512
ADC
channel

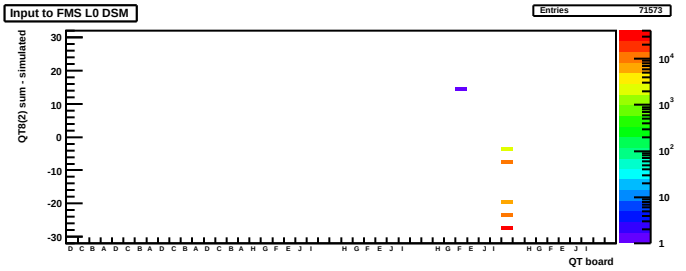

Input to FMS L0 DSM
Entries 4829232
QT8(0) sum
QT board

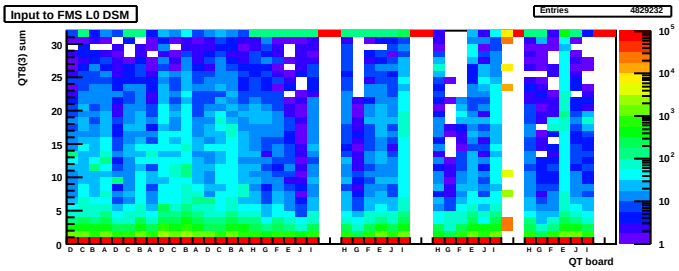

Input to FMS L0 DSM
Entries 17
QT8(0) sum - simulated
QT board

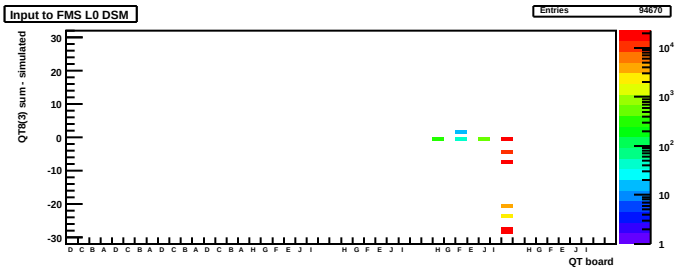

Input to FMS L0 DSM
Entries 4829232
QT8(1) sum
QT board

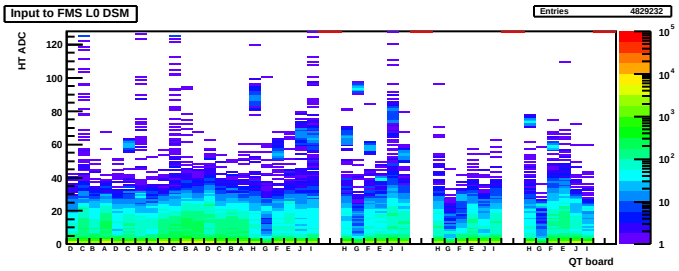

Input to FMS L0 DSM
Entries 12
QT8(1) sum - simulated
QT board

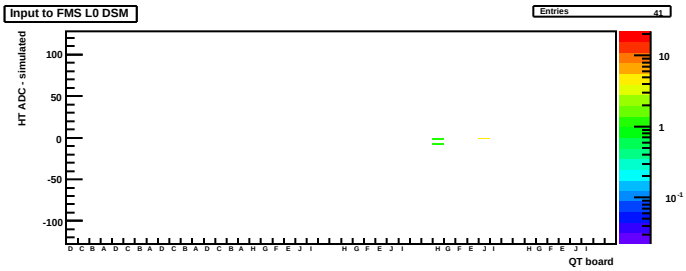

Input to FMS L0 DSM
Entries 4829232
QT8(2) sum
QT board

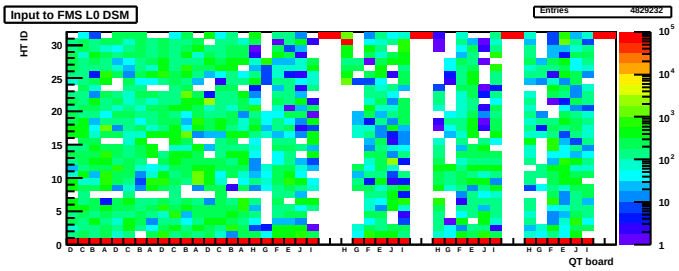

Input to FMS L0 DSM
Entries 71573
QT8(2) sum - simulated
QT board

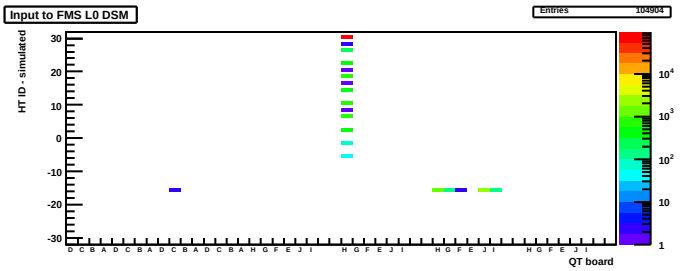

Input to FMS L0 DSM
Entries 4829232
QT8(3) sum
QT board

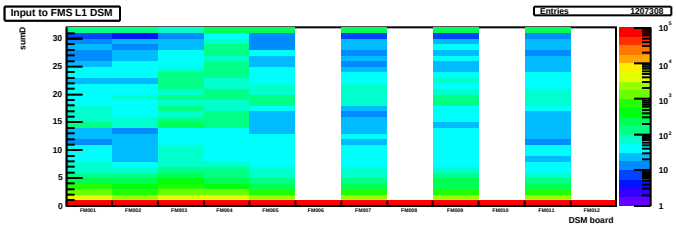

Input to FMS L0 DSM
Entries 94670
QT8(3) sum - simulated
QT board

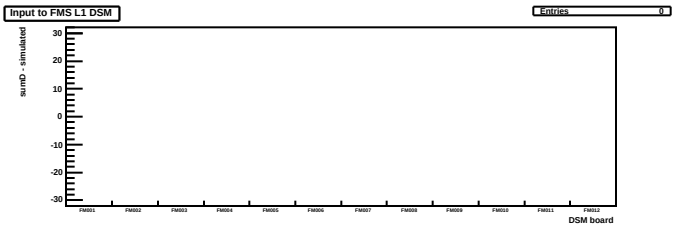

Input to FMS L0 DSM
Entries 4829232
HT ADC
QT board

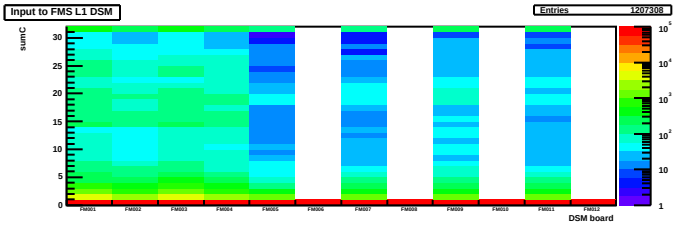

Input to FMS L0 DSM
Entries 41
HT ADC - simulated
QT board

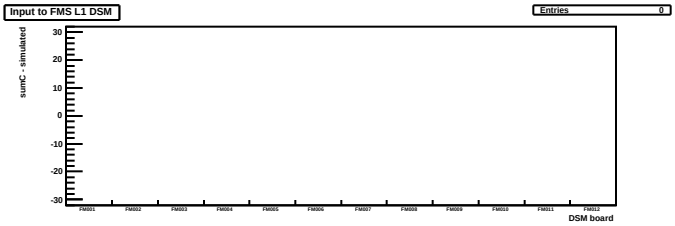

Input to FMS L0 DSM
Entries 4829232
HT ID
QT board

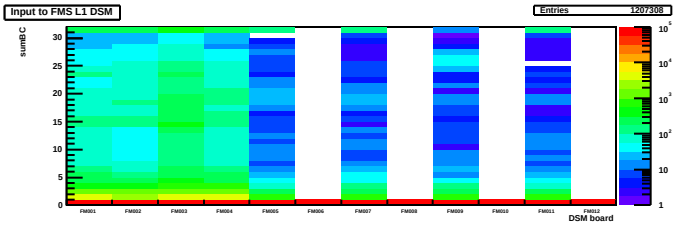

Input to FMS L0 DSM
Entries 104904
HT ID - simulated
QT board

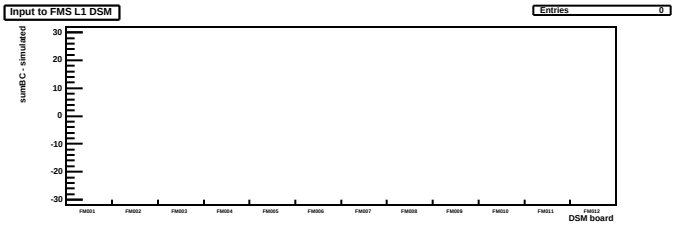

Input to FMS L1 DSM
Entries 1207308
sumD
DSM board

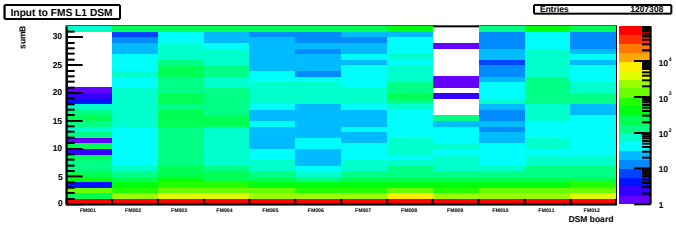

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

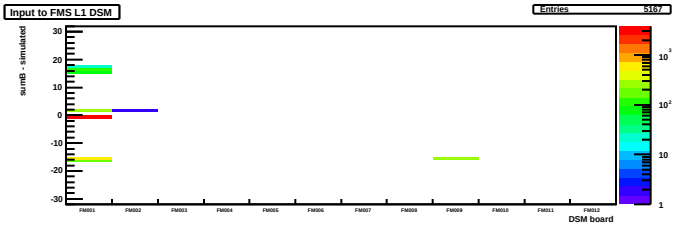

Input to FMS L1 DSM
Entries 1207308
sumC
DSM board

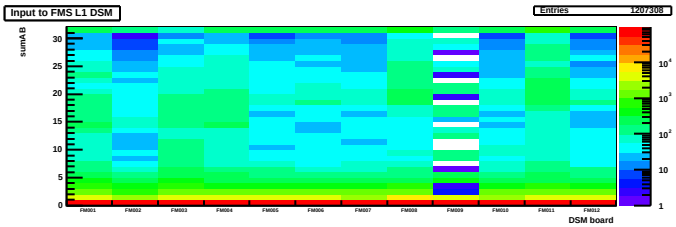

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

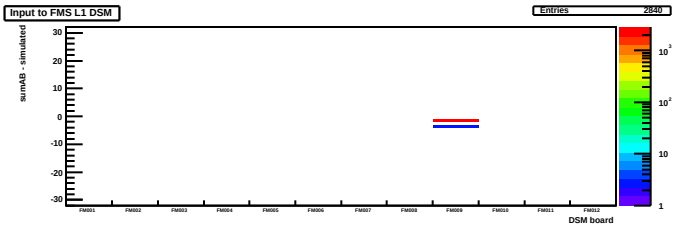

Input to FMS L1 DSM
Entries 1207308
sumBC
DSM board

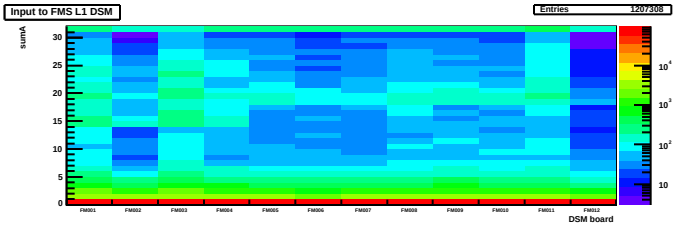

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

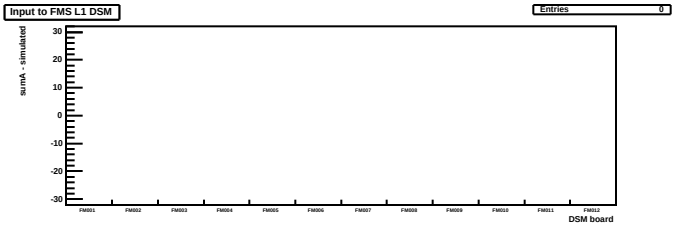

Input to FMS L1 DSM
Entries 1207308
sumB
DSM board

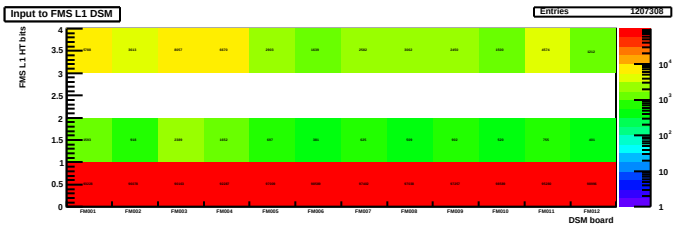

Input to FMS L1 DSM
Entries 5167
sumB - simulated
DSM board

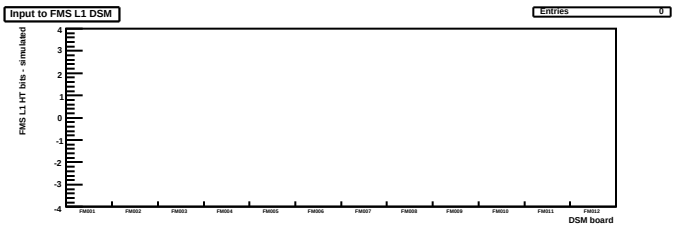

Input to FMS L1 DSM
Entries 1207308
sumAB
DSM board

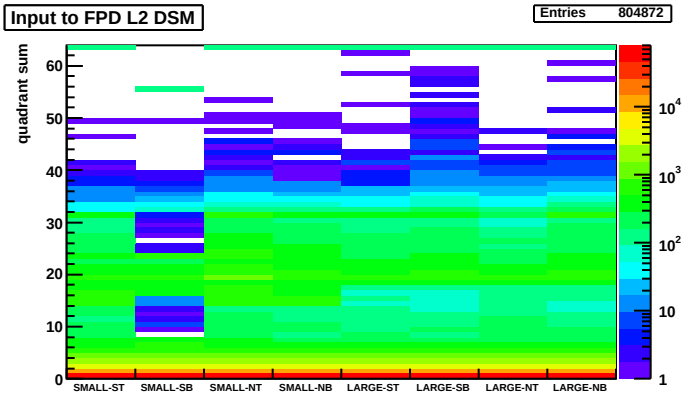

Input to FMS L1 DSM
Entries 2840
sumAB - simulated
DSM board

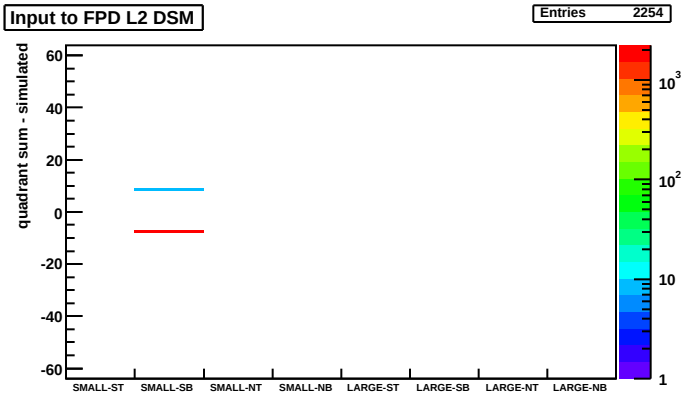

Input to FMS L1 DSM
Entries 1207308
sumA
DSM board

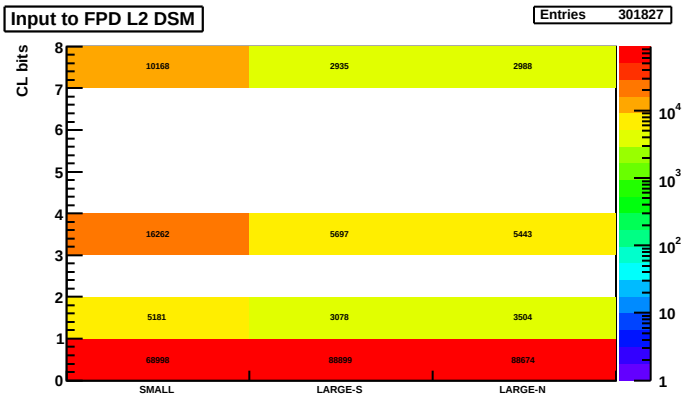

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

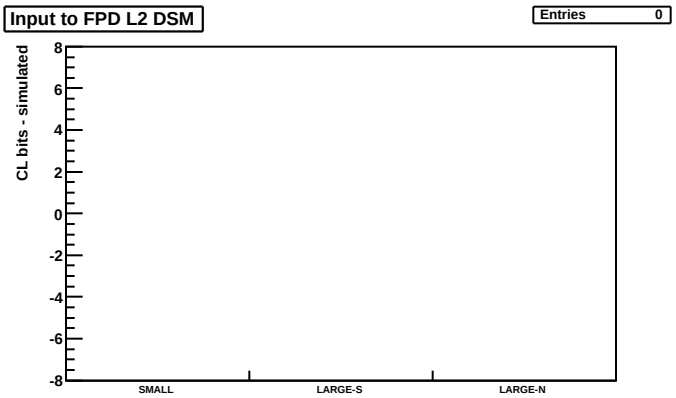

Input to FMS L1 DSM
Entries 1207308
FMS L1 HT bits
DSM board

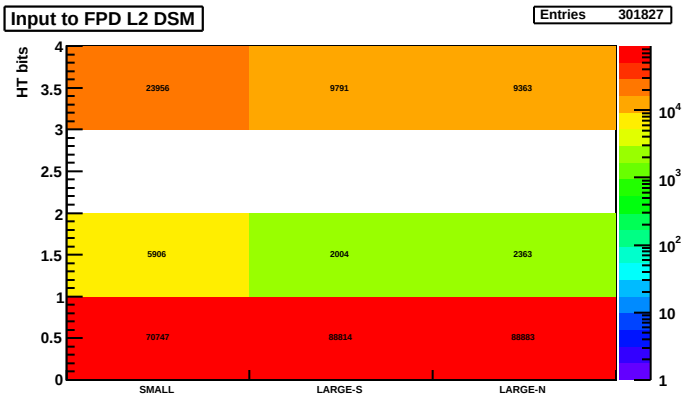

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

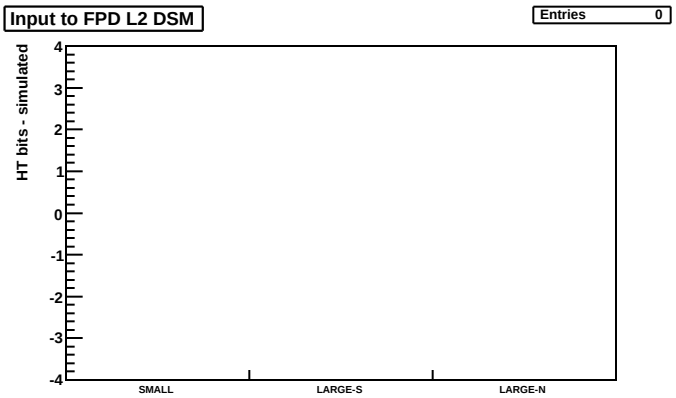

Input to FPD L2 DSM
Entries 804872
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

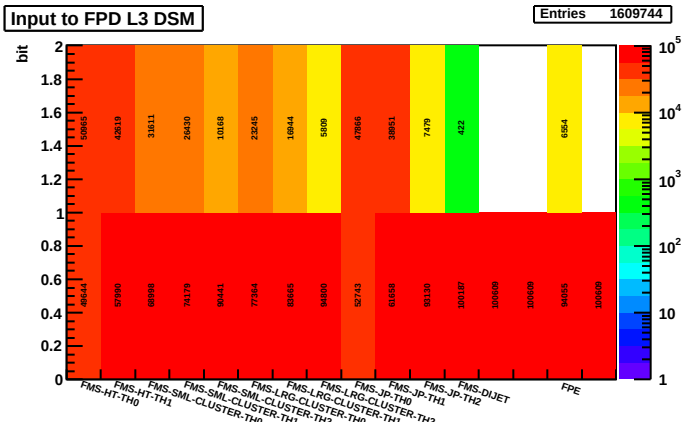

Input to FPD L2 DSM
Entries 2254
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

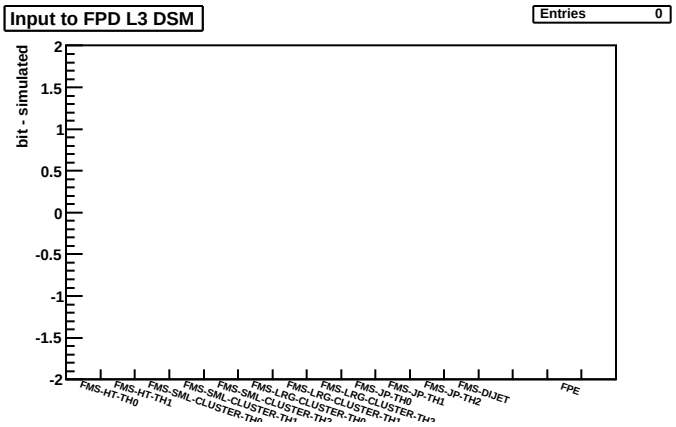

Input to FPD L2 DSM
Entries 301827
CL bits
10168
2935
2988
16262
5697
5443
5181
3078
3504
68998
88899
88674
SMALL
LARGE-S
LARGE-N

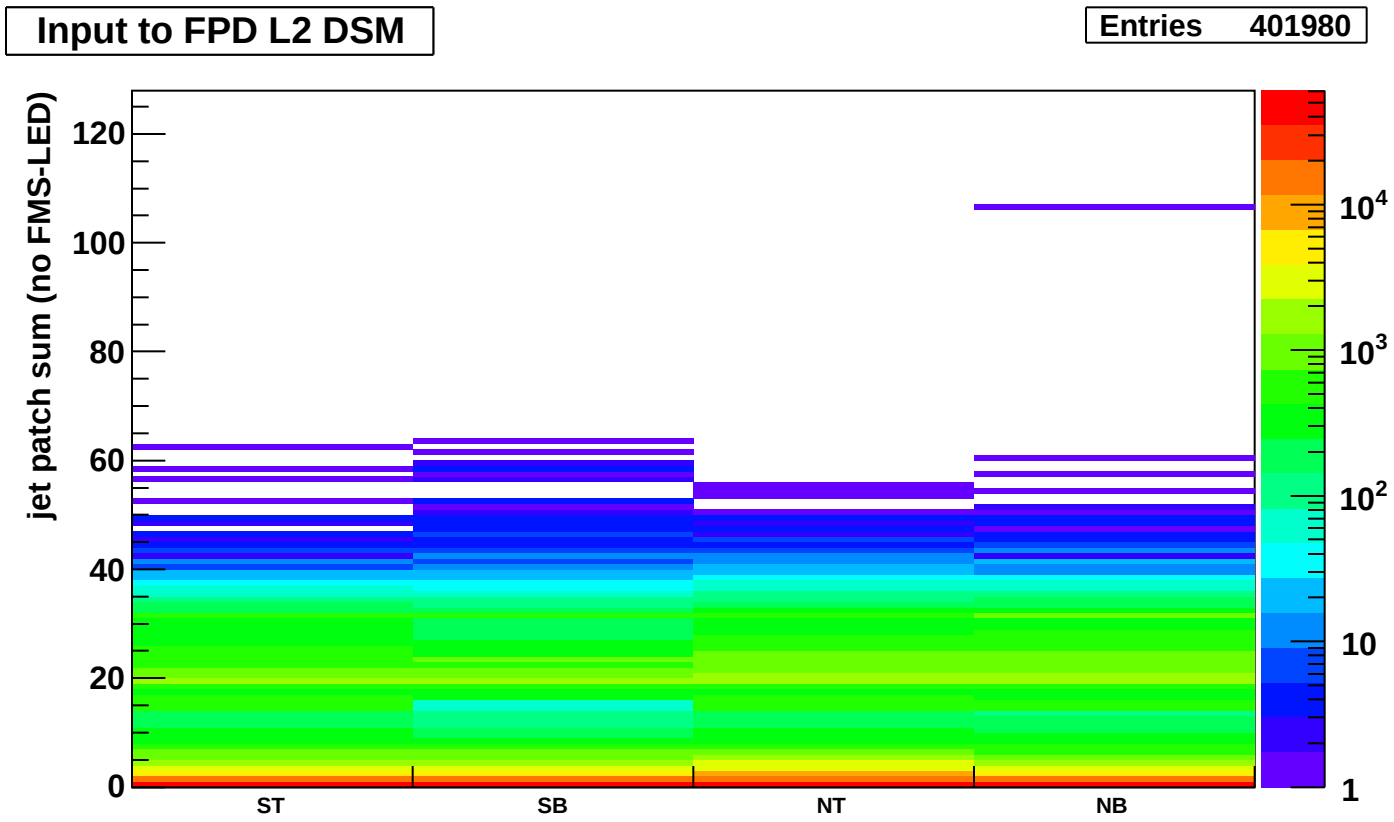

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

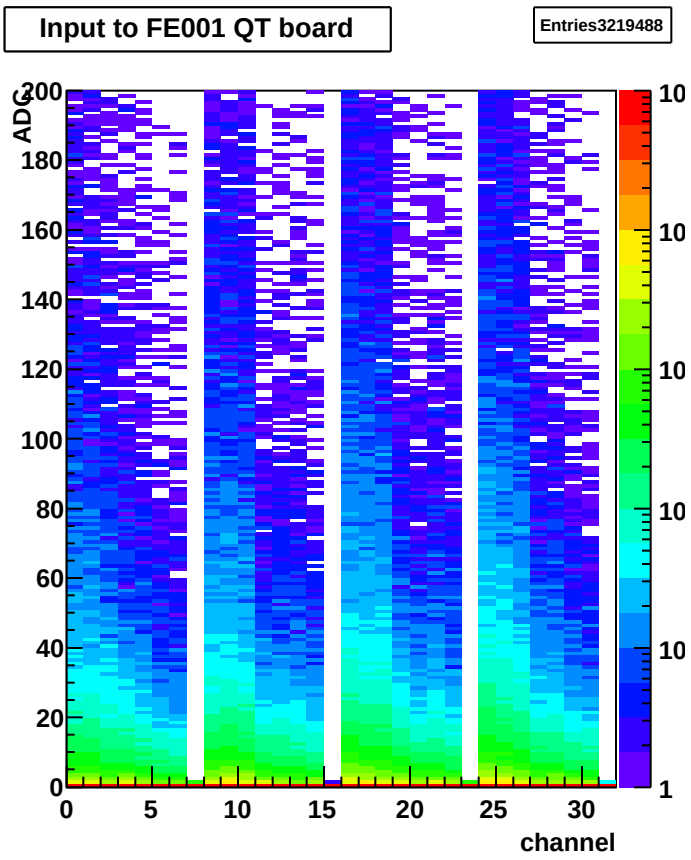

Input to FPD L2 DSM
Entries 301827
HT bits
23956
9791
9363
5906
2004
2363
70747
88814
88883
SMALL
LARGE-S
LARGE-N

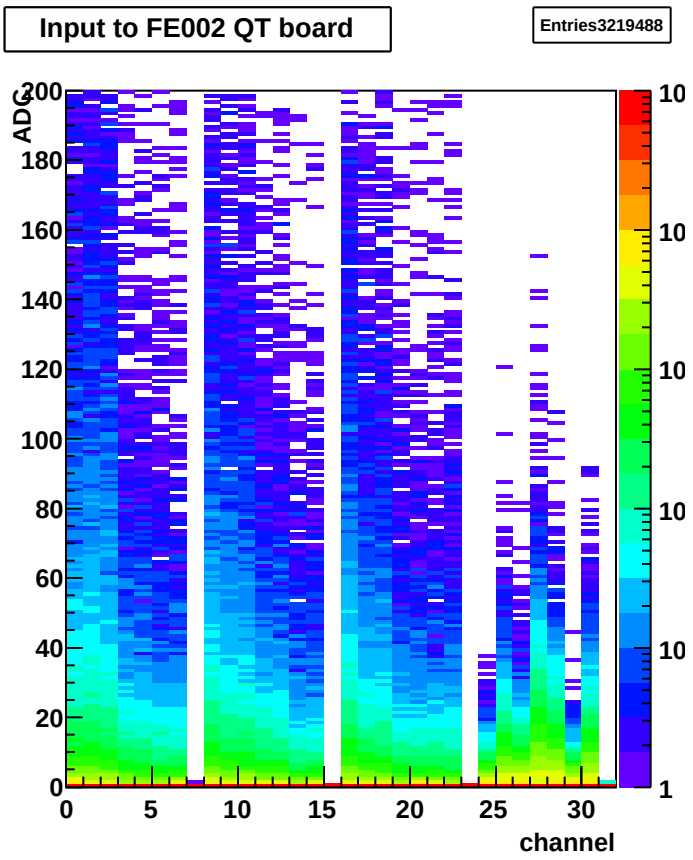

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

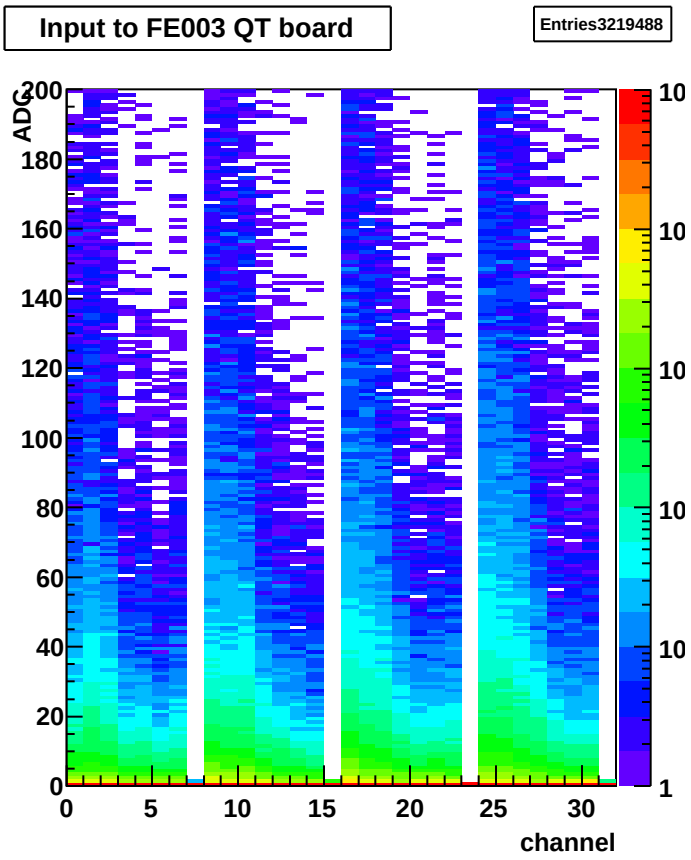

Input to FPD L3 DSM
Entries 1609744
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

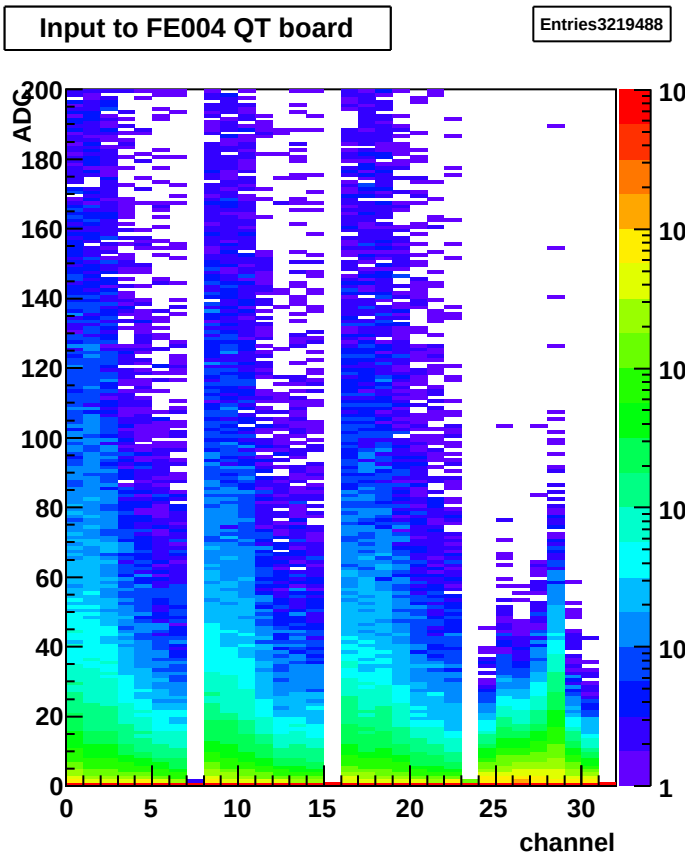

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

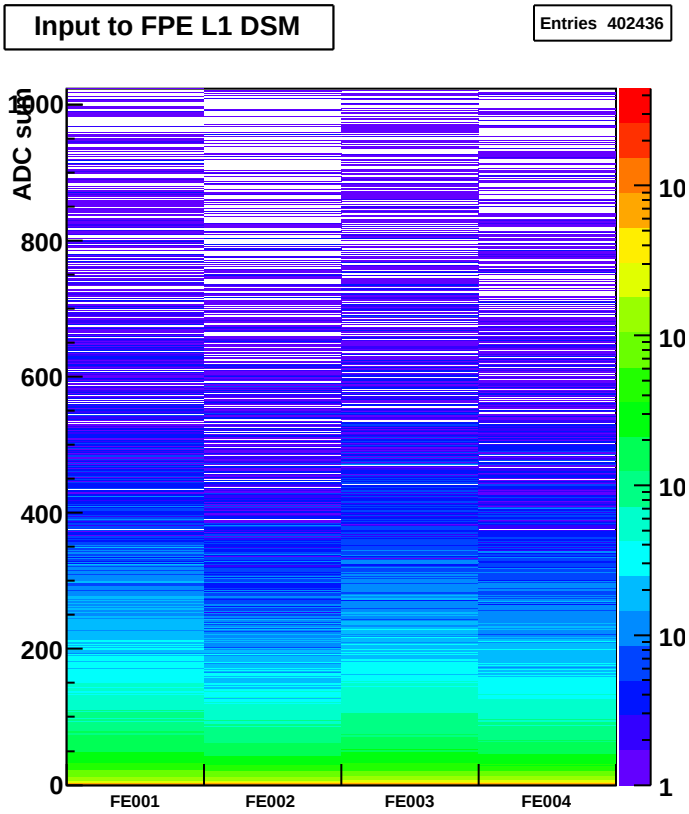

Input to FPD L2 DSM
Entries 401980
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

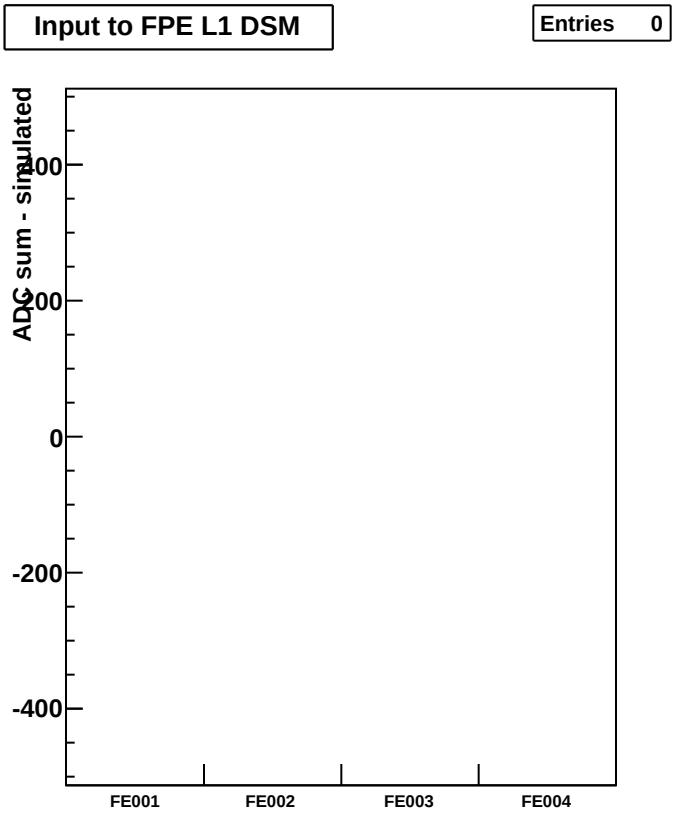

Input to FE001 QT board
Entries3219488
ADC
channel

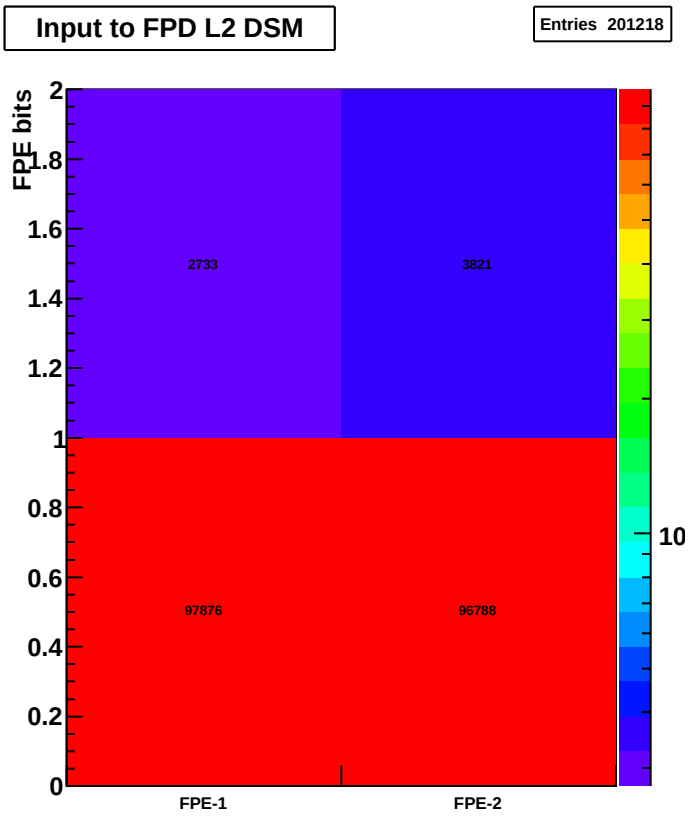

Input to FE002 QT board
Entries3219488
ADC
channel

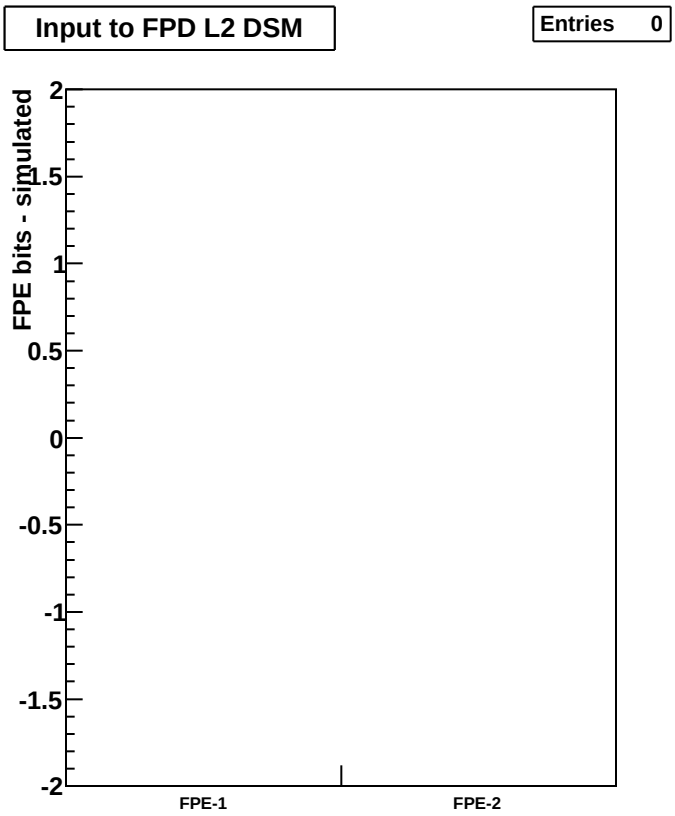

Input to FE003 QT board
Entries3219488
ADC
channel


Input to FE004 QT board
Entries3219488
ADC
channel


Input to FPE L1 DSM
Entries 402436
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 201218
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
2733
3821
97876
96788
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2