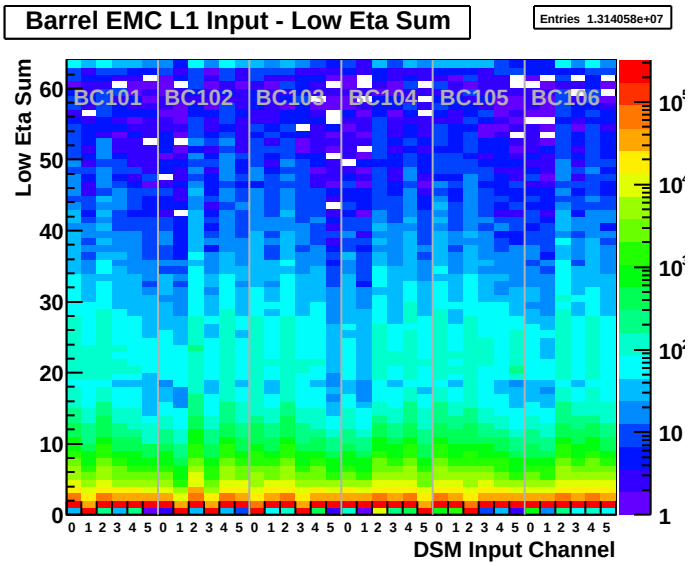
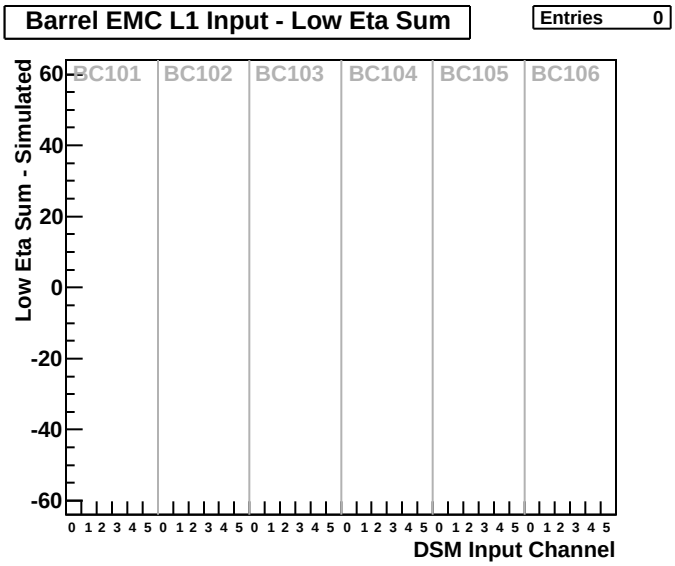
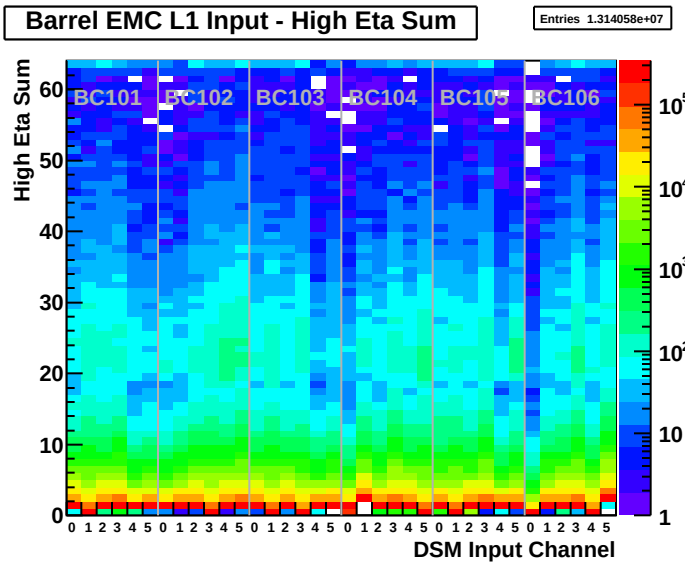
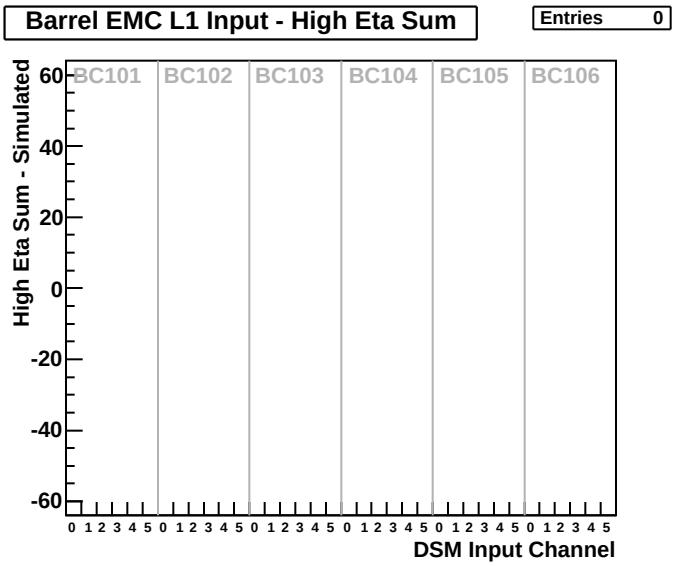
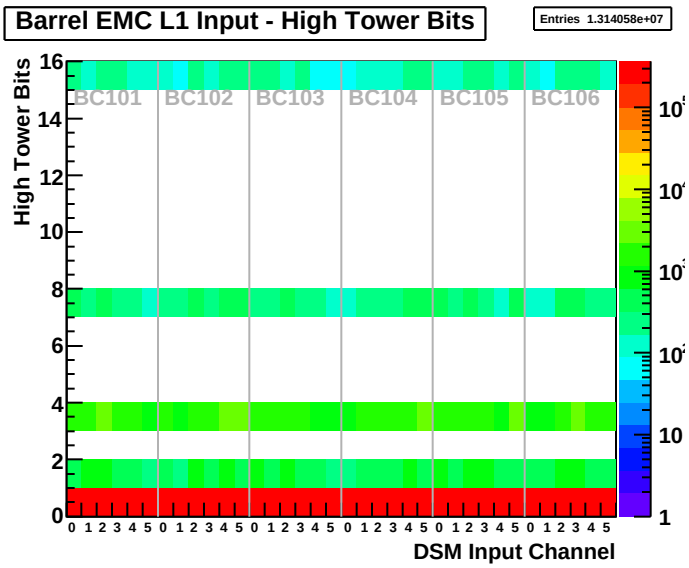
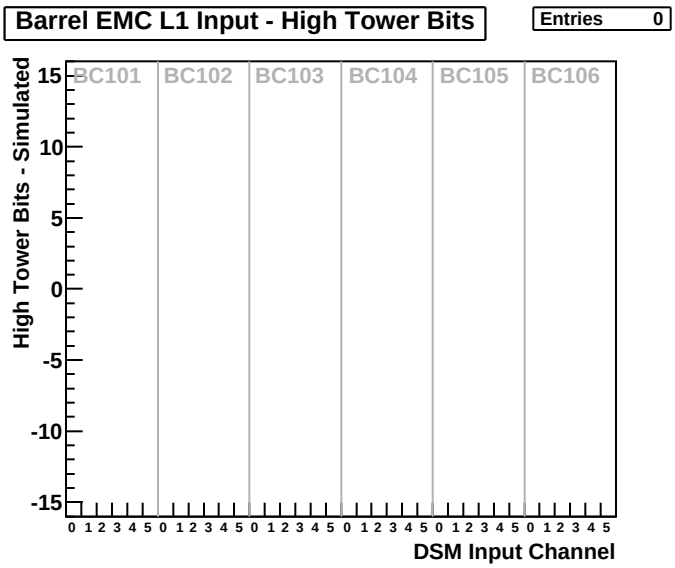

Barrel EMC L1 Input - Low Eta Sum
Entries 1.314058e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

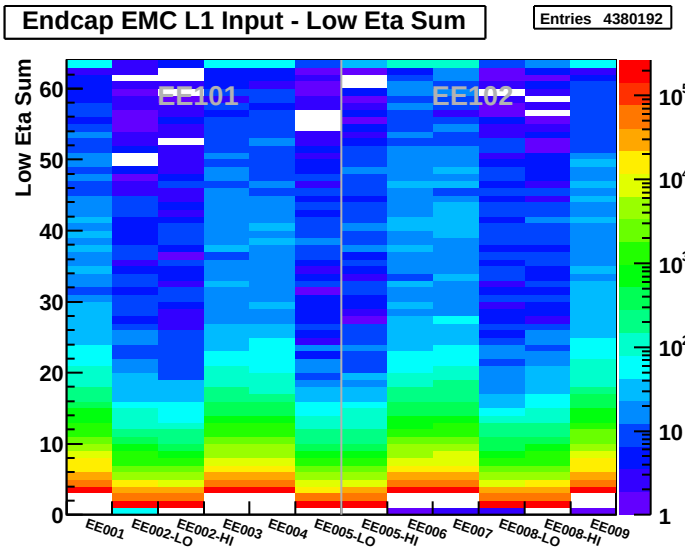

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

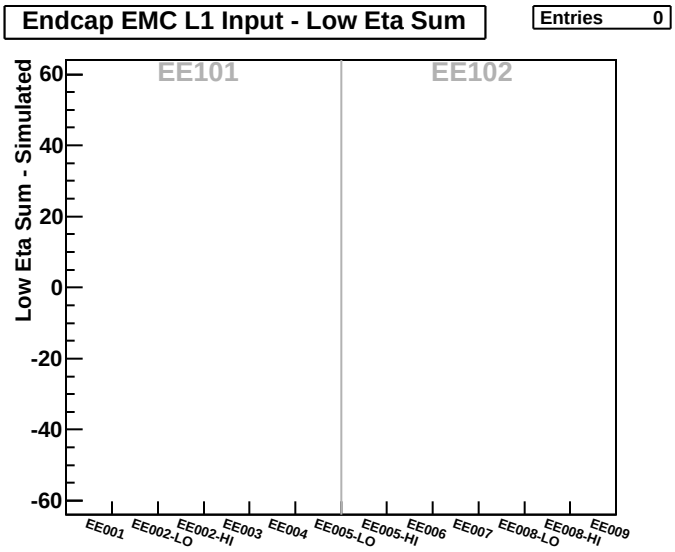

Barrel EMC L1 Input - High Eta Sum
Entries 1.314058e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

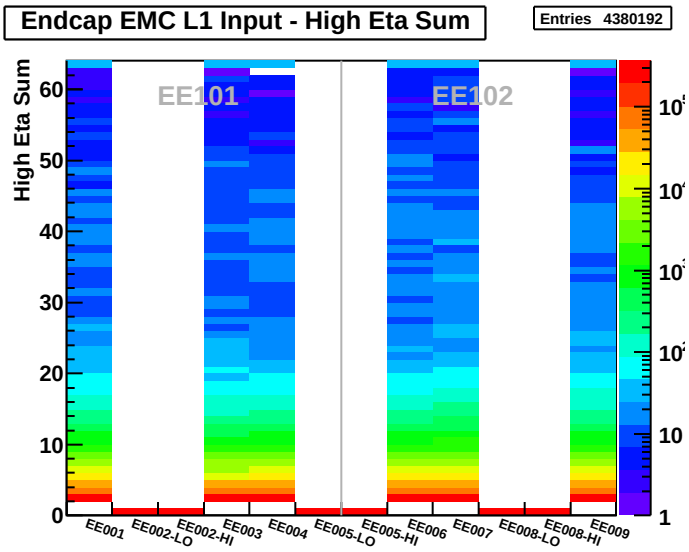

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

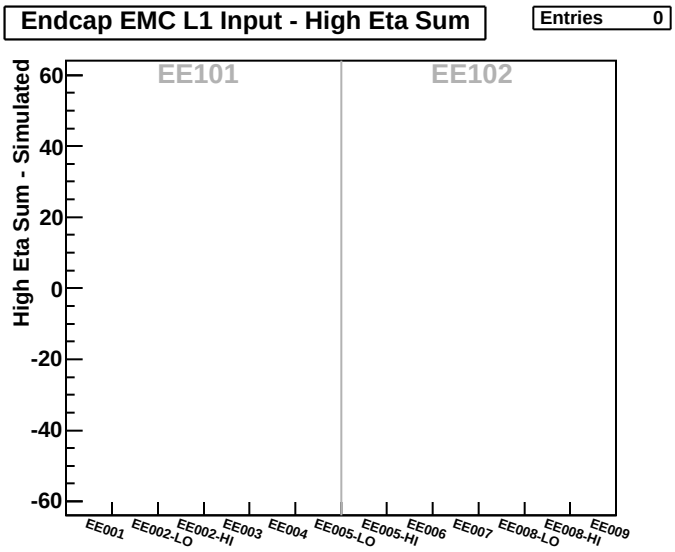

Barrel EMC L1 Input - High Tower Bits
Entries 1.314058e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

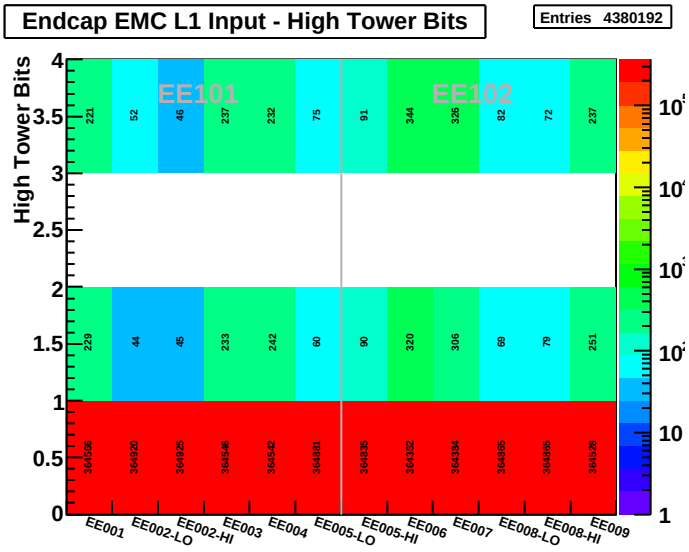

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

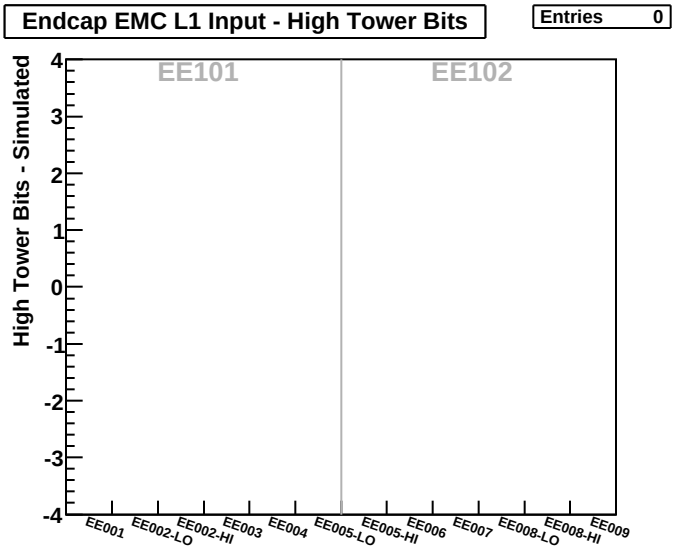

Endcap EMC L1 Input - Low Eta Sum
Entries 4380192
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

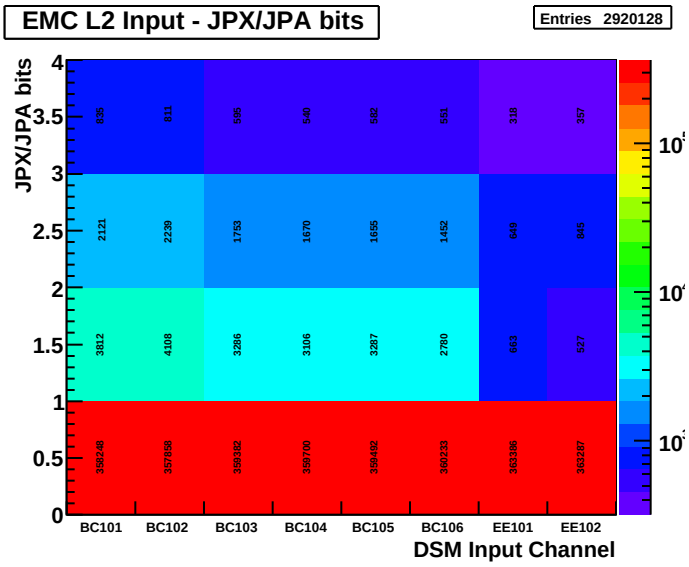

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

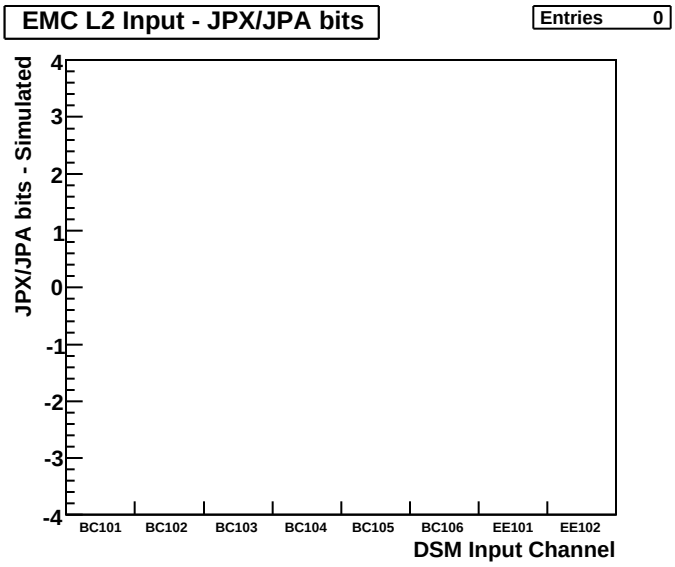

Endcap EMC L1 Input - High Eta Sum
Entries 4380192
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

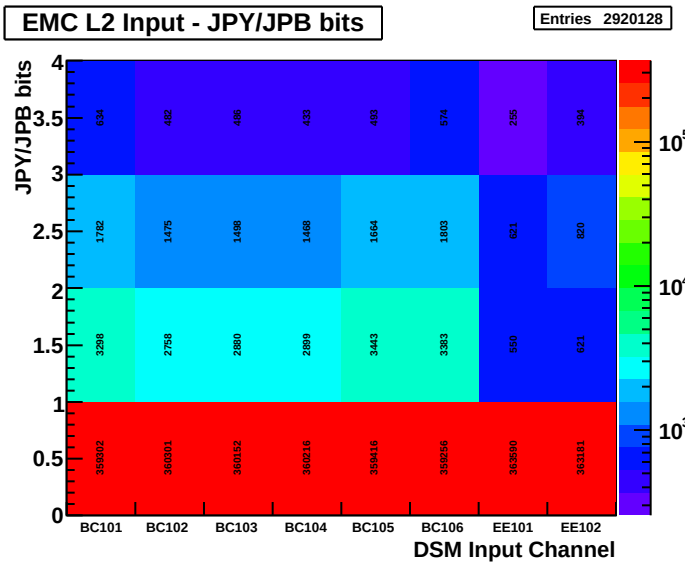

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

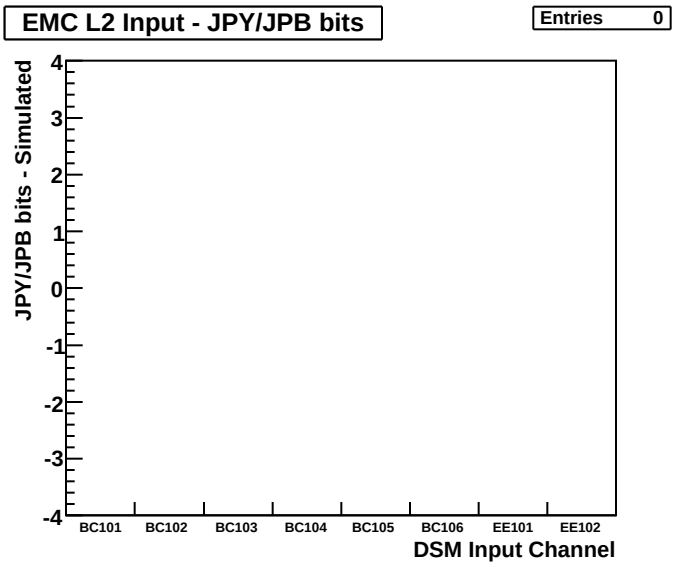

Endcap EMC L1 Input - High Tower Bits
Entries 4380192
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

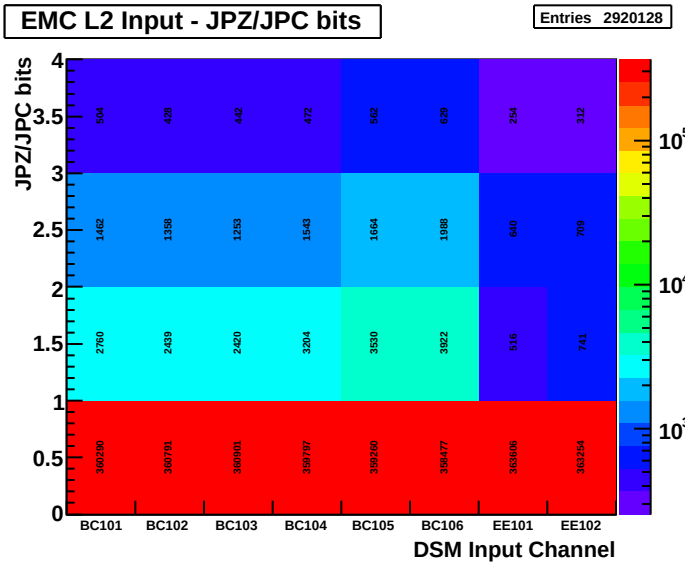

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

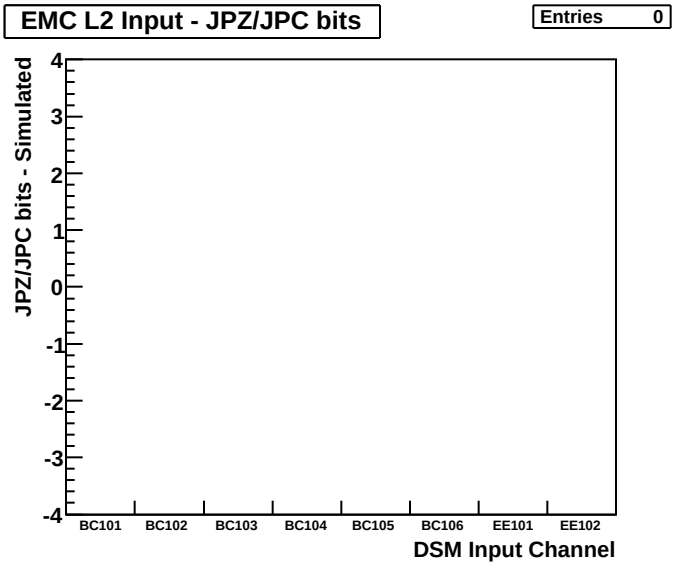

EMC L2 Input - JPX/JPA bits
Entries 2920128
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
835 811 595 540 582 551 318 357
2121 2239 1753 1670 1655 1452 649 845
3812 4108 3286 3106 3287 2780 663 527
358248 357858 358382 358700 359492 360233 363396 363287

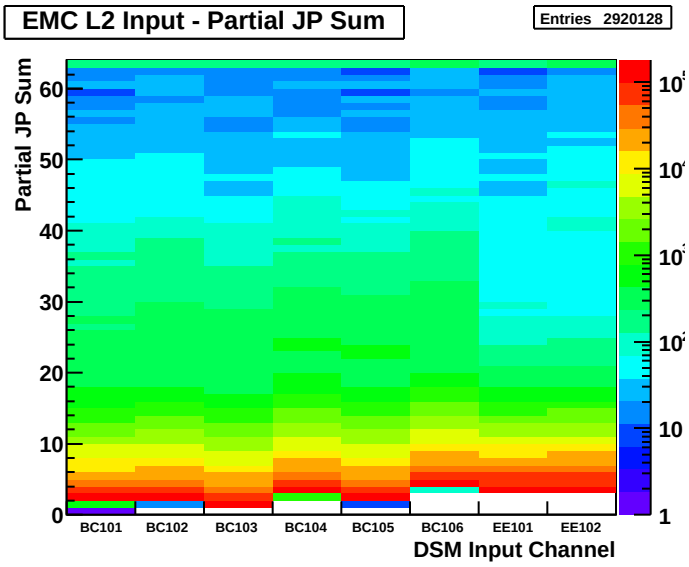

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

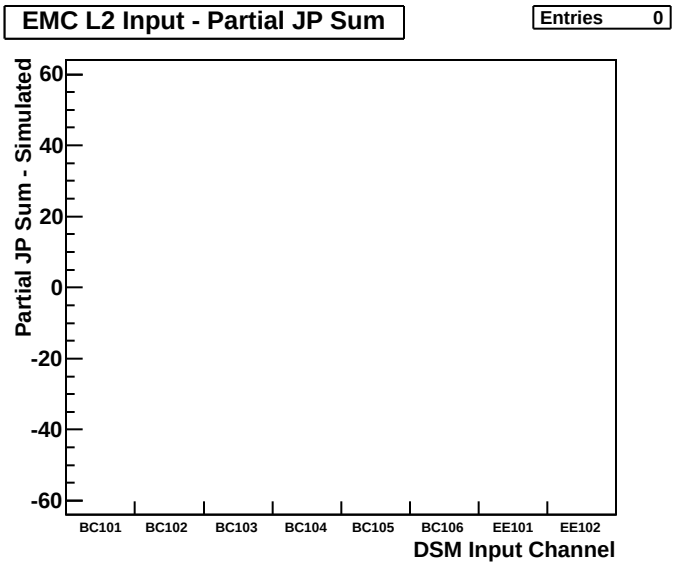

EMC L2 Input - JPY/JPB bits
Entries 2920128
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
634 482 486 433 493 574 255 394
1782 1475 1498 1468 1664 1803 621 820
3298 2758 2880 2899 3443 3383 550 621
359302 360301 360152 360216 359416 359256 363590 363181

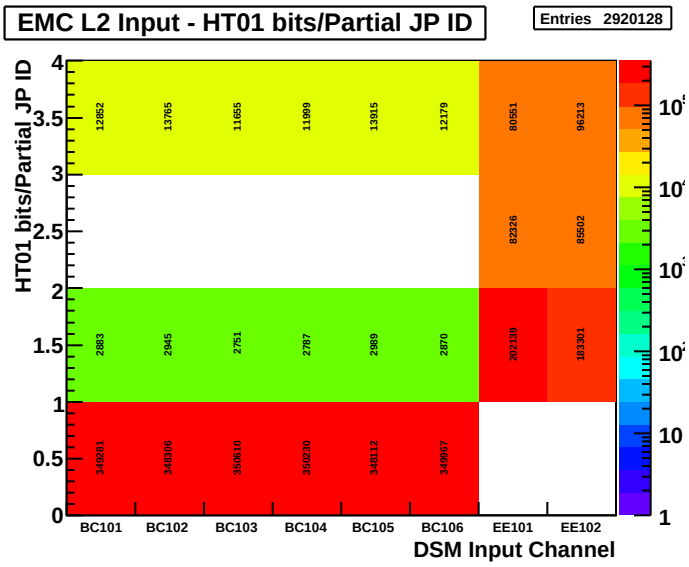

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

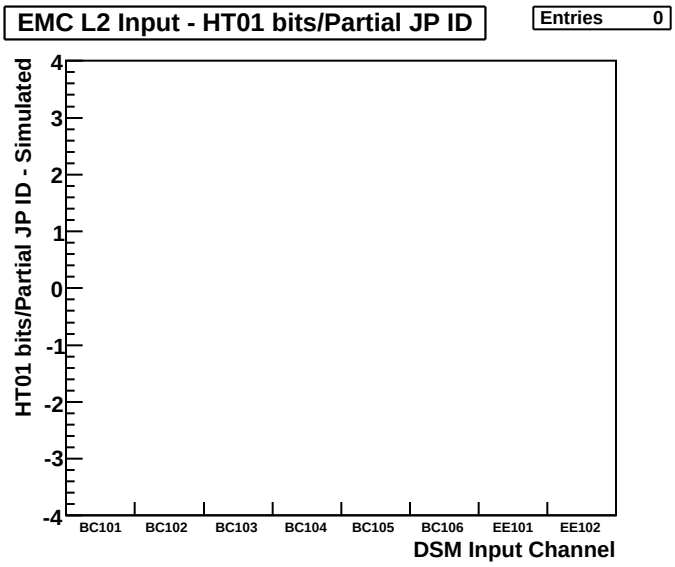

EMC L2 Input - JPZ/JPC bits
Entries 2920128
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
504 428 442 472 562 629 254 312
1462 1358 1253 1543 1664 1988 640 709
2760 2439 2420 3204 3530 3922 516 741
360290 360791 360901 359797 359260 359477 363606 363254

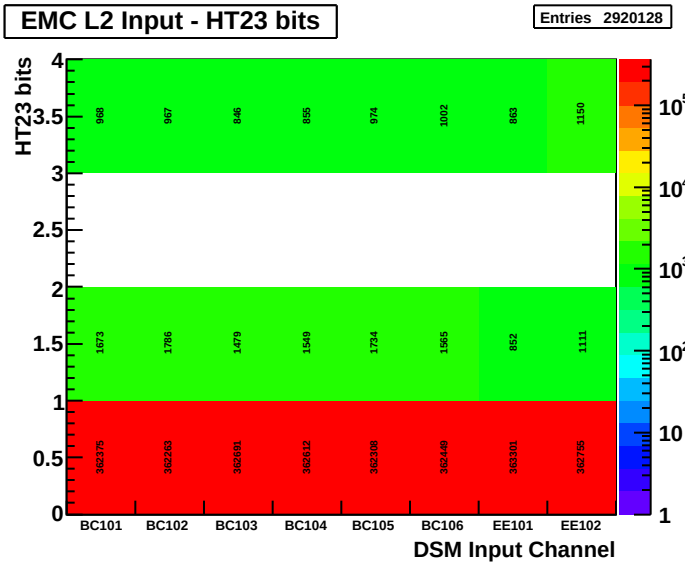

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

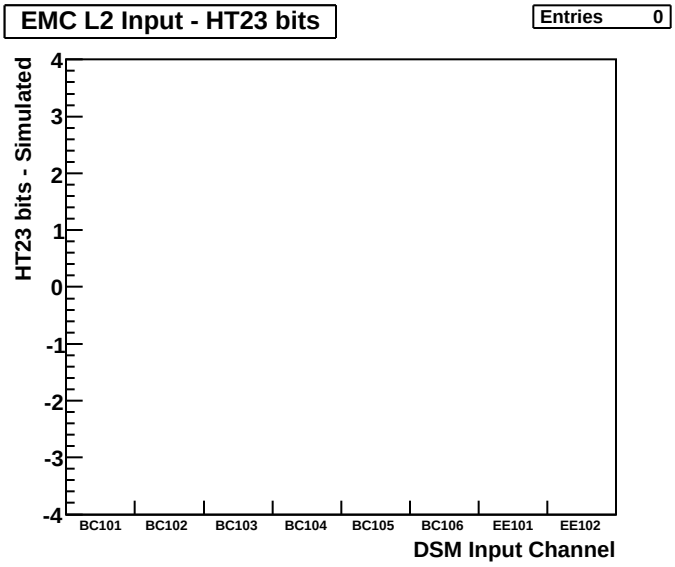

EMC L2 Input - Partial JP Sum
Entries 2920128
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

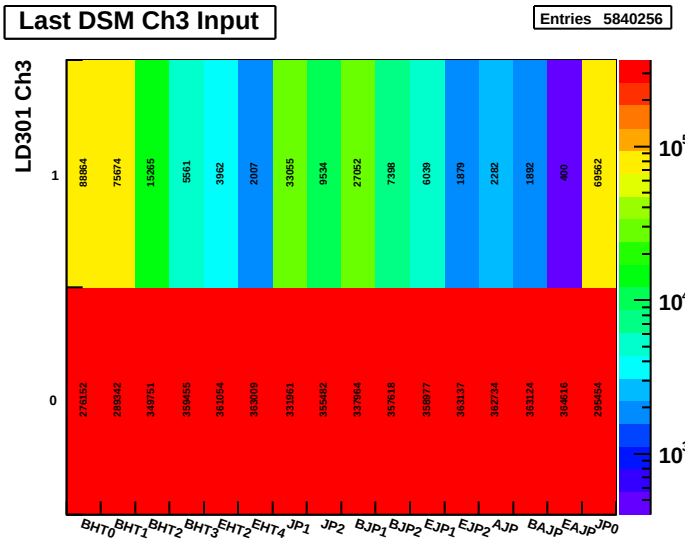

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

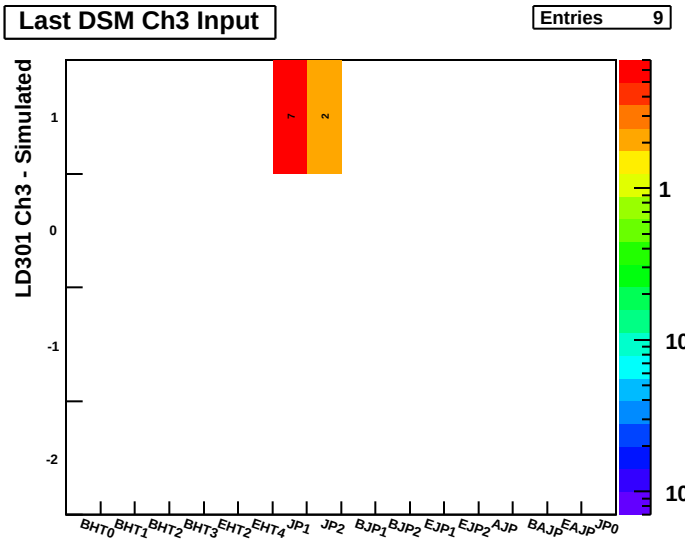

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2920128
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

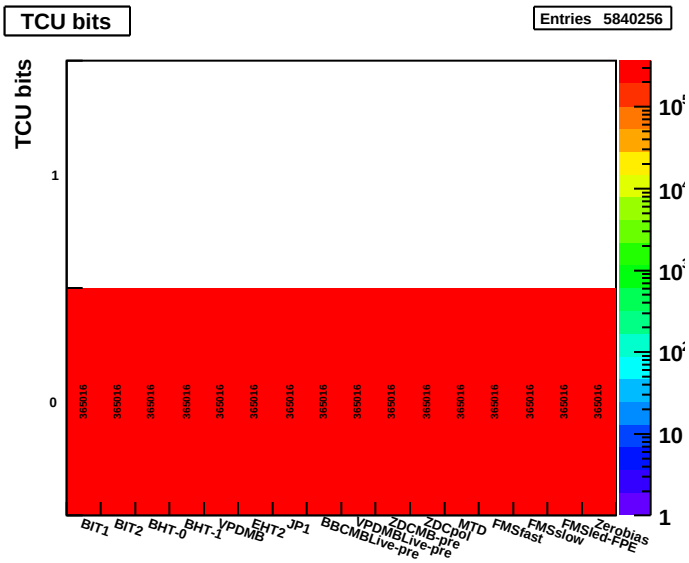

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

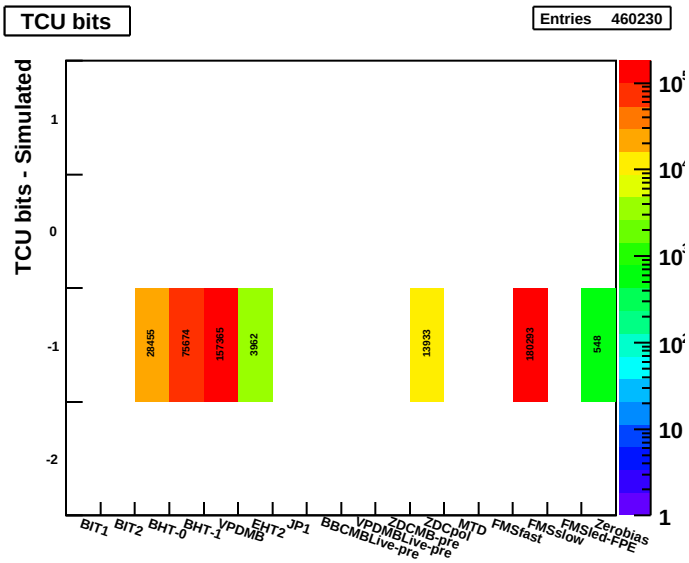

EMC L2 Input - HT23 bits
Entries 2920128
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

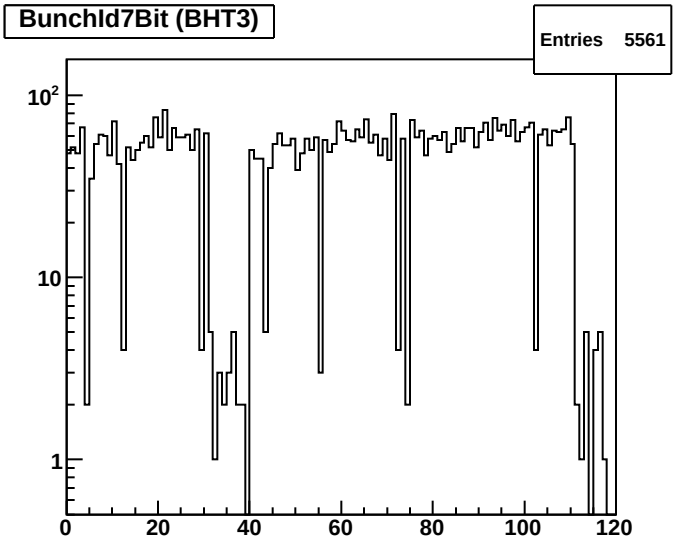

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

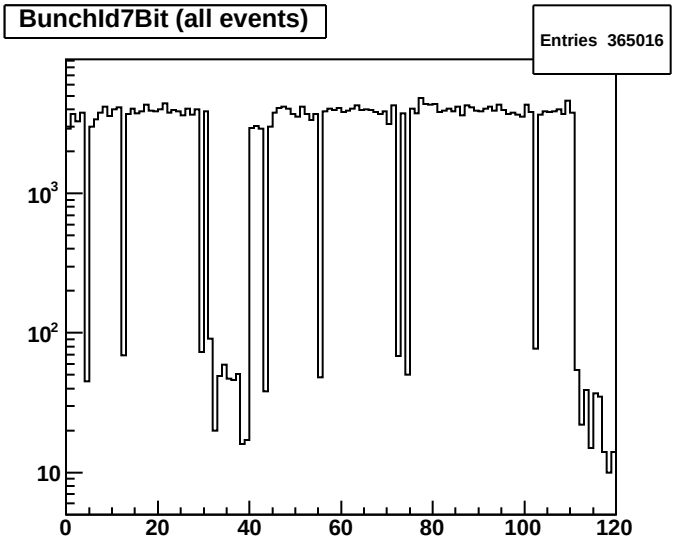

Last DSM Ch3 Input
Entries 5840256
LD301 Ch3
88864 75674 15265 5561 3962 2007 33055 9534 27052 7398 6039 1879 2282 1882 400 69562
276152 289342 349751 359455 361054 363009 331961 355482 337964 357618 358977 363137 362734 363124 364616 295454
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

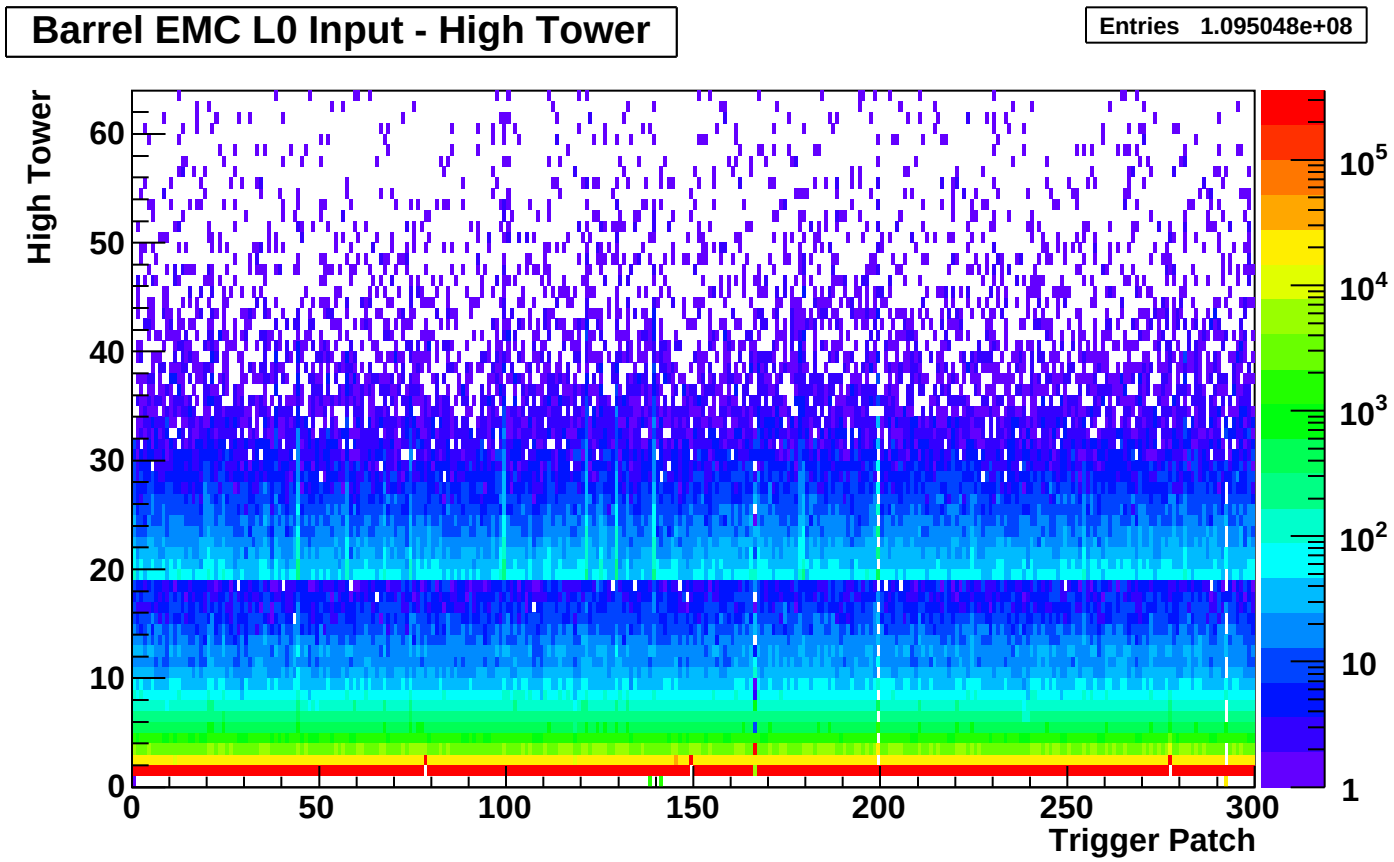

Last DSM Ch3 Input
Entries 9
LD301 Ch3 - Simulated
7 2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

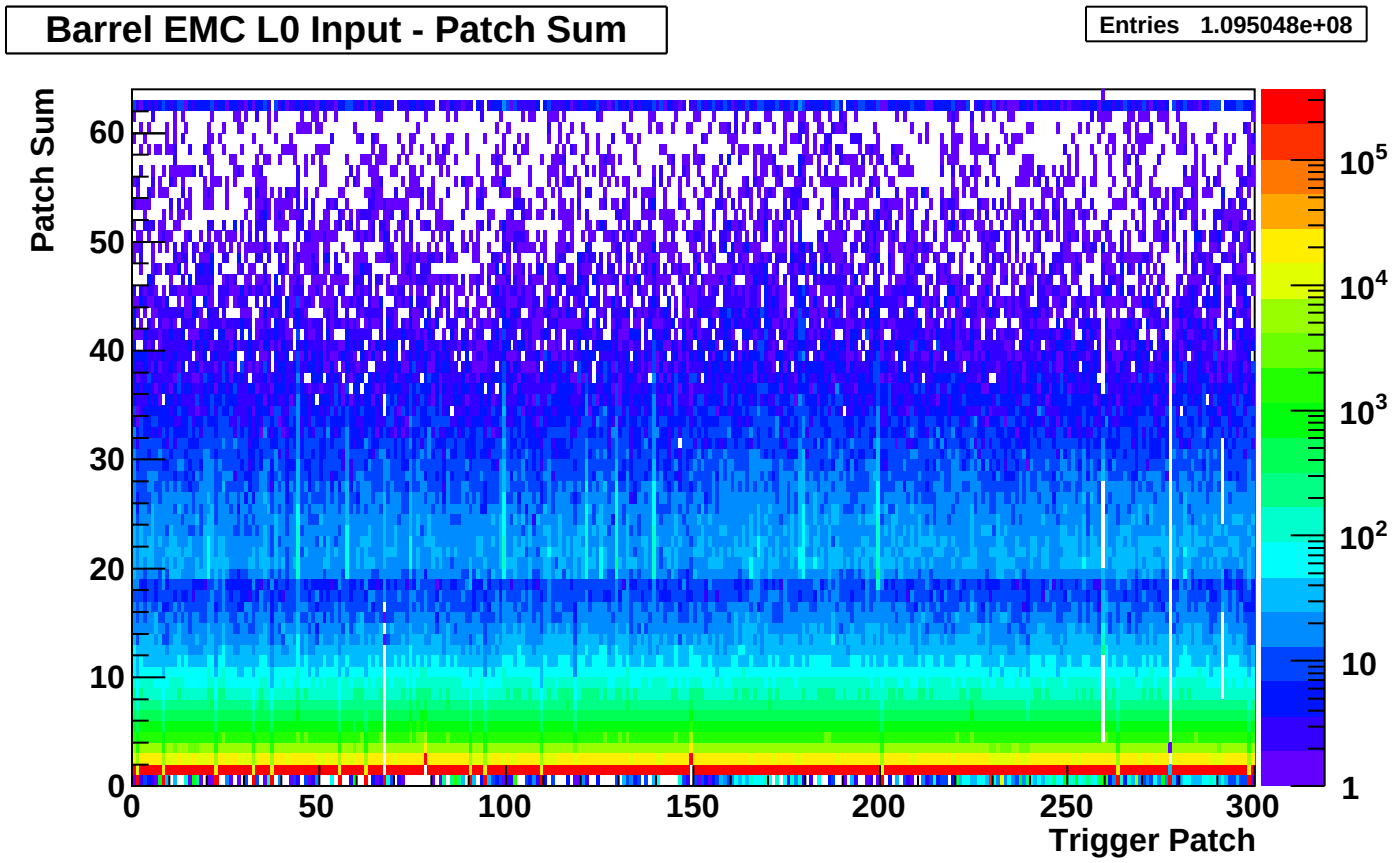

TCU bits
Entries 5840256
TCU bits
365016 365016 365016 365016 365016 365016 365016 365016 365016 365016 365016 365016 365016 365016 365016 365016
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

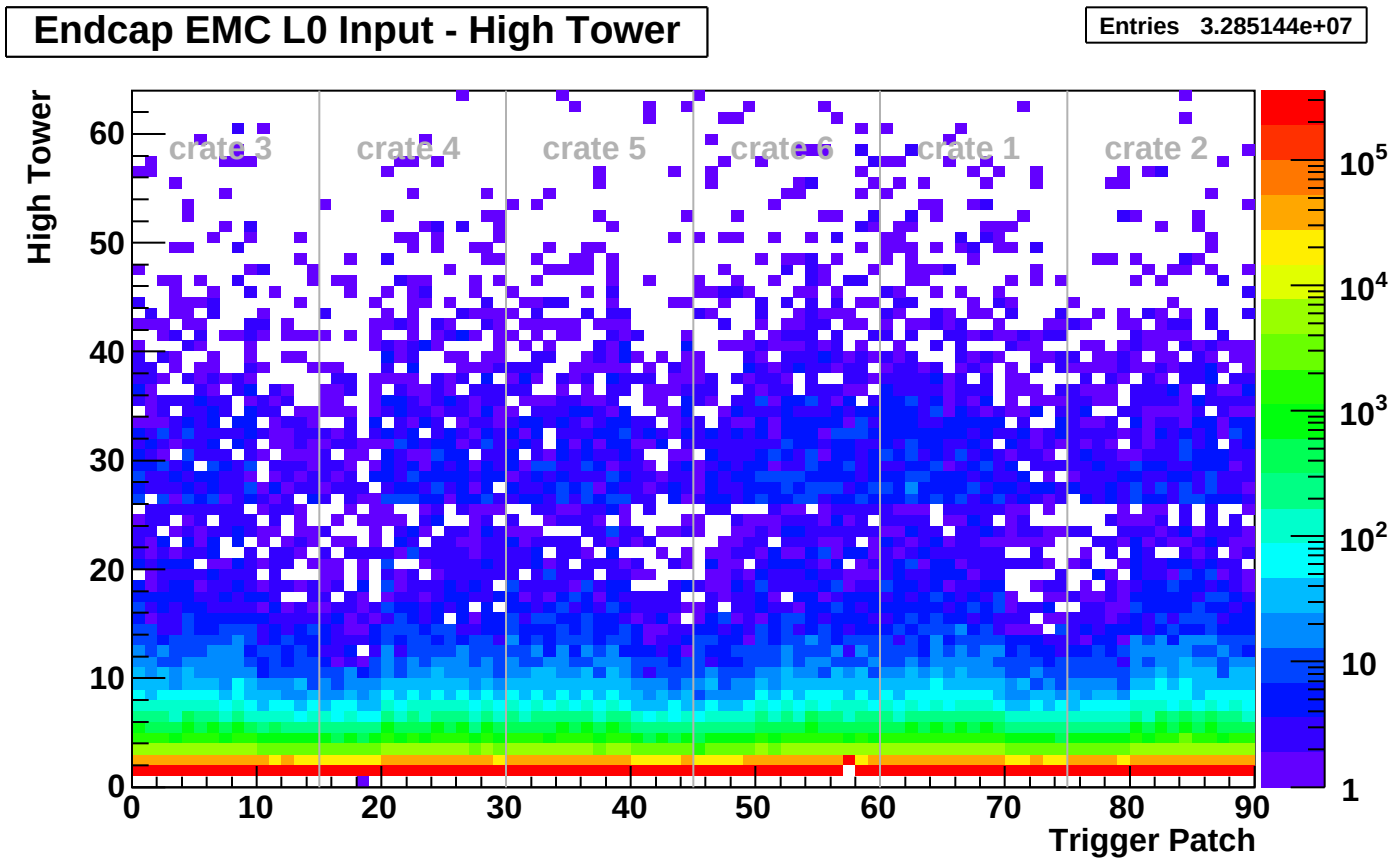

TCU bits
Entries 460230
TCU bits - Simulated
28455 75674 157365 3962 13933 180293 548
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

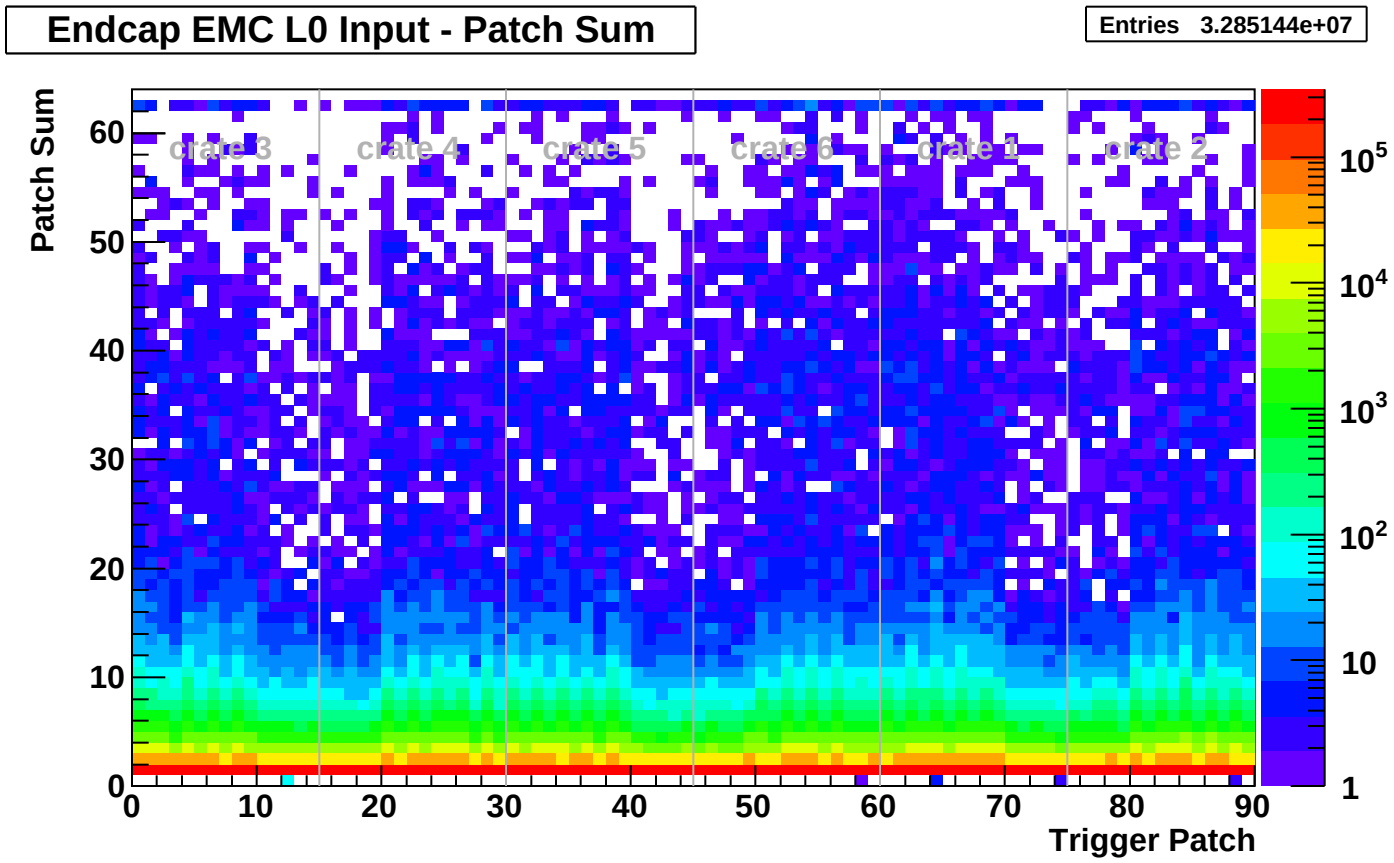

BunchId7Bit (BHT3)
Entries 5561

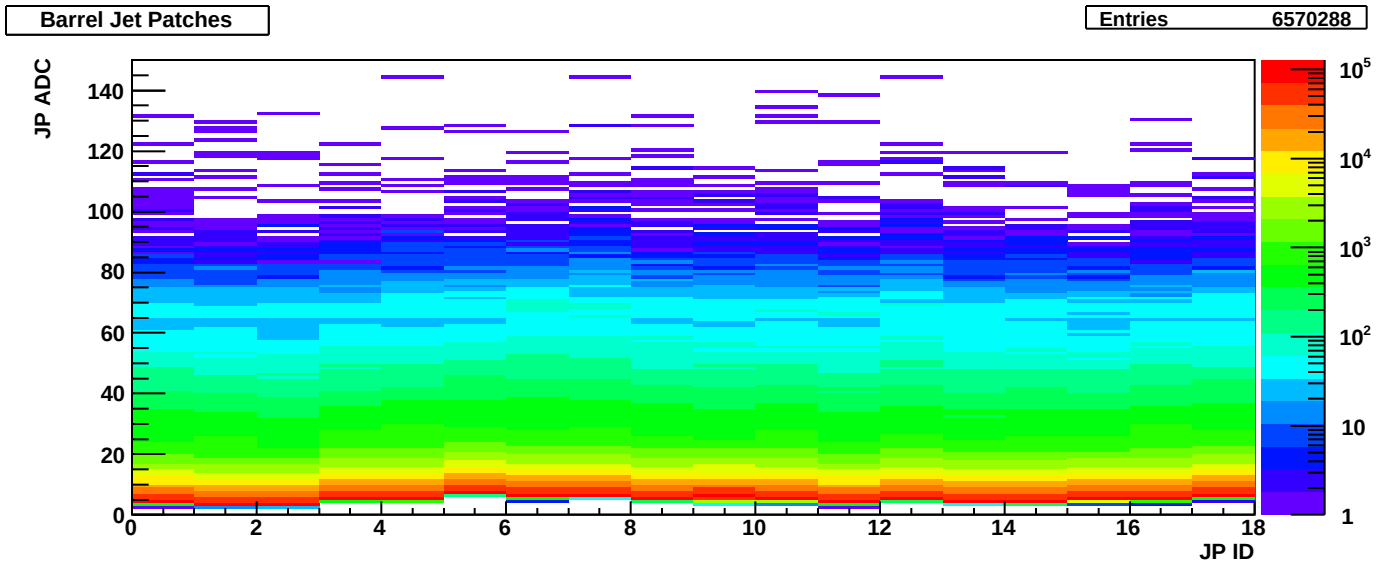

BunchId7Bit (all events)
Entries 365016

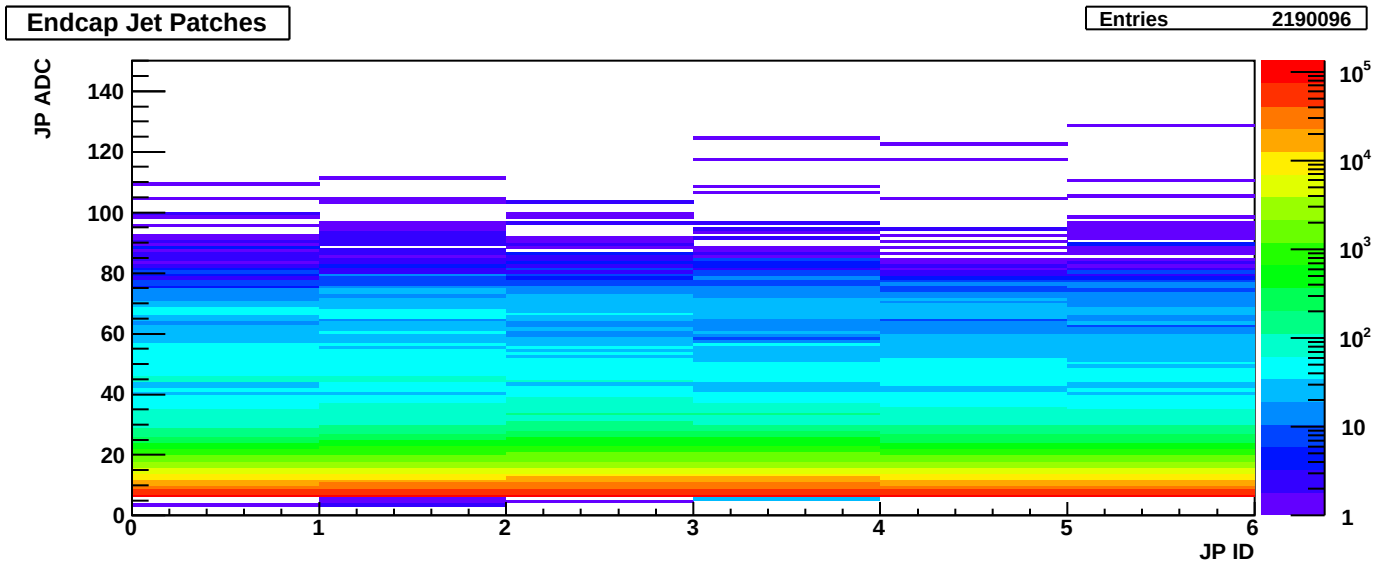

Barrel EMC L0 Input - High Tower
Entries 1.095048e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

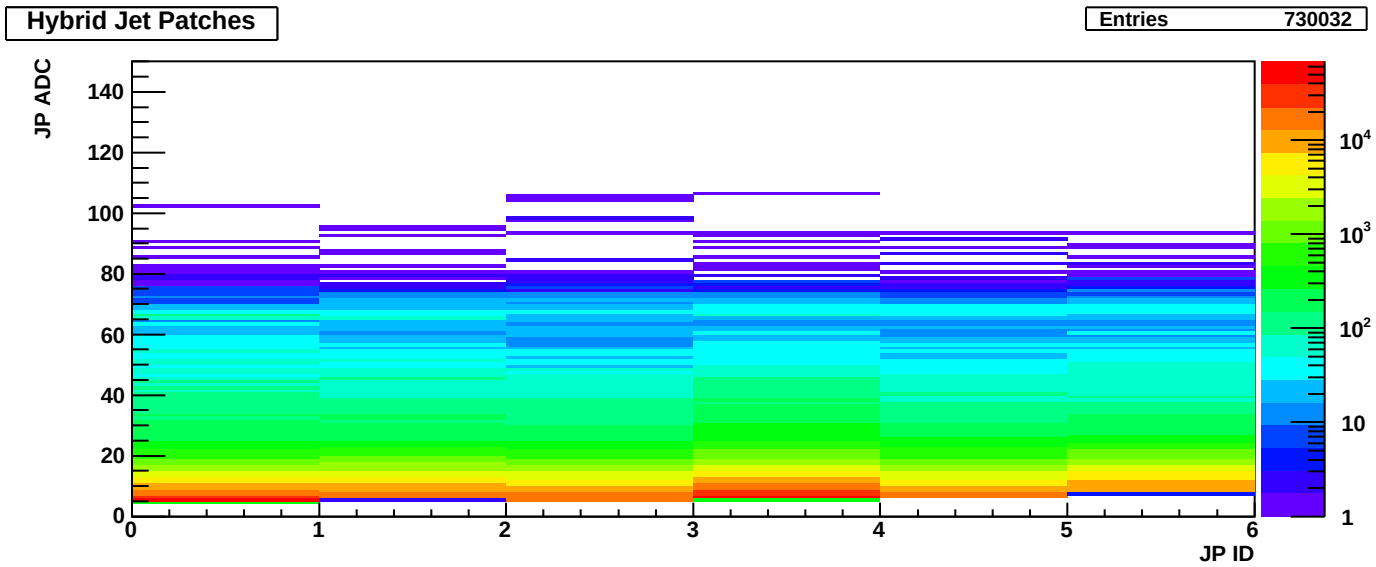

Barrel EMC L0 Input - Patch Sum
Entries 1.095048e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

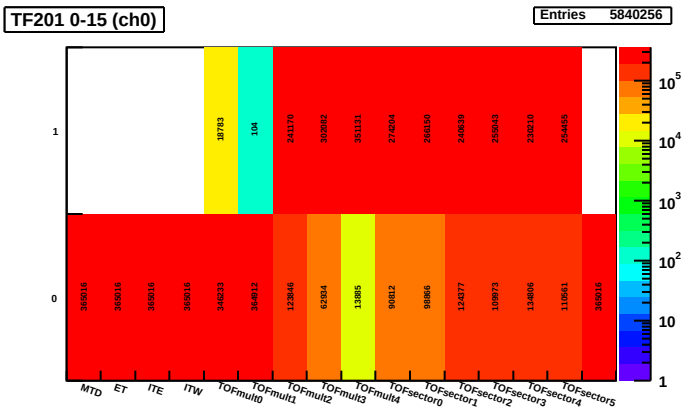

Endcap EMC L0 Input - High Tower
Entries 3.285144e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

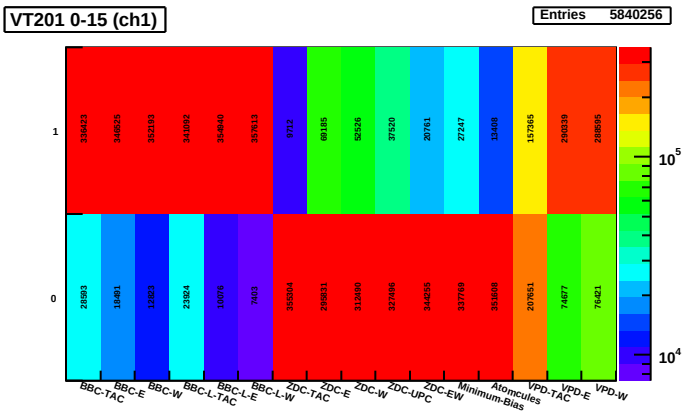

Endcap EMC L0 Input - Patch Sum
Entries 3.285144e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

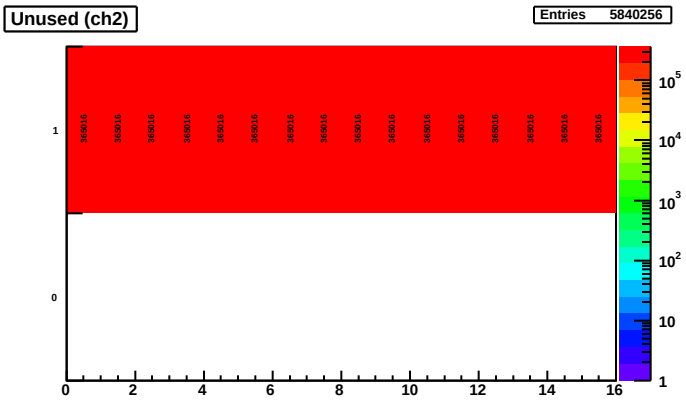

Barrel Jet Patches
Entries 6570288
JP ADC
JP ID

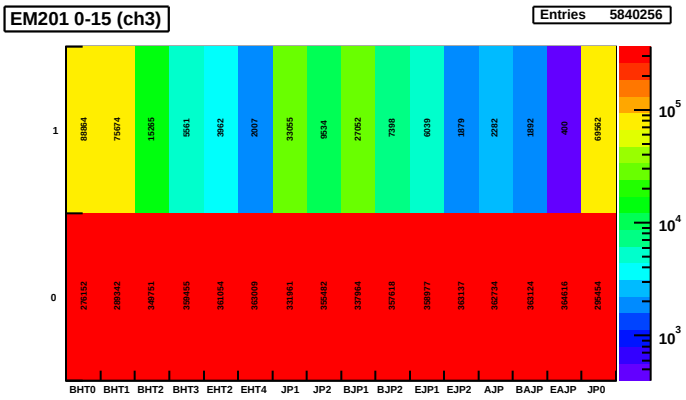

Endcap Jet Patches
Entries 2190096
JP ADC
JP ID

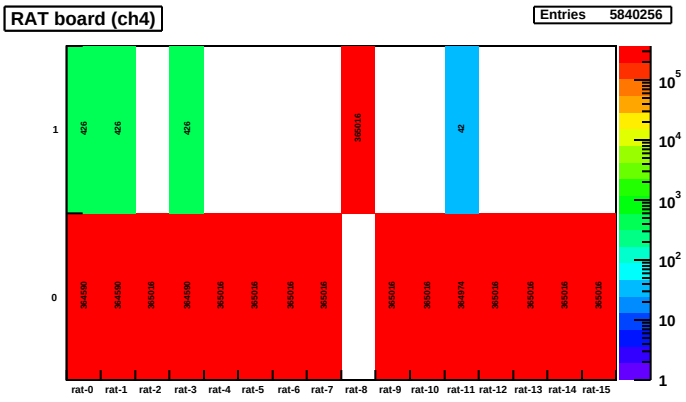

Hybrid Jet Patches
Entries 730032
JP ADC
JP ID

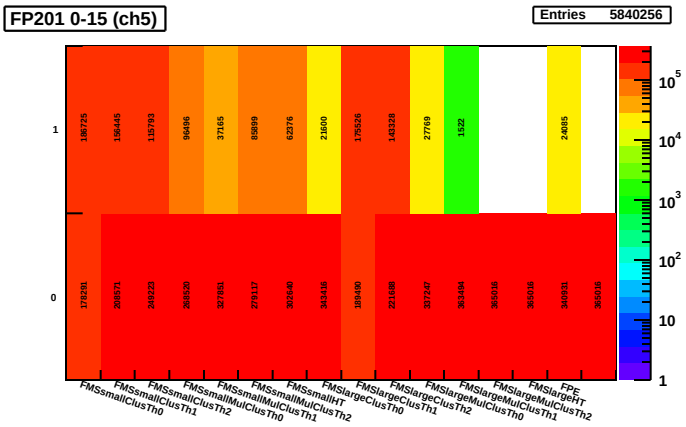

TF201 0-15 (ch0)
Entries 5840256
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

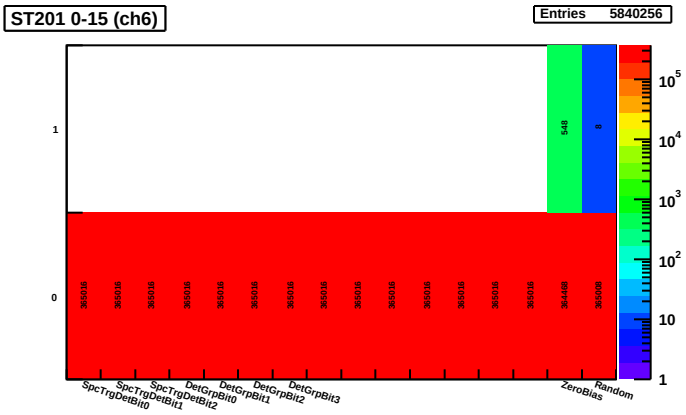

VT201 0-15 (ch1)
Entries 5840256
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

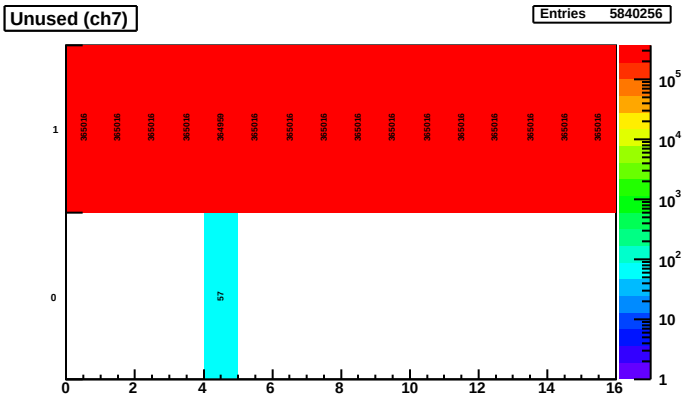

Unused (ch2)
Entries 5840256

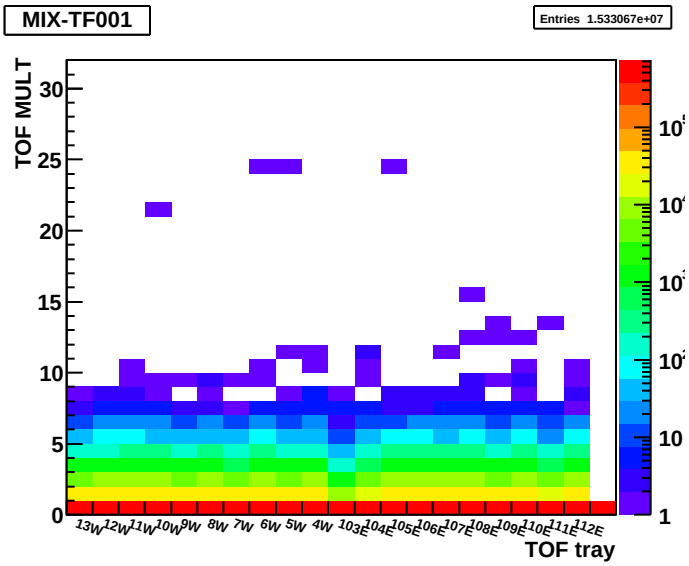

EM201 0-15 (ch3)
Entries 5840256
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

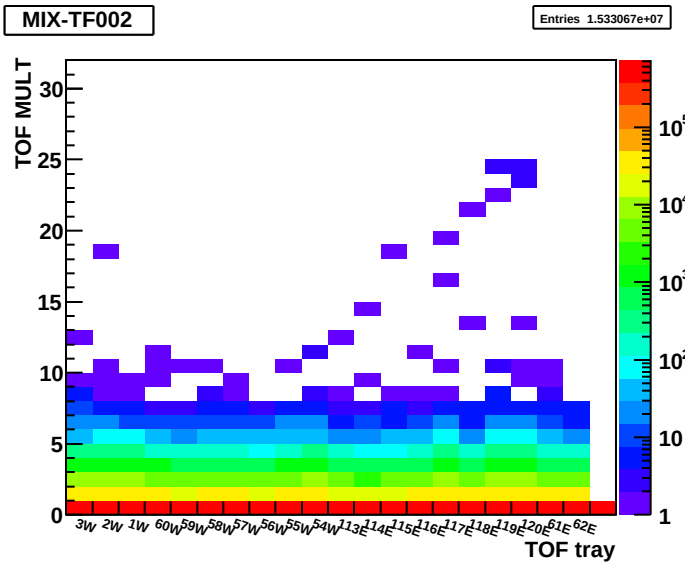

RAT board (ch4)
Entries 5840256
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

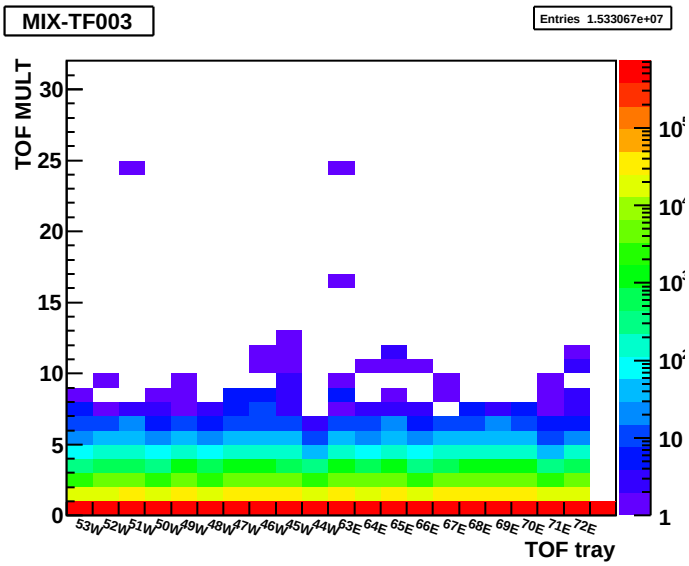

FP201 0-15 (ch5)
Entries 5840256
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

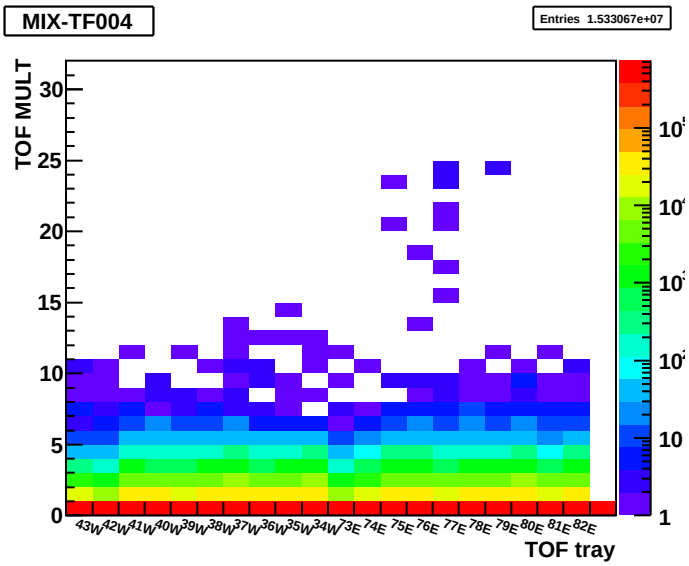

ST201 0-15 (ch6)
Entries 5840256
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

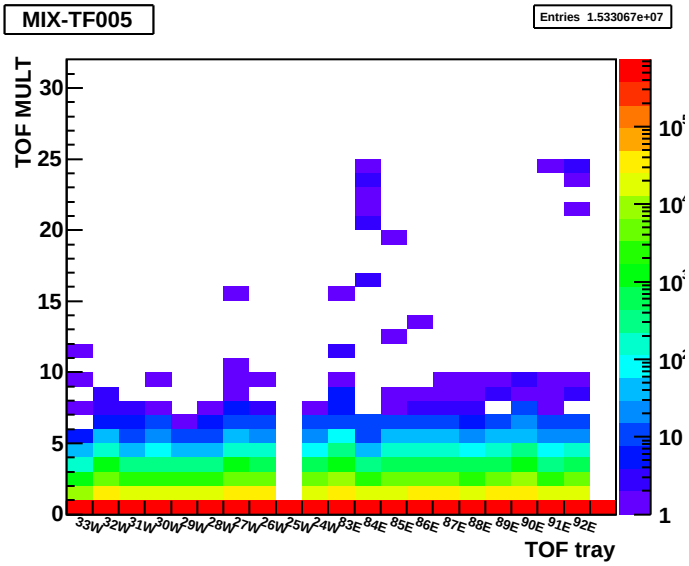

Unused (ch7)
Entries 5840256

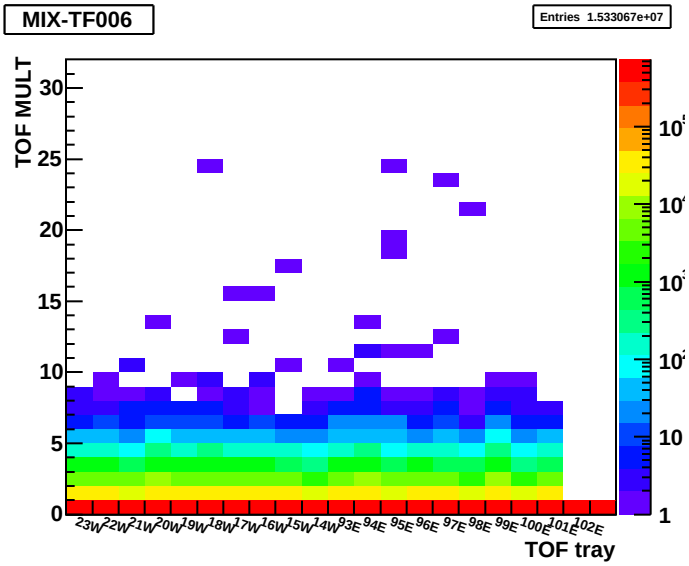

MIX-TF001
Entries 1.533067e+07
TOF MULT
TOF tray

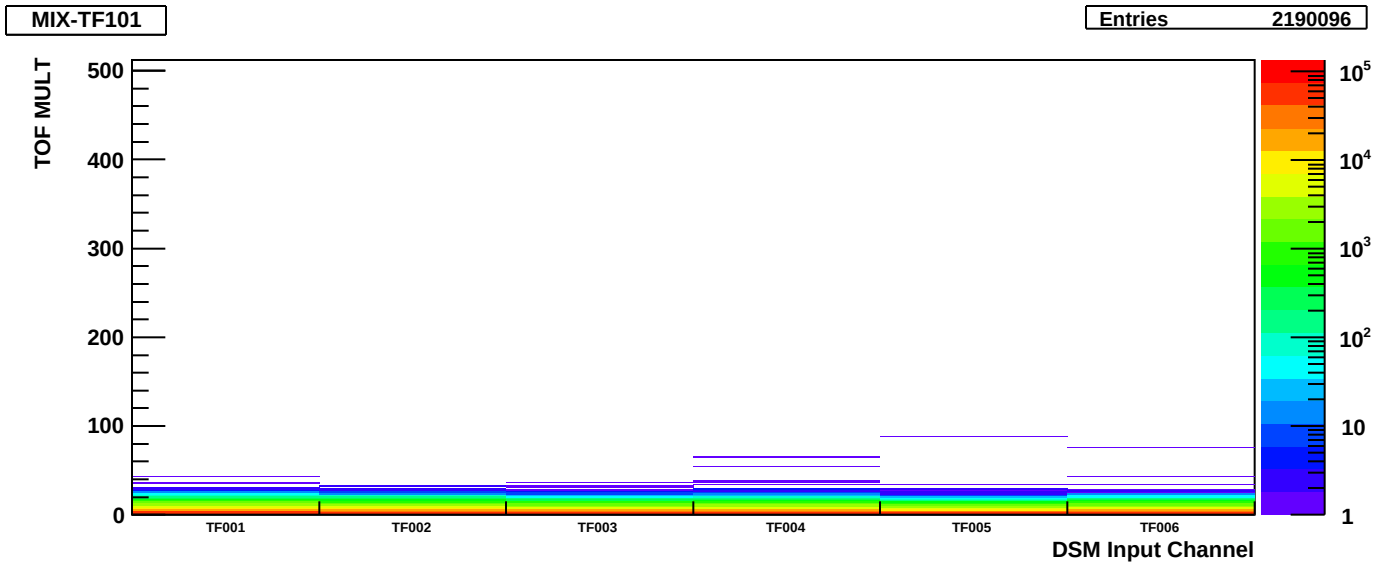

MIX-TF002
Entries 1.533067e+07
TOF MULT
TOF tray

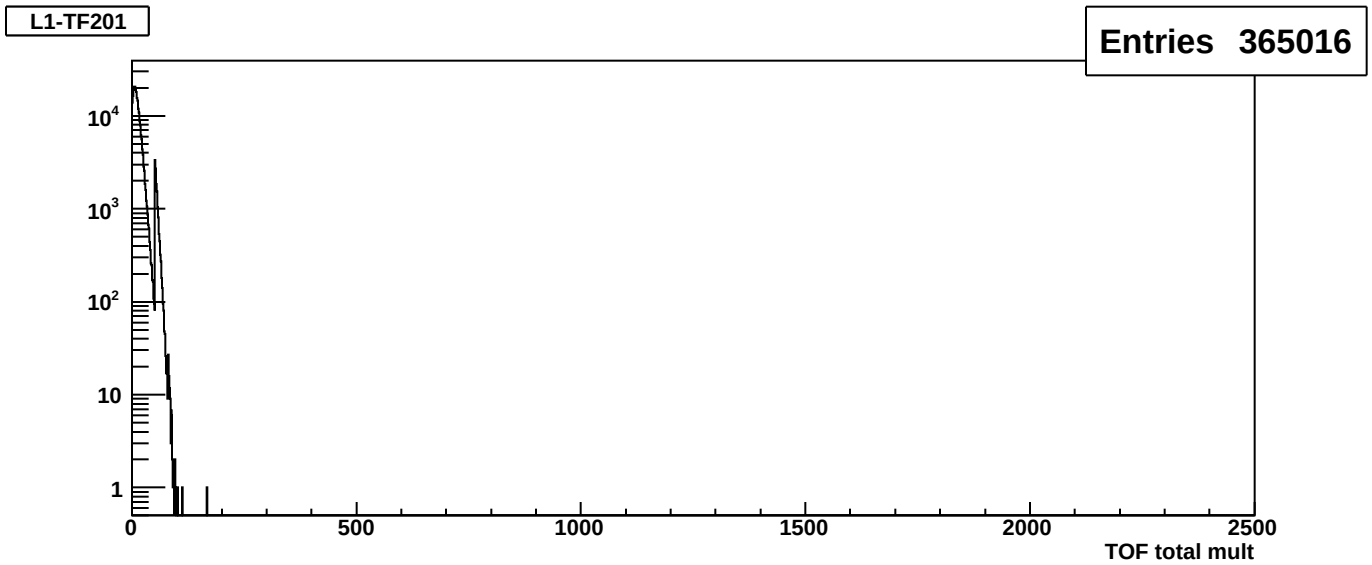

MIX-TF003
Entries 1.533067e+07
TOF MULT
TOF tray

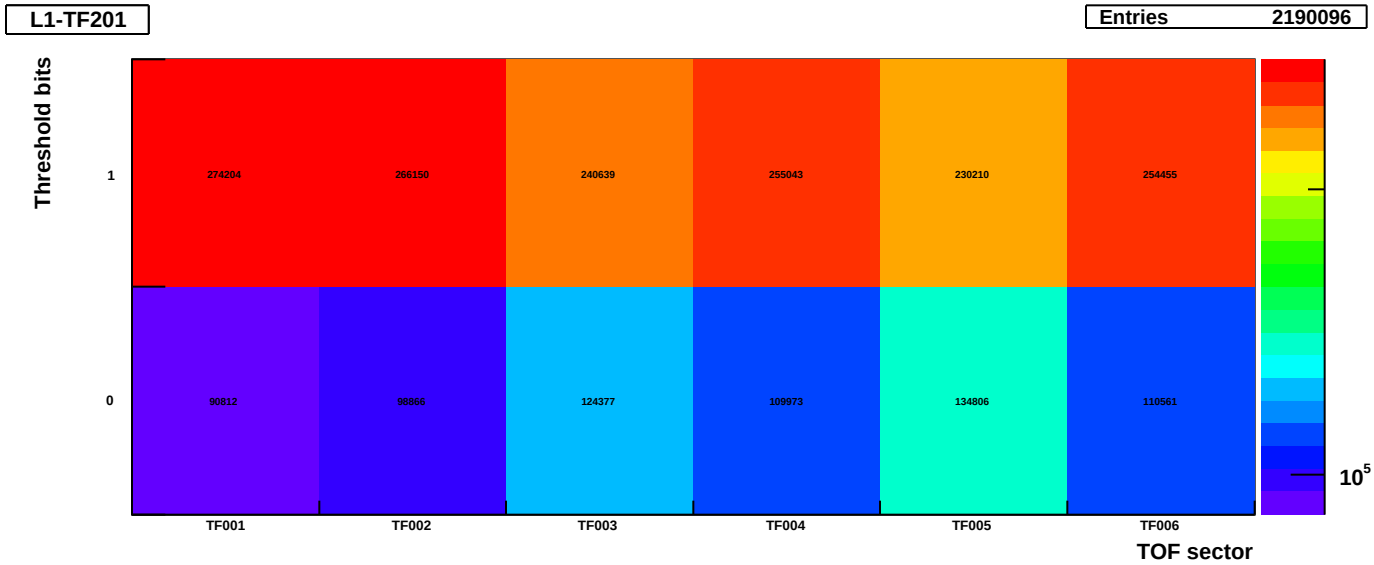

MIX-TF004
Entries 1.533067e+07
TOF MULT
TOF tray

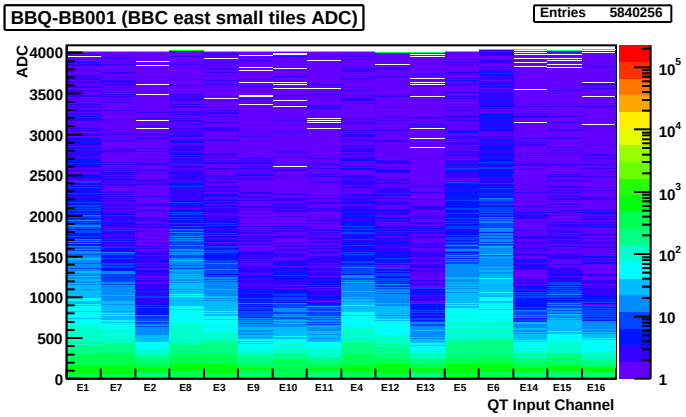

MIX-TF005
Entries 1.533067e+07
TOF MULT
TOF tray

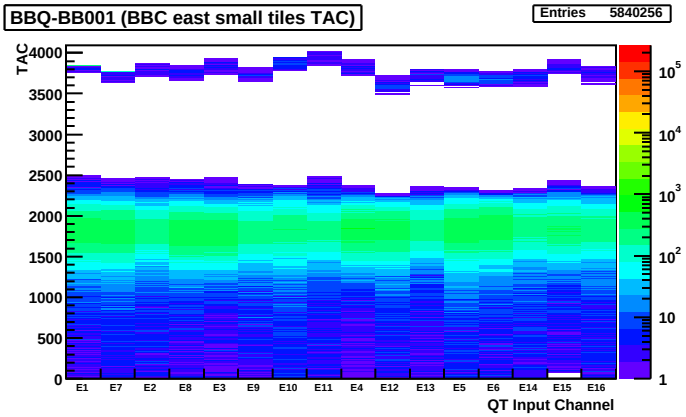

MIX-TF006
Entries 1.533067e+07
TOF MULT
TOF tray

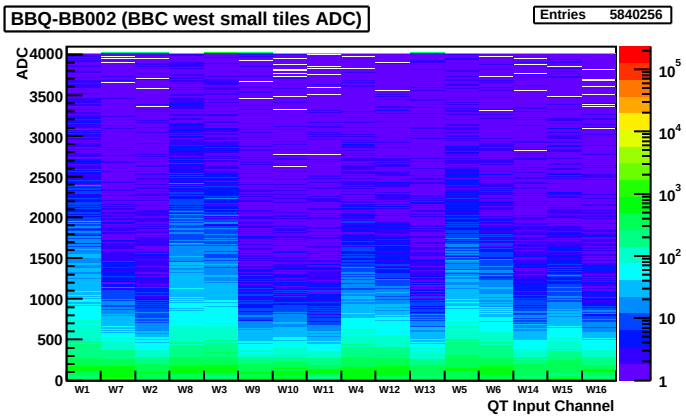

MIX-TF101
Entries 2190096
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

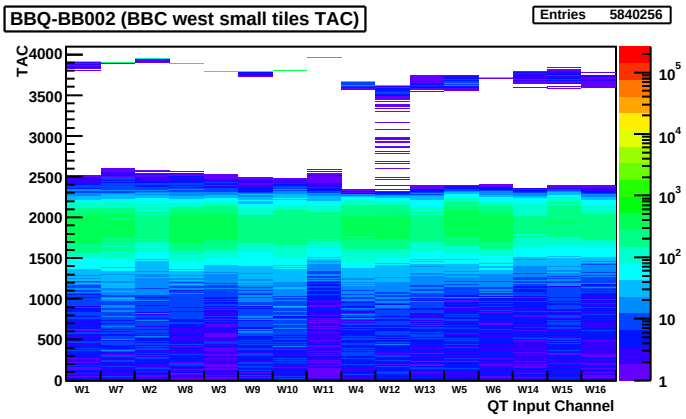

L1-TF201
Entries 365016
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

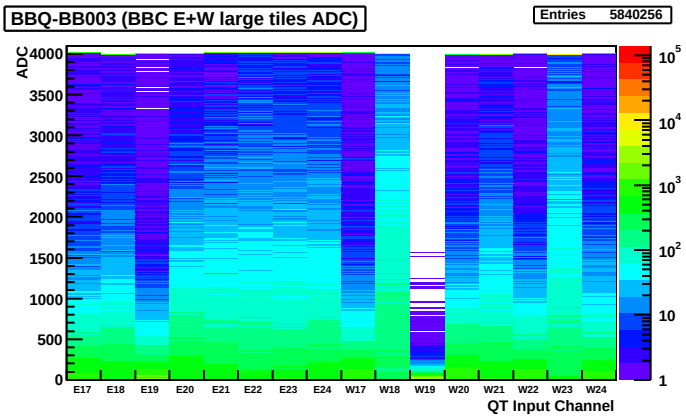

L1-TF201
Entries 2190096
Threshold bits
1
0
274204
266150
240639
255043
230210
254455
90812
98866
124377
109973
134806
110561
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

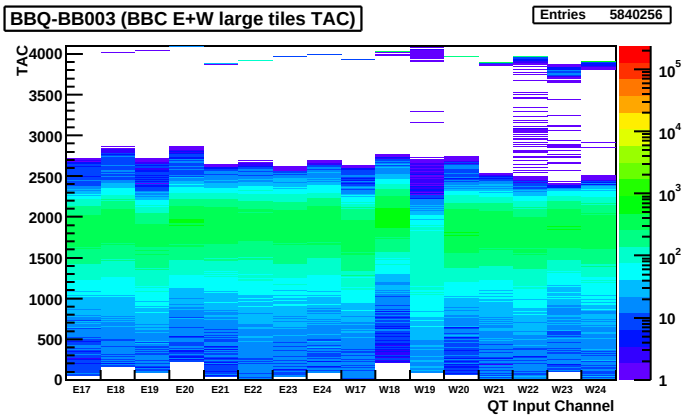

BBQ-BB001 (BBC east small tiles ADC)
Entries 5840256
ADC
QT Input Channel

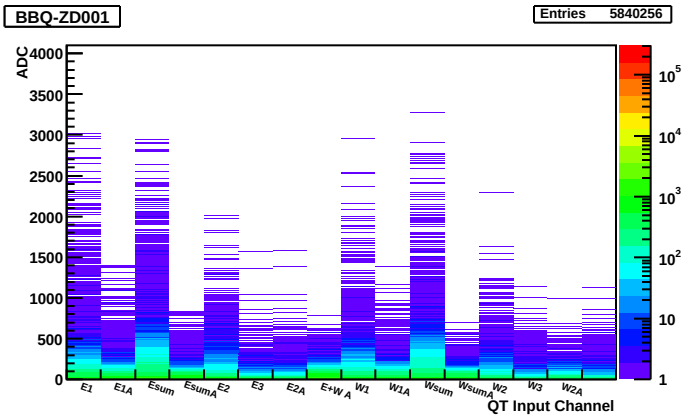

BBQ-BB001 (BBC east small tiles TAC)
Entries 5840256
TAC
QT Input Channel

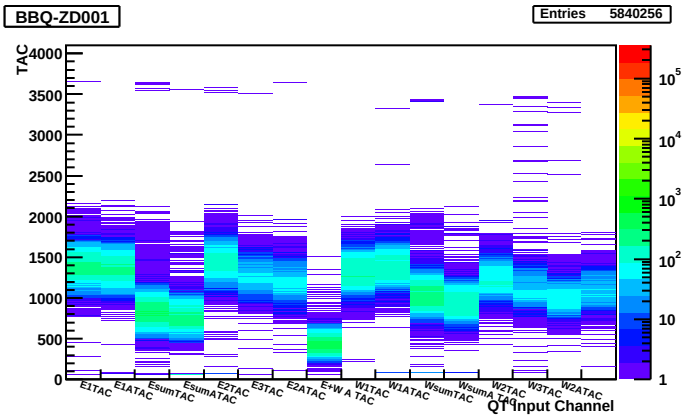

BBQ-BB002 (BBC west small tiles ADC)
Entries 5840256
ADC
QT Input Channel

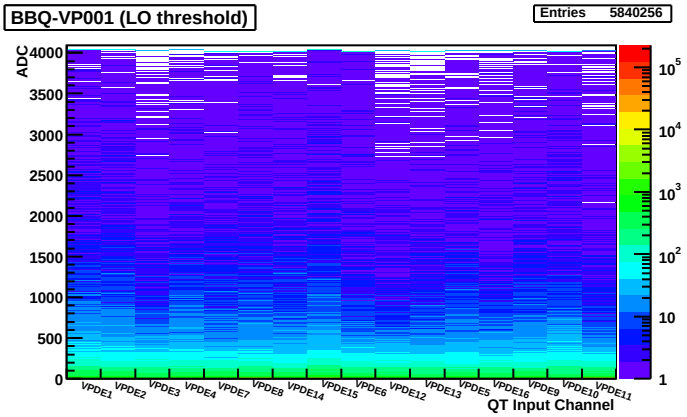

BBQ-BB002 (BBC west small tiles TAC)
Entries 5840256
TAC
QT Input Channel

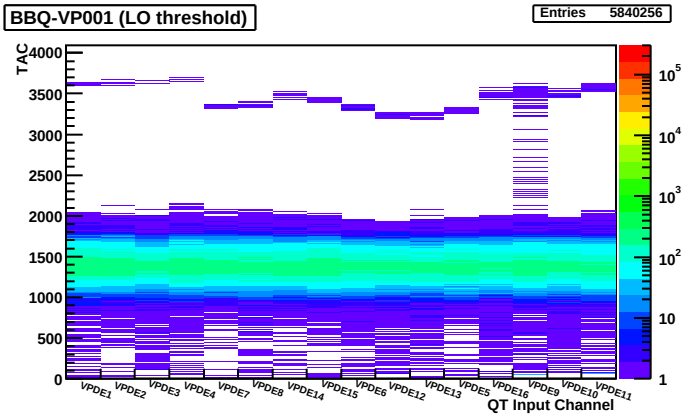

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5840256
ADC
QT Input Channel

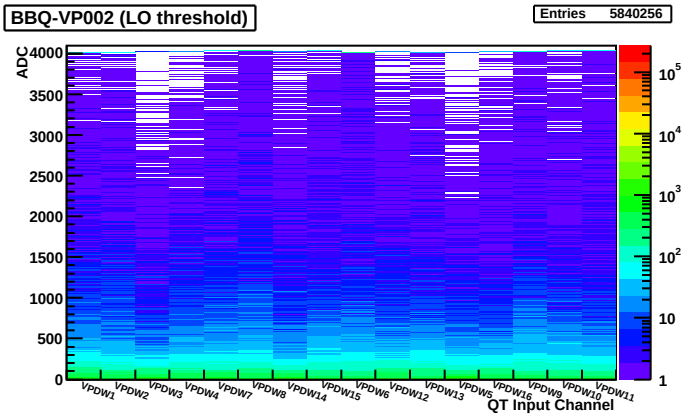

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5840256
TAC
QT Input Channel

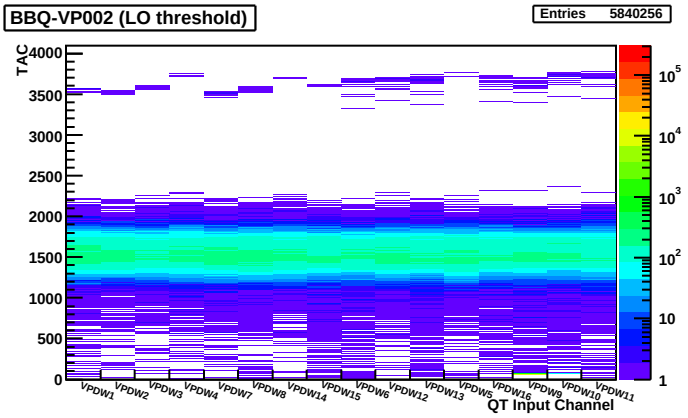

BBQ-ZD001
Entries 5840256
ADC
QT Input Channel

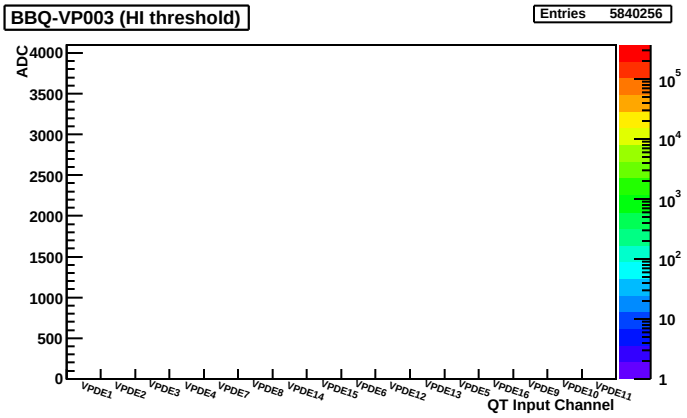

BBQ-ZD001
Entries 5840256
TAC
QT Input Channel

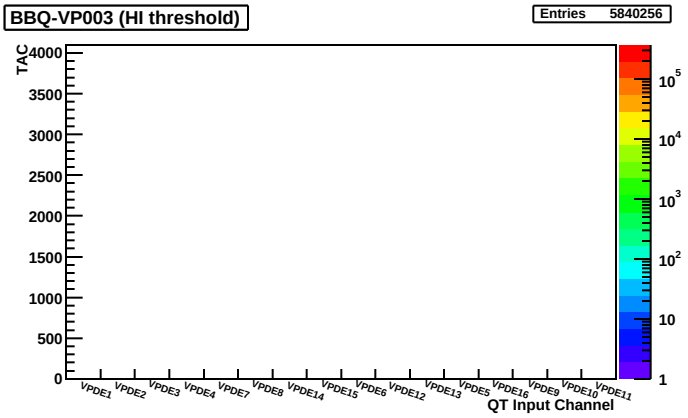

BBQ-VP001 (LO threshold)
Entries 5840256
ADC
QT Input Channel

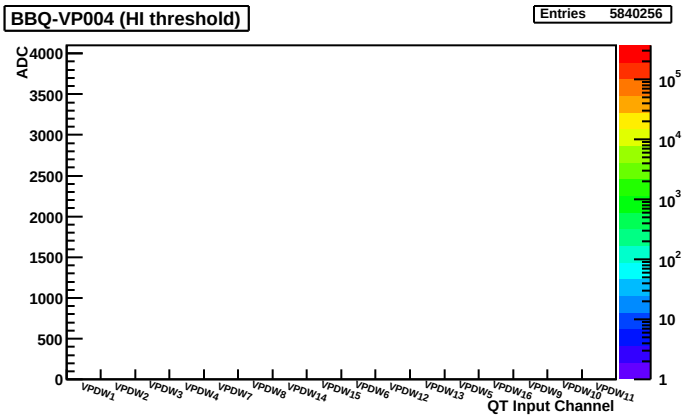

BBQ-VP001 (LO threshold)
Entries 5840256
TAC
QT Input Channel

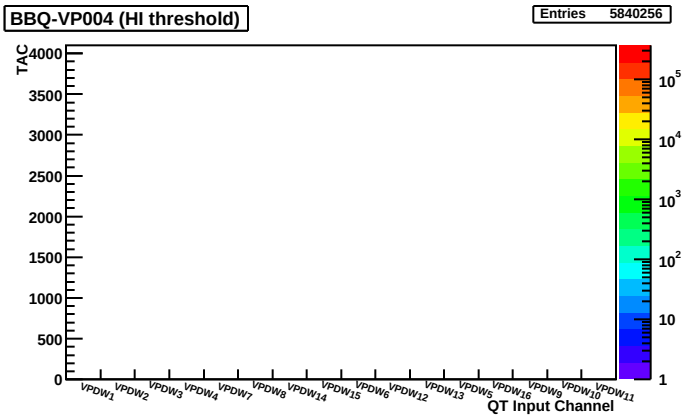

BBQ-VP002 (LO threshold)
Entries 5840256
ADC
QT Input Channel

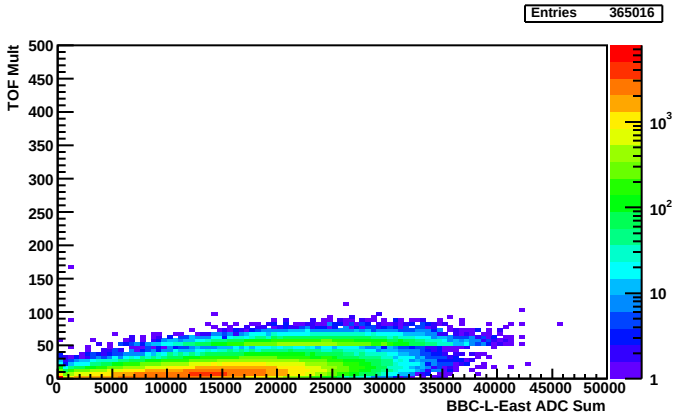

BBQ-VP002 (LO threshold)
Entries 5840256
TAC
QT Input Channel

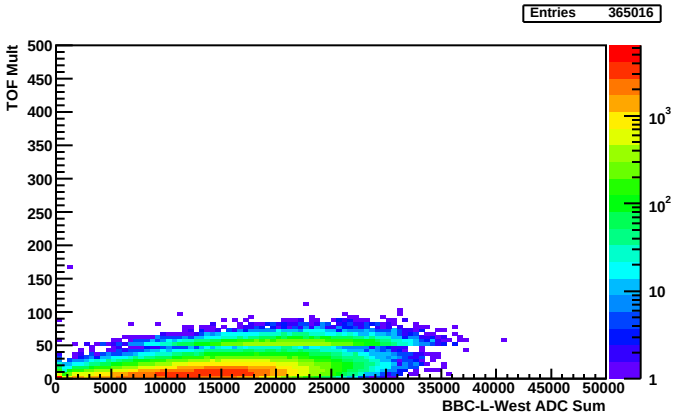

BBQ-VP003 (HI threshold)
Entries 5840256
ADC
QT Input Channel

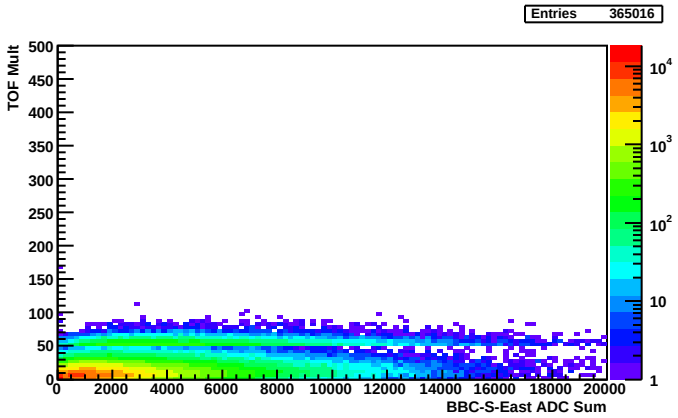

BBQ-VP003 (HI threshold)
Entries 5840256
TAC
QT Input Channel

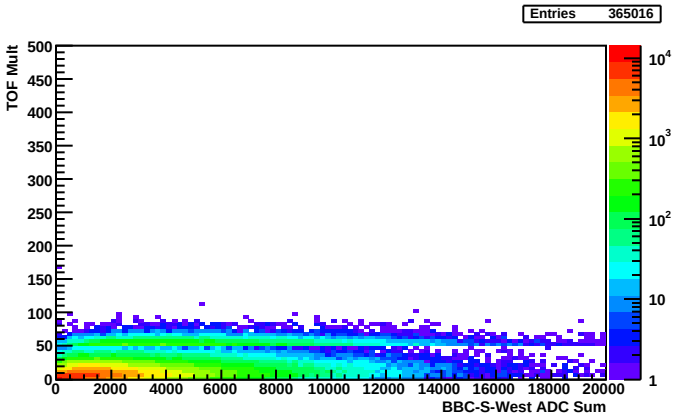

BBQ-VP004 (HI threshold)
Entries 5840256
ADC
QT Input Channel

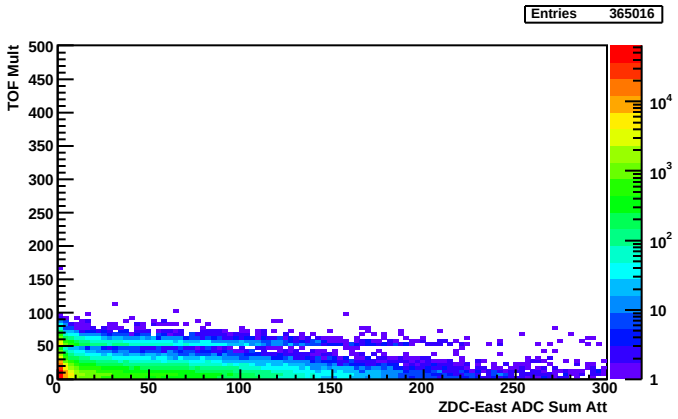

BBQ-VP004 (HI threshold)
Entries 5840256
TAC
QT Input Channel

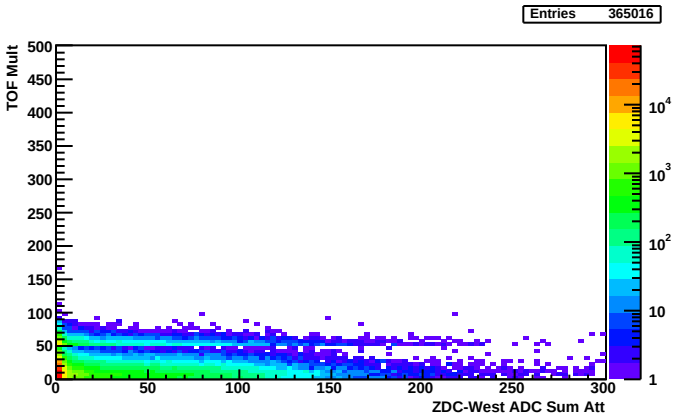

Entries 365016
TOF Mult
BBC-L-East ADC Sum

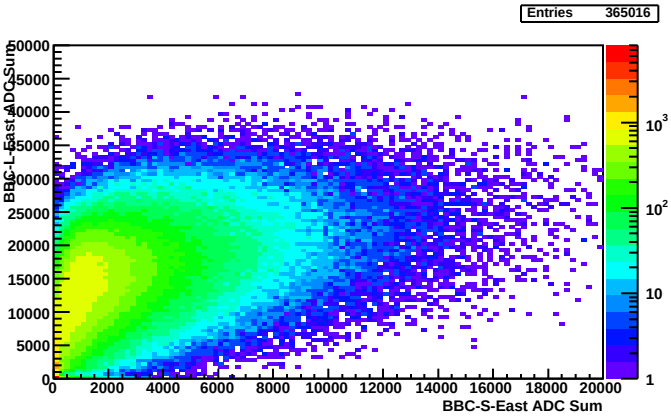

Entries 365016
TOF Mult
BBC-L-West ADC Sum

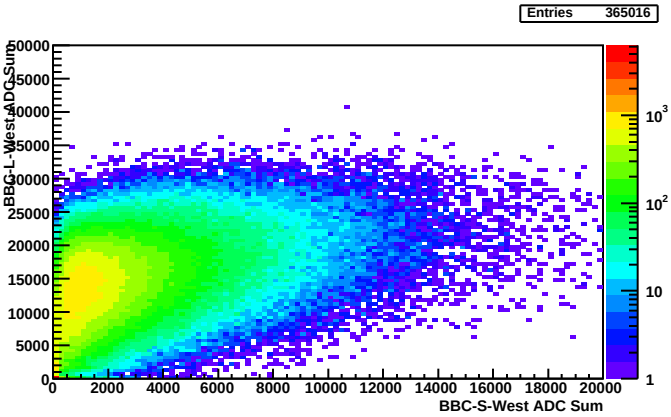

Entries 365016
TOF Mult
BBC-S-East ADC Sum

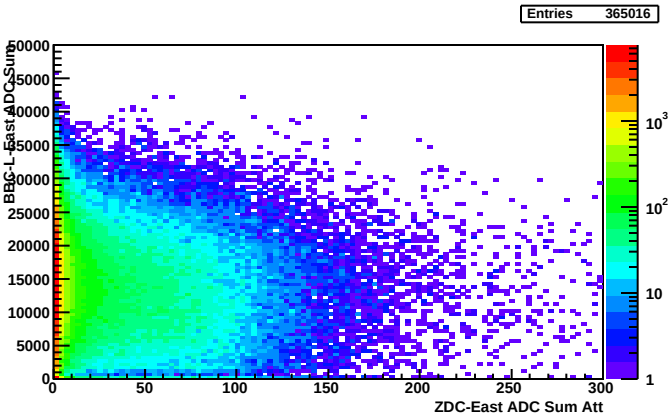

Entries 365016
TOF Mult
BBC-S-West ADC Sum

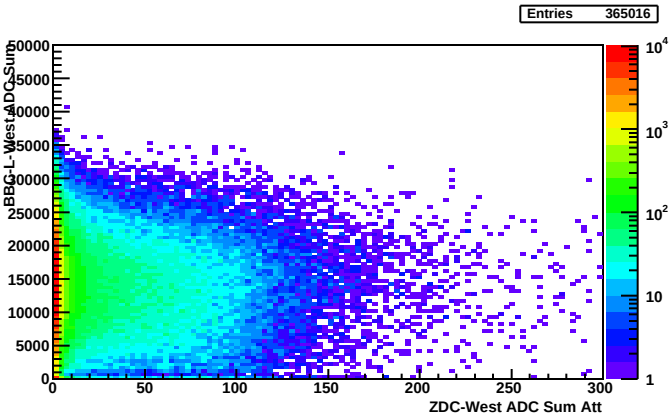

Entries 365016
TOF Mult
ZDC-East ADC Sum Att

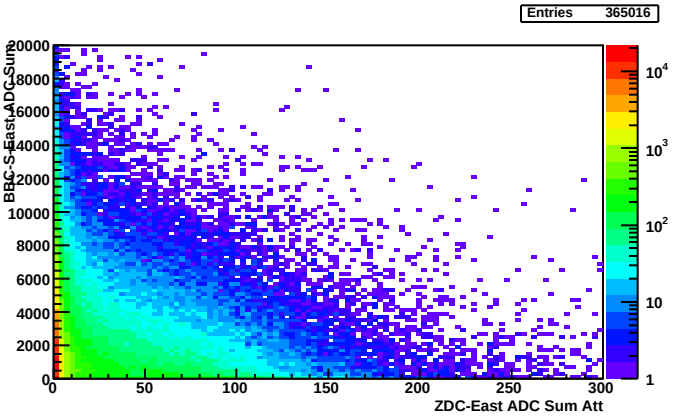

Entries 365016
TOF Mult
ZDC-West ADC Sum Att

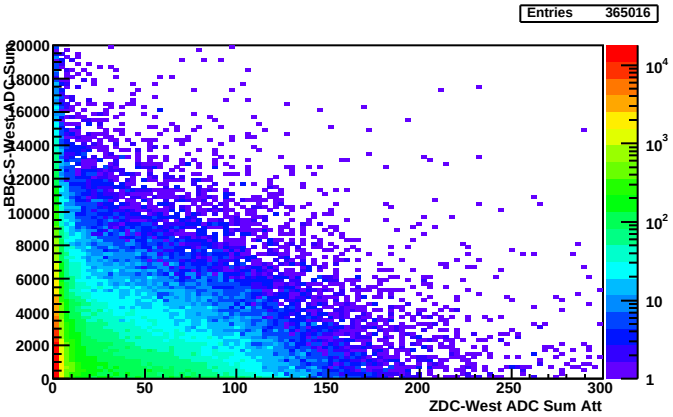

Entries 365016
BBC-L-East ADC Sum
BBC-S-East ADC Sum

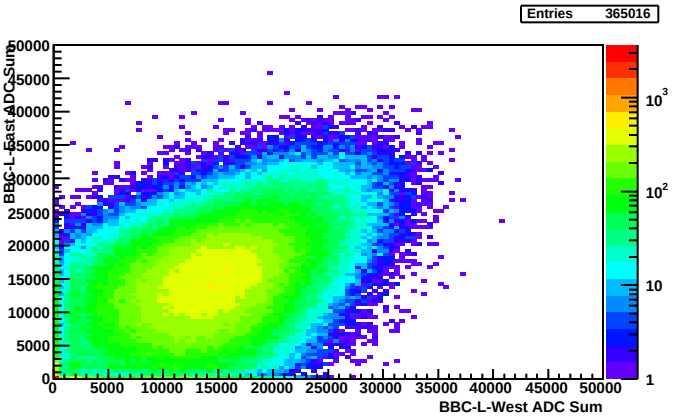

Entries 365016
BBC-L-West ADC Sum
BBC-S-West ADC Sum

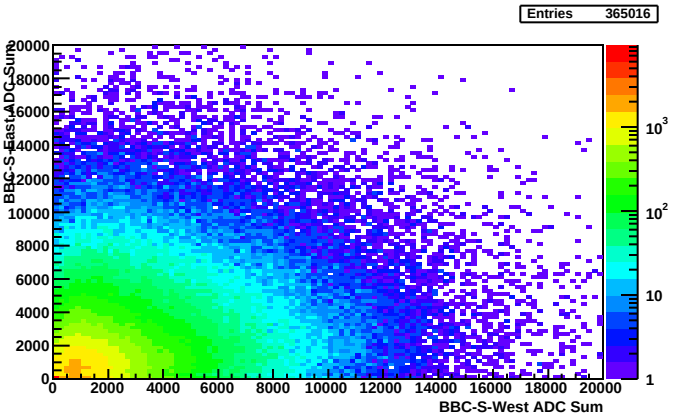

Entries 365016
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

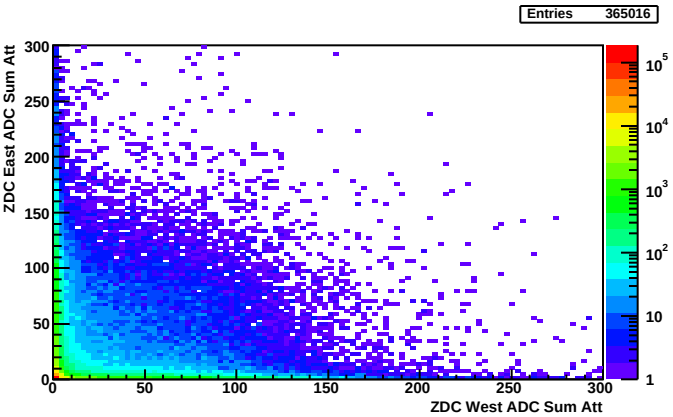

Entries 365016
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

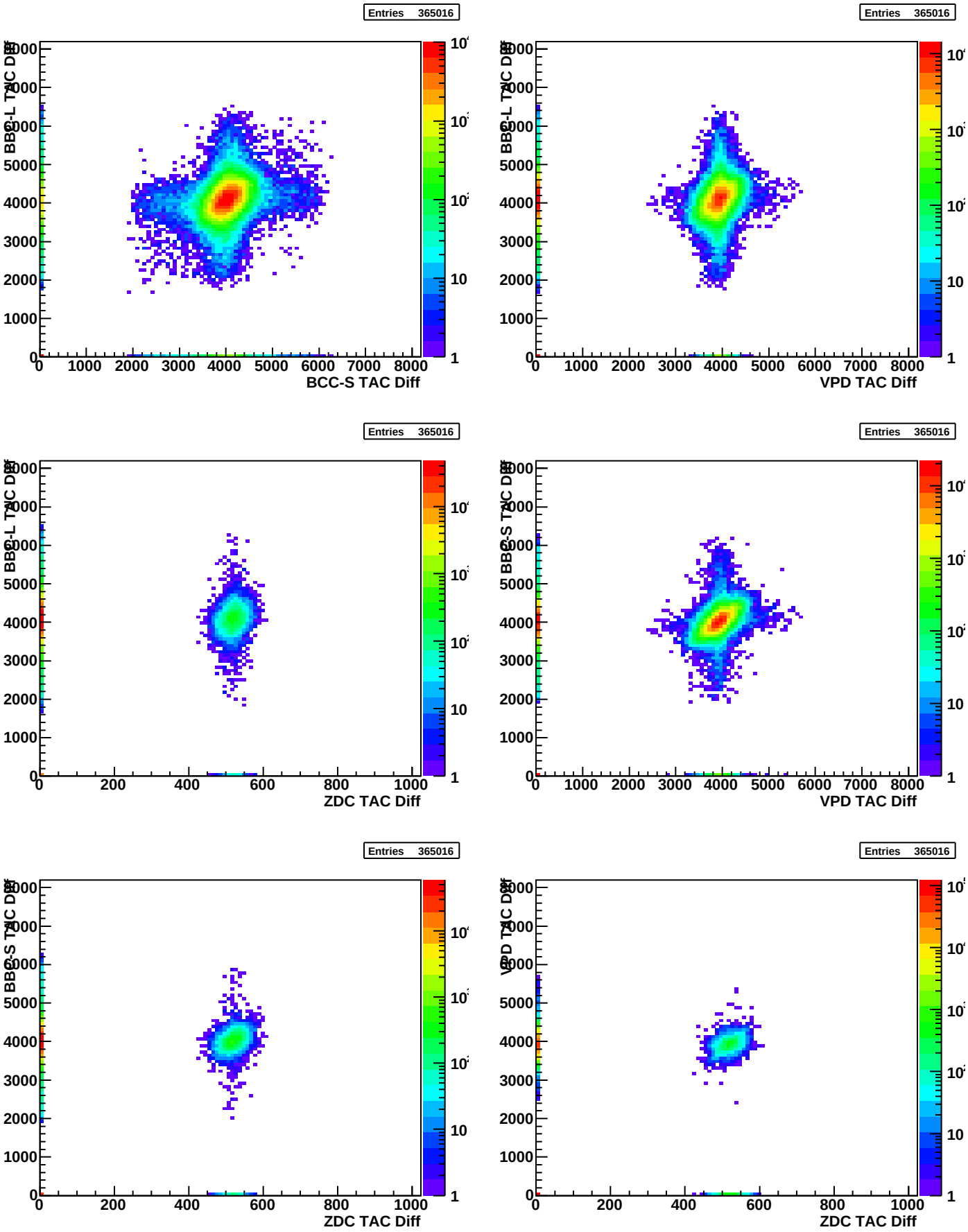

Entries 365016
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

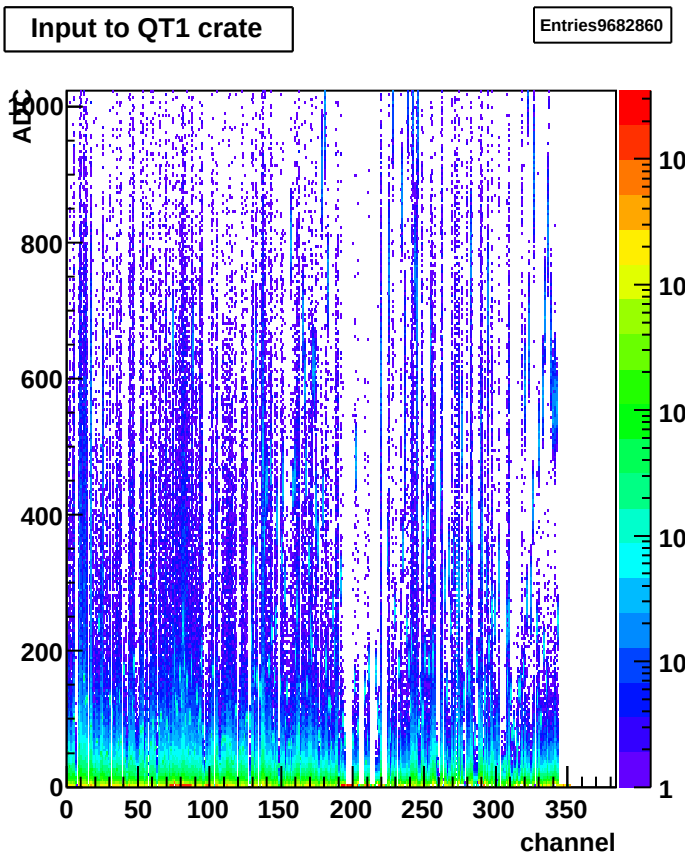

Entries 365016
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

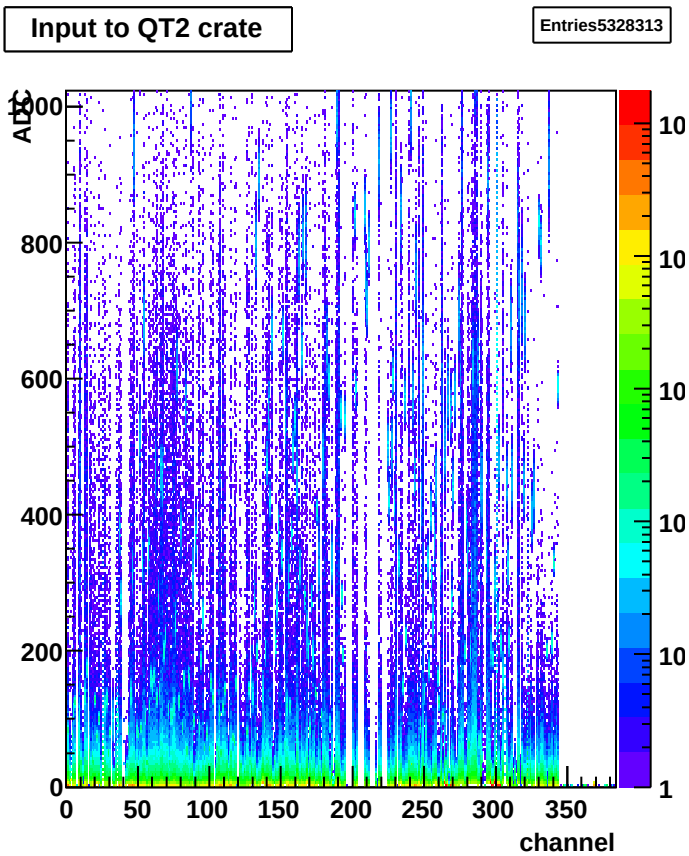

Entries 365016
BBC-L-East ADC Sum
BBC-L-West ADC Sum

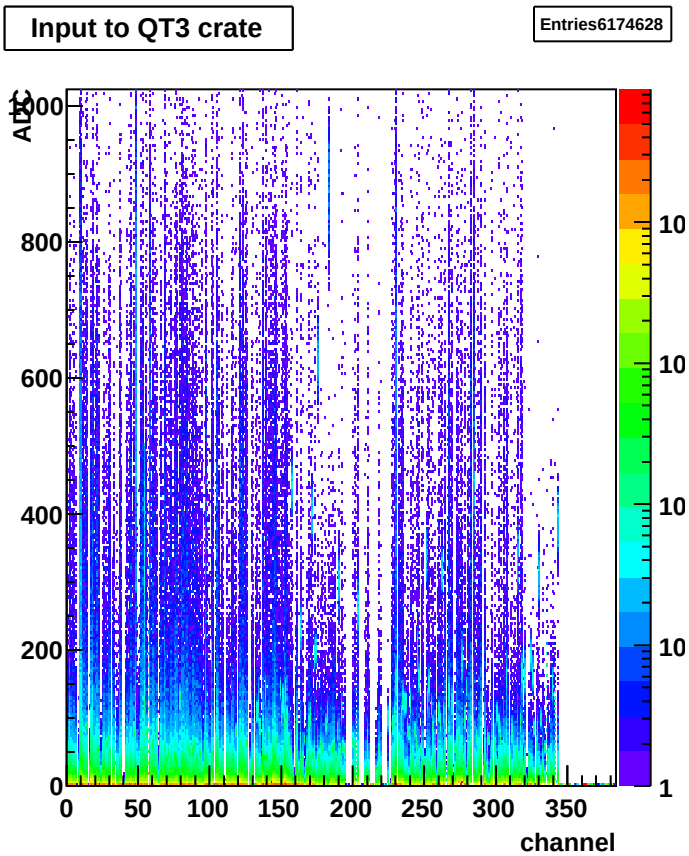

Entries 365016
BBC-S-East ADC Sum
BBC-S-West ADC Sum

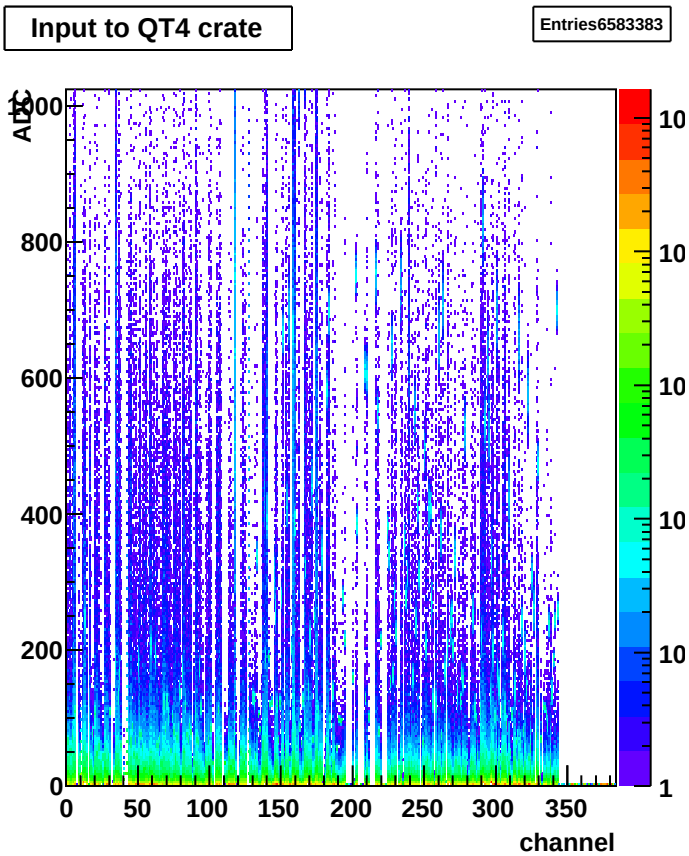

Entries 365016
ZDC East ADC Sum Att
ZDC West ADC Sum Att

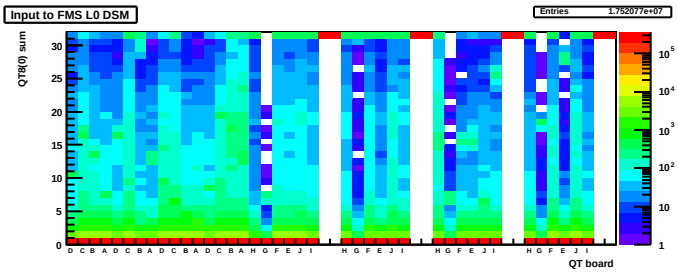

Entries 365016
BBC-L TAC Diff
BCC-S TAC Diff
Entries 365016
BBC-L TAC Diff
VPD TAC Diff
Entries 365016
BBC-L TAC Diff
ZDC TAC Diff
Entries 365016
BBC-S TAC Diff
VPD TAC Diff
Entries 365016
BBC-S TAC Diff
ZDC TAC Diff
Entries 365016
VPD TAC Diff
ZDC TAC Diff

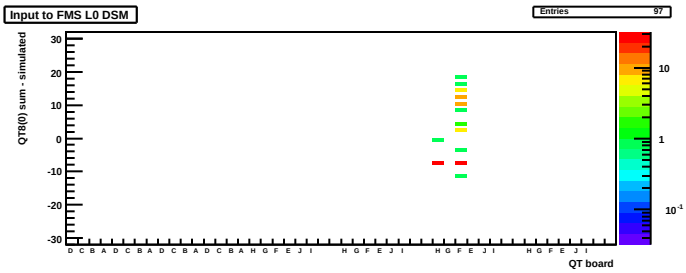

Input to QT1 crate
Entries9682860
ADC
channel

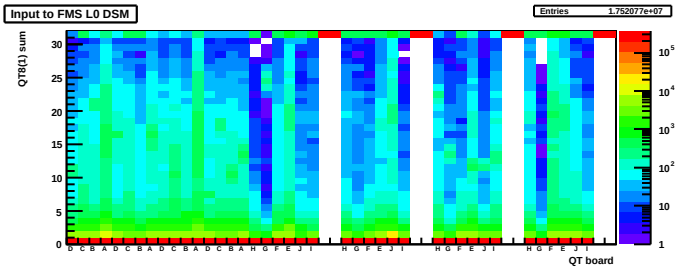

Input to QT2 crate
Entries5328313
ADC
channel

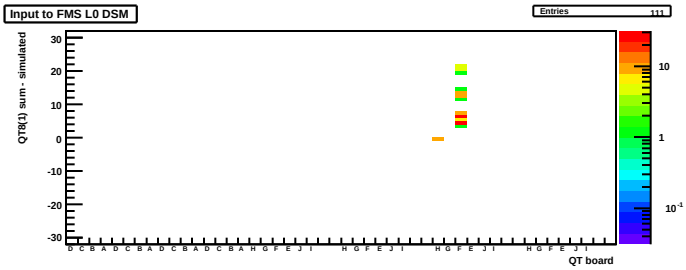

Input to QT3 crate
Entries6174628
ADC
channel

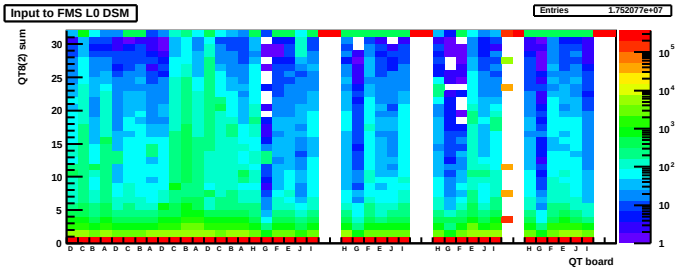

Input to QT4 crate
Entries6583383
ADC
channel

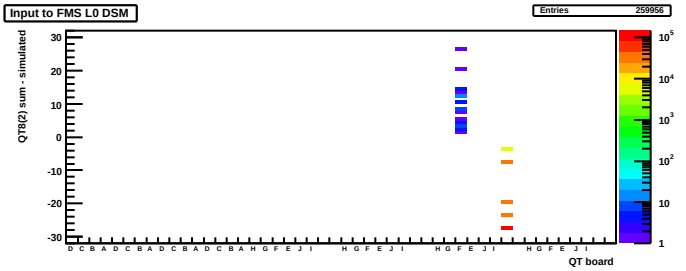

Input to FMS L0 DSM
Entries 1.752077e+07
QT8(0) sum
QT board

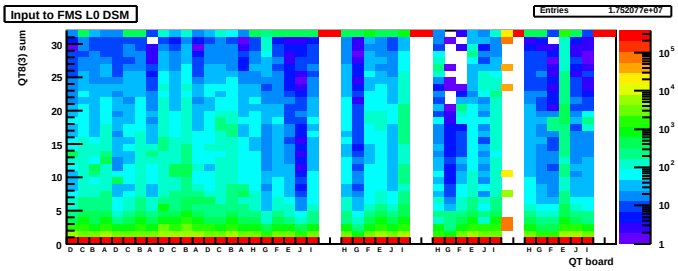

Input to FMS L0 DSM
Entries 97
QT8(0) sum - simulated
QT board

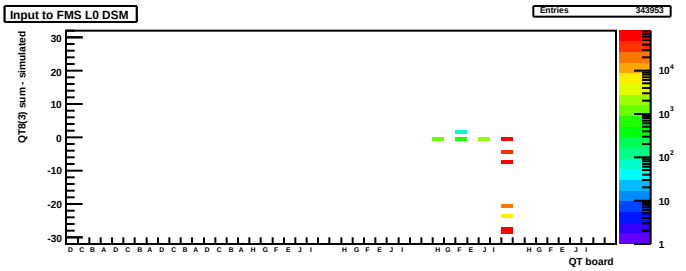

Input to FMS L0 DSM
Entries 1.752077e+07
QT8(1) sum
QT board

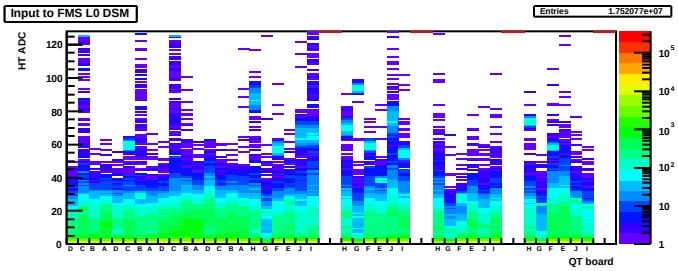

Input to FMS L0 DSM
Entries 111
QT8(1) sum - simulated
QT board

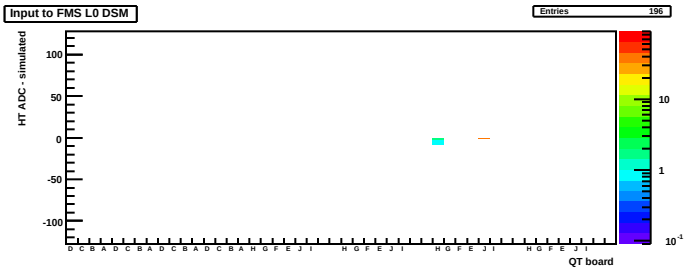

Input to FMS L0 DSM
Entries 1.752077e+07
QT8(2) sum
QT board

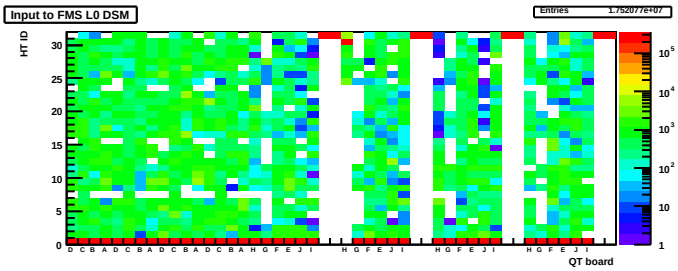

Input to FMS L0 DSM
Entries 259956
QT8(2) sum - simulated
QT board

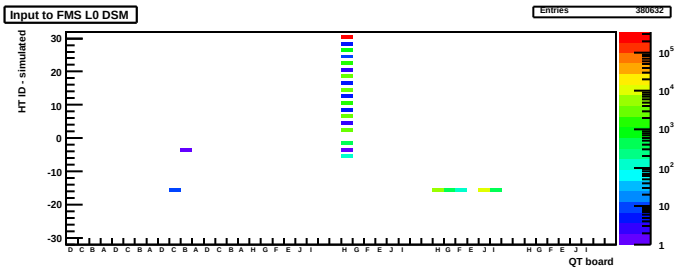

Input to FMS L0 DSM
Entries 1.752077e+07
QT8(3) sum
QT board

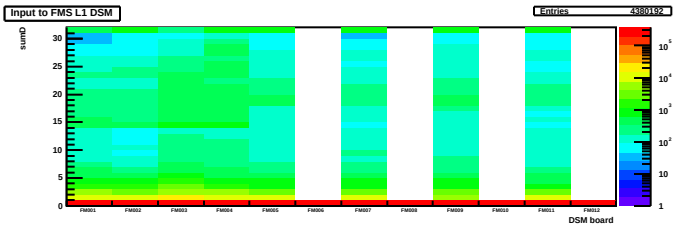

Input to FMS L0 DSM
Entries 343953
QT8(3) sum - simulated
QT board

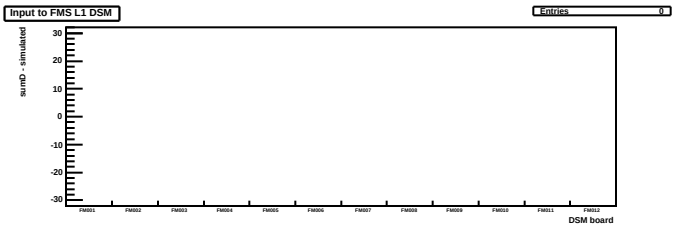

Input to FMS L0 DSM
Entries 1.752077e+07
HT ADC
QT board

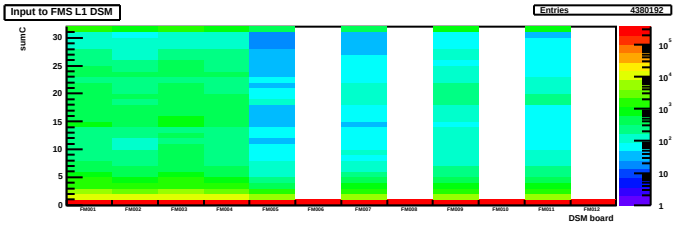

Input to FMS L0 DSM
Entries 196
HT ADC - simulated
QT board

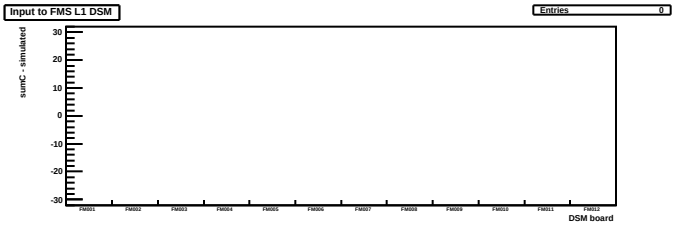

Input to FMS L0 DSM
Entries 1.752077e+07
HT ID
QT board

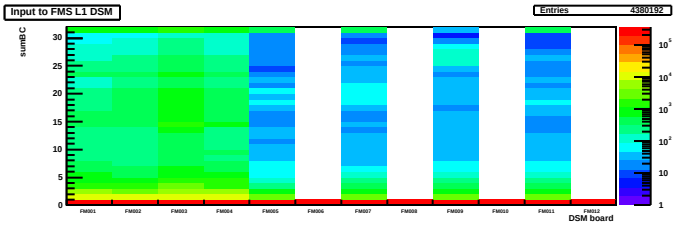

Input to FMS L0 DSM
Entries 380632
HT ID - simulated
QT board

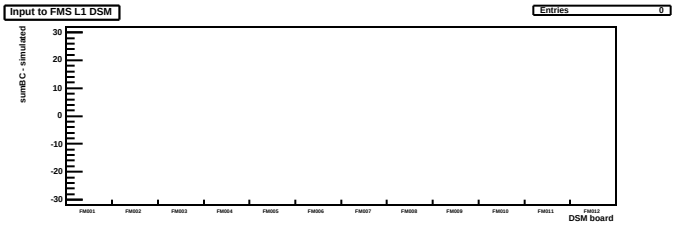

Input to FMS L1 DSM
Entries 4380192
sumD
DSM board

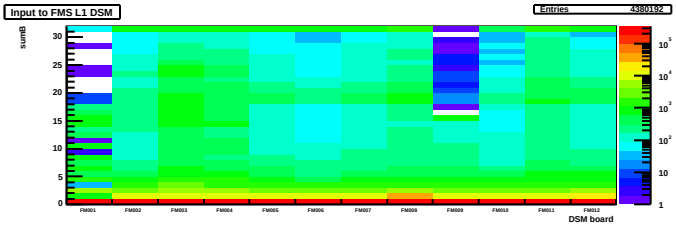

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

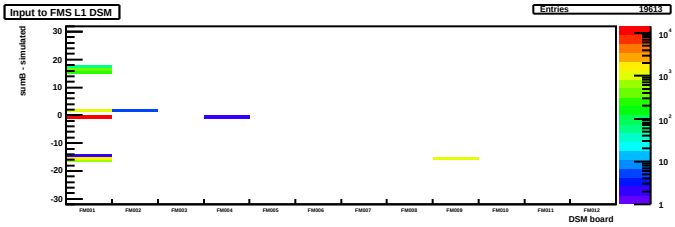

Input to FMS L1 DSM
Entries 4380192
sumC
DSM board

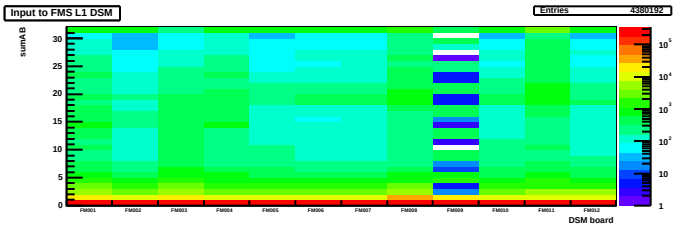

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

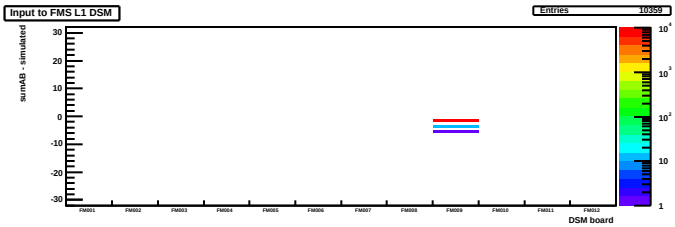

Input to FMS L1 DSM
Entries 4380192
sumBC
DSM board

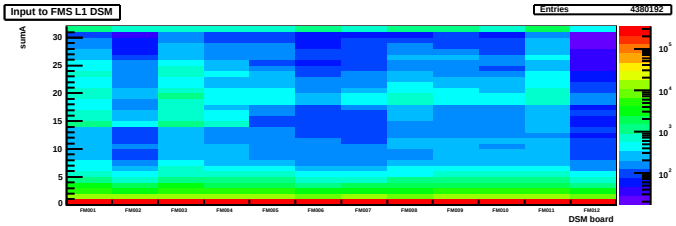

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

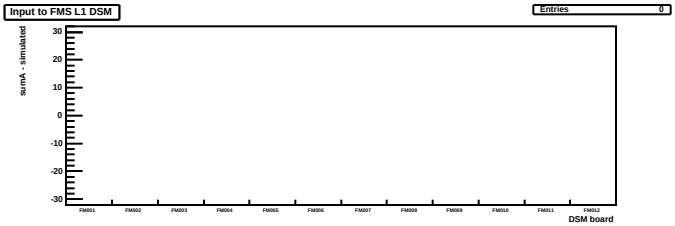

Input to FMS L1 DSM
Entries 4380192
sumB
DSM board

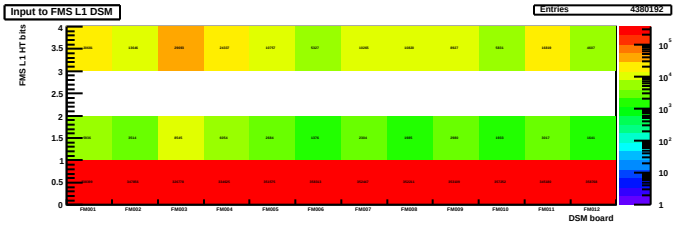

Input to FMS L1 DSM
Entries 19613
sumB - simulated
DSM board

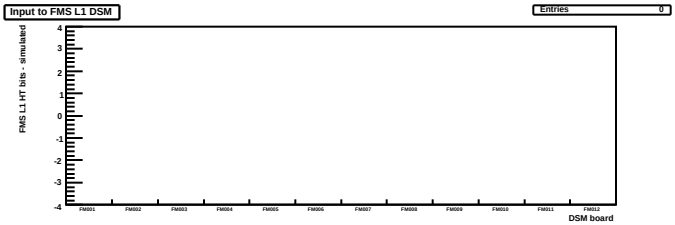

Input to FMS L1 DSM
Entries 4380192
sumAB
DSM board

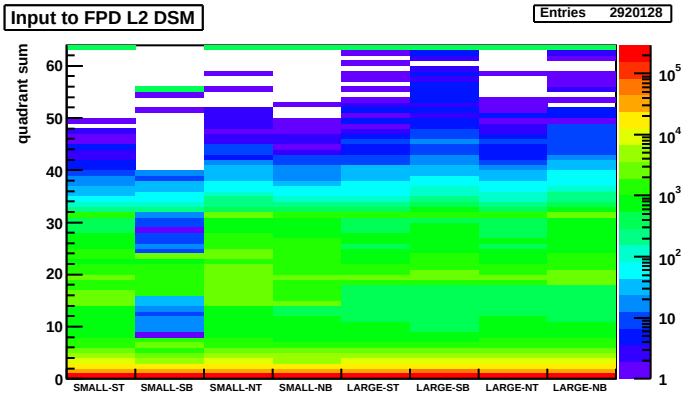

Input to FMS L1 DSM
Entries 10359
sumAB - simulated
DSM board

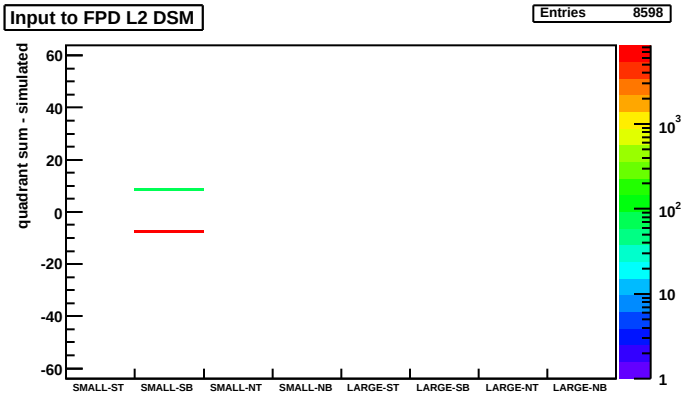

Input to FMS L1 DSM
Entries 4380192
sumA
DSM board

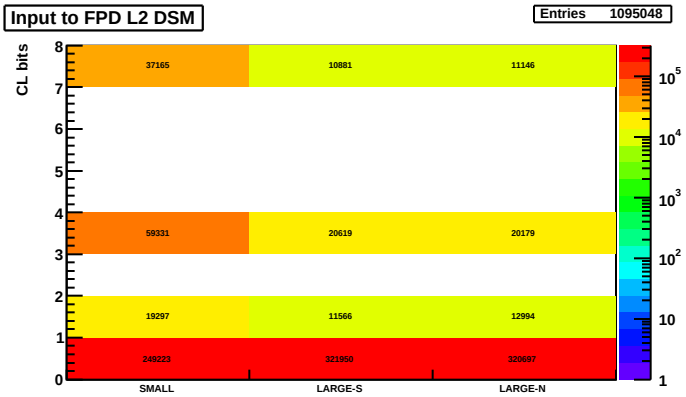

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

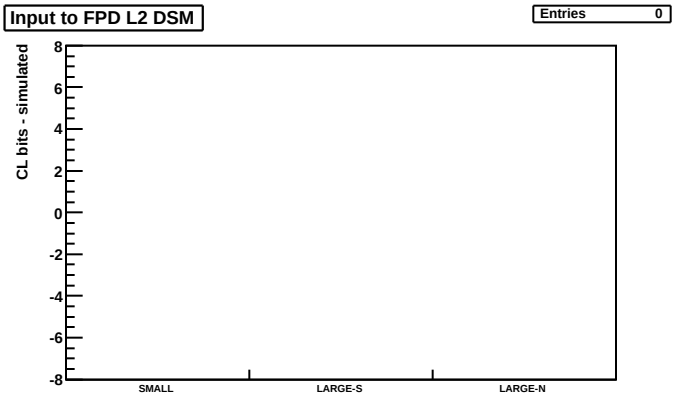

Input to FMS L1 DSM
Entries 4380192
FMS L1 HT bits
DSM board

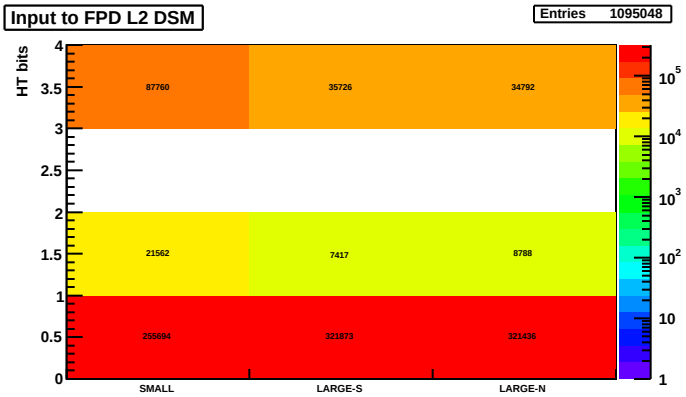

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

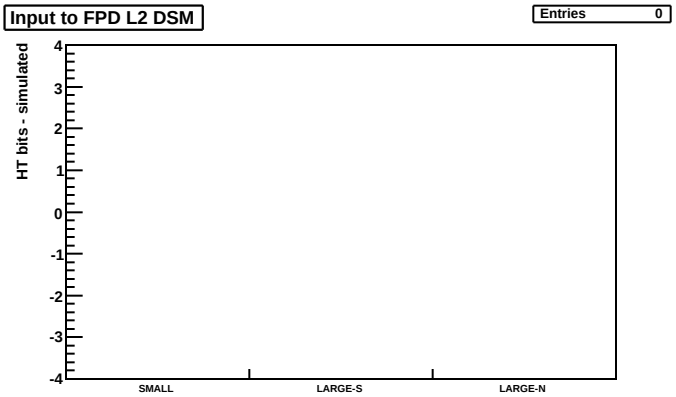

Input to FPD L2 DSM
Entries 2920128
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

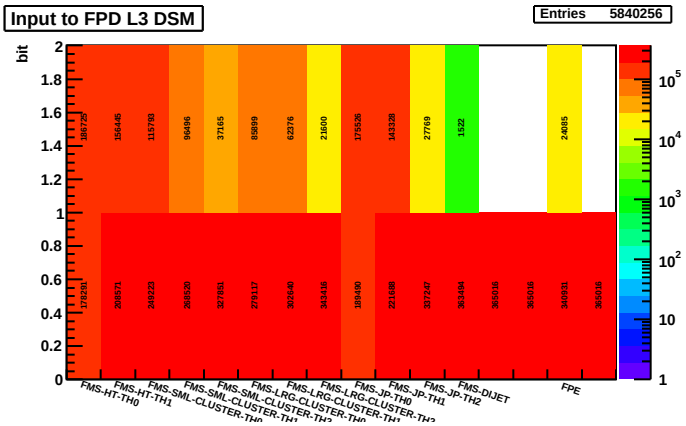

Input to FPD L2 DSM
Entries 8598
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

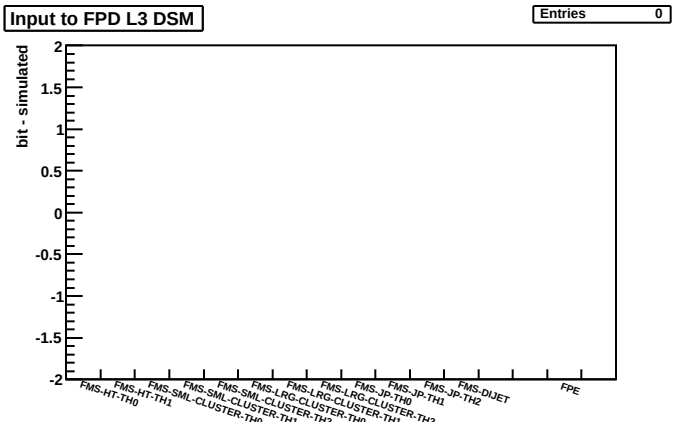

Input to FPD L2 DSM
Entries 1095048
CL bits
37165
10881
11146
59331
20619
20179
19297
11566
12994
249223
321950
320697
SMALL
LARGE-S
LARGE-N

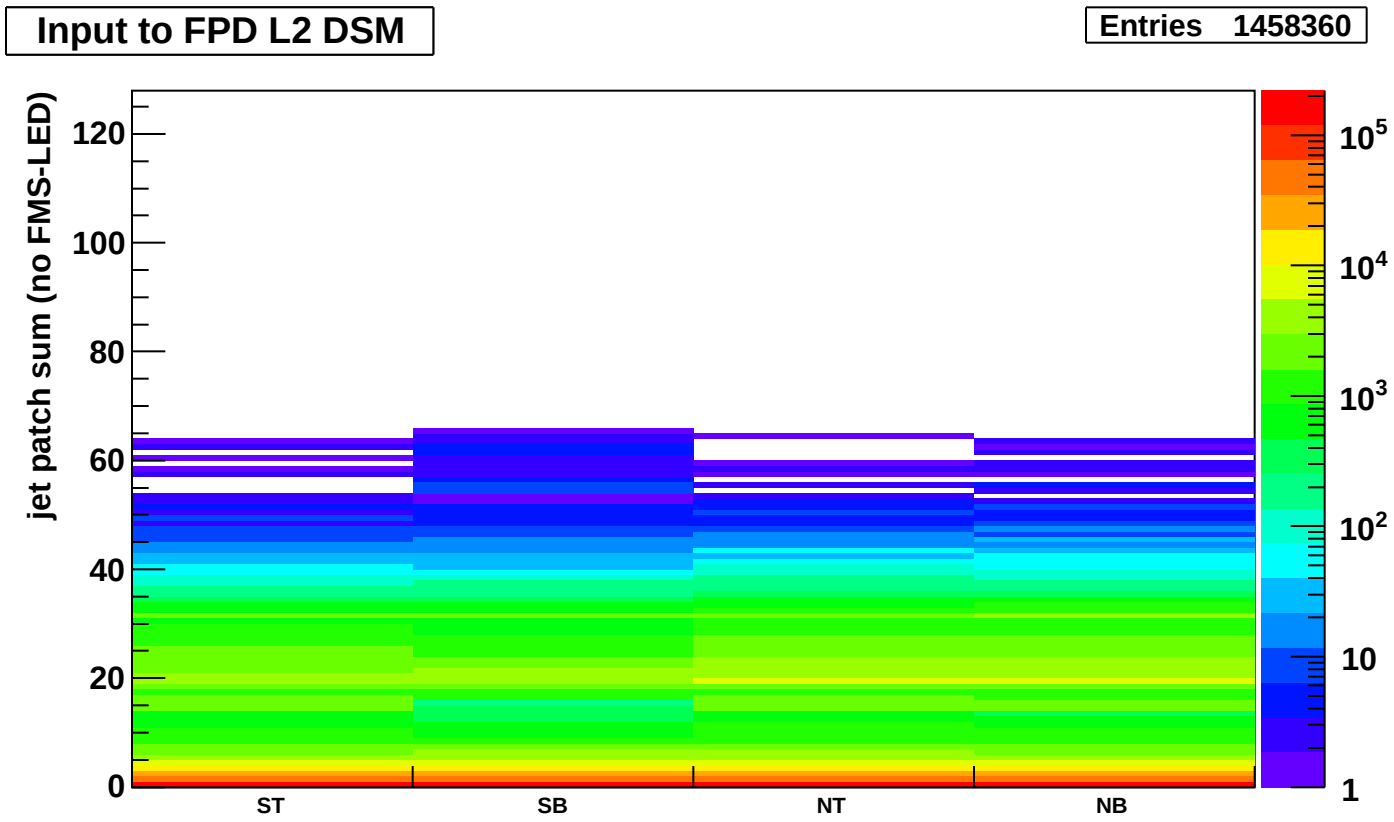

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

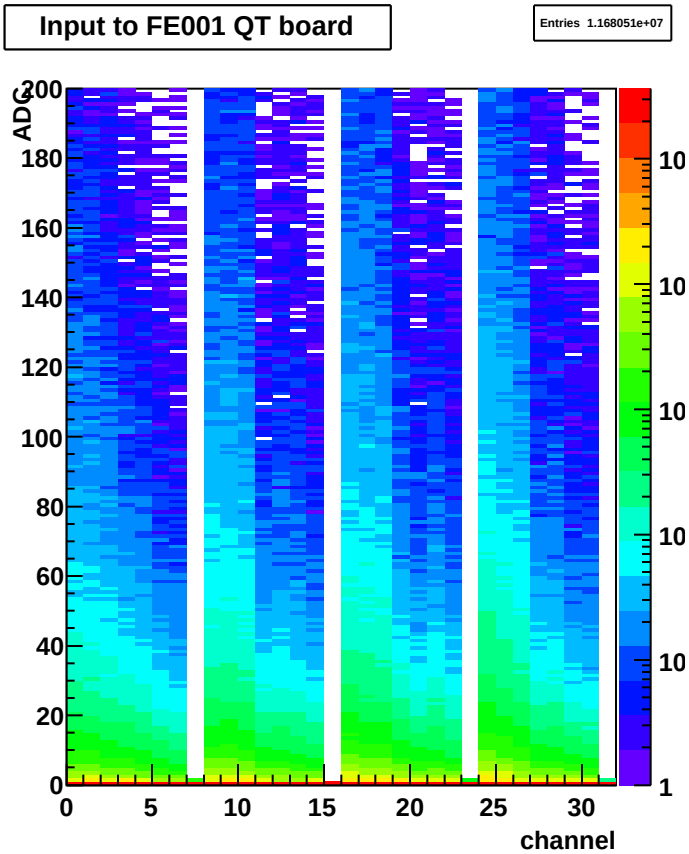

Input to FPD L2 DSM
Entries 1095048
HT bits
87760
35726
34792
21562
7417
8788
255694
321873
321436
SMALL
LARGE-S
LARGE-N

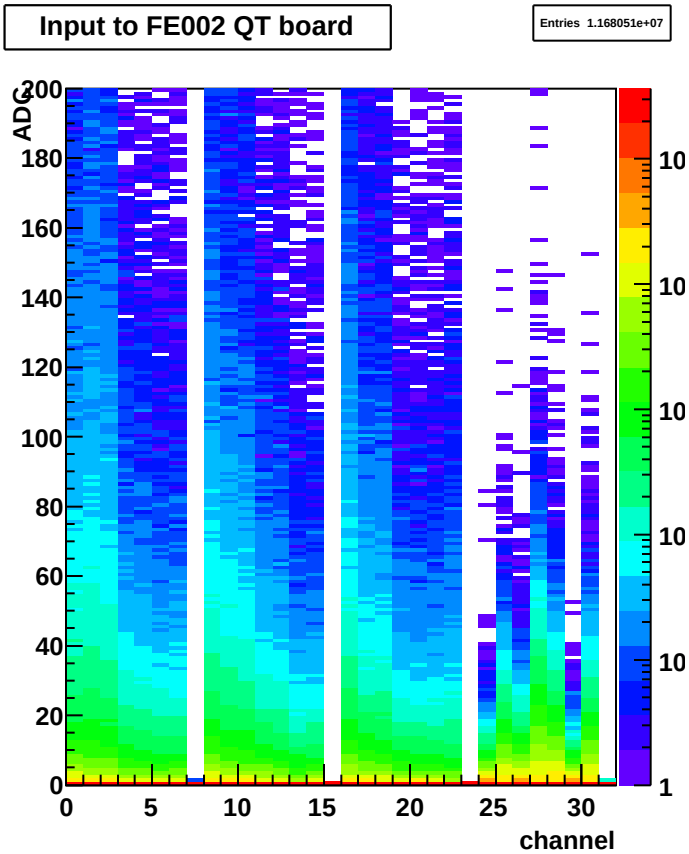

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

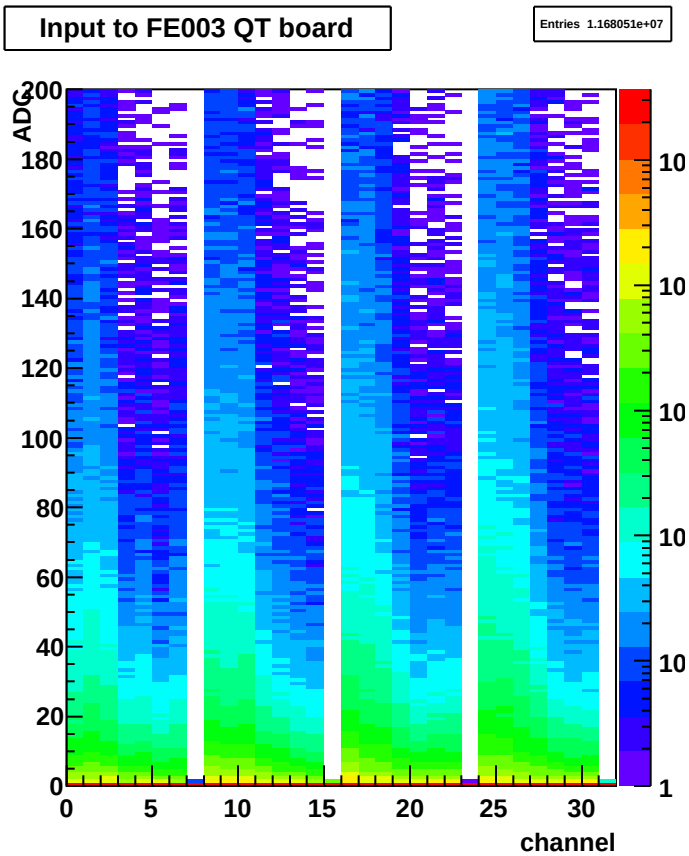

Input to FPD L3 DSM
Entries 5840256
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

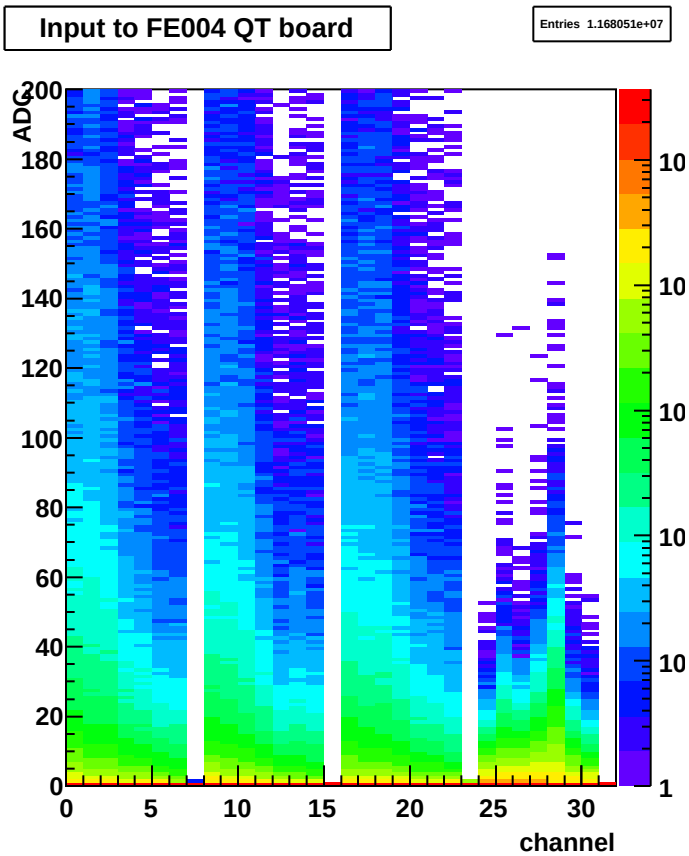

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

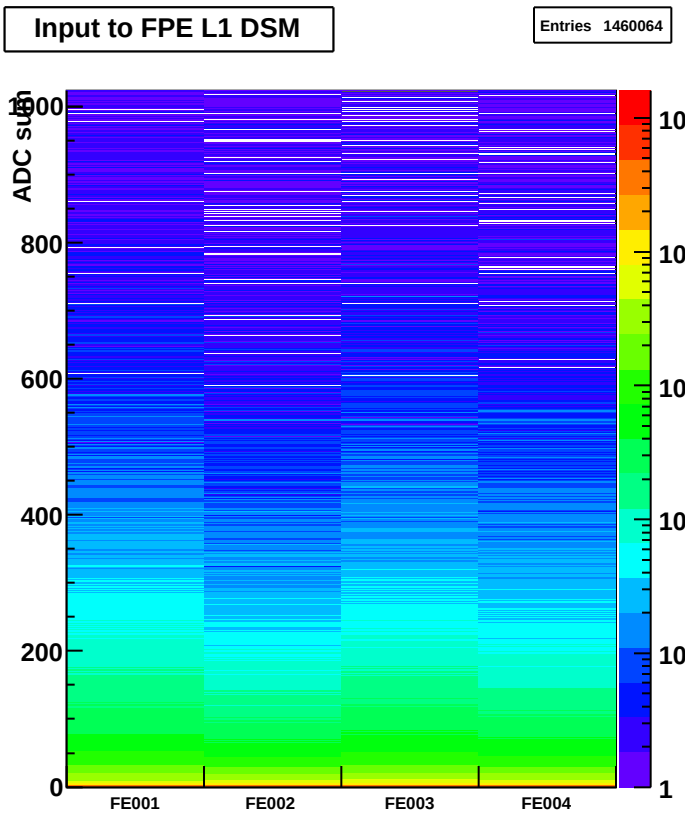

Input to FPD L2 DSM
Entries 1458360
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

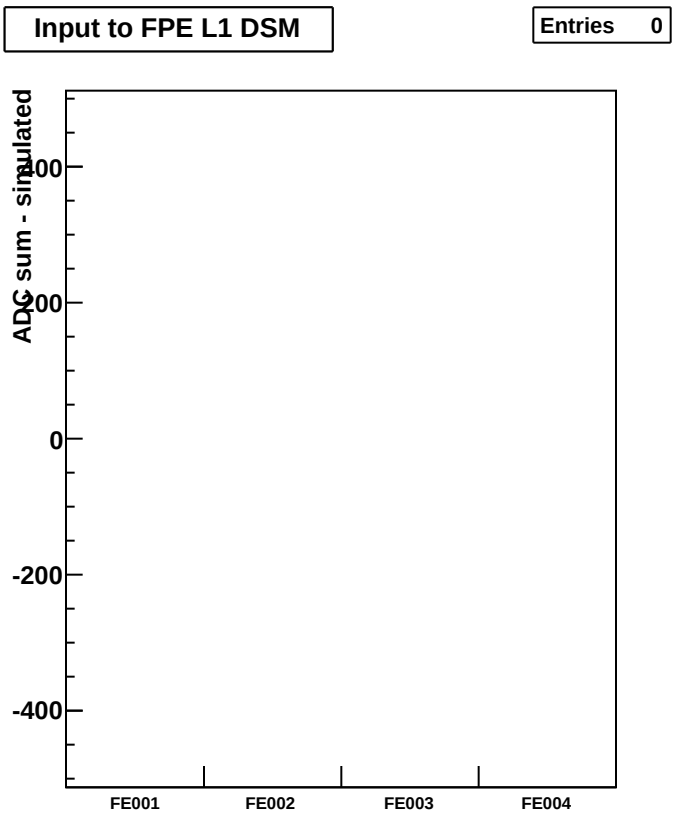

Input to FE001 QT board
Entries 1.168051e+07
ADC
channel

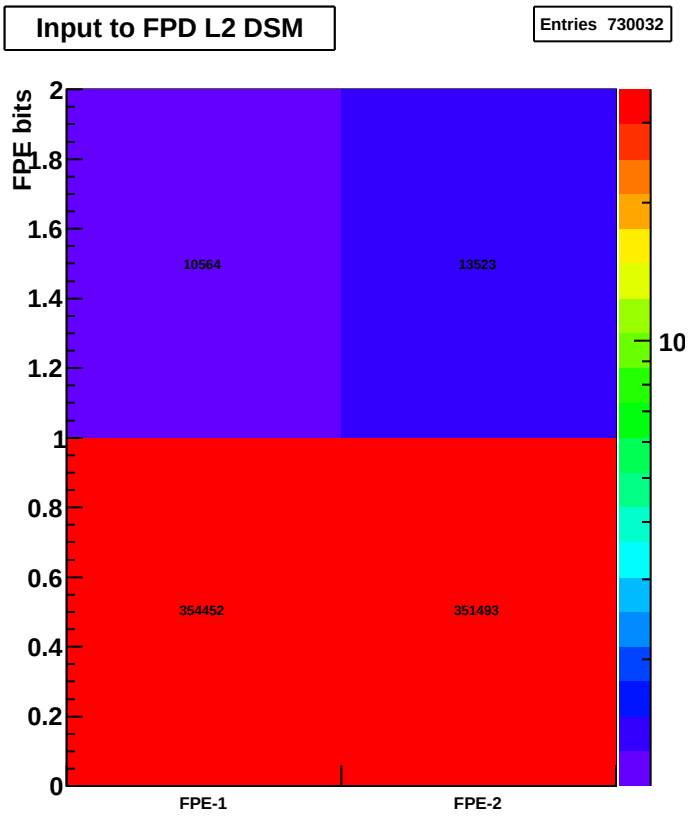

Input to FE002 QT board
Entries 1.168051e+07
ADC
channel

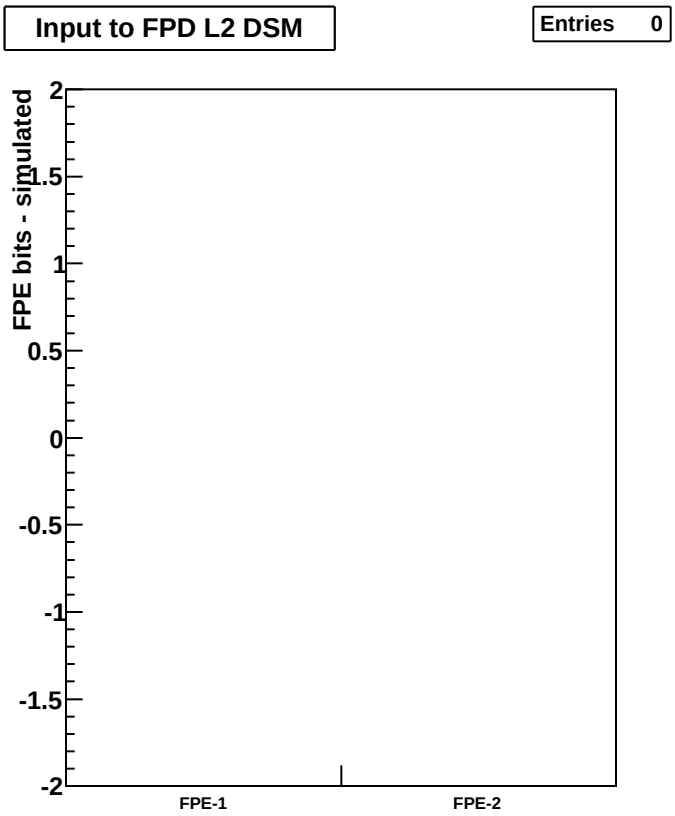

Input to FE003 QT board
Entries 1.168051e+07
ADC
channel


Input to FE004 QT board
Entries 1.168051e+07
ADC
channel


Input to FPE L1 DSM
Entries 1460064
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 730032
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
10564
13523
354452
351493
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2