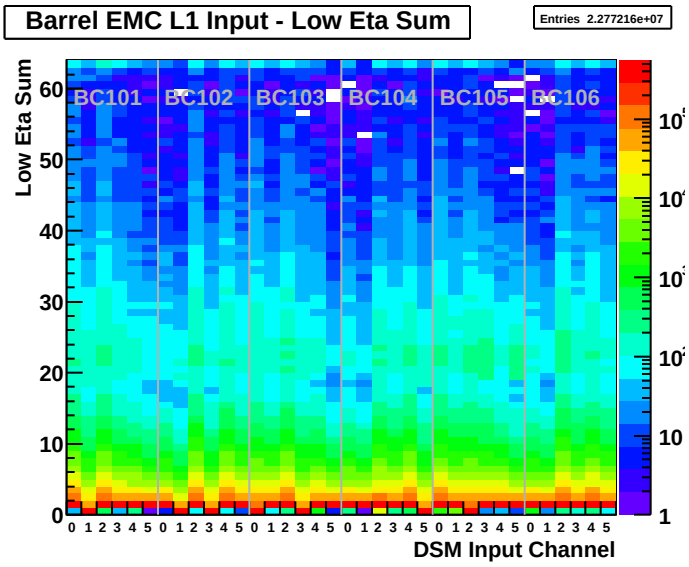
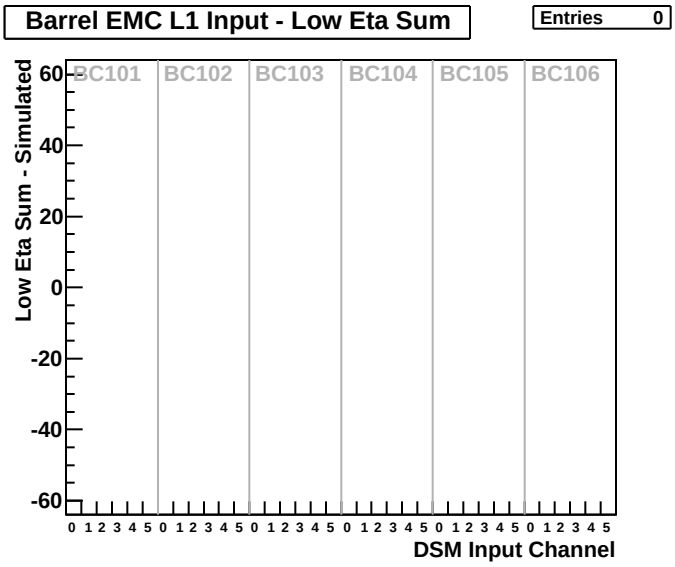
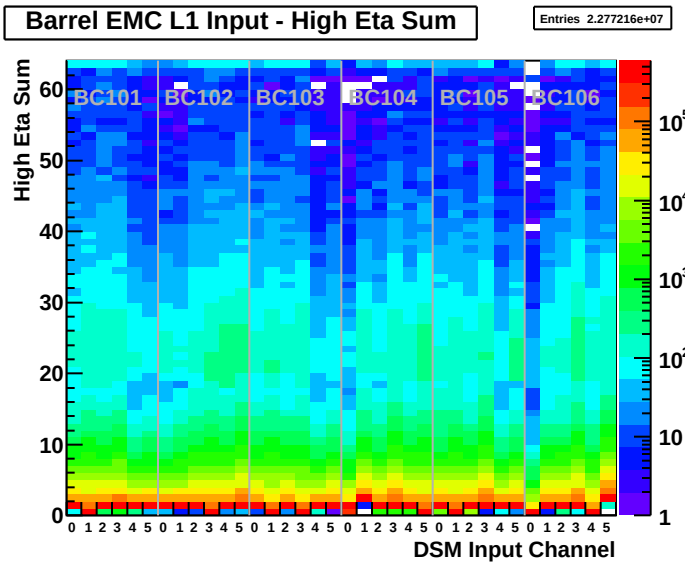
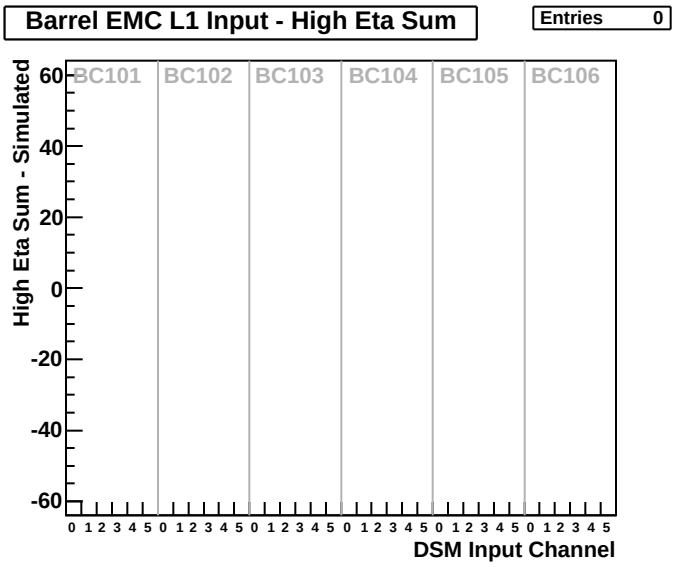
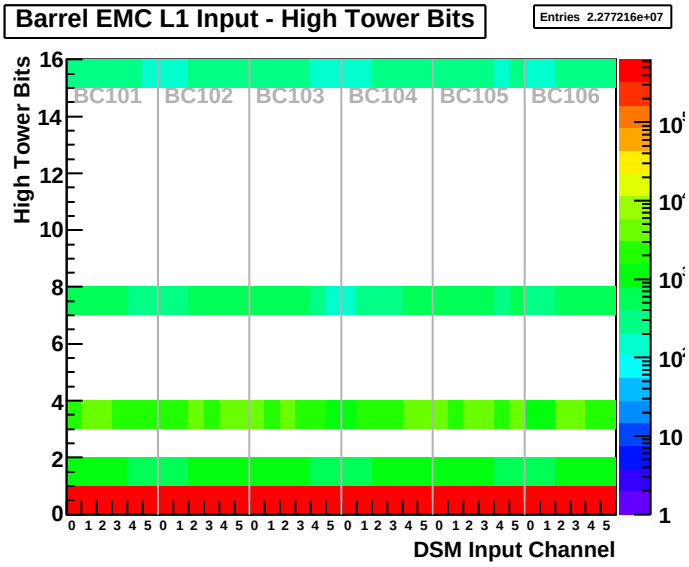
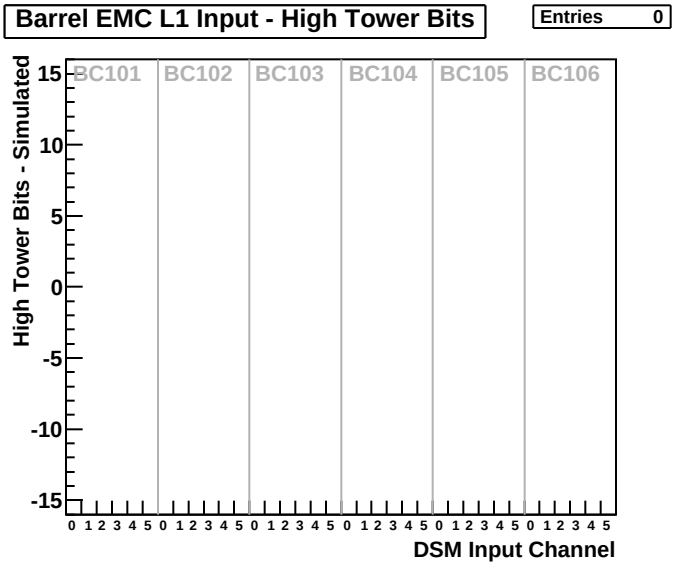

Barrel EMC L1 Input - Low Eta Sum
Entries 2.277216e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

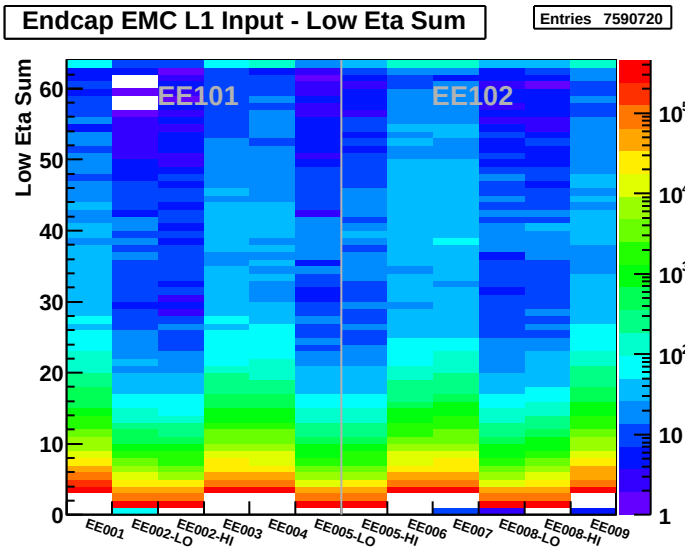

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

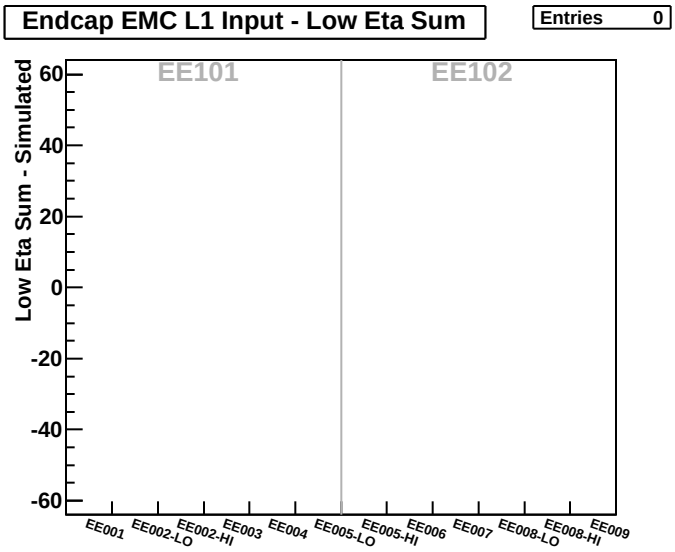

Barrel EMC L1 Input - High Eta Sum
Entries 2.277216e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

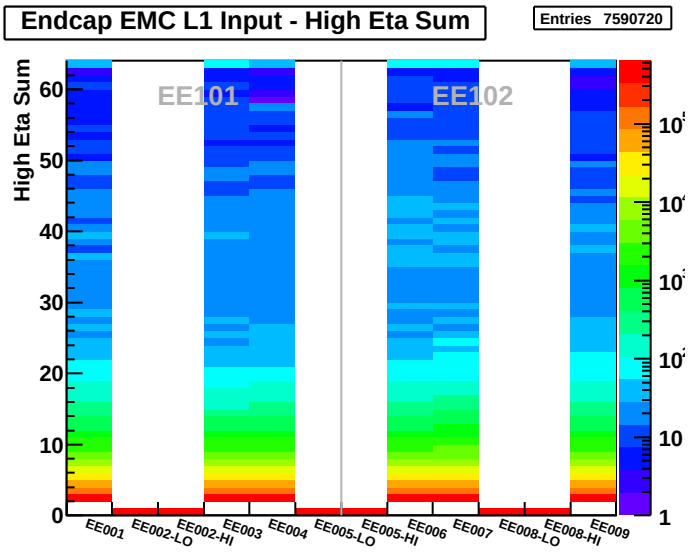

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

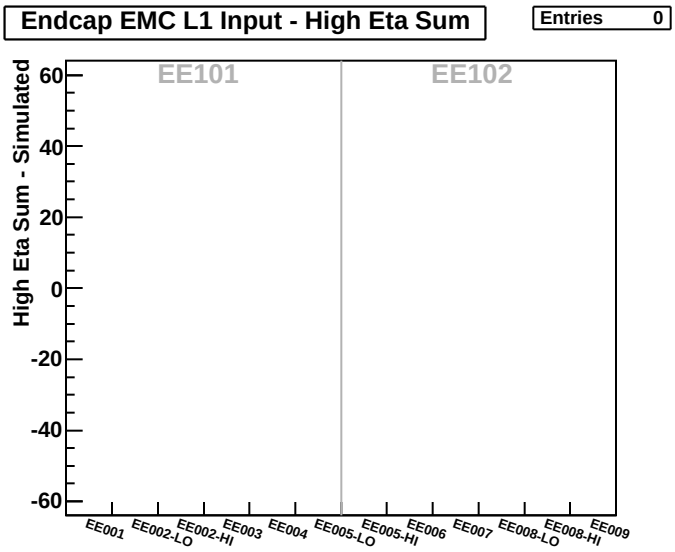

Barrel EMC L1 Input - High Tower Bits
Entries 2.277216e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

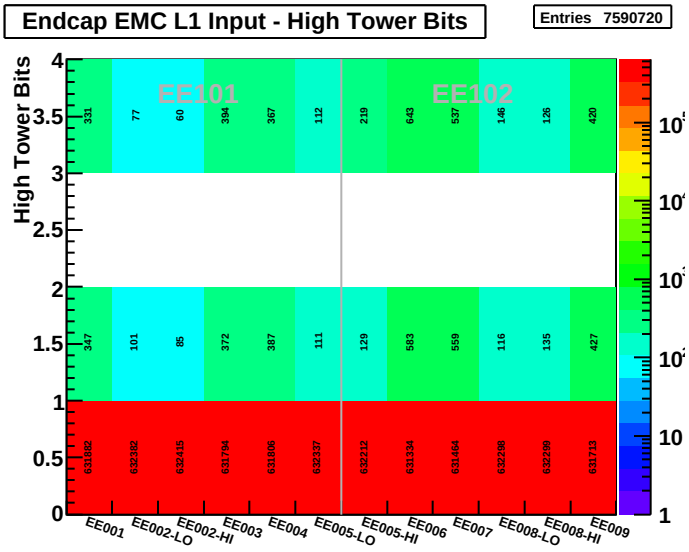

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

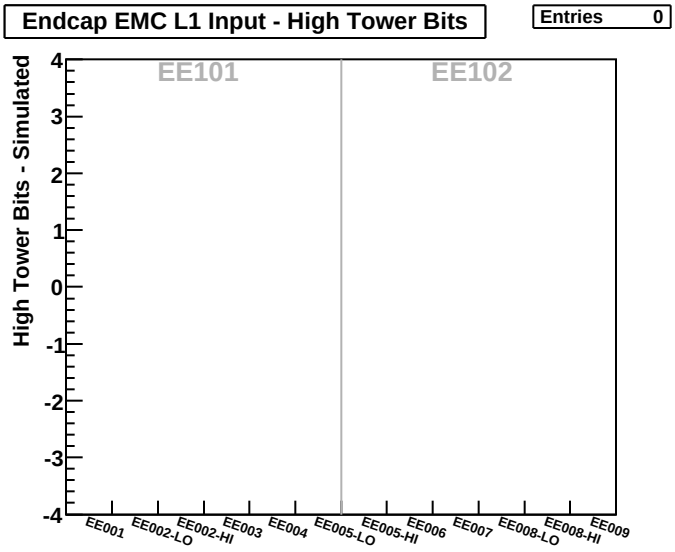

Endcap EMC L1 Input - Low Eta Sum
Entries 7590720
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

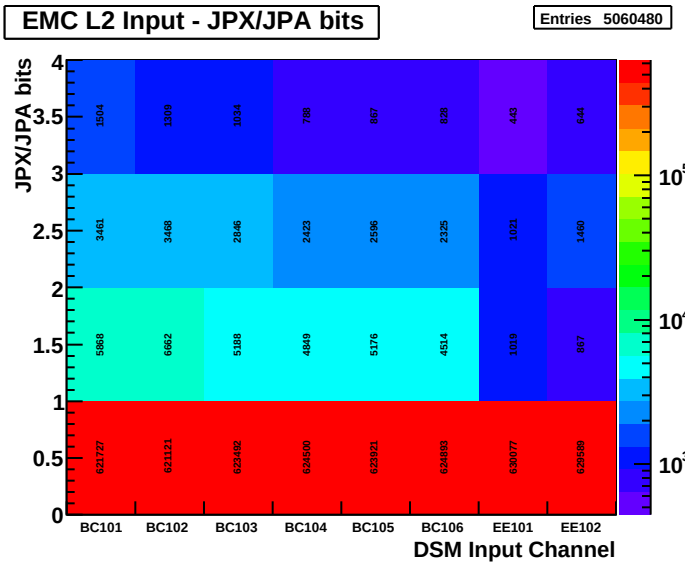

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

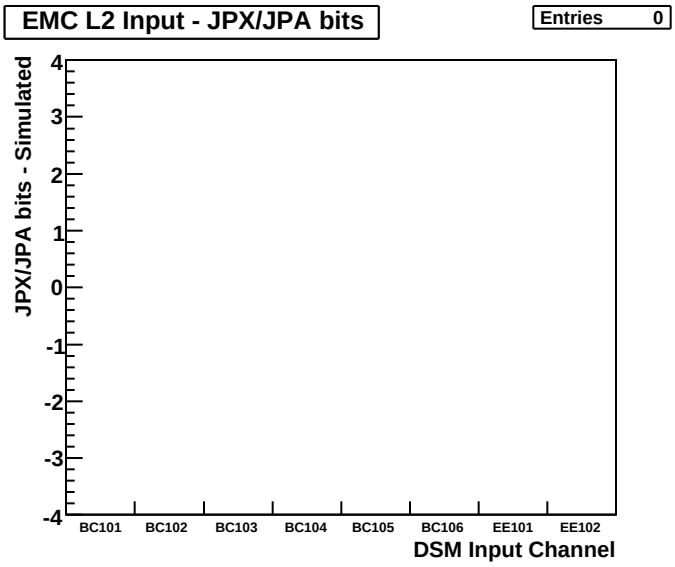

Endcap EMC L1 Input - High Eta Sum
Entries 7590720
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

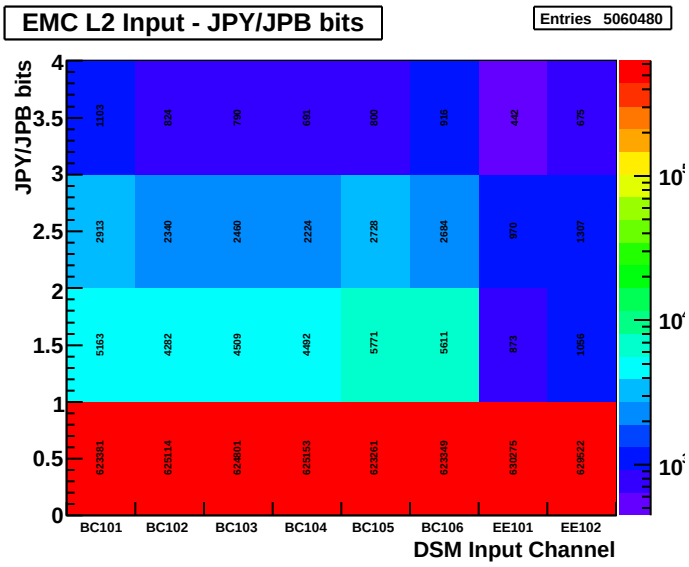

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

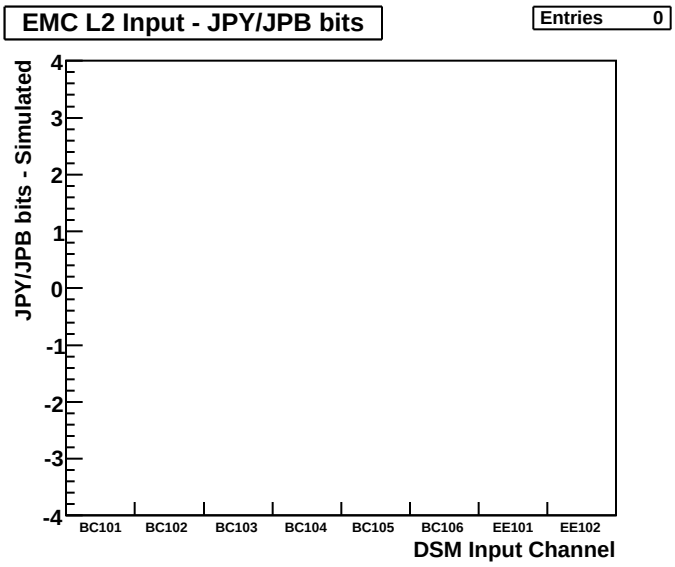

Endcap EMC L1 Input - High Tower Bits
Entries 7590720
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

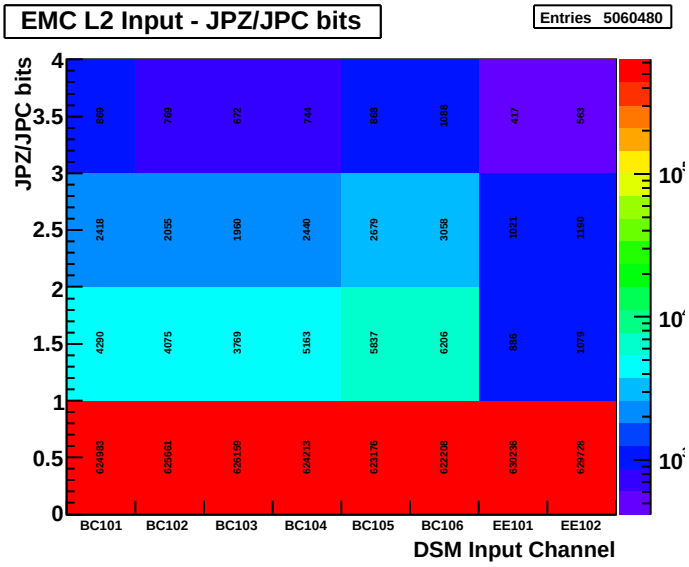

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

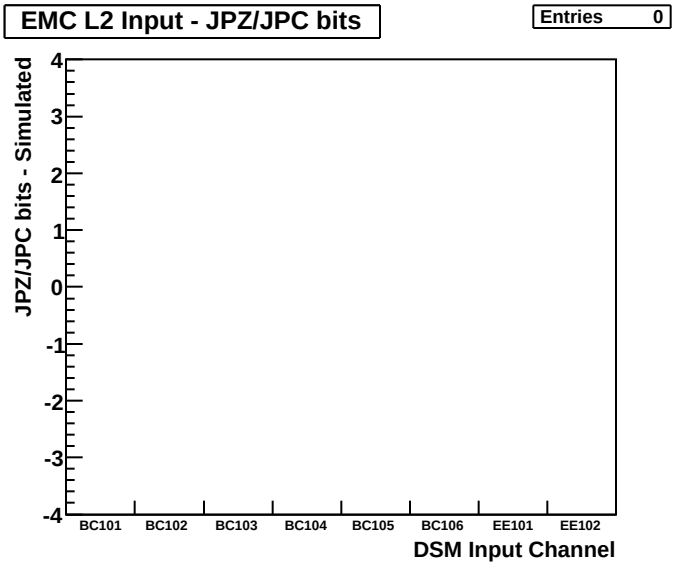

EMC L2 Input - JPX/JPA bits
Entries 5060480
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1504 1309 1034 788 867 828 443 644
3461 3468 2846 2423 2596 2325 1021 1460
5868 6662 5188 4849 5176 4514 1019 867
621727 621121 622492 624500 623921 624893 630077 629589

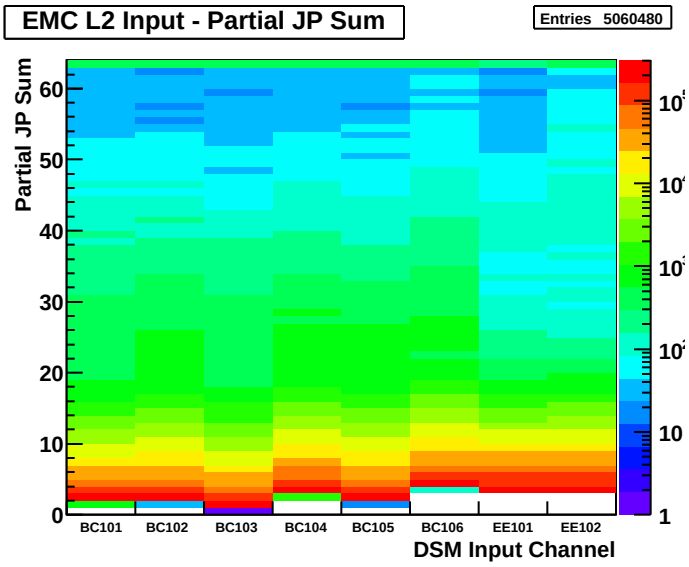

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

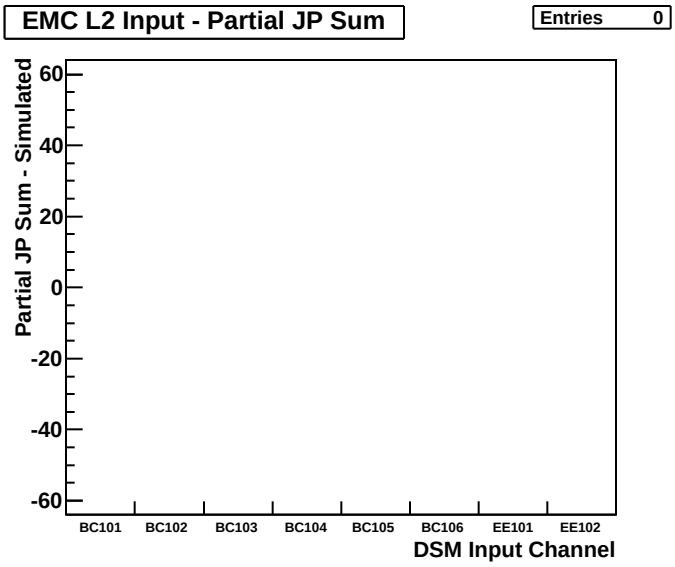

EMC L2 Input - JPY/JPB bits
Entries 5060480
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1103 824 790 691 800 916 442 675
2913 2340 2460 2224 2728 2684 970 1307
5163 4282 4509 4492 5771 5611 873 1056
623381 623114 624801 625153 623261 623349 630275 629522

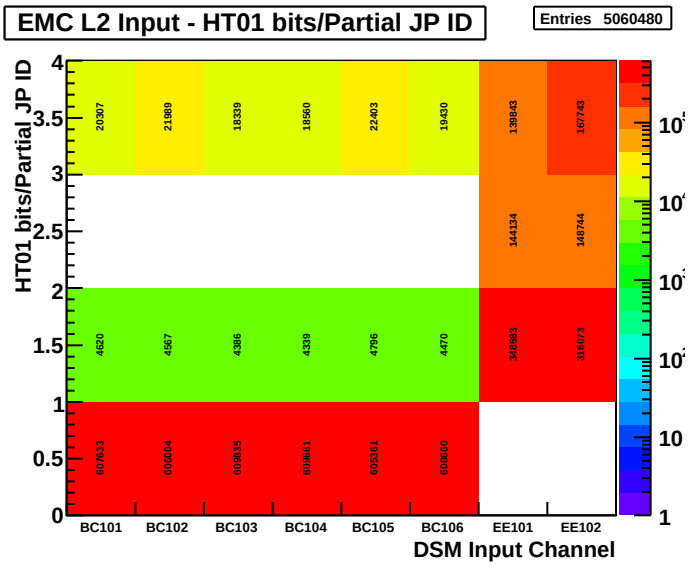

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

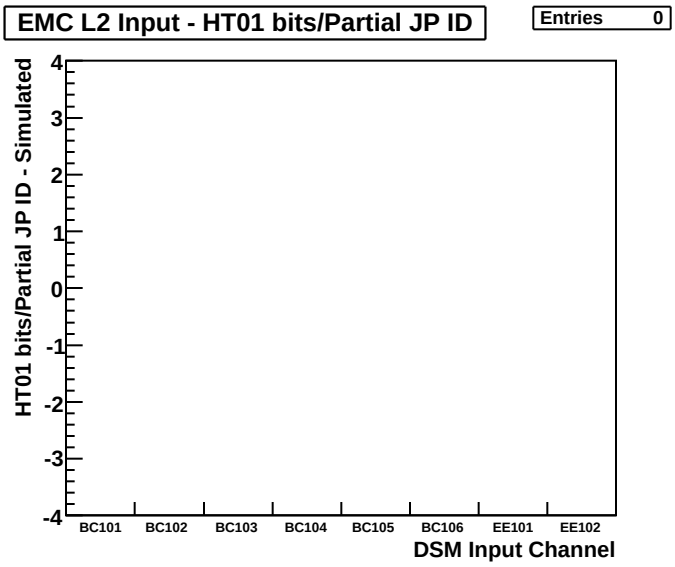

EMC L2 Input - JPZ/JPC bits
Entries 5060480
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
889 769 672 744 868 1088 417 563
2418 2055 1960 2440 2679 3058 1021 1190
4290 4075 3769 5163 5837 6206 866 1079
624983 625661 626159 624213 623176 622208 630236 629728

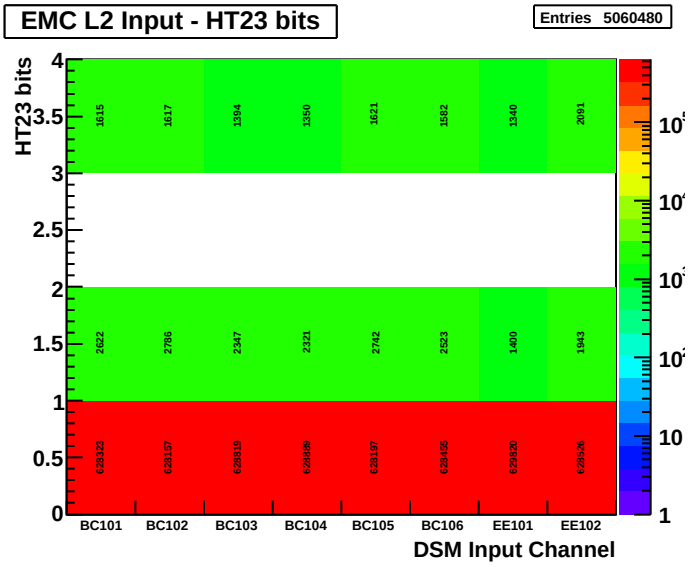

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

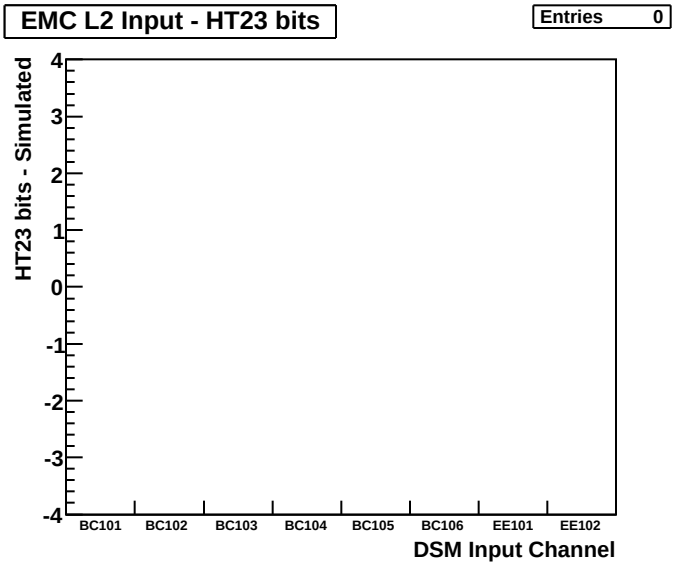

EMC L2 Input - Partial JP Sum
Entries 5060480
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

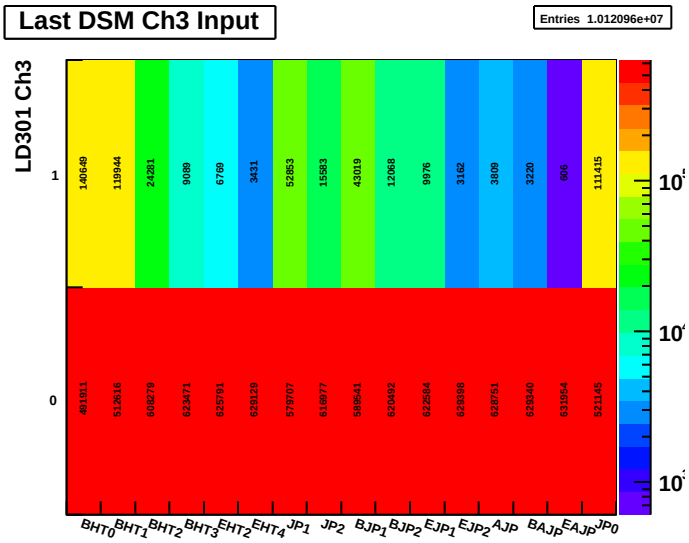

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

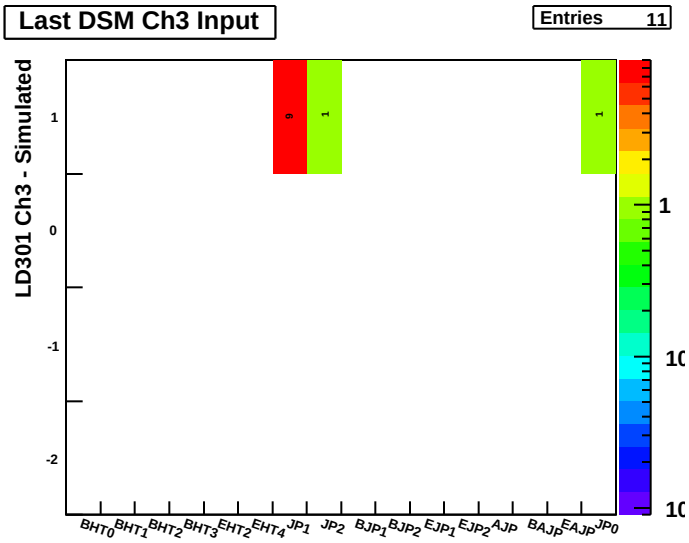

EMC L2 Input - HT01 bits/Partial JP ID
Entries 5060480
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

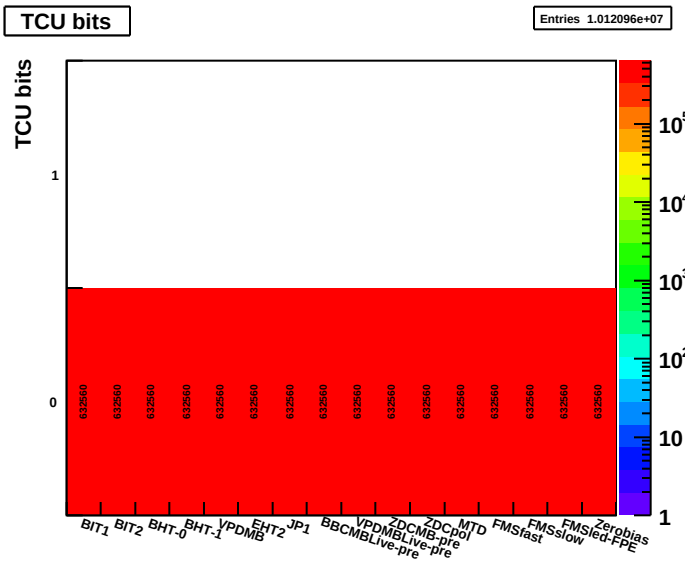

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

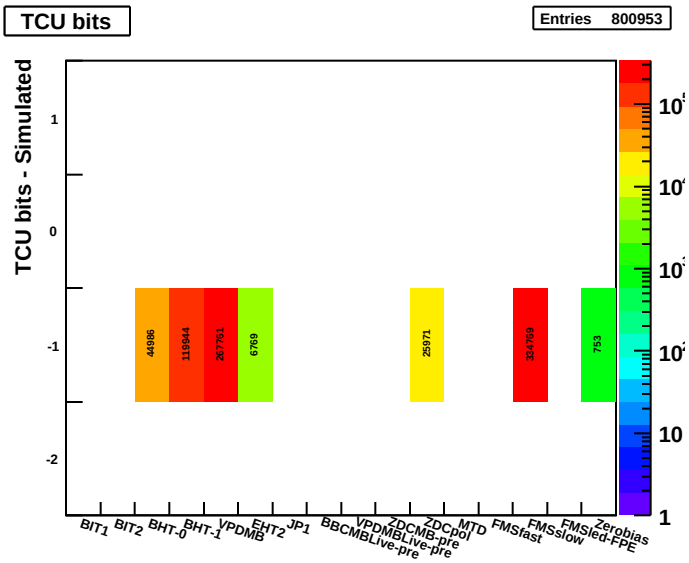

EMC L2 Input - HT23 bits
Entries 5060480
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

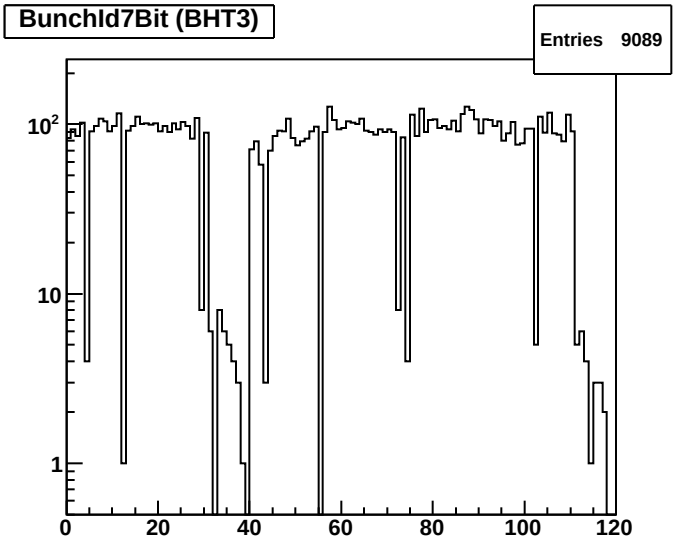

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

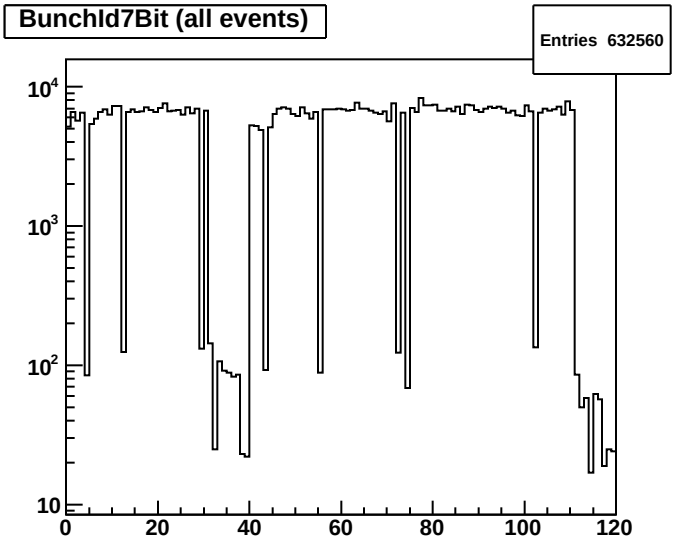

Last DSM Ch3 Input
Entries 1.012096e+07
LD301 Ch3
140649 119944 24281 9089 6769 3431 52853 15583 43019 12068 9976 3162 3809 3220 606 111415
491911 512616 608279 623471 625791 628129 579707 616977 589541 620482 622584 628398 628751 628340 631954 521145
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

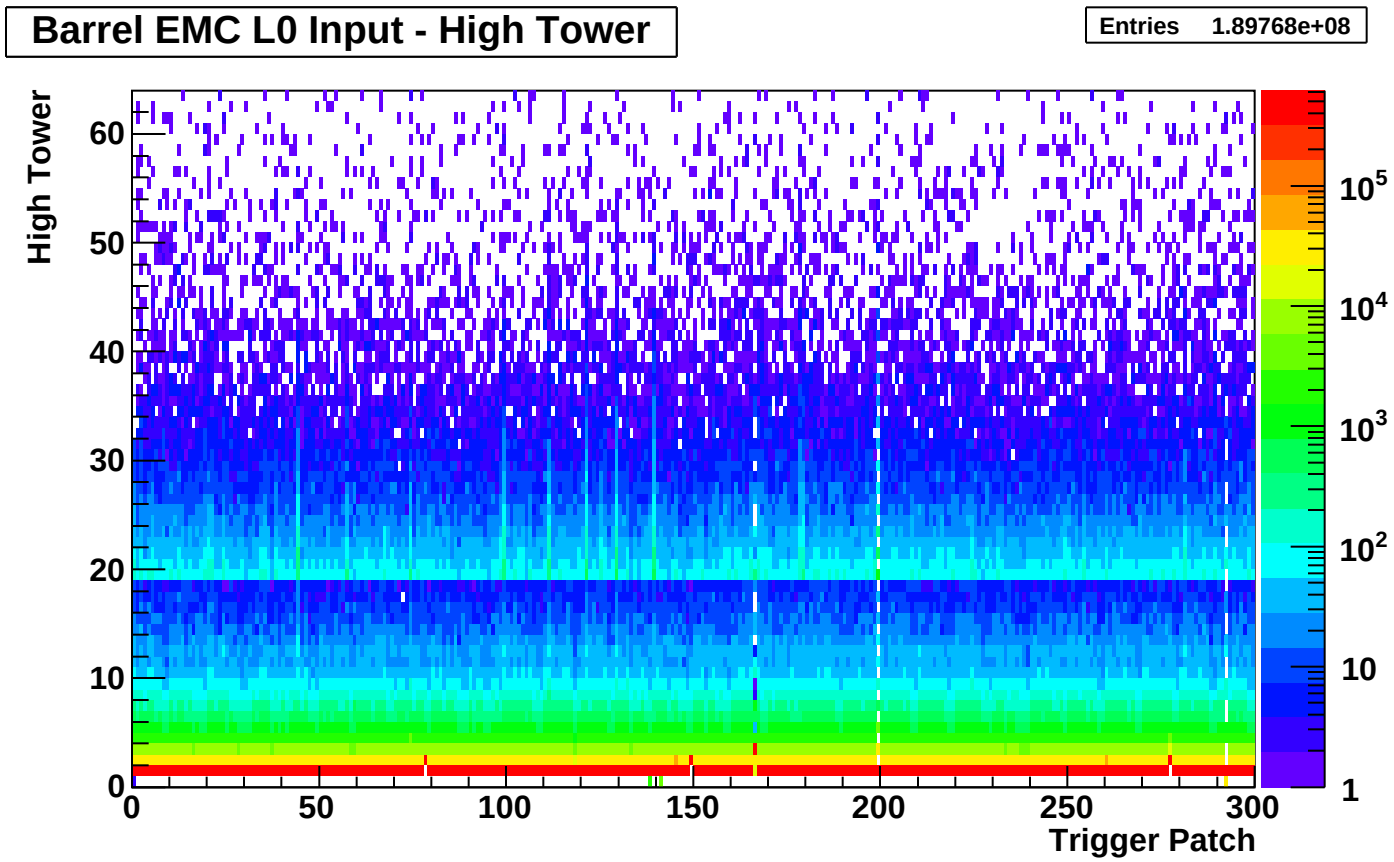

Last DSM Ch3 Input
Entries 11
LD301 Ch3 - Simulated
9 1 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

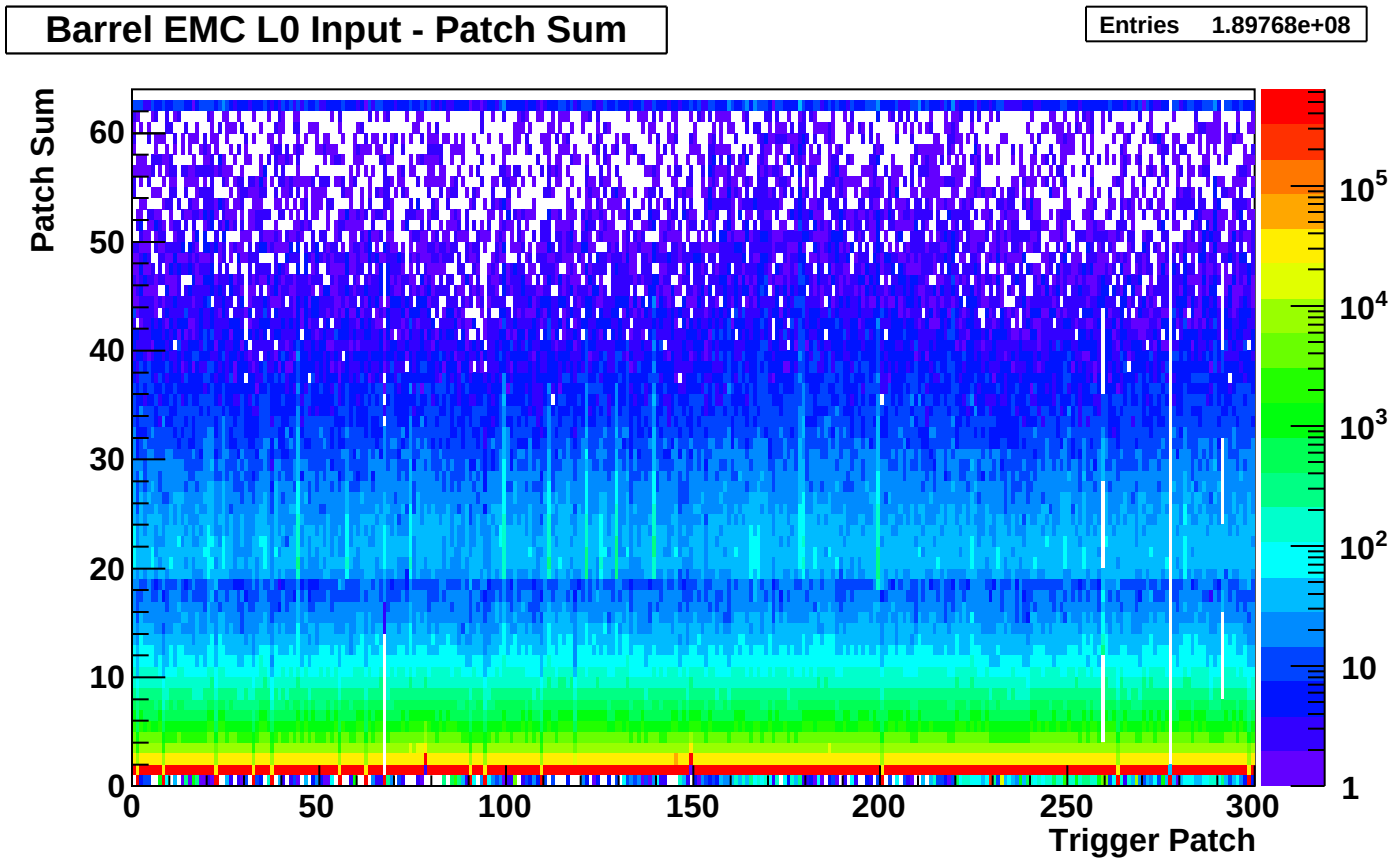

TCU bits
Entries 1.012096e+07
632560
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

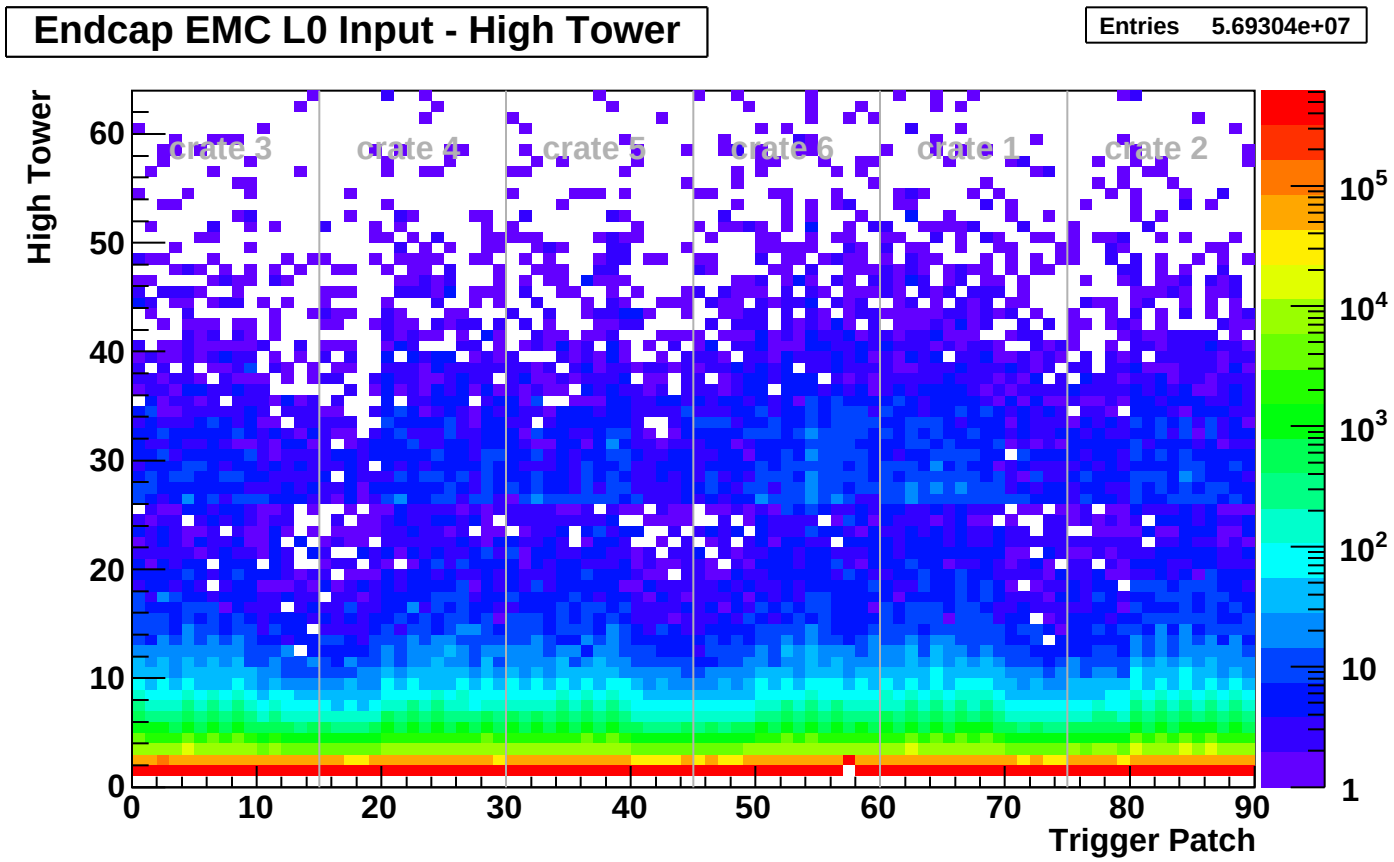

TCU bits
Entries 800953
TCU bits - Simulated
44986 119944 267761 6769 25971 334769 753
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

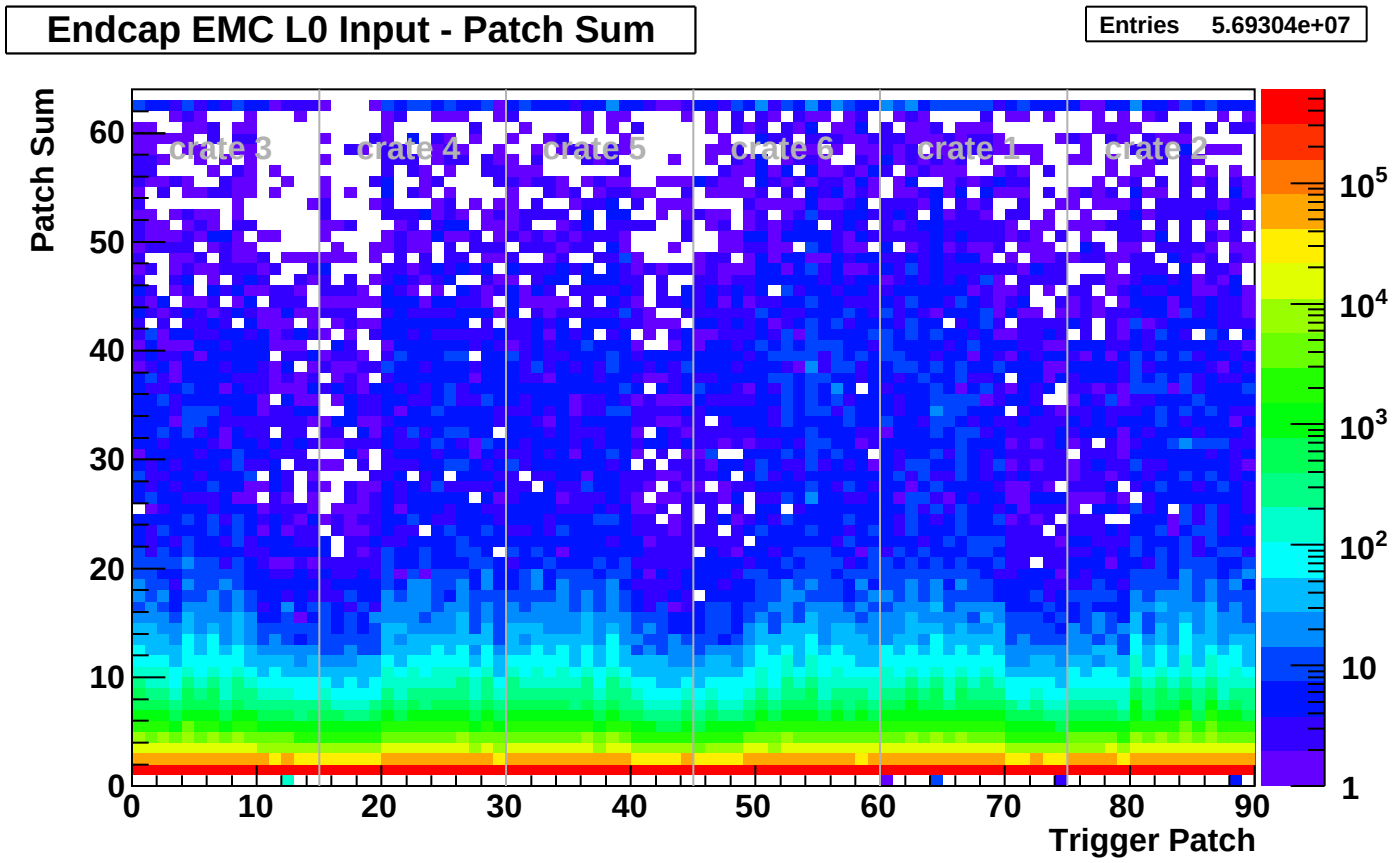

BunchId7Bit (BHT3)
Entries 9089

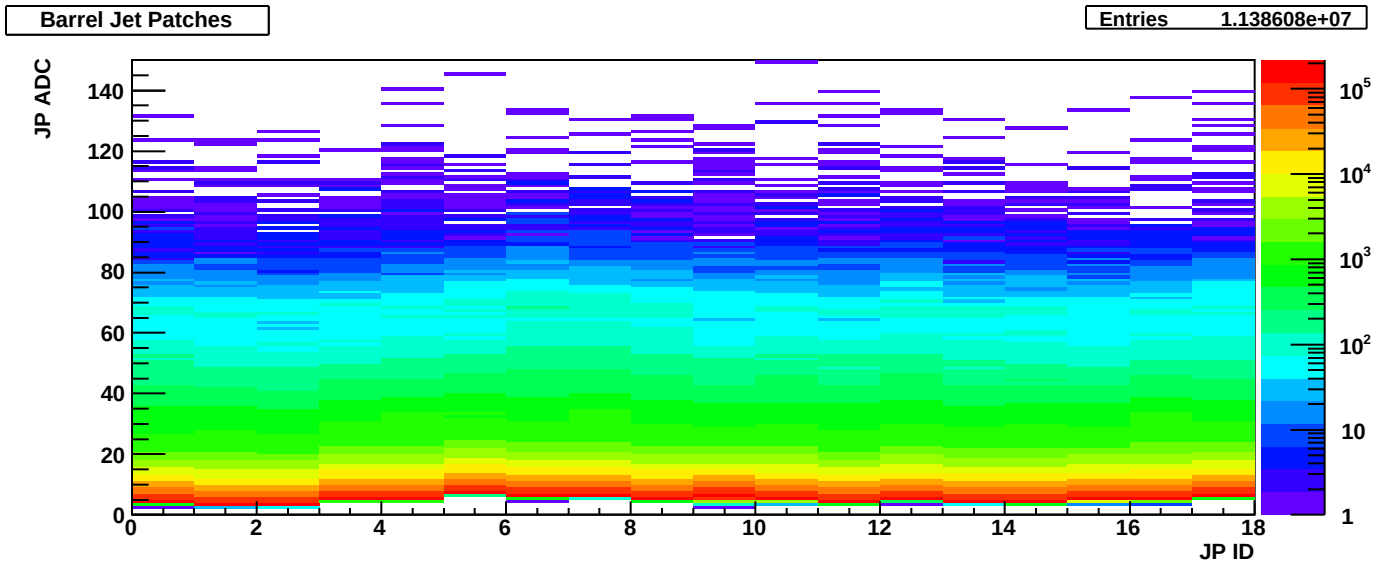

BunchId7Bit (all events)
Entries 632560

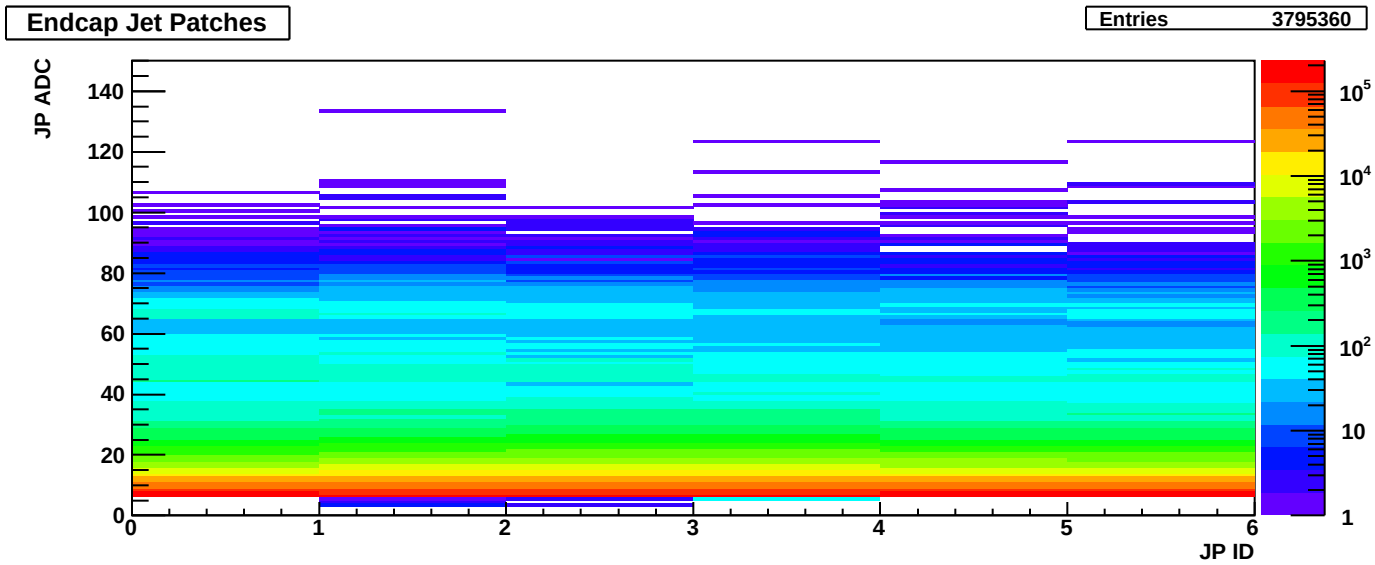

Barrel EMC L0 Input - High Tower
Entries 1.89768e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

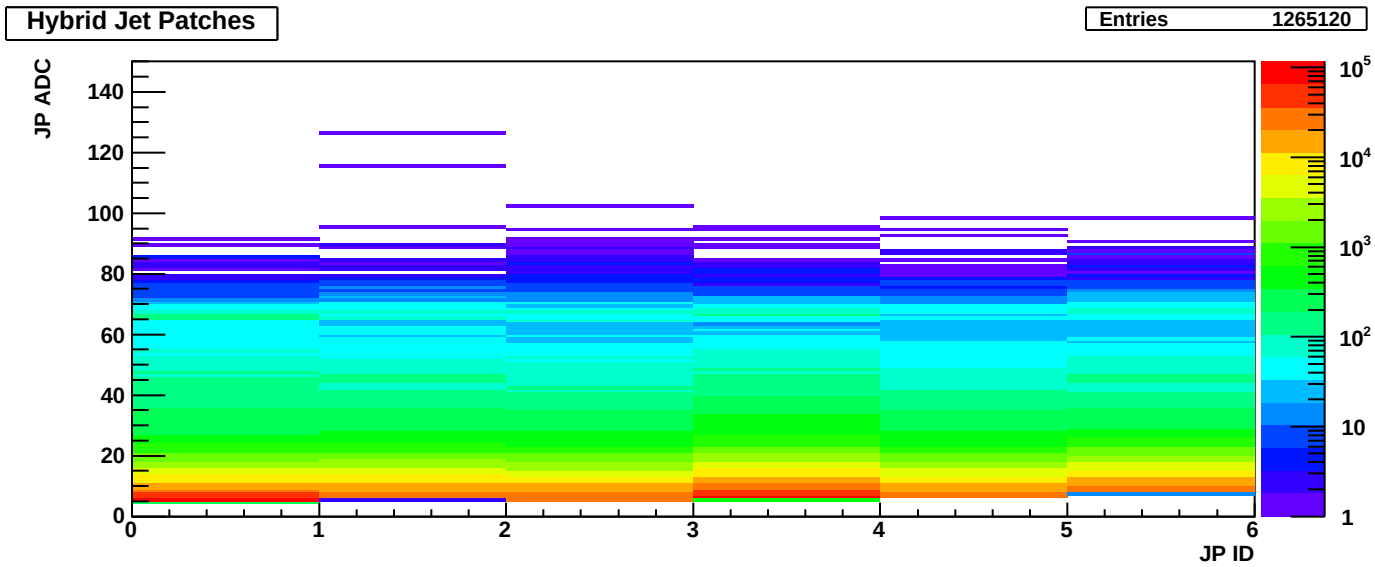

Barrel EMC L0 Input - Patch Sum
Entries 1.89768e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

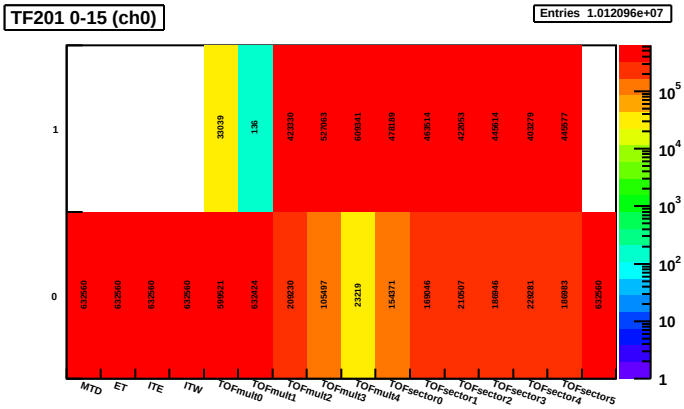

Endcap EMC L0 Input - High Tower
Entries 5.69304e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

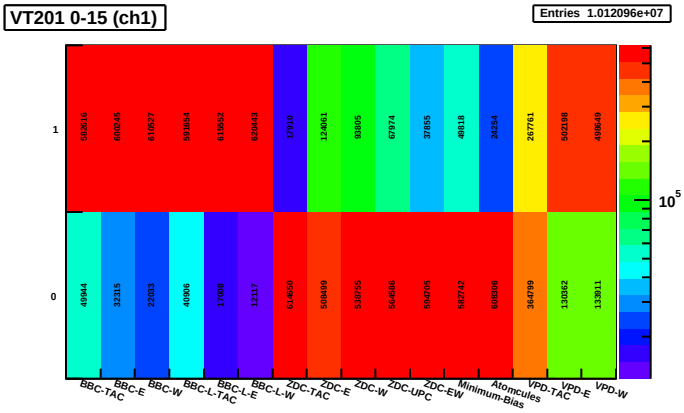

Endcap EMC L0 Input - Patch Sum
Entries 5.69304e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

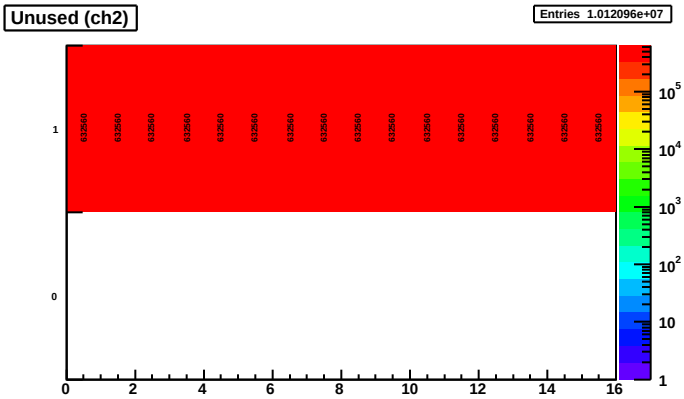

Barrel Jet Patches
Entries 1.138608e+07
JP ADC
JP ID

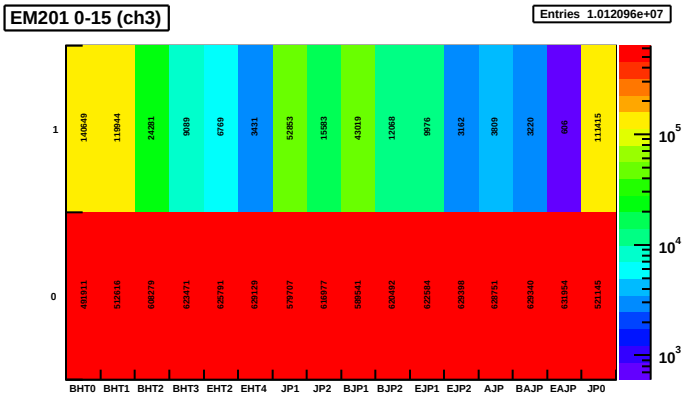

Endcap Jet Patches
Entries 3795360
JP ADC
JP ID

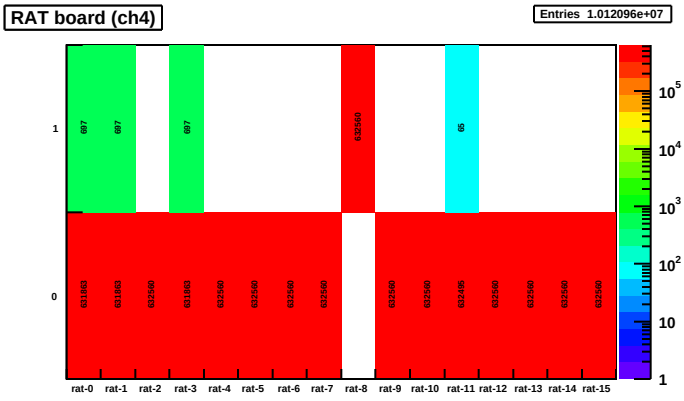

Hybrid Jet Patches
Entries 1265120
JP ADC
JP ID

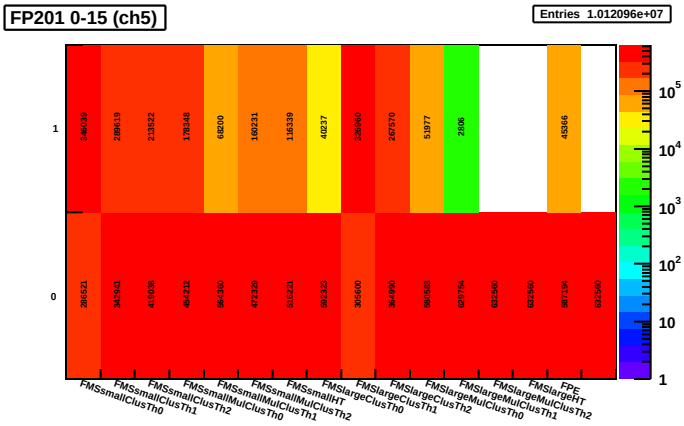

TF201 0-15 (ch0)
Entries 1.012096e+07
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

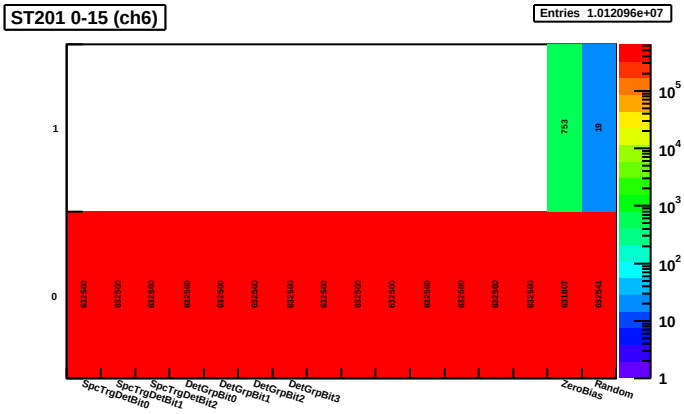

VT201 0-15 (ch1)
Entries 1.012096e+07
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

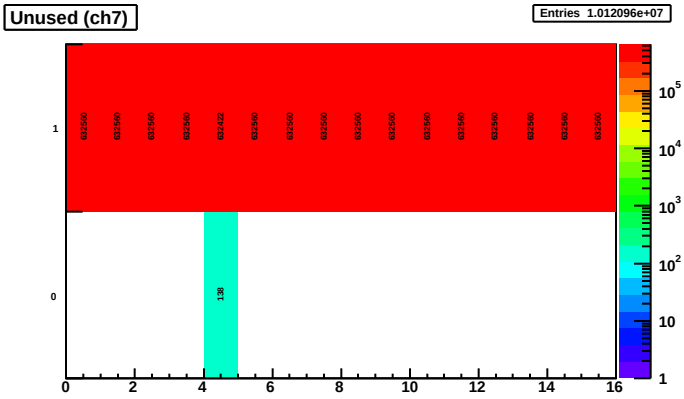

Unused (ch2)
Entries 1.012096e+07

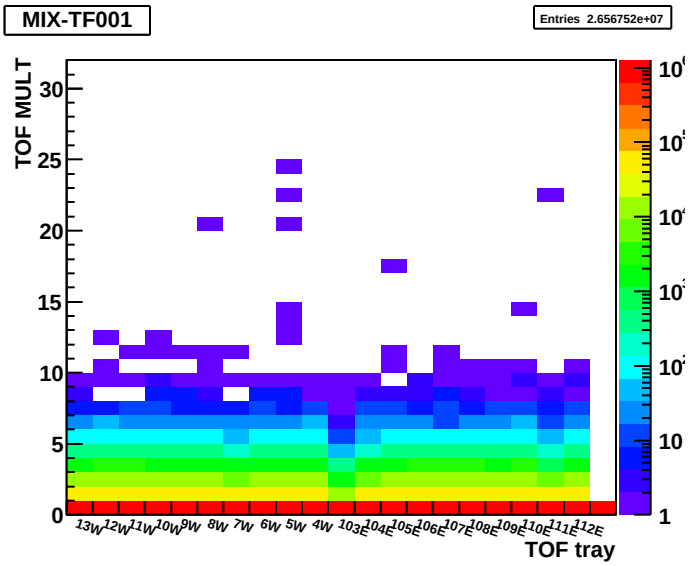

EM201 0-15 (ch3)
Entries 1.012096e+07
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

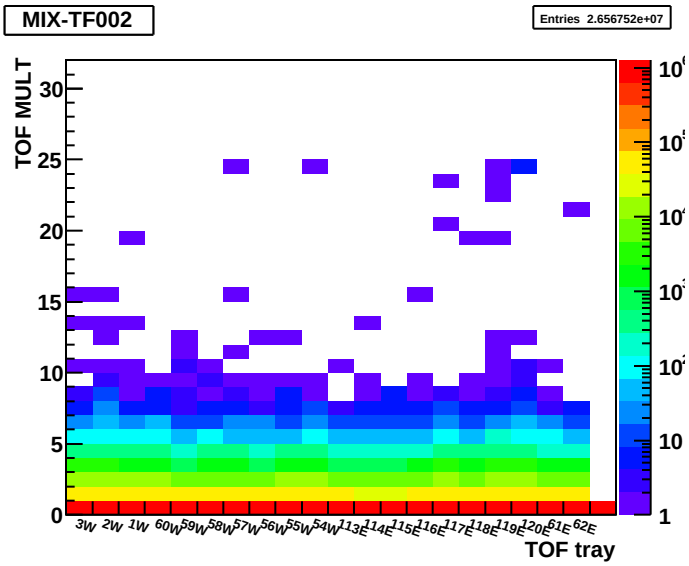

RAT board (ch4)
Entries 1.012096e+07
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

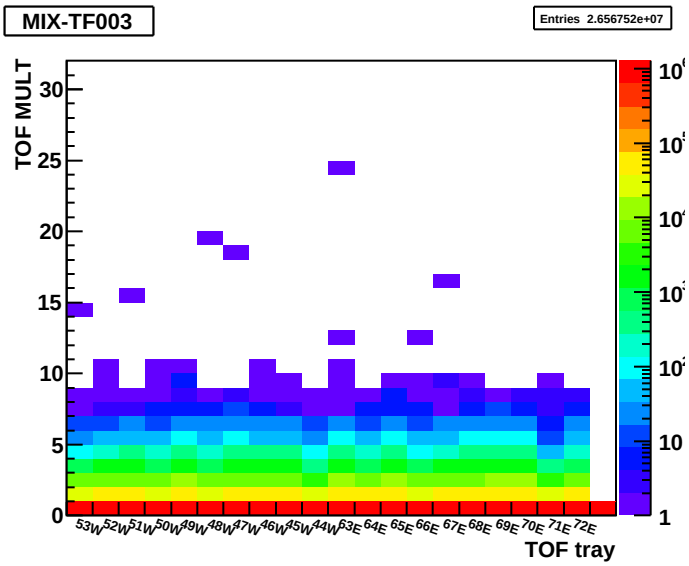

FP201 0-15 (ch5)
Entries 1.012096e+07
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

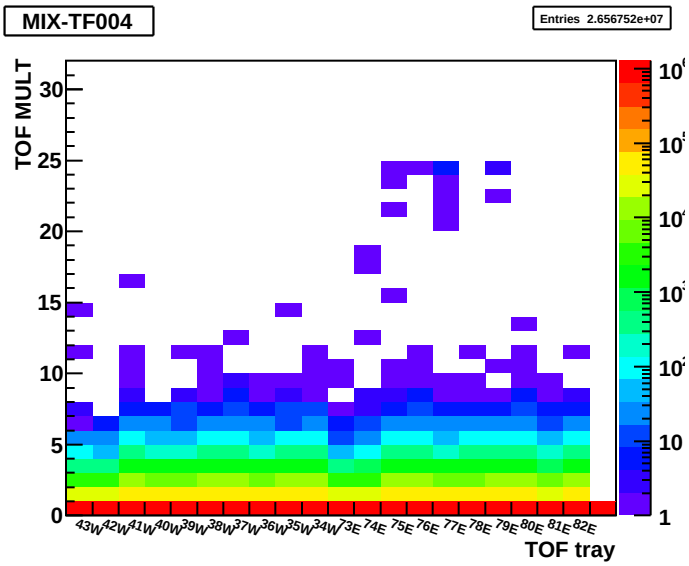

ST201 0-15 (ch6)
Entries 1.012096e+07
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

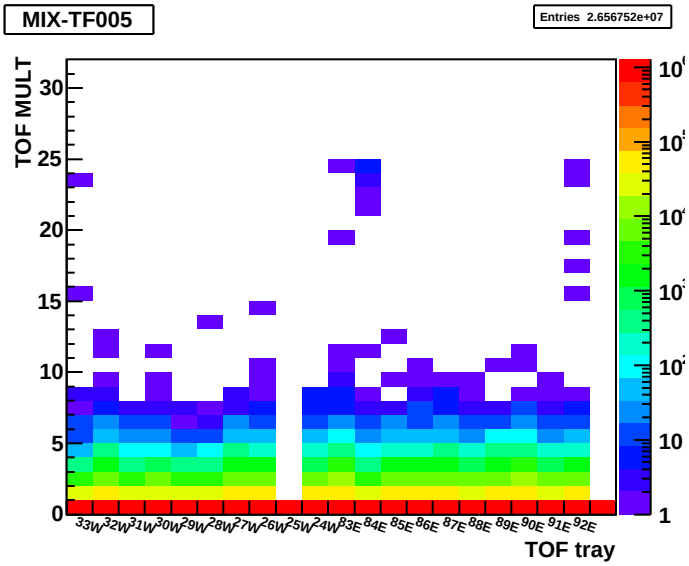

Unused (ch7)
Entries 1.012096e+07

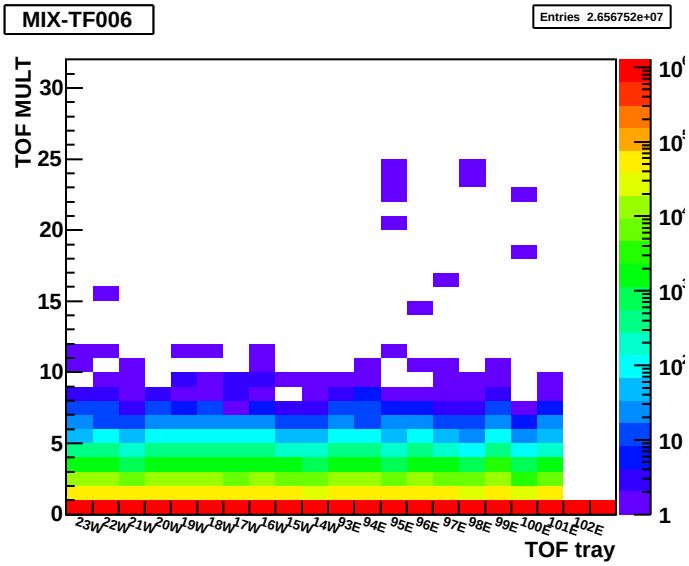

MIX-TF001
Entries 2.656752e+07
TOF MULT
TOF tray

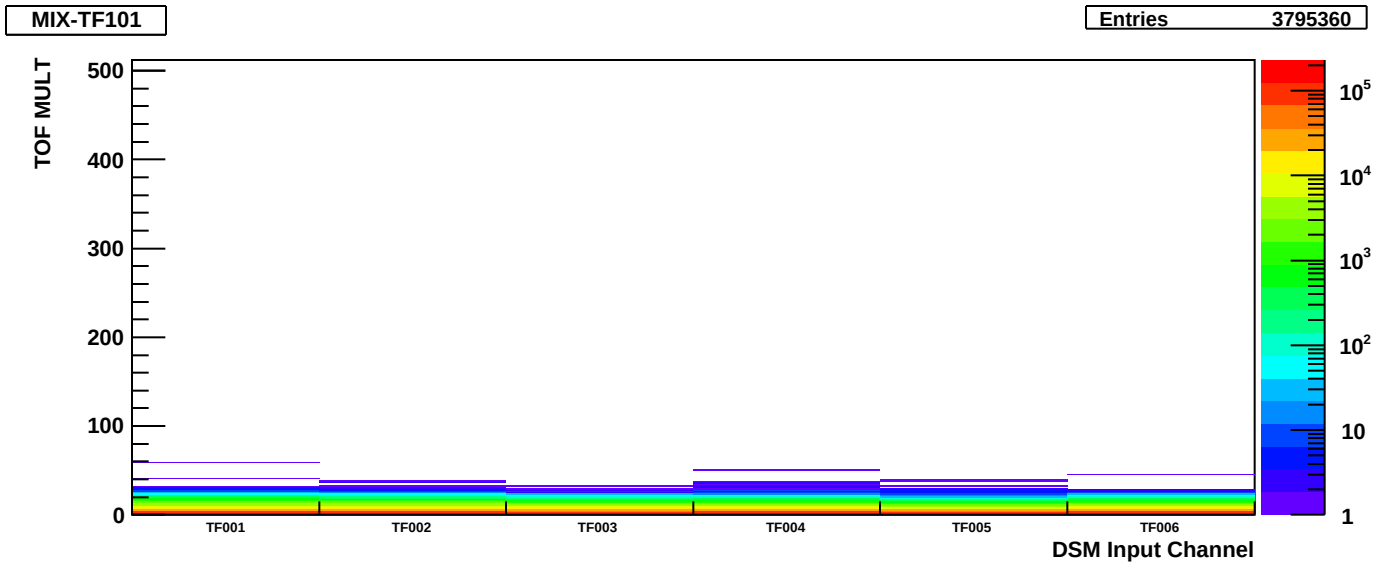

MIX-TF002
Entries 2.656752e+07
TOF MULT
TOF tray

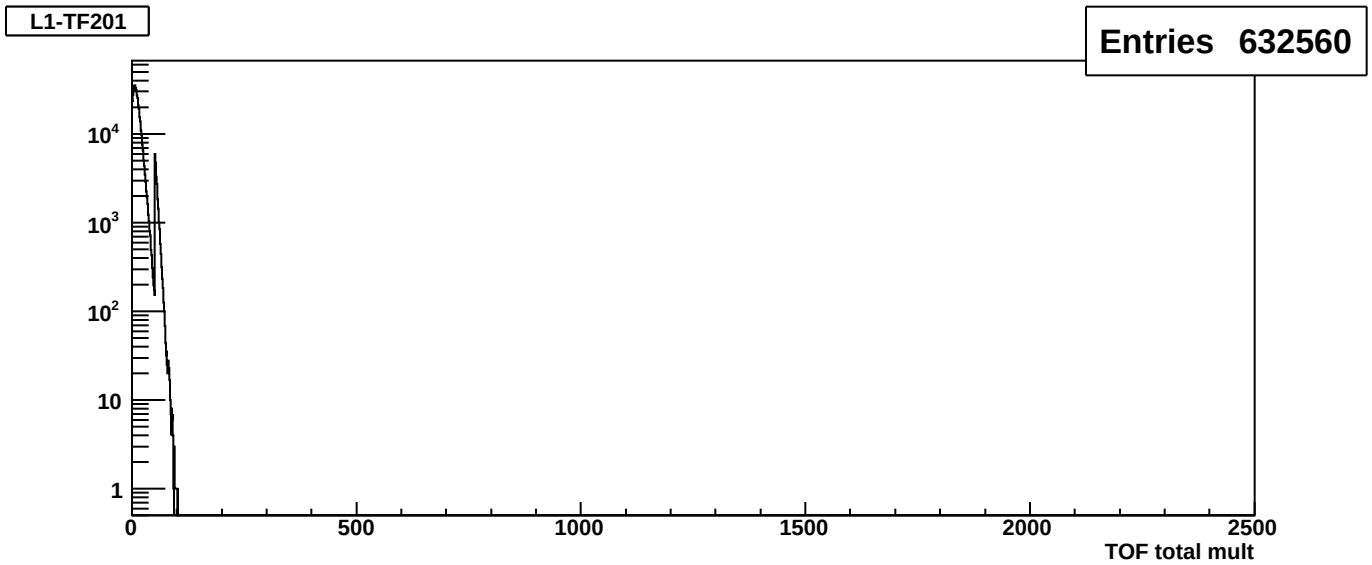

MIX-TF003
Entries 2.656752e+07
TOF MULT
TOF tray

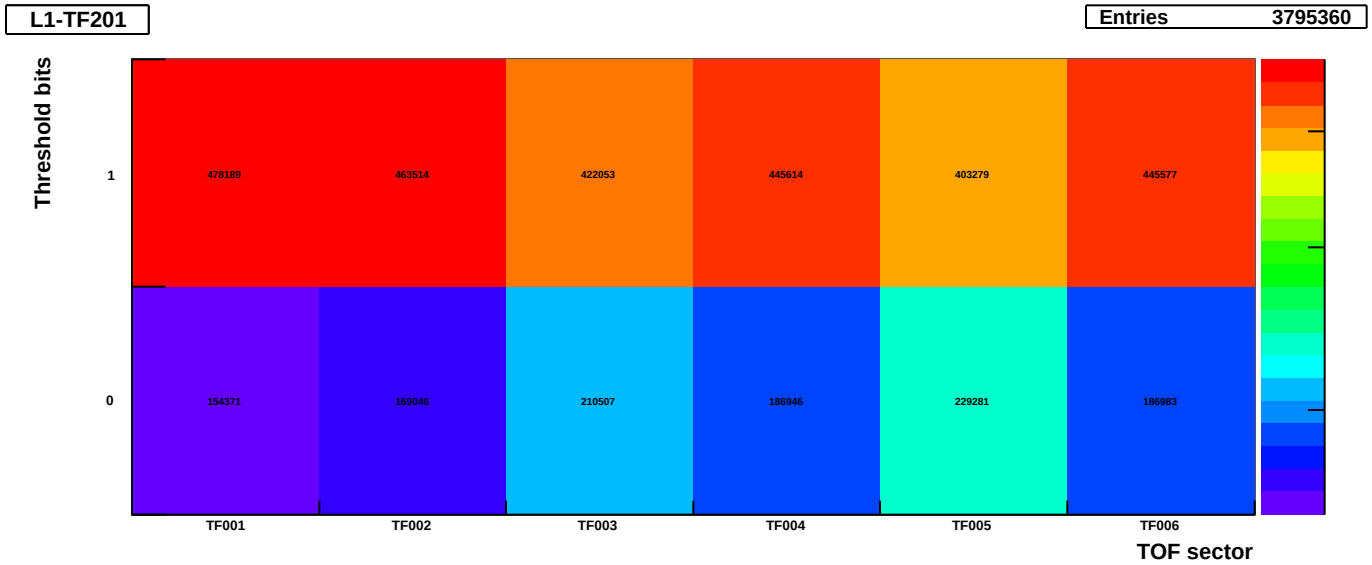

MIX-TF004
Entries 2.656752e+07
TOF MULT
TOF tray

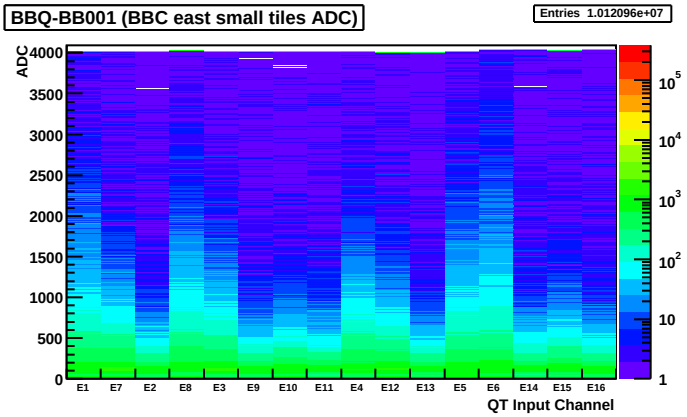

MIX-TF005
Entries 2.656752e+07
TOF MULT
TOF tray

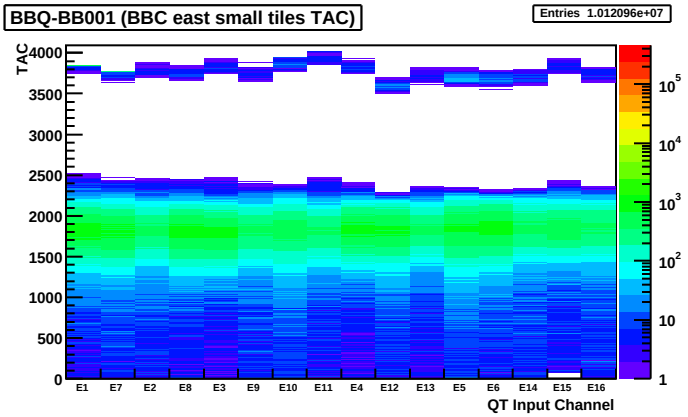

MIX-TF006
Entries 2.656752e+07
TOF MULT
TOF tray

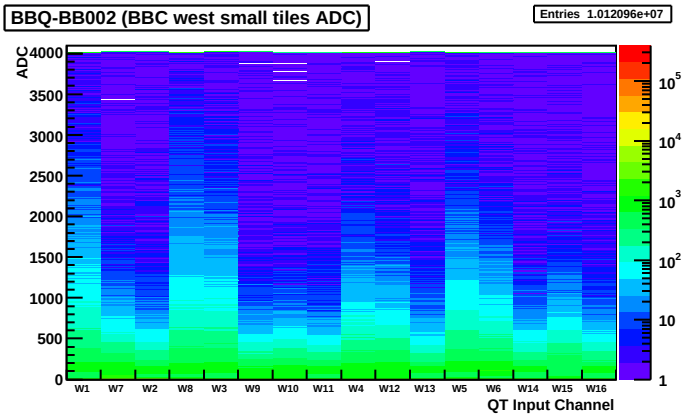

MIX-TF101
Entries 3795360
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

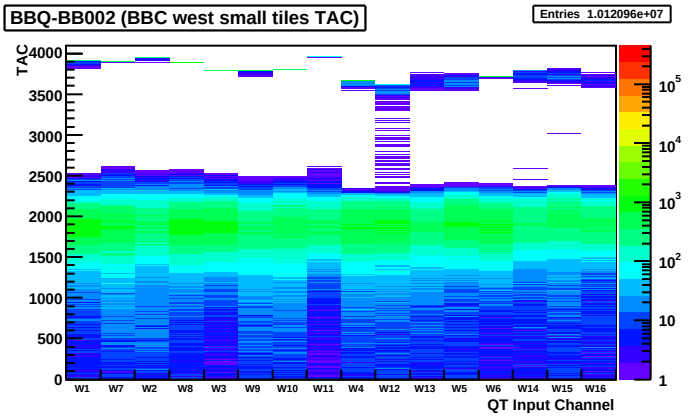

L1-TF201
Entries 632560
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

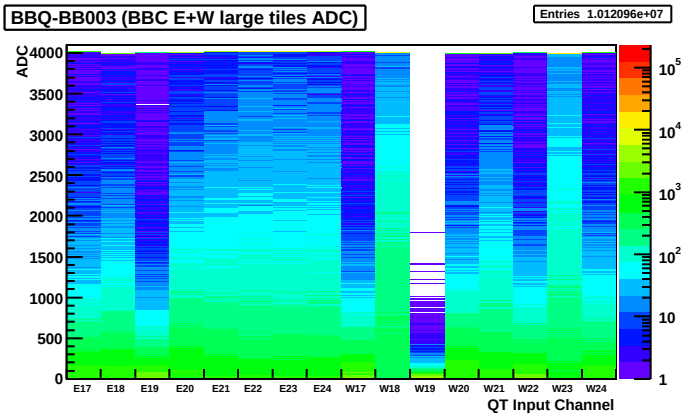

L1-TF201
Entries 3795360
Threshold bits
1
0
478189
463514
422053
445614
403279
445577
154371
169046
210507
186946
229281
186983
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

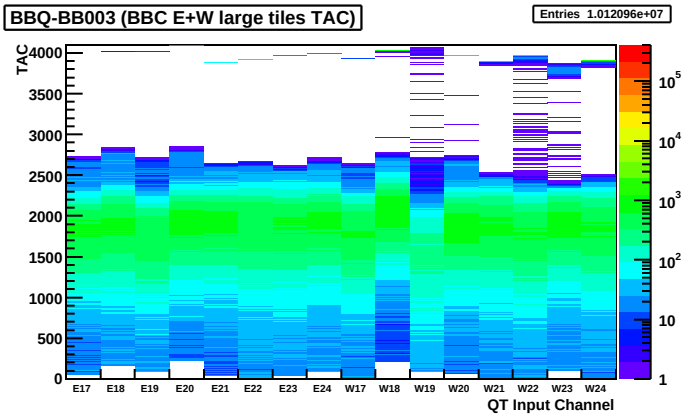

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.012096e+07
ADC
QT Input Channel

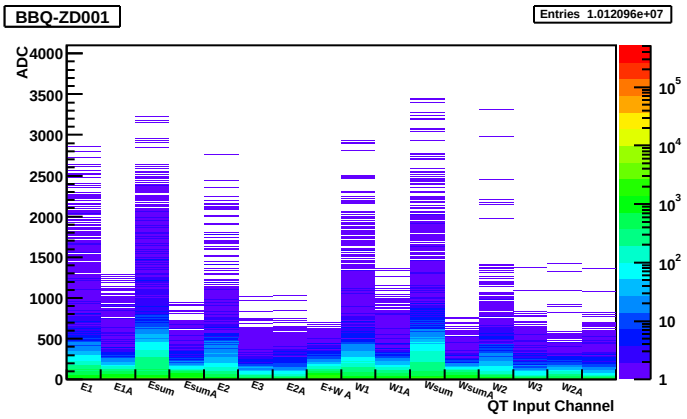

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.012096e+07
TAC
QT Input Channel

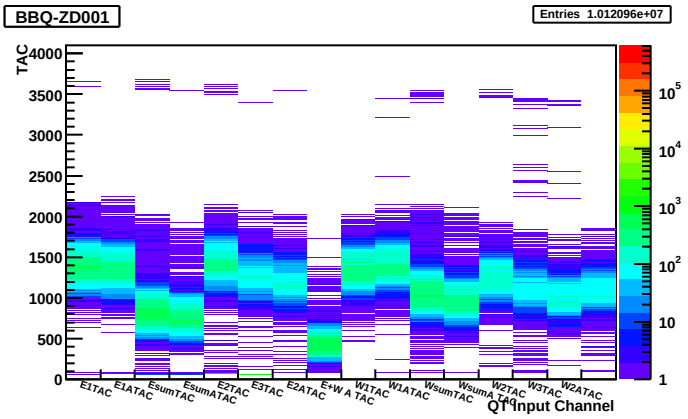

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.012096e+07
ADC
QT Input Channel

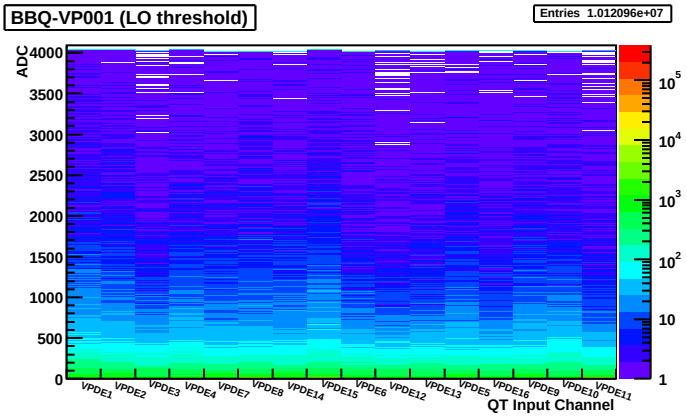

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.012096e+07
TAC
QT Input Channel

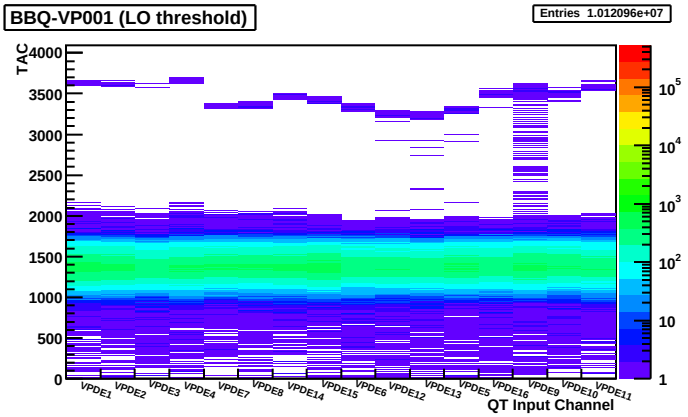

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.012096e+07
ADC
QT Input Channel

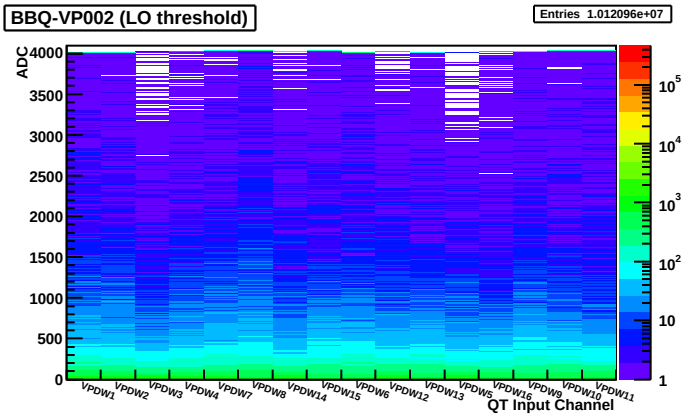

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.012096e+07
TAC
QT Input Channel

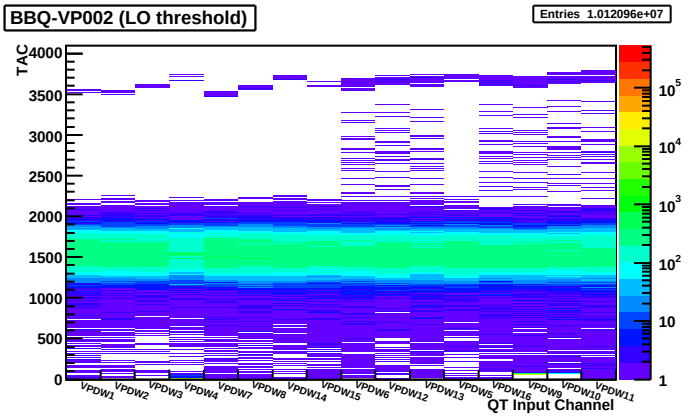

BBQ-ZD001
Entries 1.012096e+07
ADC
QT Input Channel

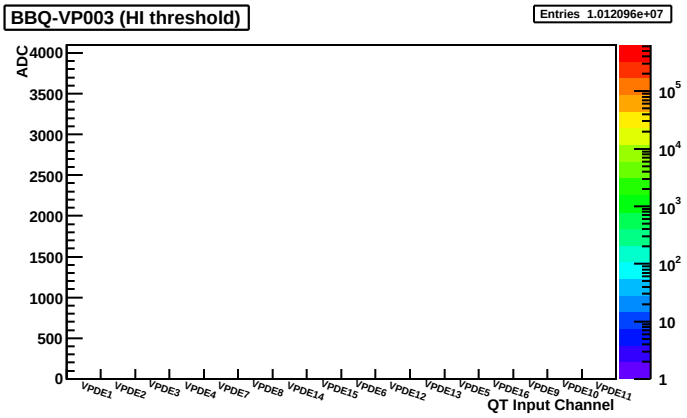

BBQ-ZD001
Entries 1.012096e+07
TAC
QT Input Channel

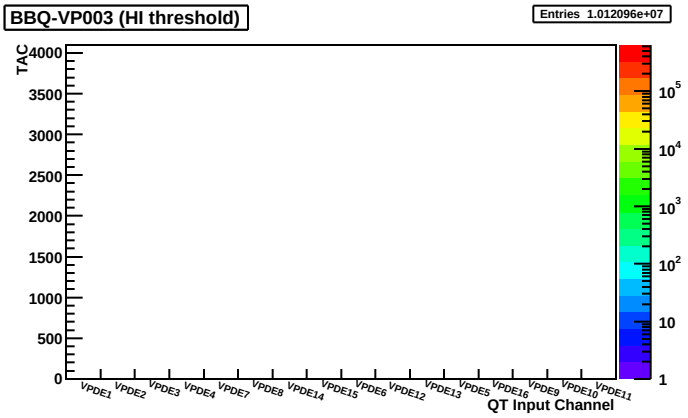

BBQ-VP001 (LO threshold)
Entries 1.012096e+07
ADC
QT Input Channel

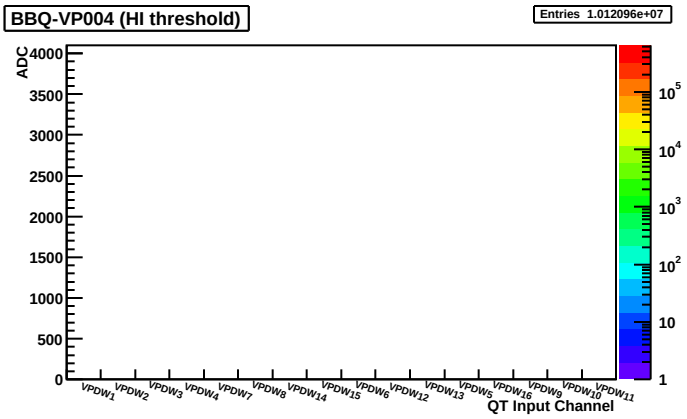

BBQ-VP001 (LO threshold)
Entries 1.012096e+07
TAC
QT Input Channel

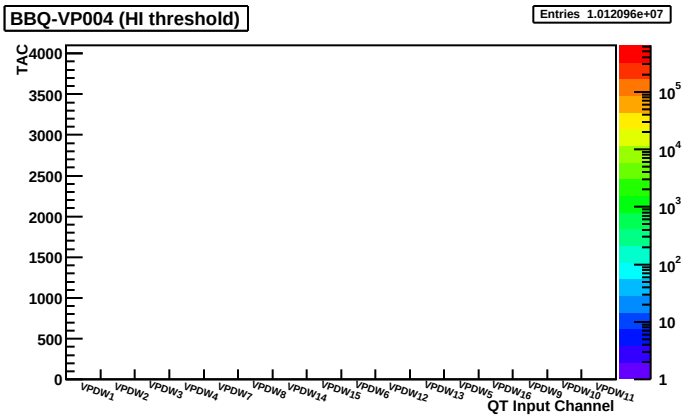

BBQ-VP002 (LO threshold)
Entries 1.012096e+07
ADC
QT Input Channel

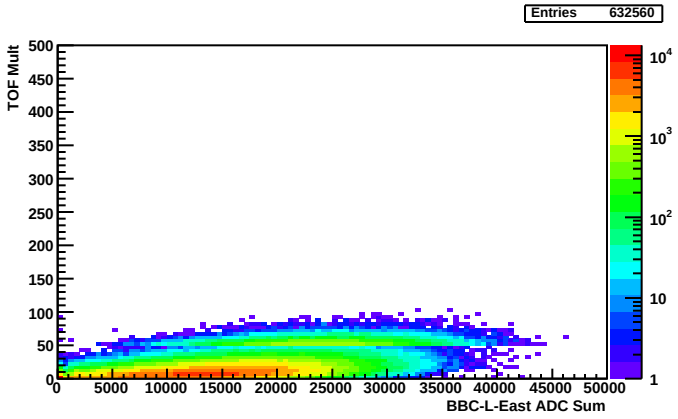

BBQ-VP002 (LO threshold)
Entries 1.012096e+07
TAC
QT Input Channel

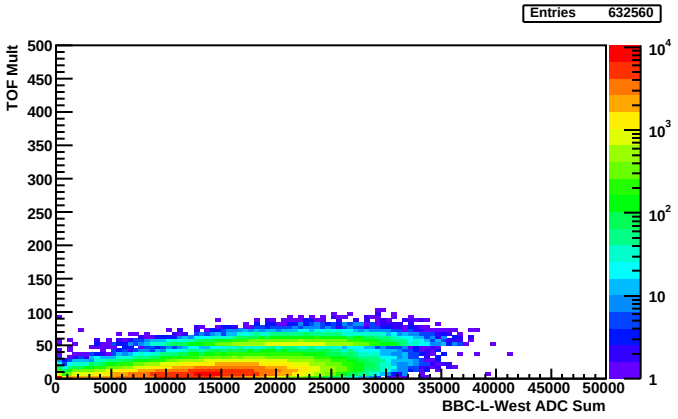

BBQ-VP003 (HI threshold)
Entries 1.012096e+07
ADC
QT Input Channel

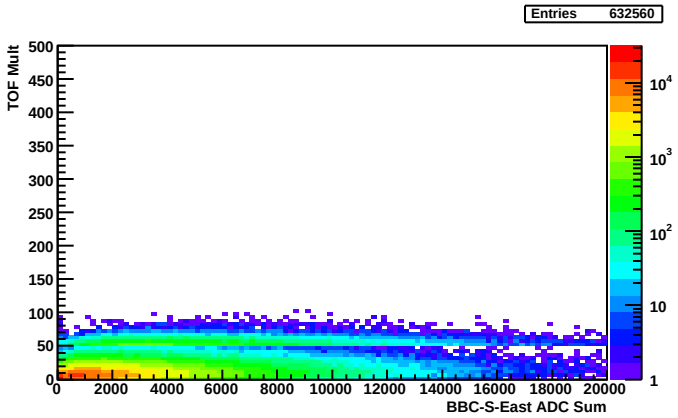

BBQ-VP003 (HI threshold)
Entries 1.012096e+07
TAC
QT Input Channel

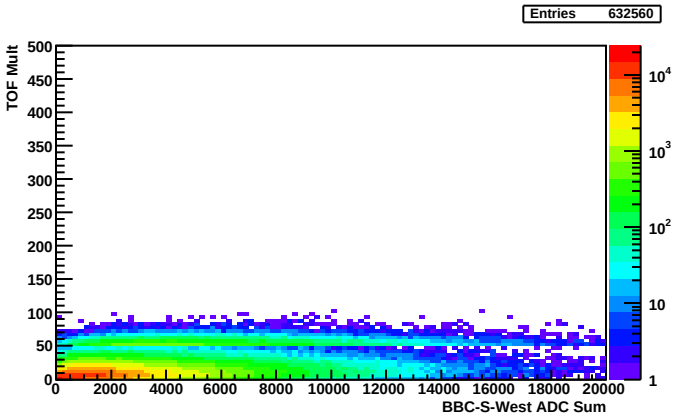

BBQ-VP004 (HI threshold)
Entries 1.012096e+07
ADC
QT Input Channel

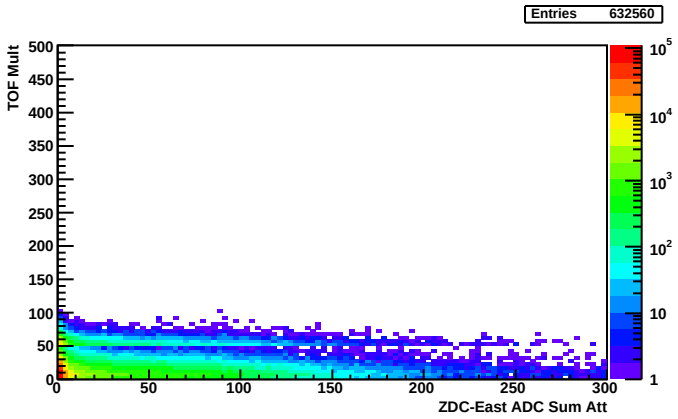

BBQ-VP004 (HI threshold)
Entries 1.012096e+07
TAC
QT Input Channel

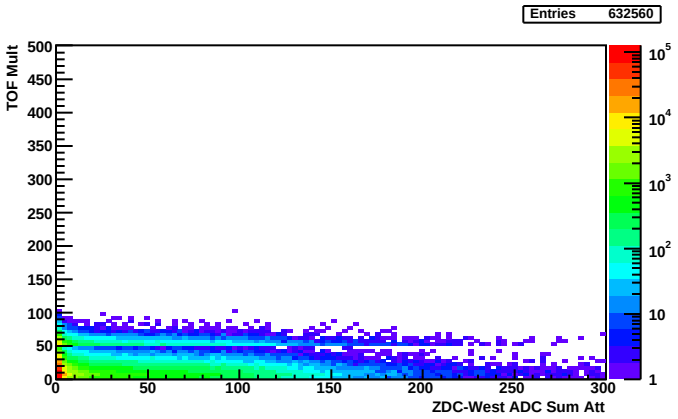

Entries 632560
TOF Mult
BBC-L-East ADC Sum

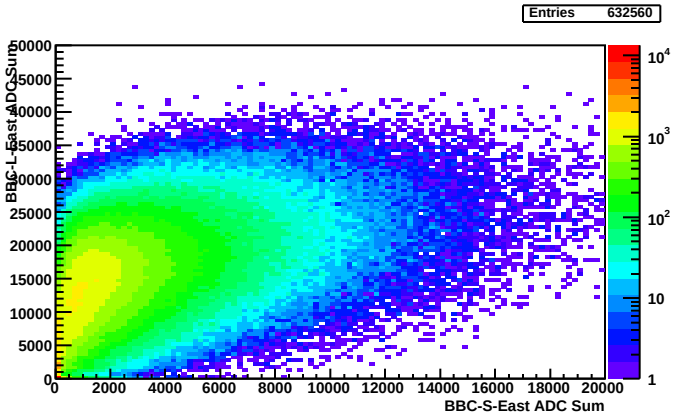

Entries 632560
TOF Mult
BBC-L-West ADC Sum

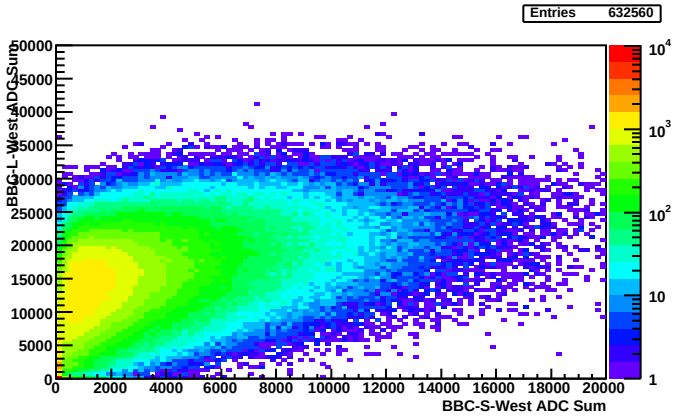

Entries 632560
TOF Mult
BBC-S-East ADC Sum

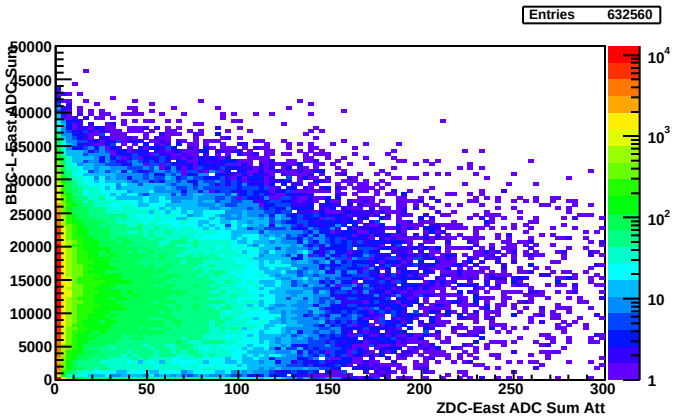

Entries 632560
TOF Mult
BBC-S-West ADC Sum

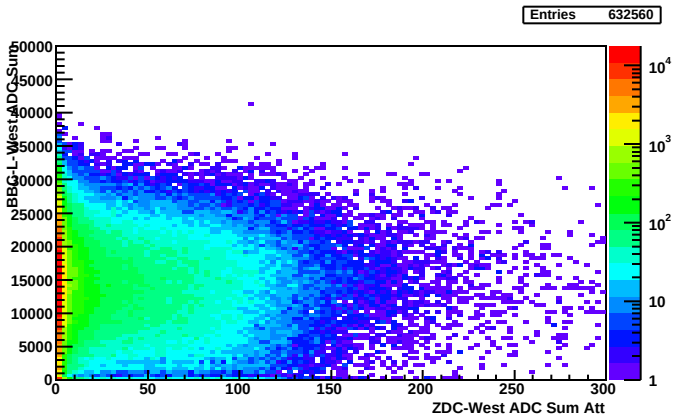

Entries 632560
TOF Mult
ZDC-East ADC Sum Att

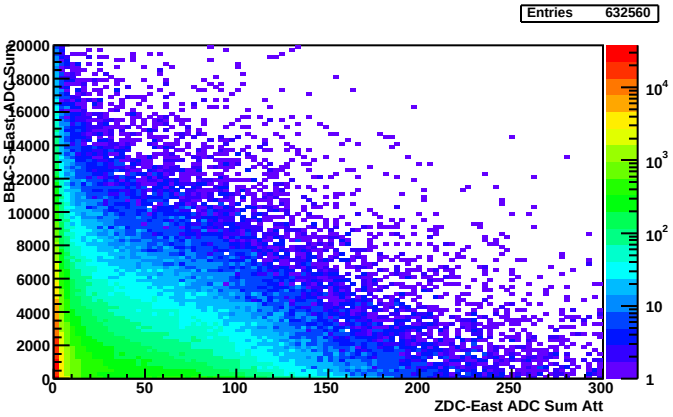

Entries 632560
TOF Mult
ZDC-West ADC Sum Att

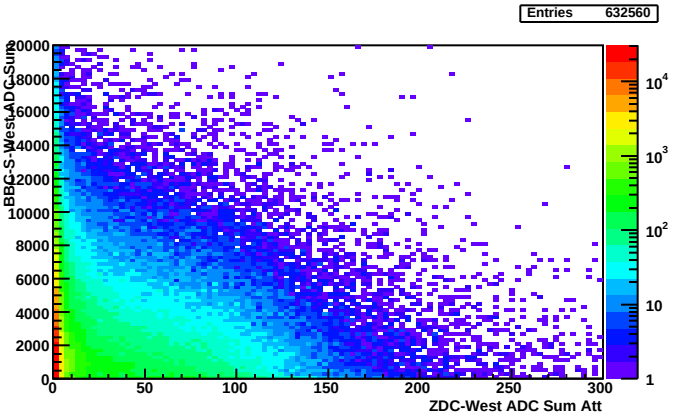

Entries 632560
BBC-L-East ADC Sum
BBC-S-East ADC Sum

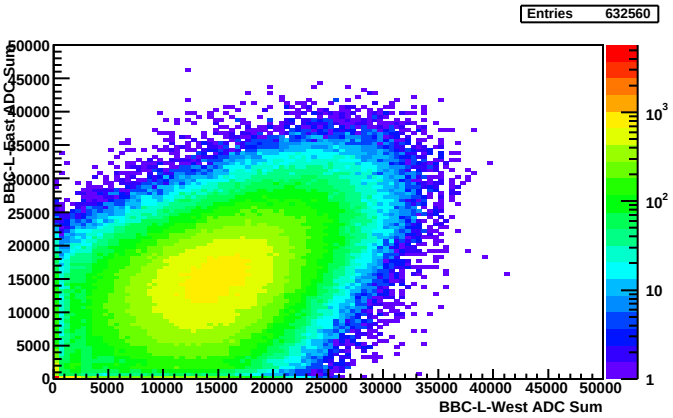

Entries 632560
BBC-L-West ADC Sum
BBC-S-West ADC Sum

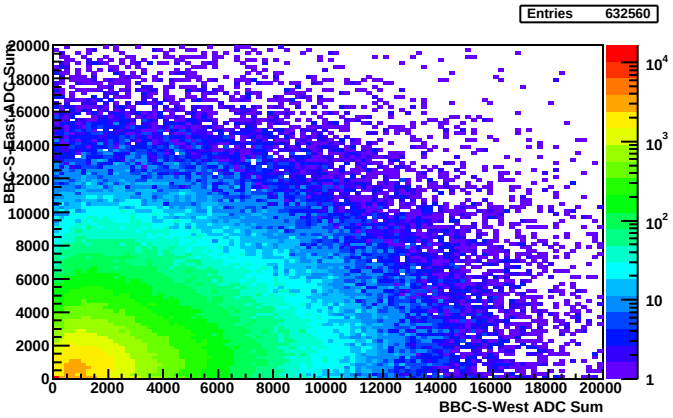

Entries 632560
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

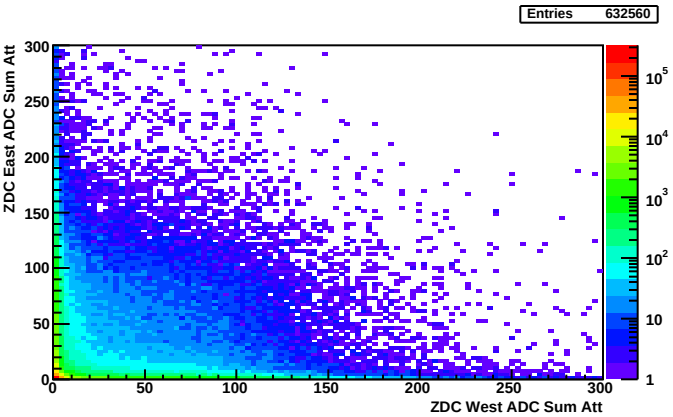

Entries 632560
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

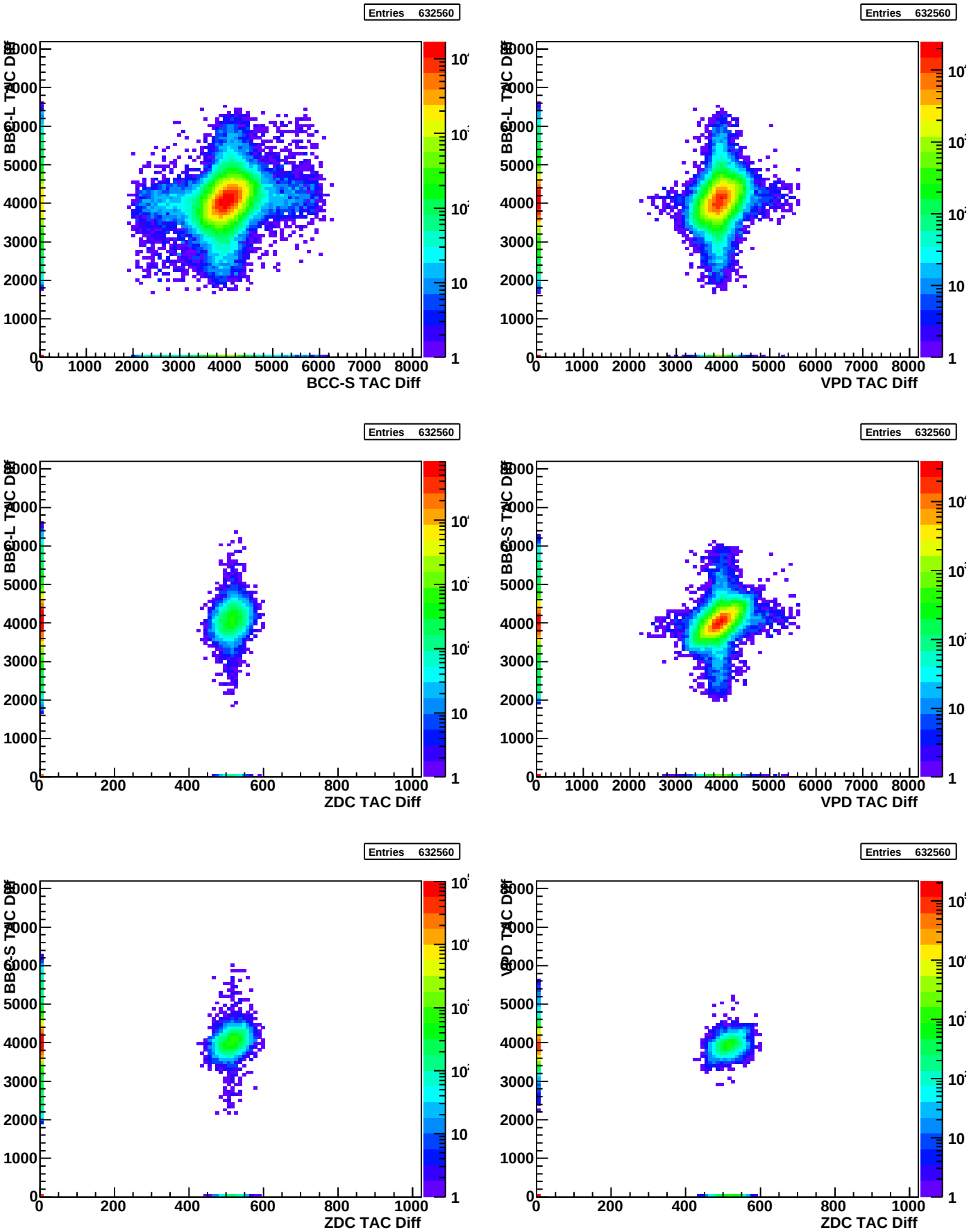

Entries 632560
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

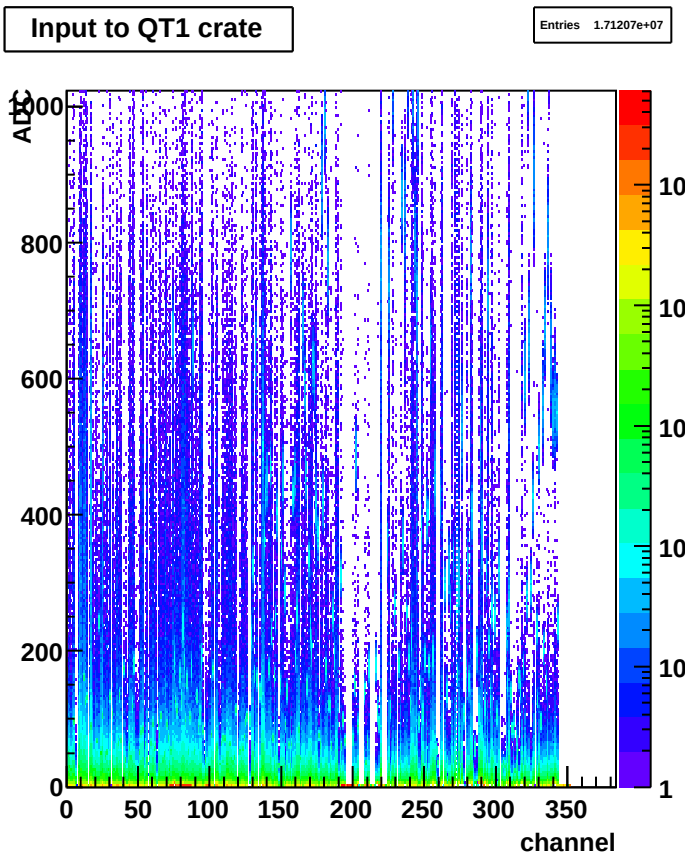

Entries 632560
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

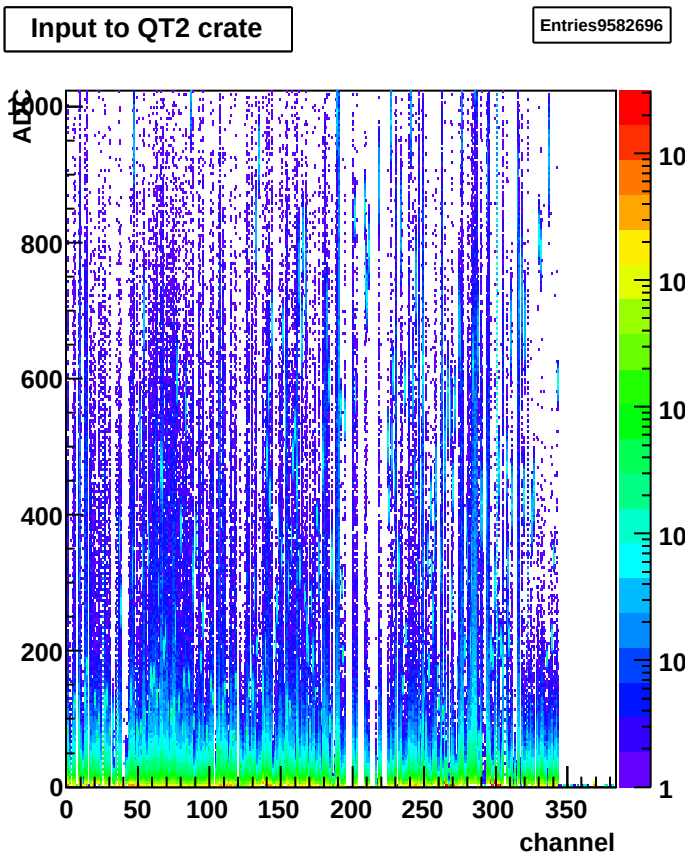

Entries 632560
BBC-L-East ADC Sum
BBC-L-West ADC Sum

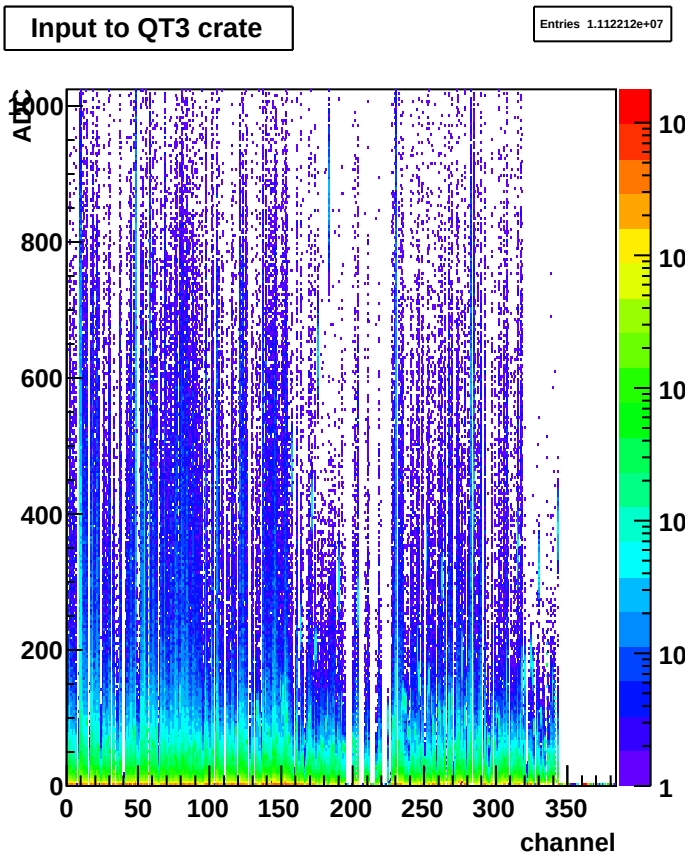

Entries 632560
BBC-S-East ADC Sum
BBC-S-West ADC Sum

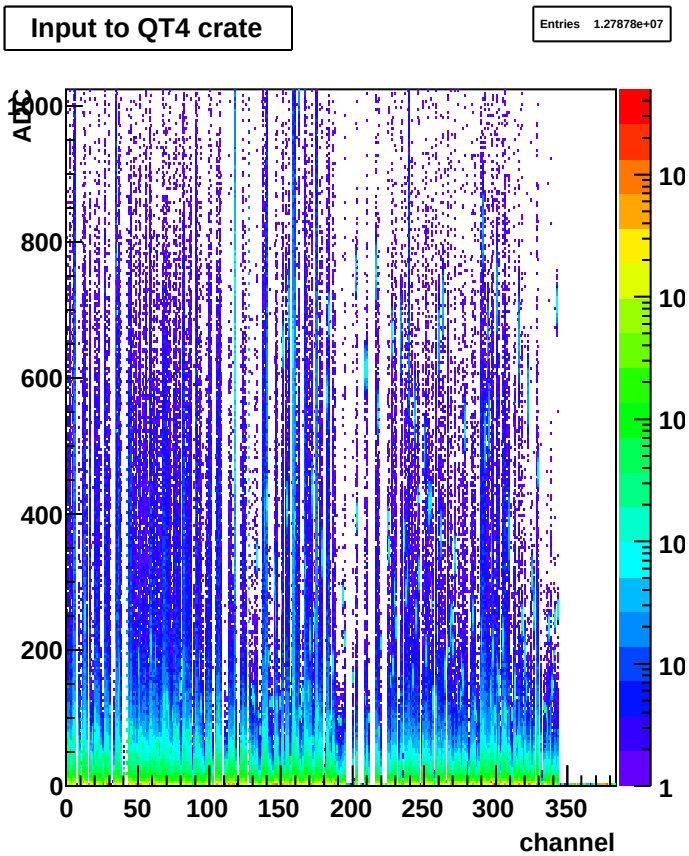

Entries 632560
ZDC East ADC Sum Att
ZDC West ADC Sum Att

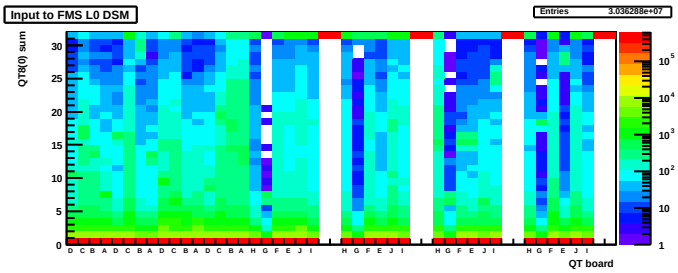

Entries 632560
BBC-L TAC Diff
BCC-S TAC Diff
Entries 632560
BBC-L TAC Diff
VPD TAC Diff
Entries 632560
BBC-L TAC Diff
ZDC TAC Diff
Entries 632560
BBC-S TAC Diff
VPD TAC Diff
Entries 632560
BBC-S TAC Diff
ZDC TAC Diff
Entries 632560
VPD TAC Diff
ZDC TAC Diff

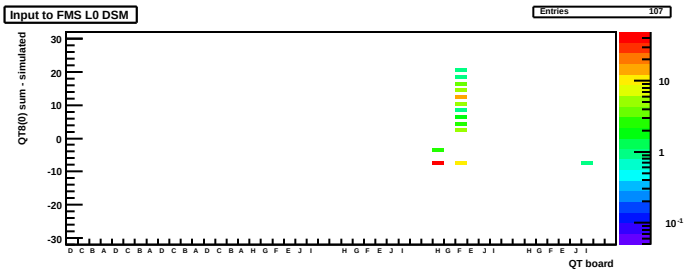

Input to QT1 crate
Entries 1.71207e+07
ADC
channel

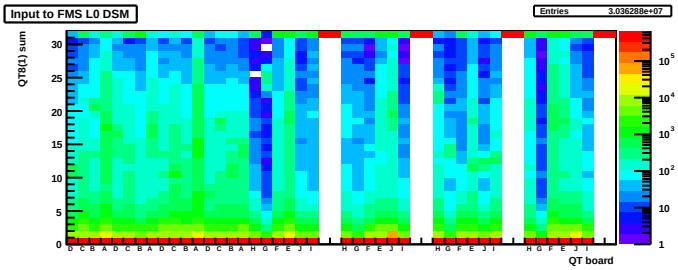

Input to QT2 crate
Entries9582696
ADC
channel

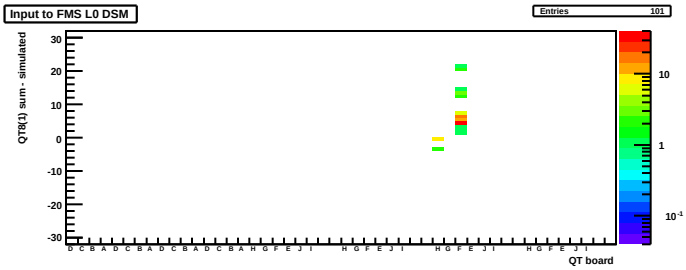

Input to QT3 crate
Entries 1.112212e+07
ADC
channel

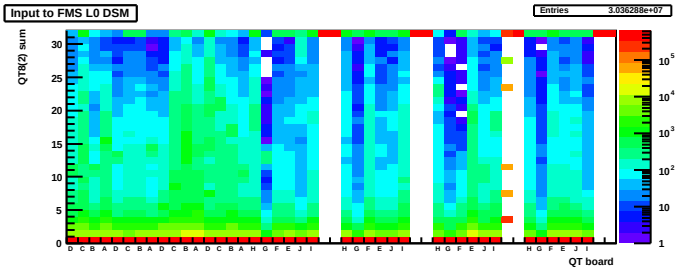

Input to QT4 crate
Entries 1.27878e+07
ADC
channel

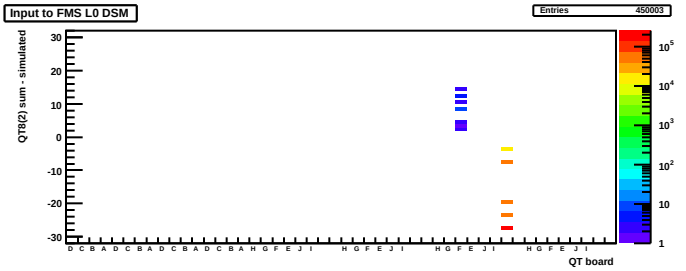

Input to FMS L0 DSM
Entries 3.036288e+07
QT8(0) sum
QT board

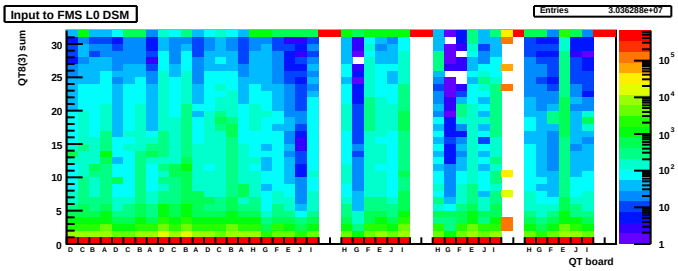

Input to FMS L0 DSM
Entries 107
QT8(0) sum - simulated
QT board

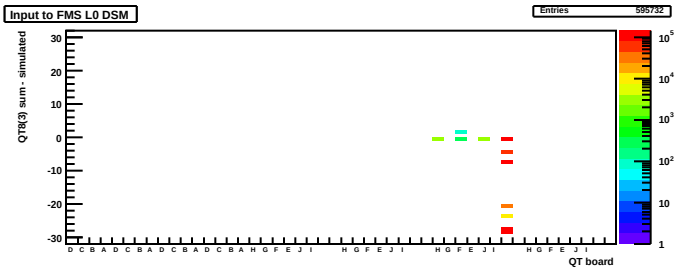

Input to FMS L0 DSM
Entries 3.036288e+07
QT8(1) sum
QT board

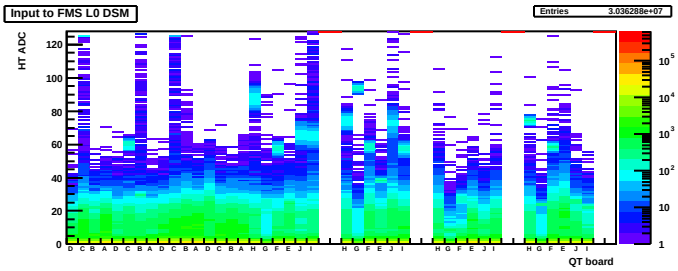

Input to FMS L0 DSM
Entries 101
QT8(1) sum - simulated
QT board

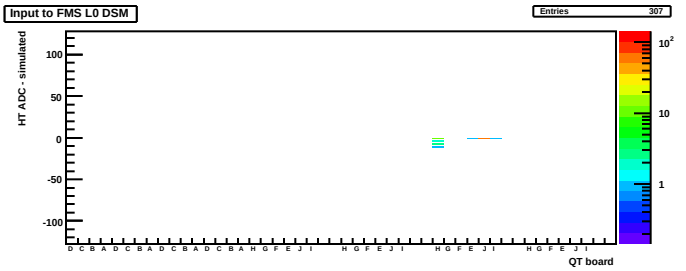

Input to FMS L0 DSM
Entries 3.036288e+07
QT8(2) sum
QT board

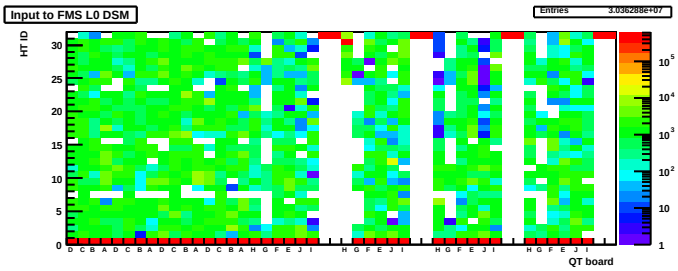

Input to FMS L0 DSM
Entries 450003
QT8(2) sum - simulated
QT board

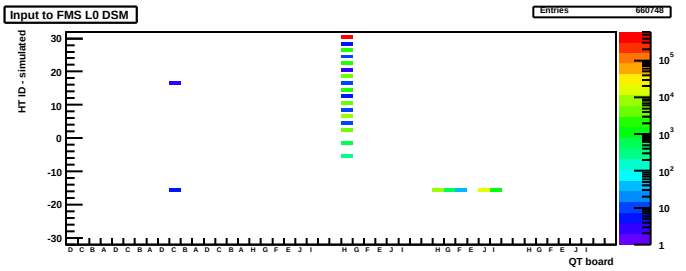

Input to FMS L0 DSM
Entries 3.036288e+07
QT8(3) sum
QT board

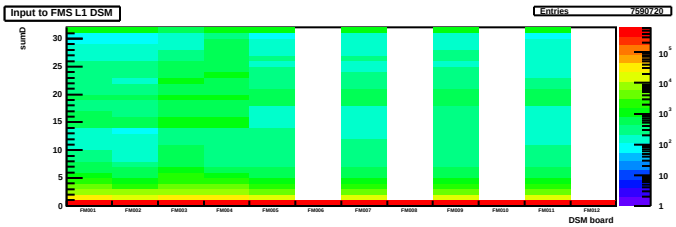

Input to FMS L0 DSM
Entries 595732
QT8(3) sum - simulated
QT board

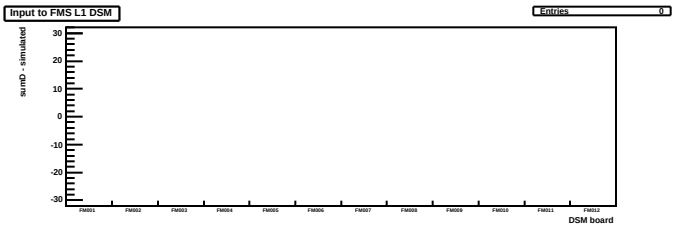

Input to FMS L0 DSM
Entries 3.036288e+07
HT ADC
QT board

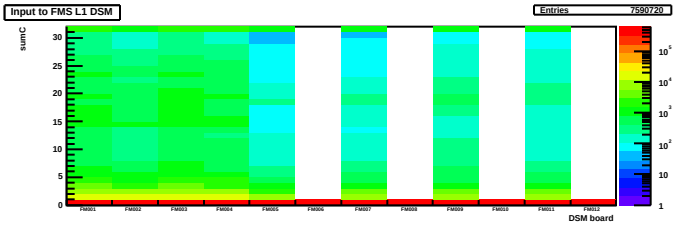

Input to FMS L0 DSM
Entries 307
HT ADC - simulated
QT board

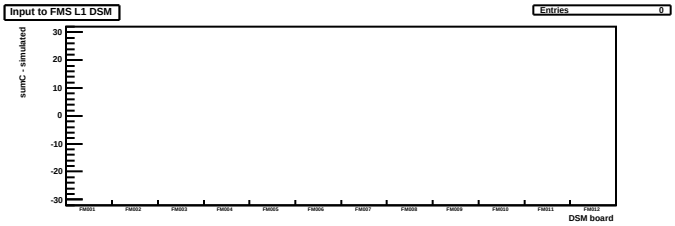

Input to FMS L0 DSM
Entries 3.036288e+07
HT ID
QT board

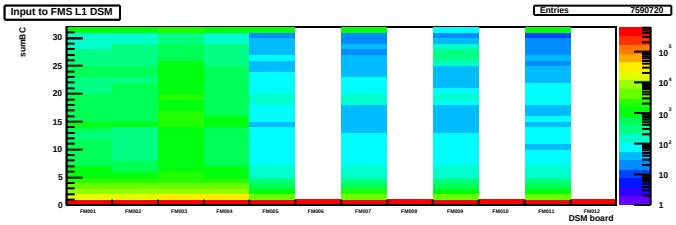

Input to FMS L0 DSM
Entries 660748
HT ID - simulated
QT board

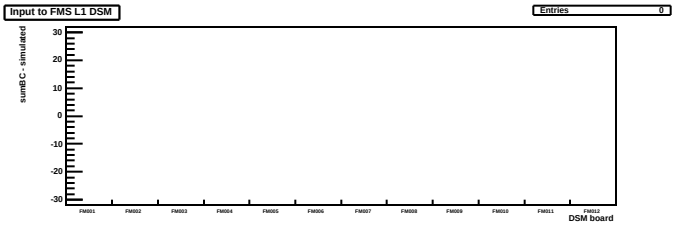

Input to FMS L1 DSM
Entries 7590720
sumD
DSM board

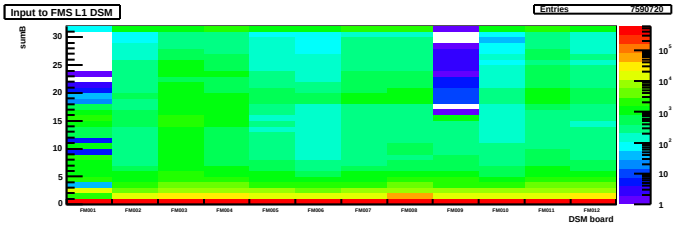

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

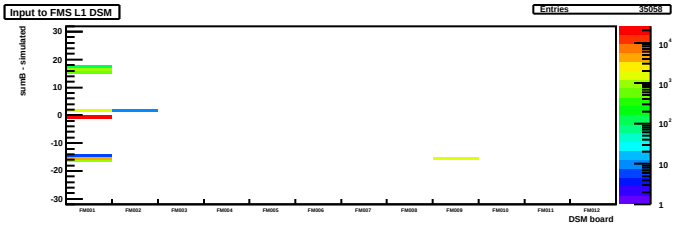

Input to FMS L1 DSM
Entries 7590720
sumC
DSM board

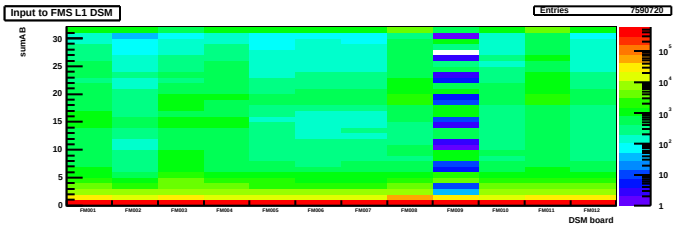

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

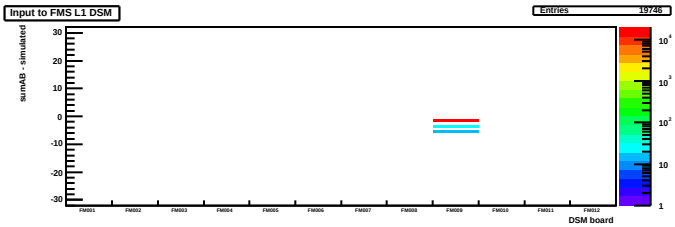

Input to FMS L1 DSM
Entries 7590720
sumBC
DSM board

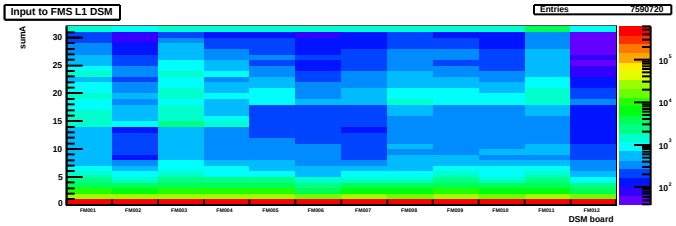

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

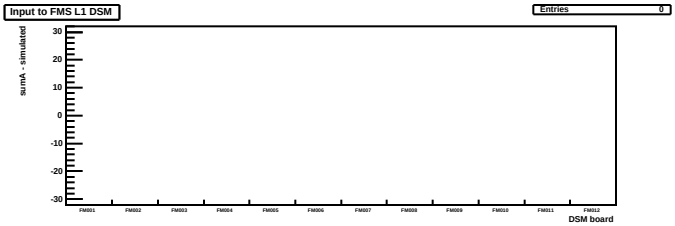

Input to FMS L1 DSM
Entries 7590720
sumB
DSM board

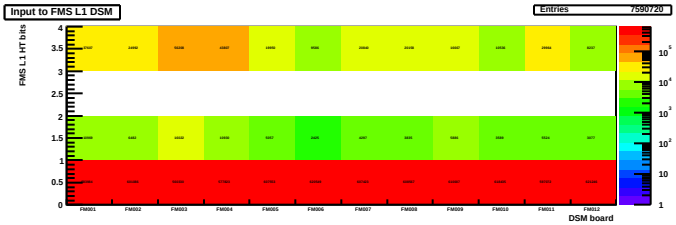

Input to FMS L1 DSM
Entries 35058
sumB - simulated
DSM board

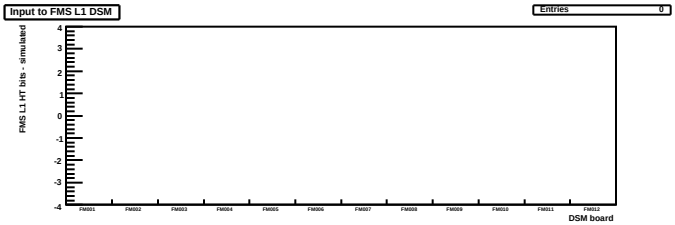

Input to FMS L1 DSM
Entries 7590720
sumAB
DSM board

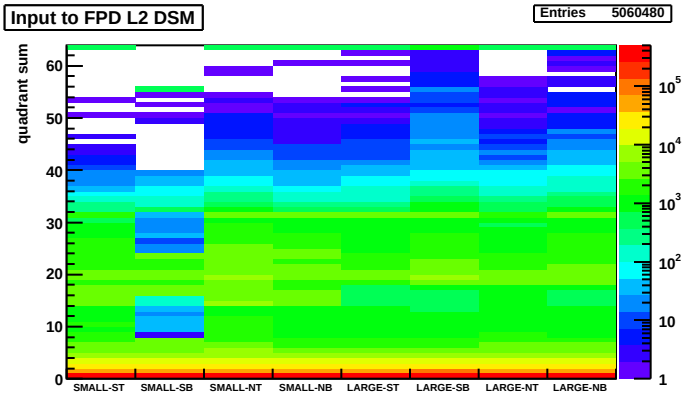

Input to FMS L1 DSM
Entries 19746
sumAB - simulated
DSM board

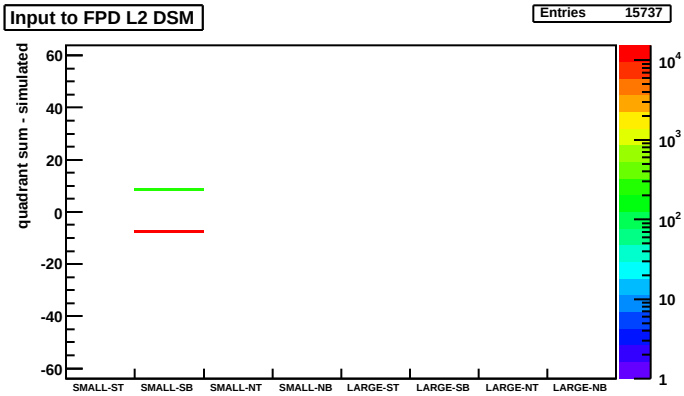

Input to FMS L1 DSM
Entries 7590720
sumA
DSM board

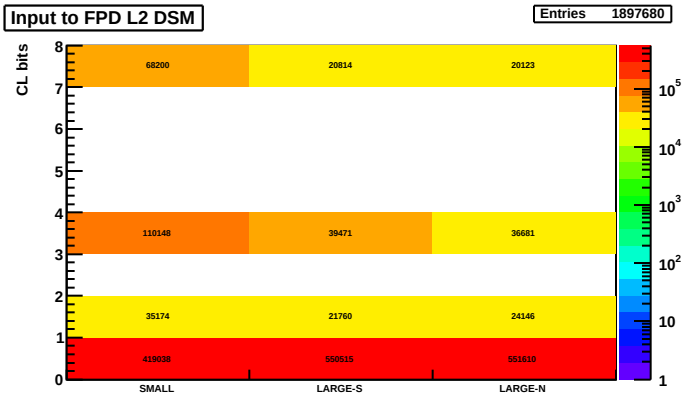

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

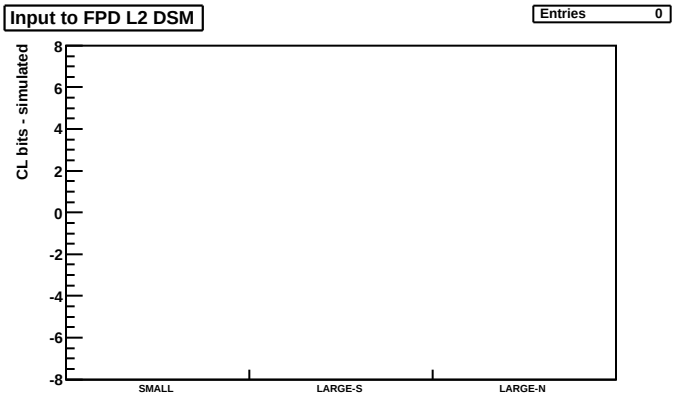

Input to FMS L1 DSM
Entries 7590720
FMS L1 HT bits
DSM board

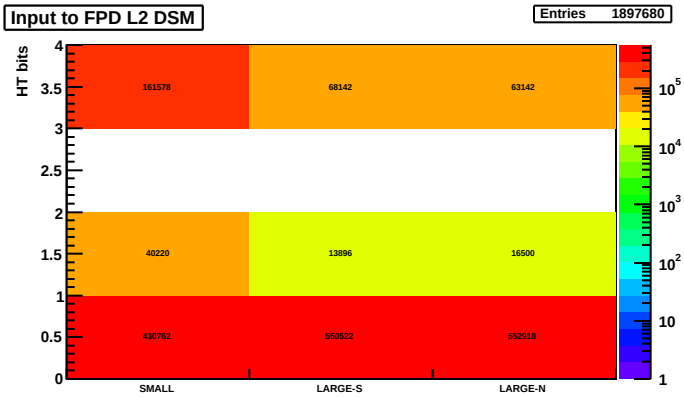

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

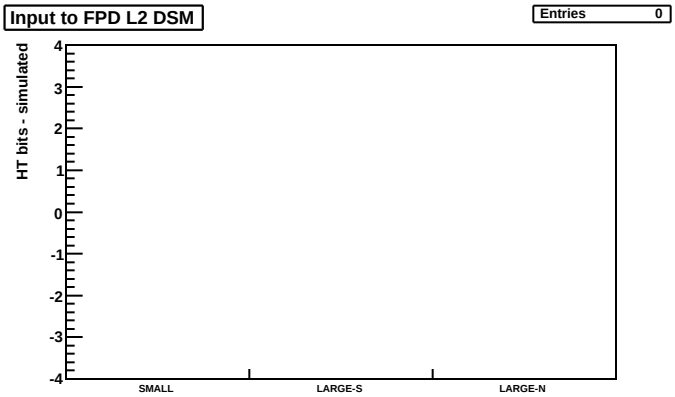

Input to FPD L2 DSM
Entries 5060480
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

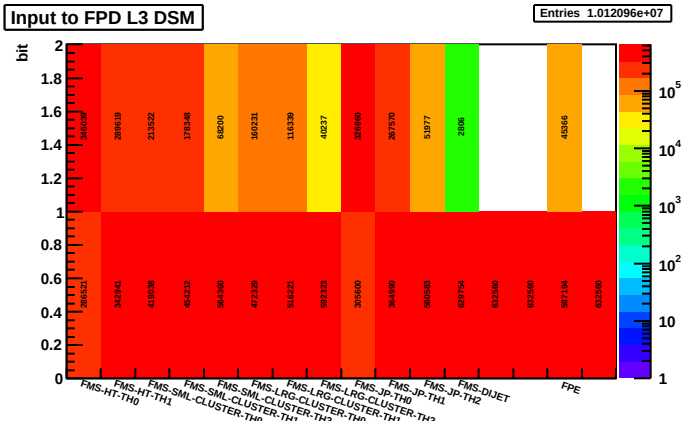

Input to FPD L2 DSM
Entries 15737
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

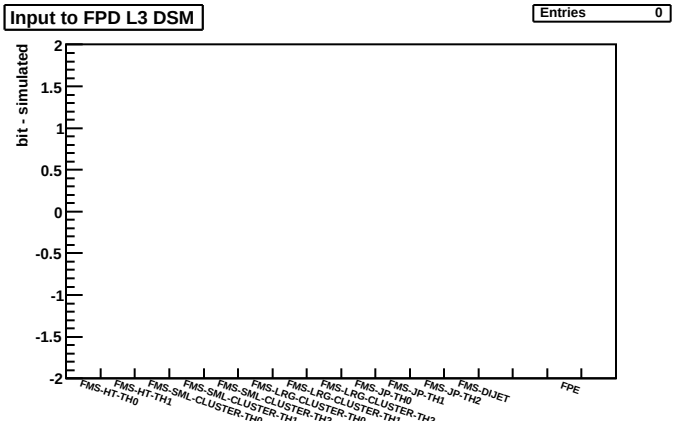

Input to FPD L2 DSM
Entries 1897680
CL bits
68200
20814
20123
110148
39471
36681
35174
21760
24146
419038
550515
551610
SMALL
LARGE-S
LARGE-N

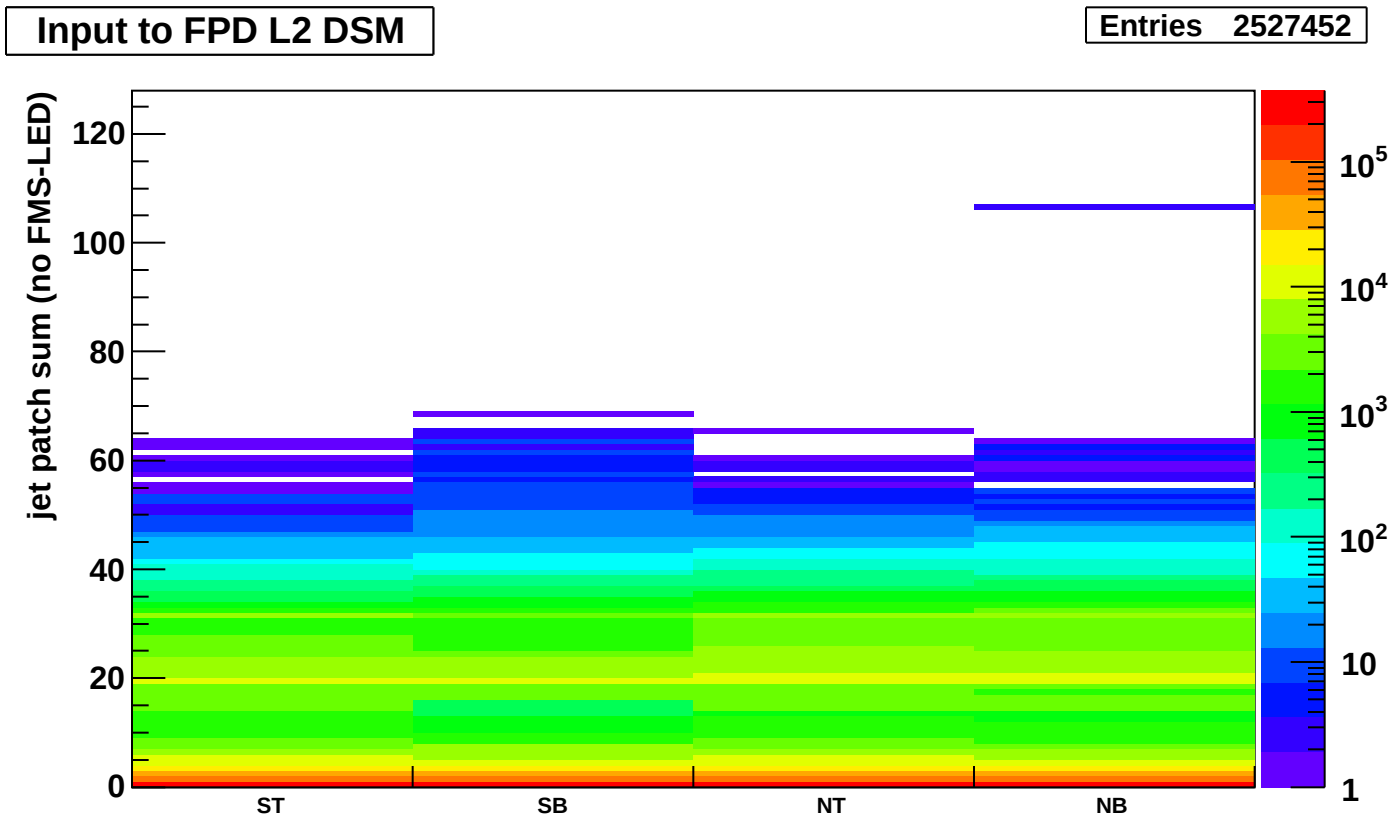

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

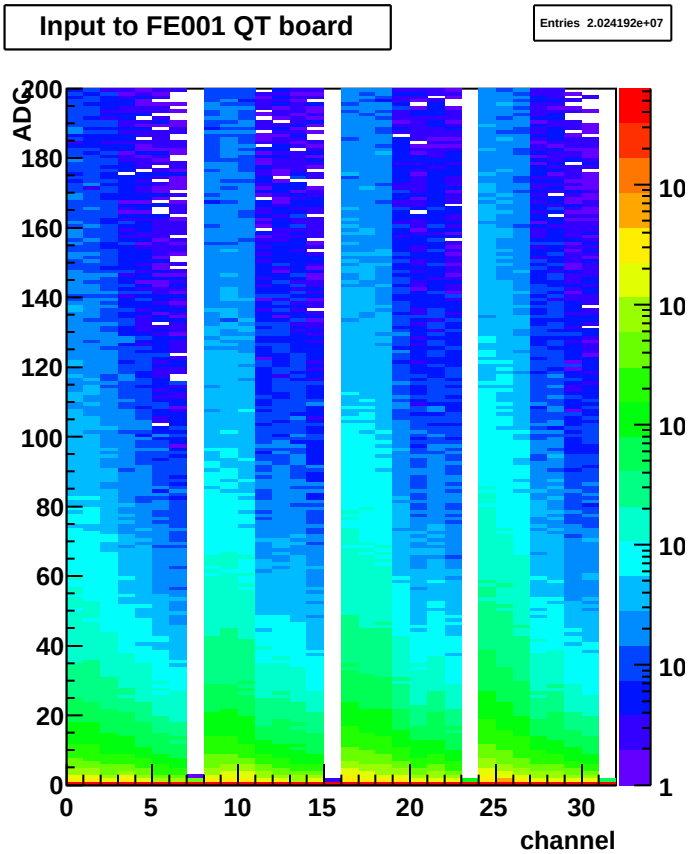

Input to FPD L2 DSM
Entries 1897680
HT bits
161578
68142
63142
40220
13896
16500
430762
550522
552918
SMALL
LARGE-S
LARGE-N

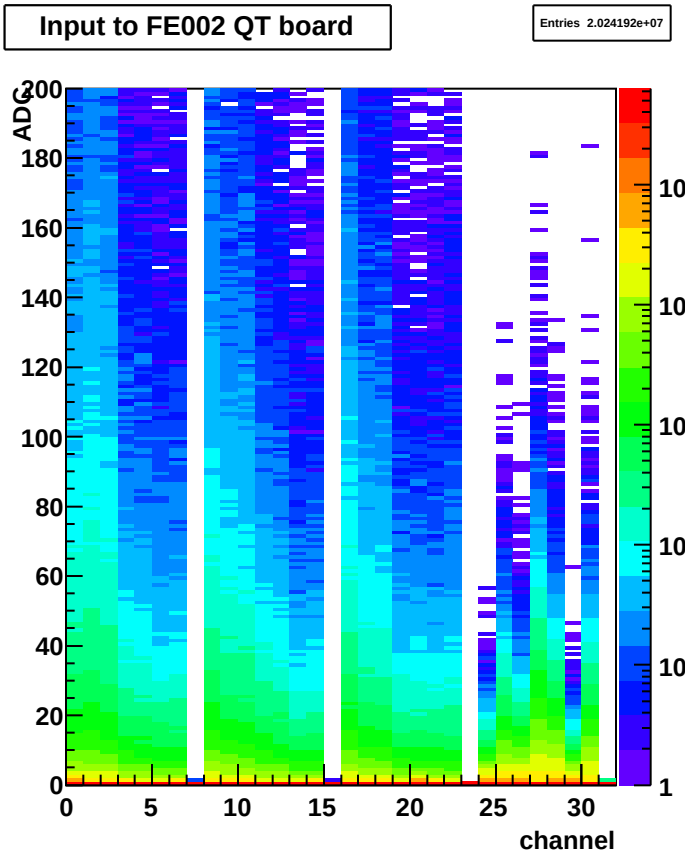

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

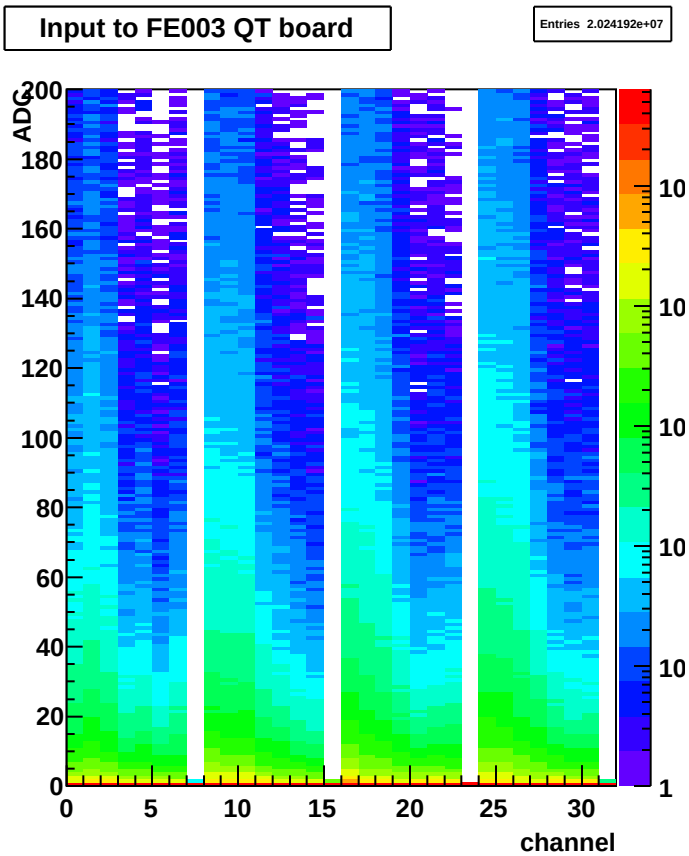

Input to FPD L3 DSM
Entries 1.012096e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

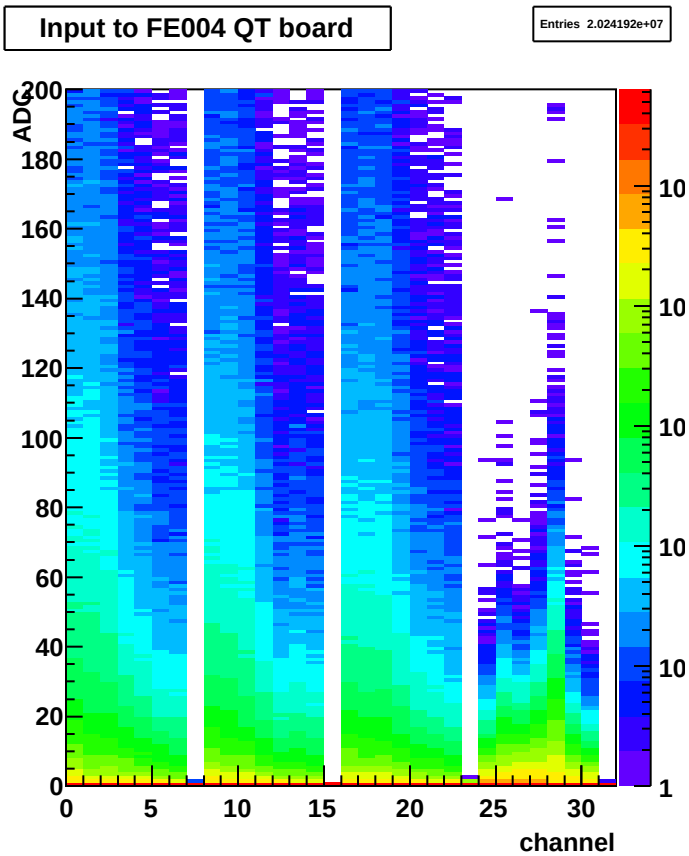

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

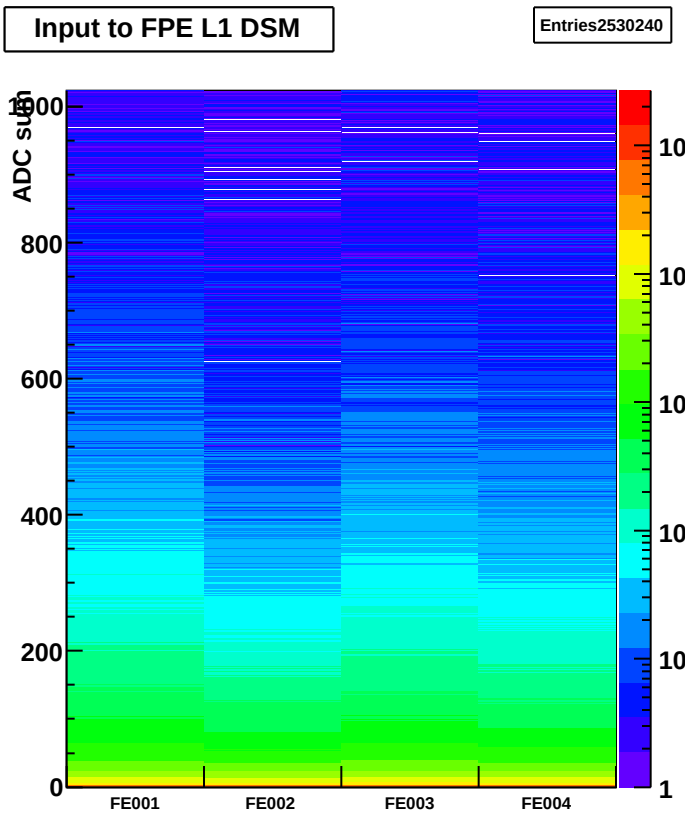

Input to FPD L2 DSM
Entries 2527452
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

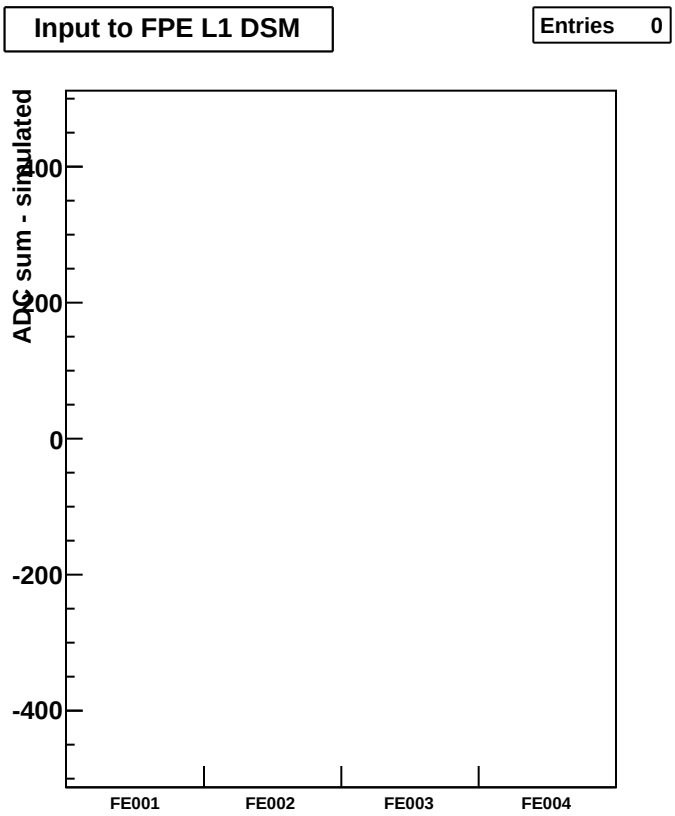

Input to FE001 QT board
Entries 2.024192e+07
ADC
channel

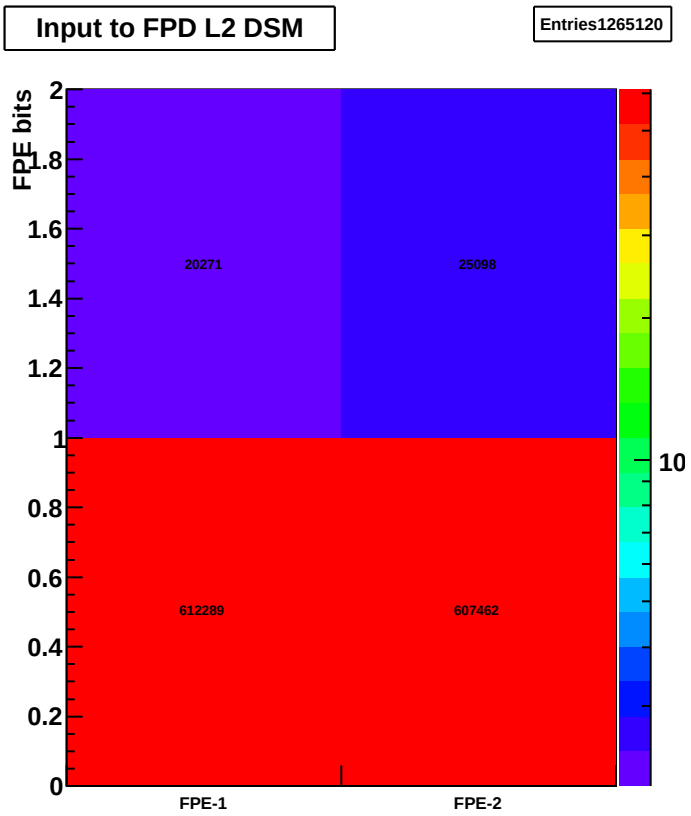

Input to FE002 QT board
Entries 2.024192e+07
ADC
channel

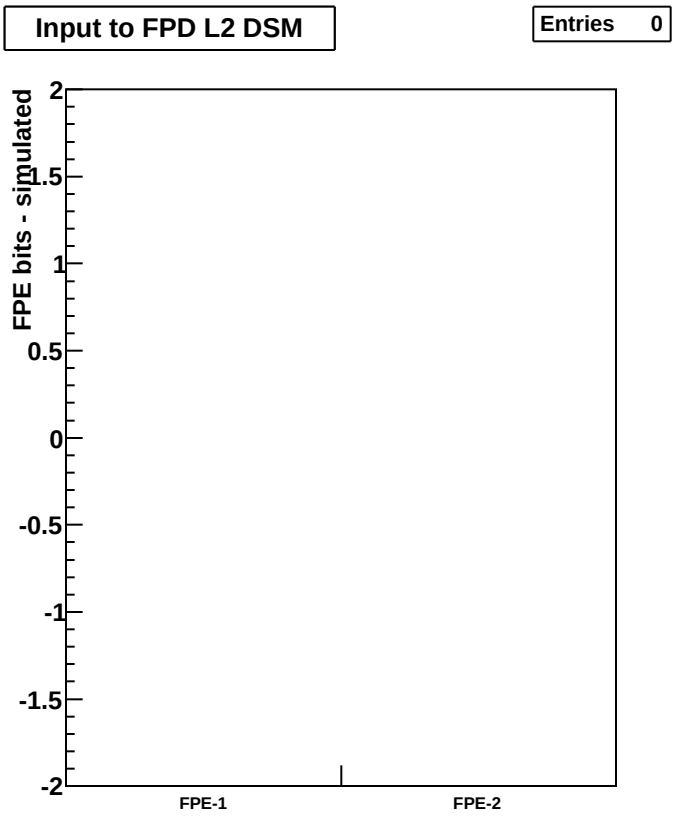

Input to FE003 QT board
Entries 2.024192e+07
ADC
channel


Input to FE004 QT board
Entries 2.024192e+07
ADC
channel


Input to FPE L1 DSM
Entries2530240
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1265120
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
20271
25098
612289
607462
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2