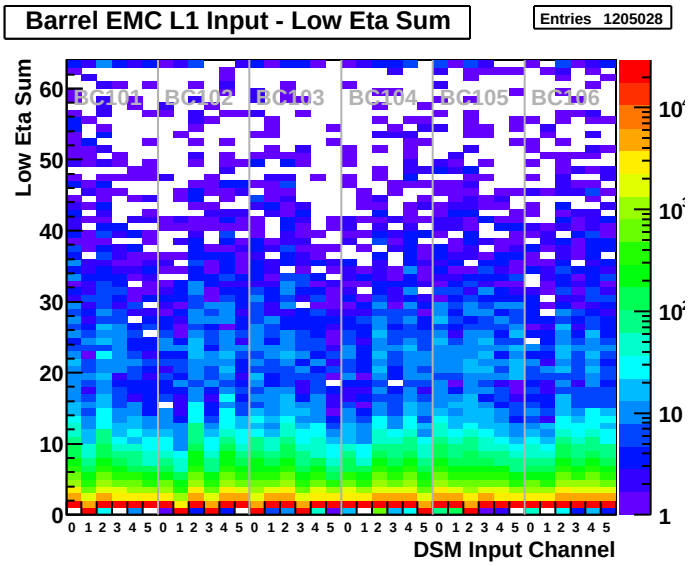
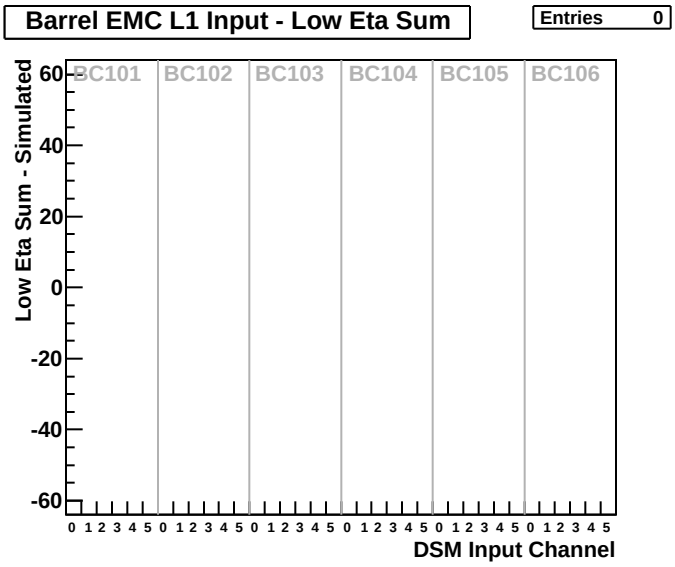
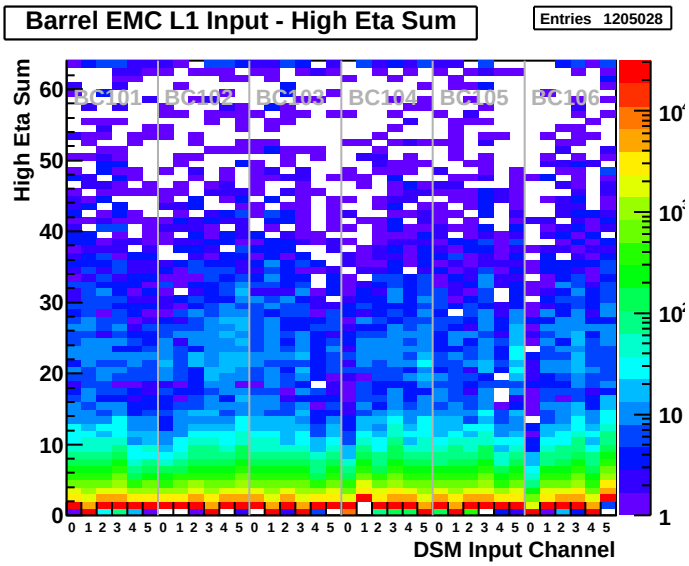
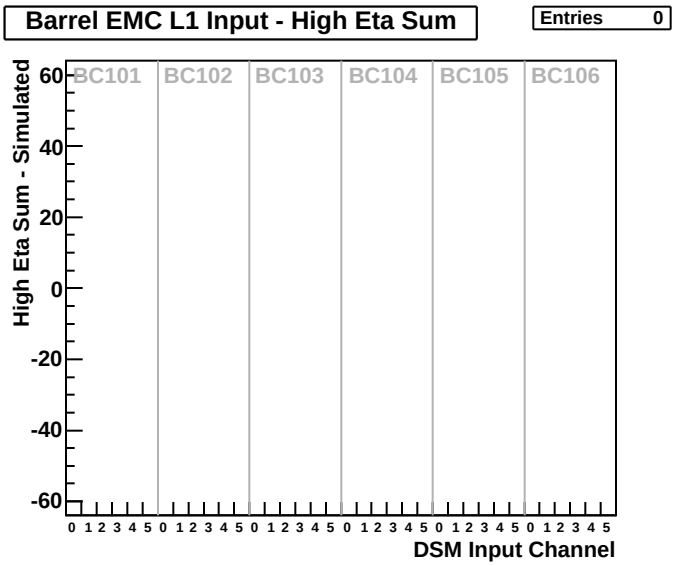
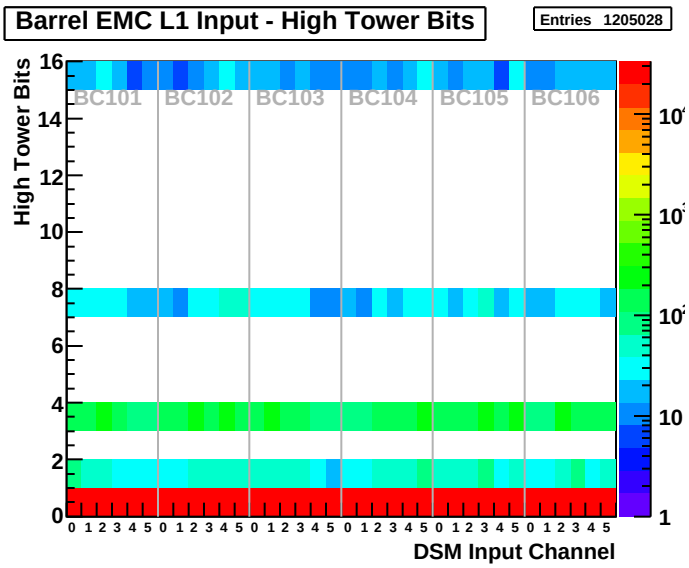
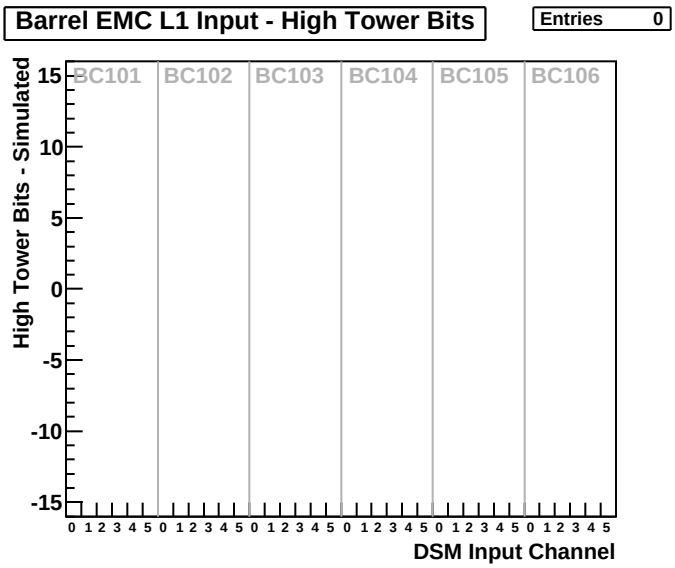

Barrel EMC L1 Input - Low Eta Sum
Entries 1205028
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

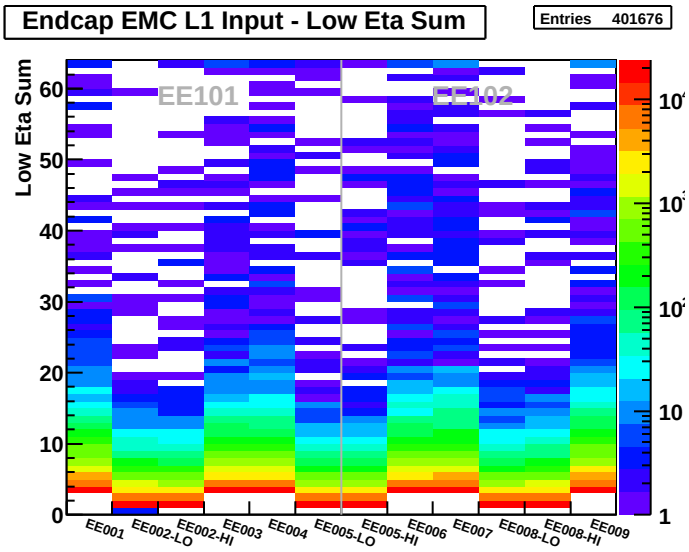

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

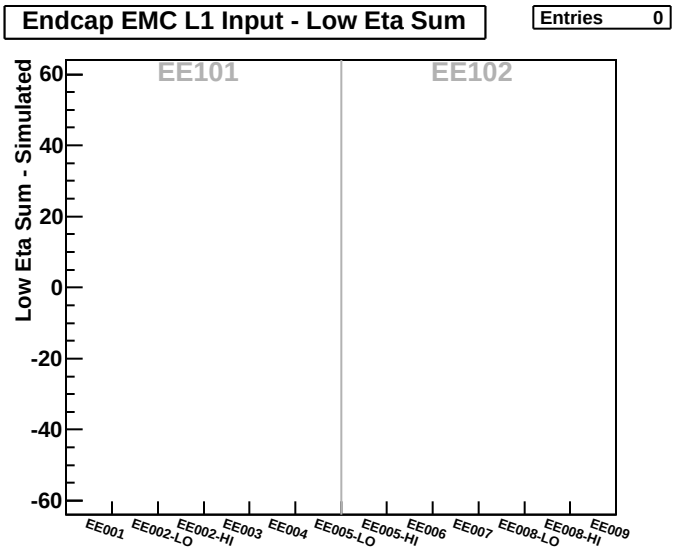

Barrel EMC L1 Input - High Eta Sum
Entries 1205028
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

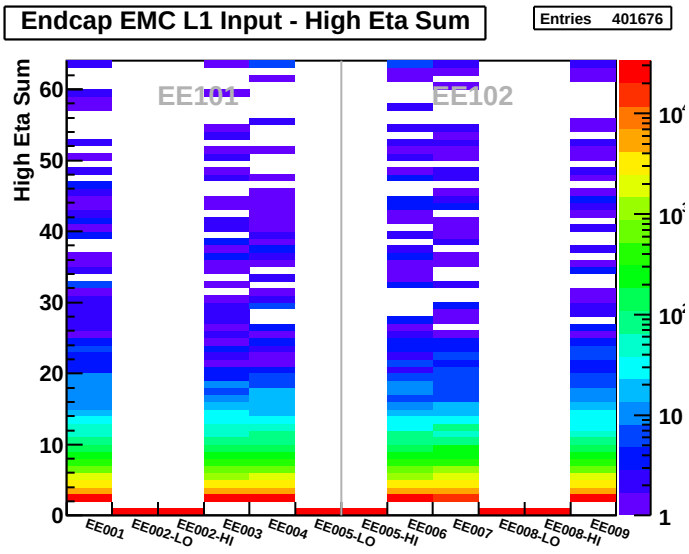

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

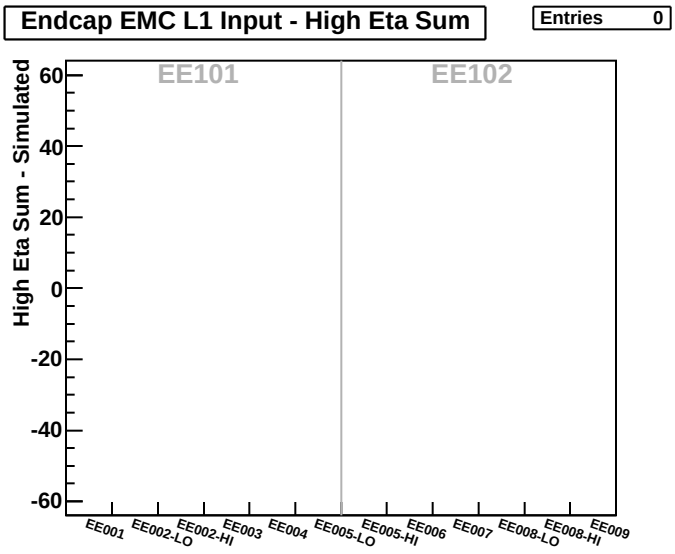

Barrel EMC L1 Input - High Tower Bits
Entries 1205028
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

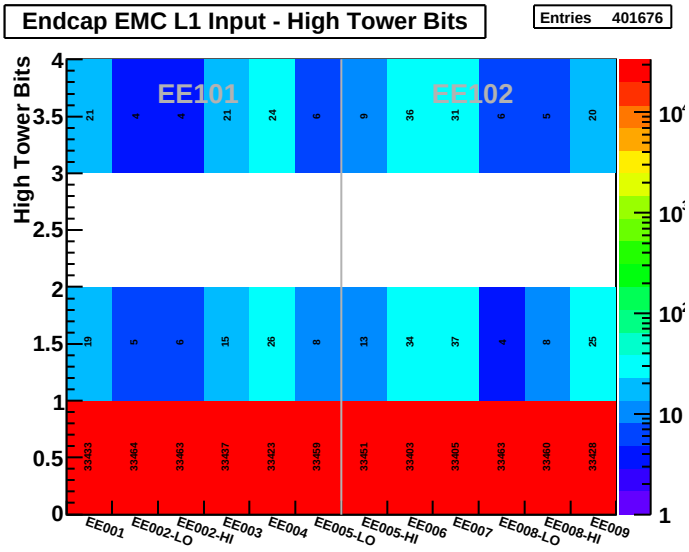

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

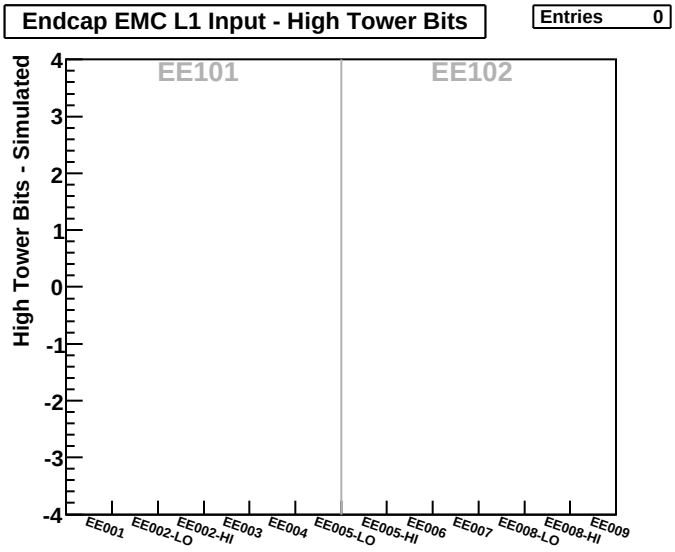

Endcap EMC L1 Input - Low Eta Sum
Entries 401676
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

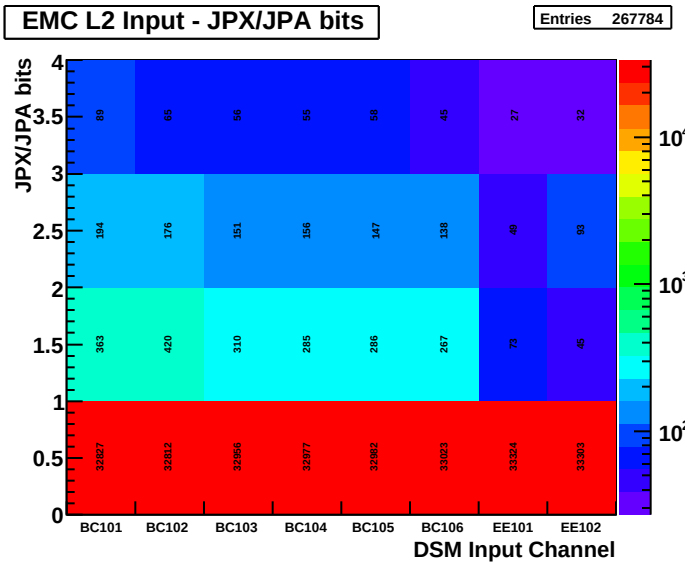

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

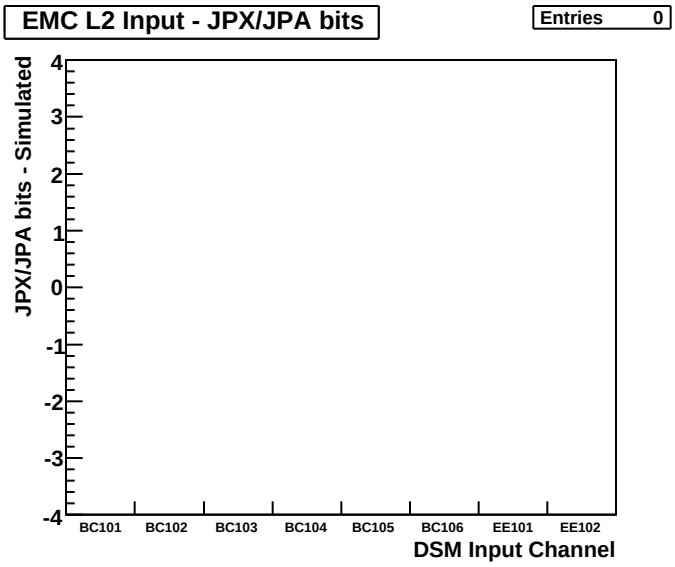

Endcap EMC L1 Input - High Eta Sum
Entries 401676
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

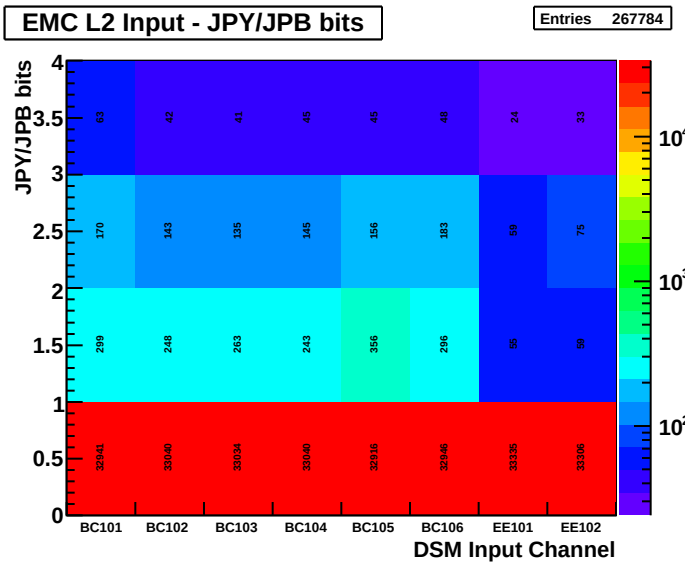

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

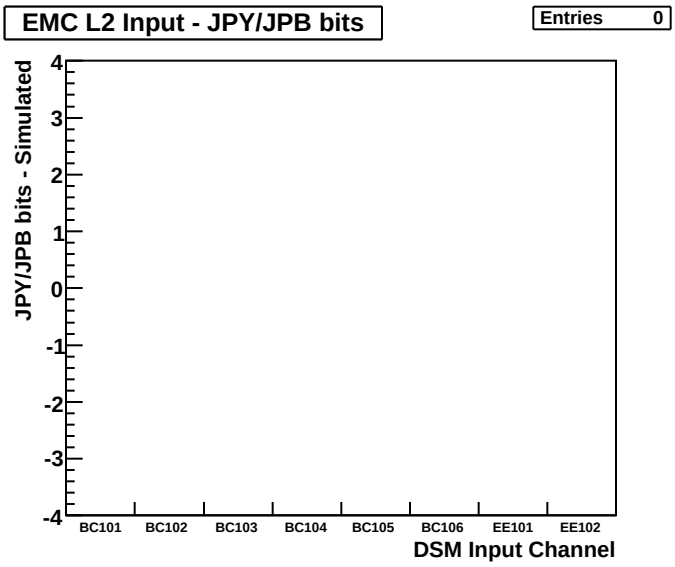

Endcap EMC L1 Input - High Tower Bits
Entries 401676
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

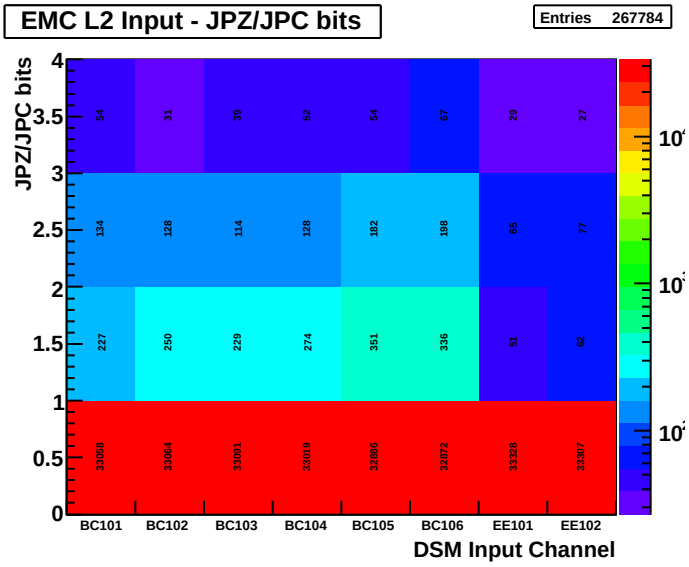

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

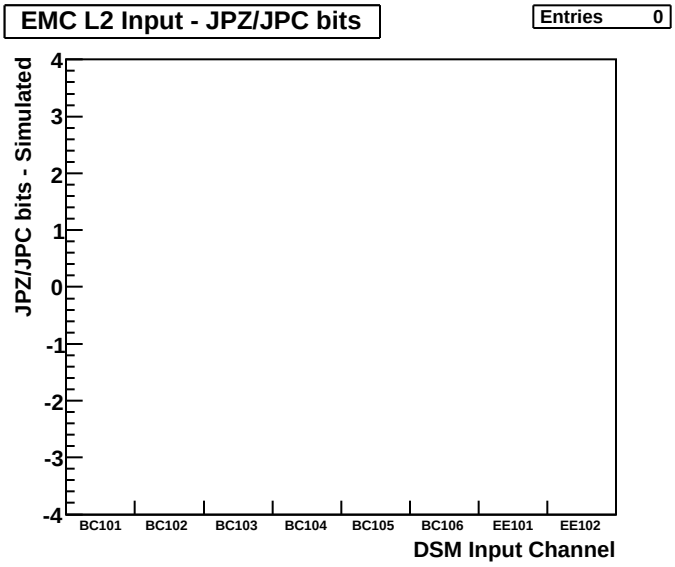

EMC L2 Input - JPX/JPA bits
Entries 267784
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
89 65 56 55 58 45 27 32
194 176 151 156 147 138 49 93
363 420 310 285 286 267 73 45
32827 32812 32856 32977 32982 33023 33324 33303

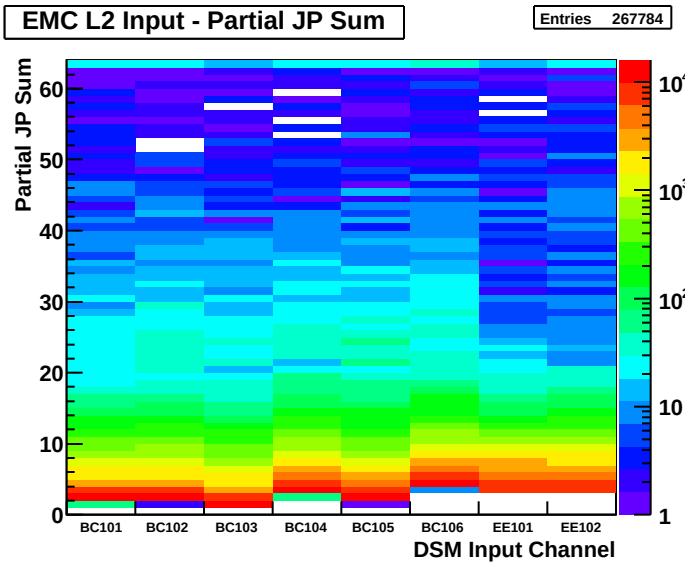

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

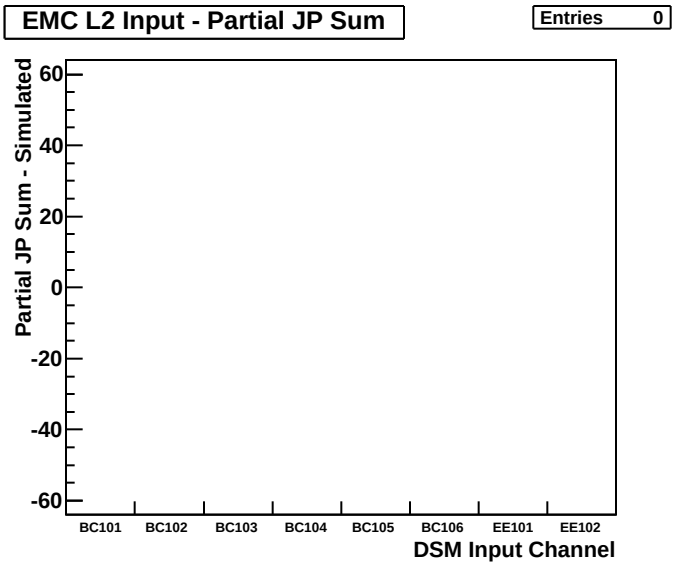

EMC L2 Input - JPY/JPB bits
Entries 267784
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
63 42 41 45 45 48 24 33
170 143 135 145 156 183 59 75
299 248 263 243 356 296 55 59
32941 33040 33034 33040 32916 32946 33335 33306

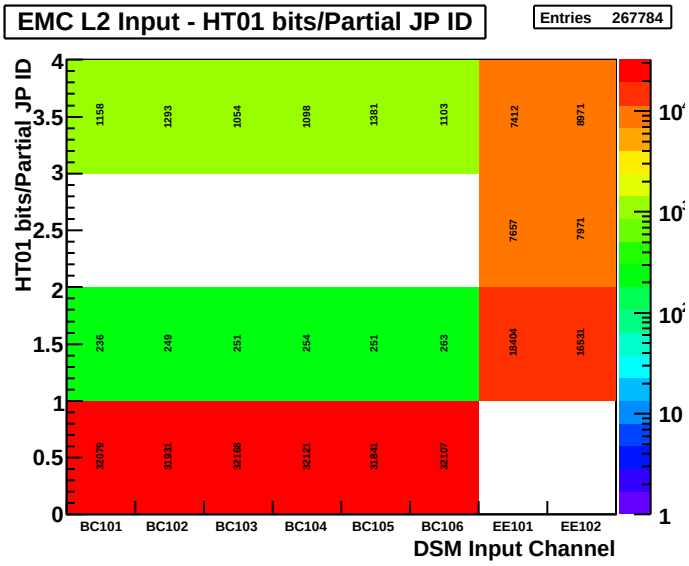

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

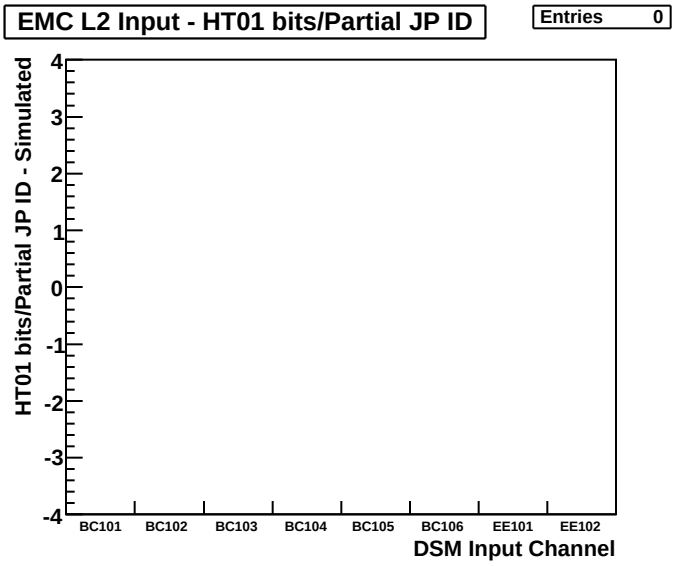

EMC L2 Input - JPZ/JPC bits
Entries 267784
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
54 31 39 52 54 67 29 27
134 128 114 128 182 198 65 77
227 250 229 274 351 336 51 62
33058 33064 33091 33019 32886 32872 33328 33307

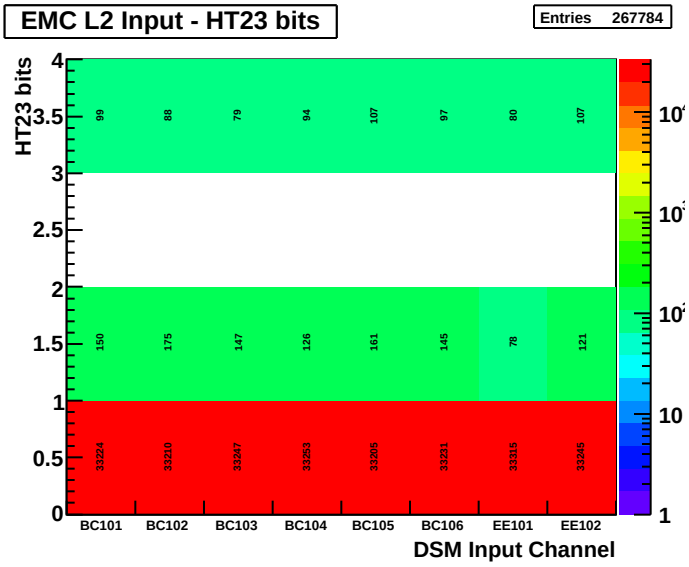

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

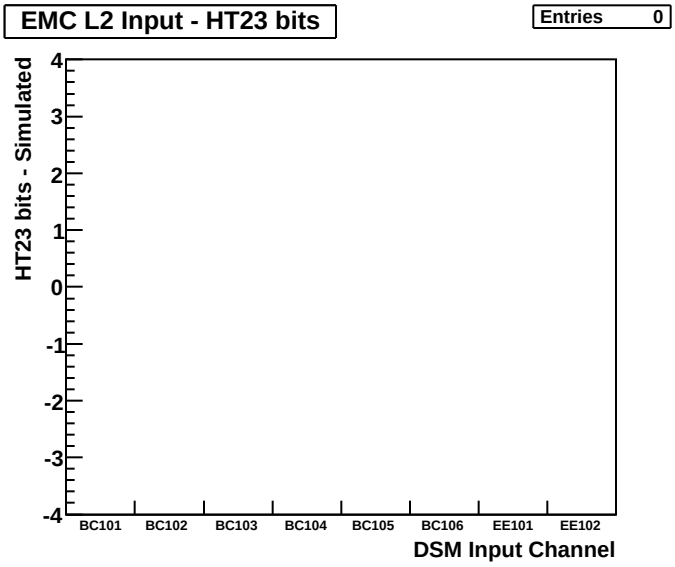

EMC L2 Input - Partial JP Sum
Entries 267784
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

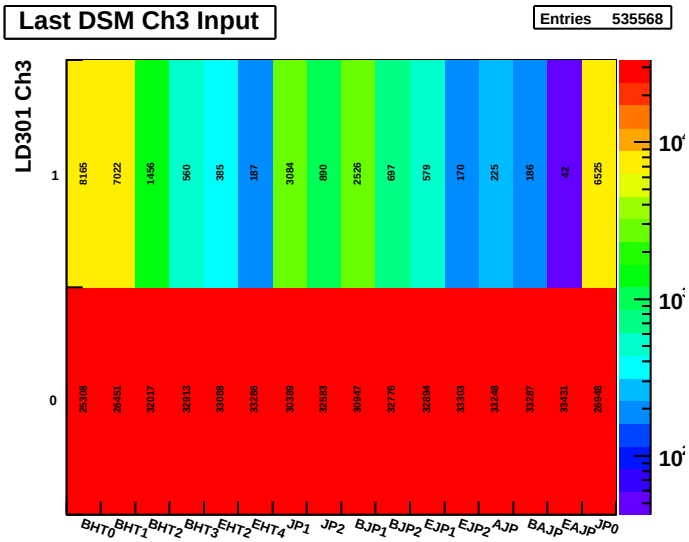

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

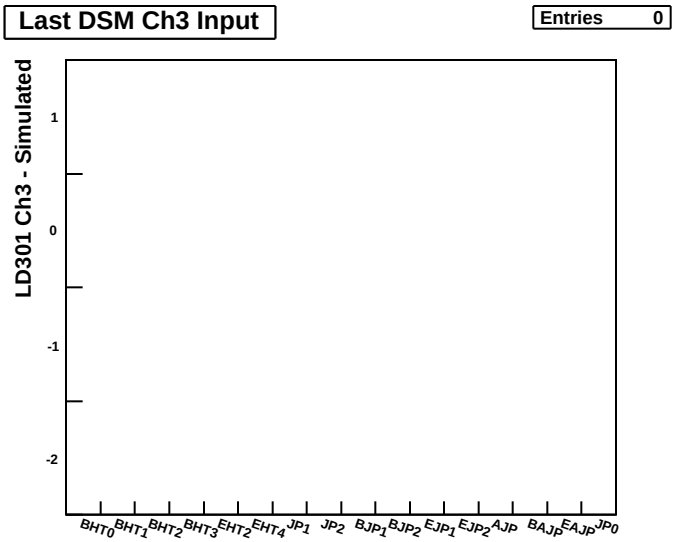

EMC L2 Input - HT01 bits/Partial JP ID
Entries 267784
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

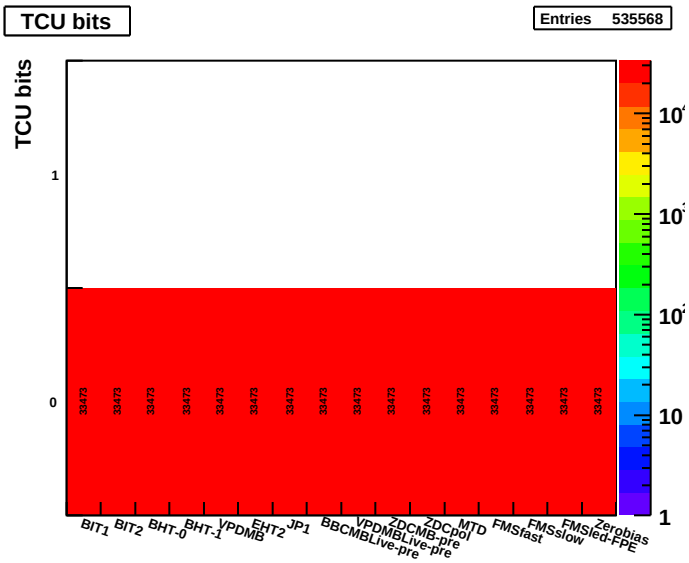

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

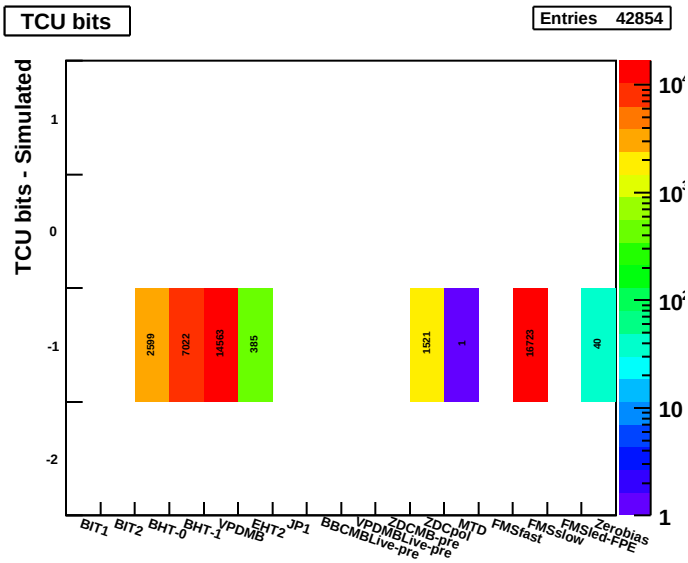

EMC L2 Input - HT23 bits
Entries 267784
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

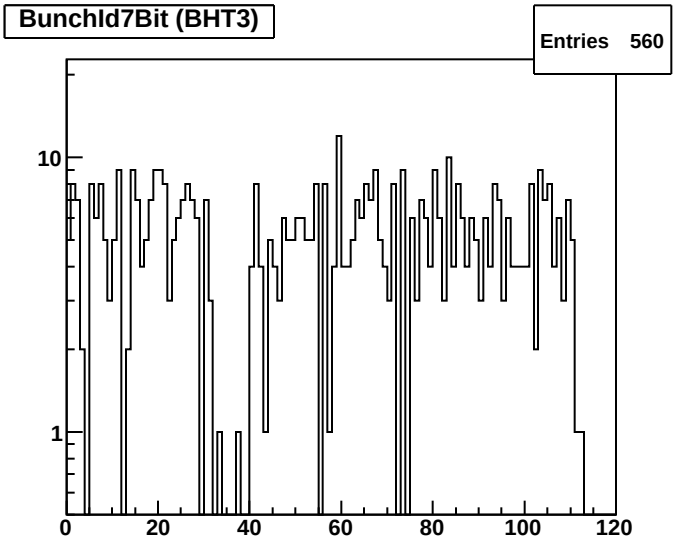

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

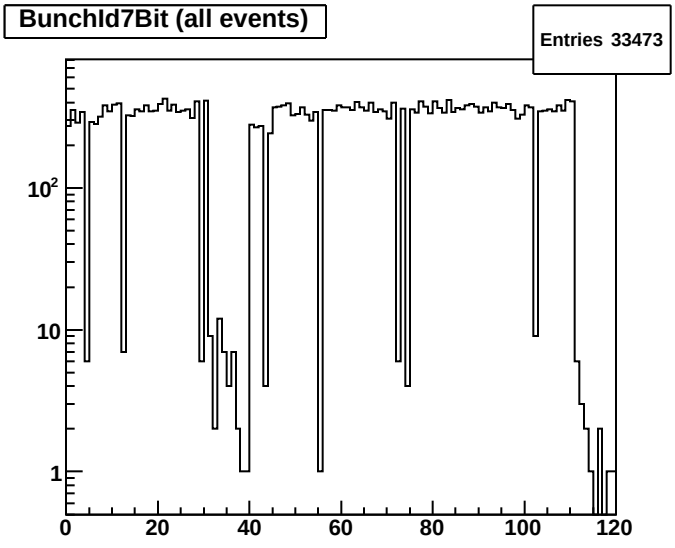

Last DSM Ch3 Input
Entries 535568
LD301 Ch3
8165 7022 1456 560 385 187 3084 890 2526 697 579 170 225 186 42 6525
25308 29451 32017 32913 33088 33266 30389 32563 30947 32776 32894 33303 33248 33287 33431 26948
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

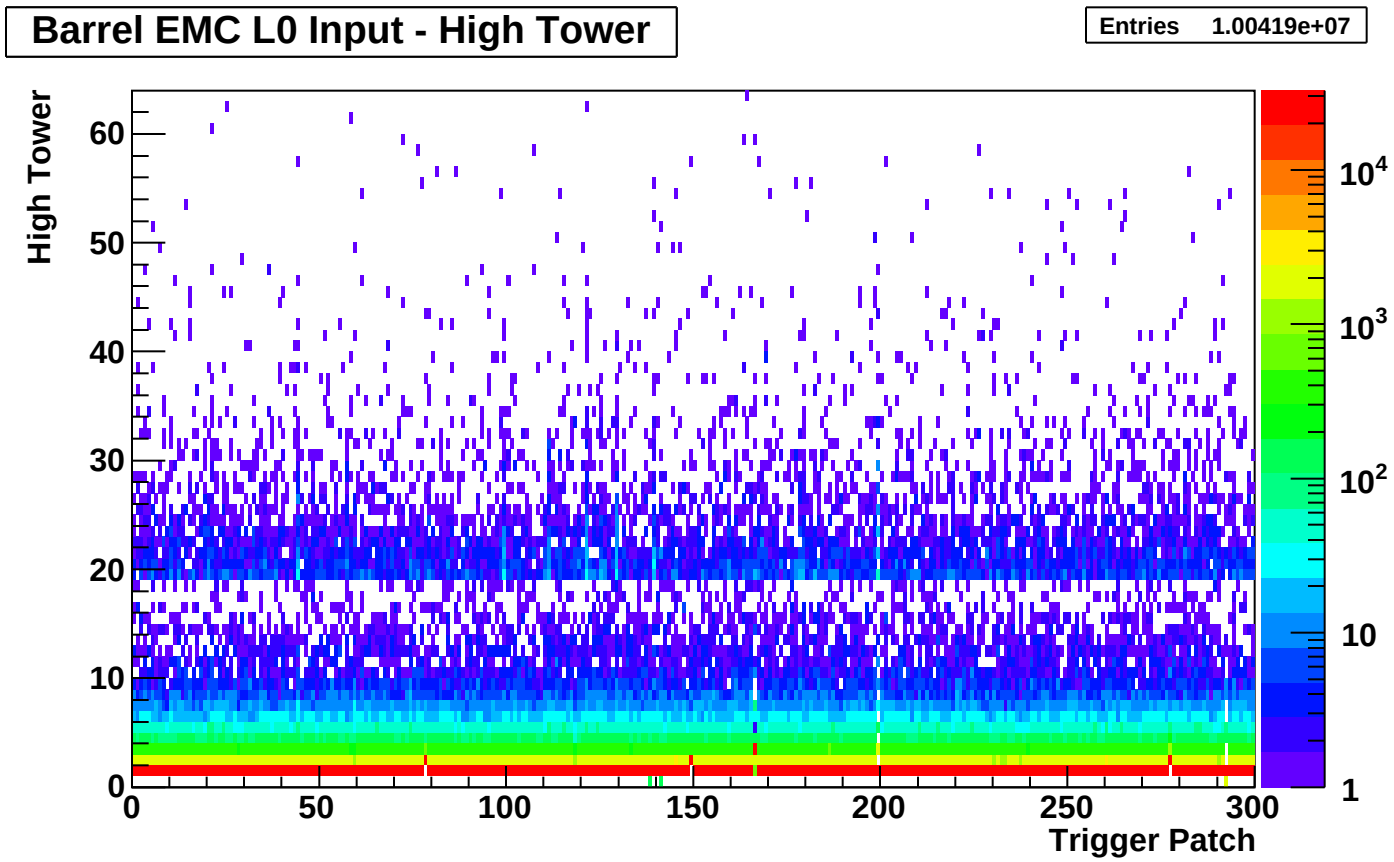

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

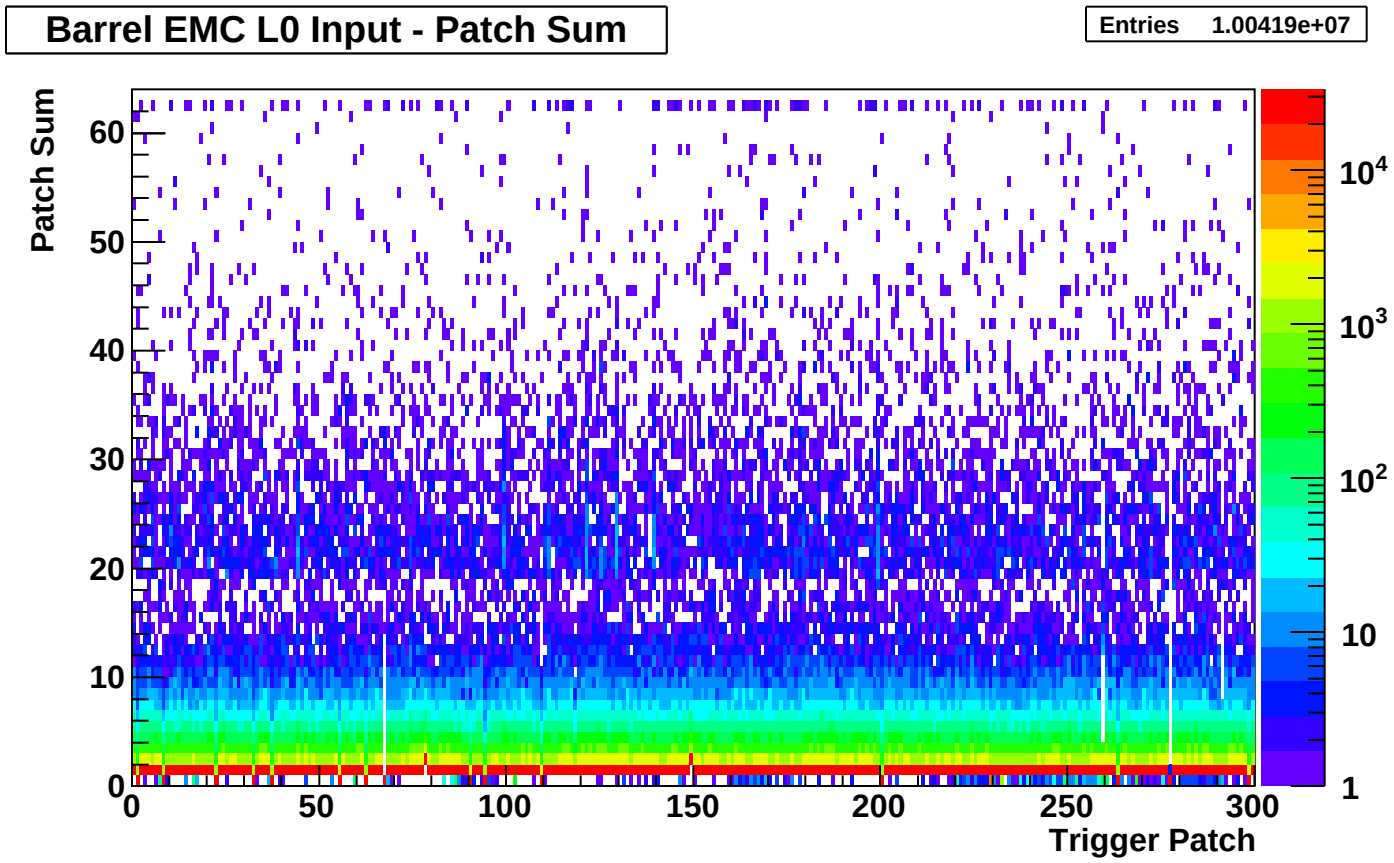

TCU bits
Entries 535568
TCU bits
33473 33473 33473 33473 33473 33473 33473 33473 33473 33473 33473 33473 33473 33473 33473 33473
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

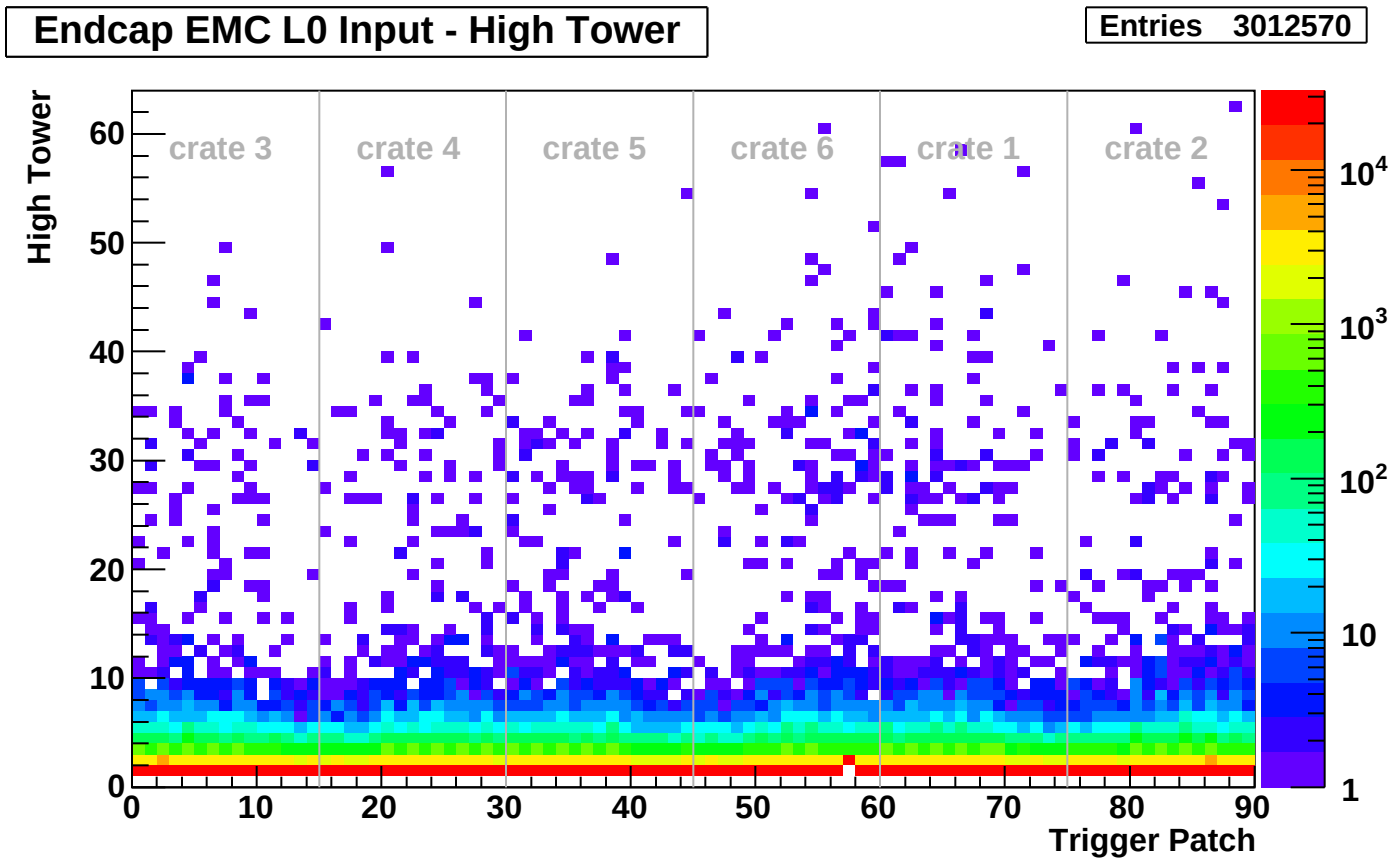

TCU bits
Entries 42854
TCU bits - Simulated
2599 7022 14563 385 1521 1 16723 40
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

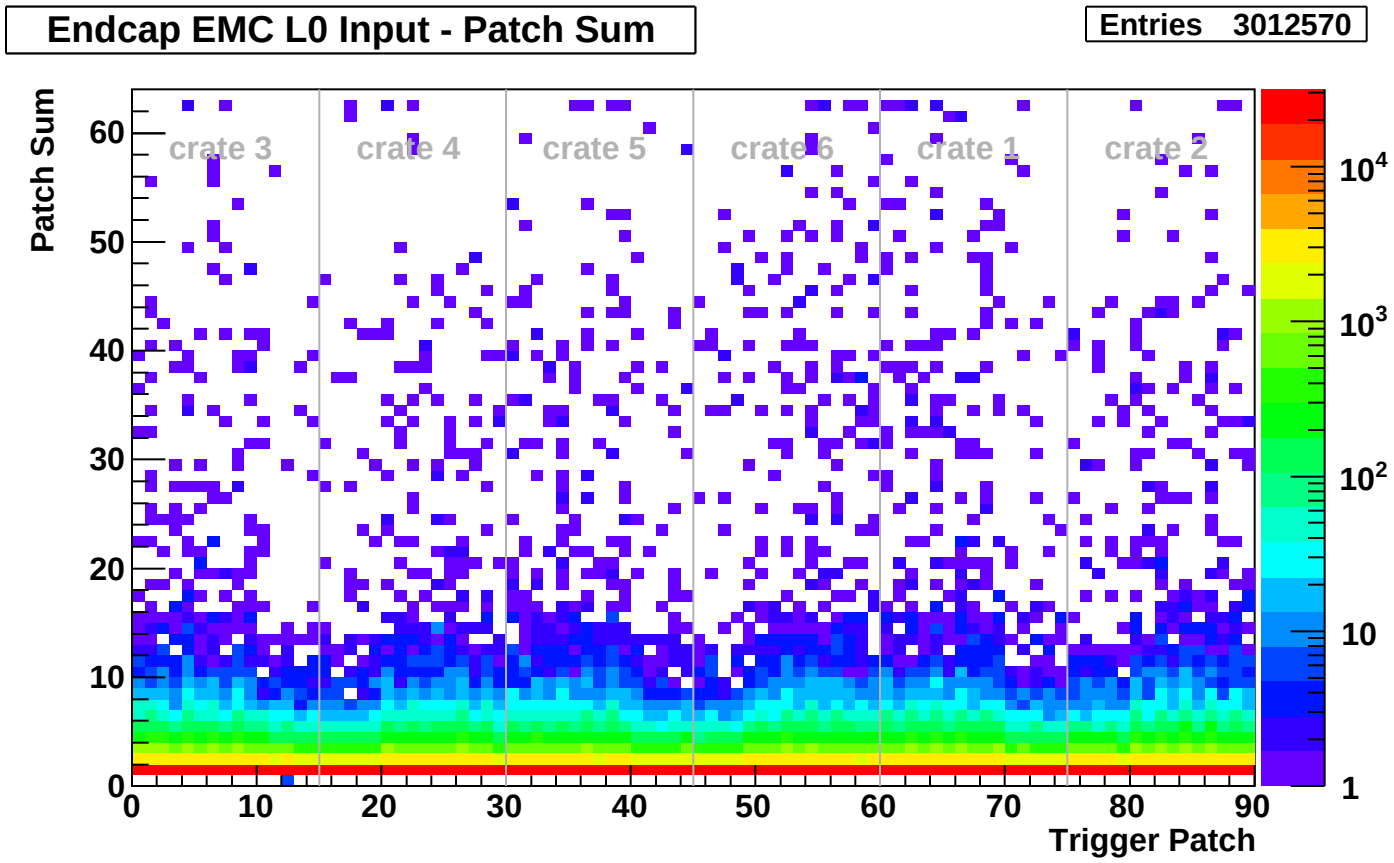

BunchId7Bit (BHT3)
Entries 560

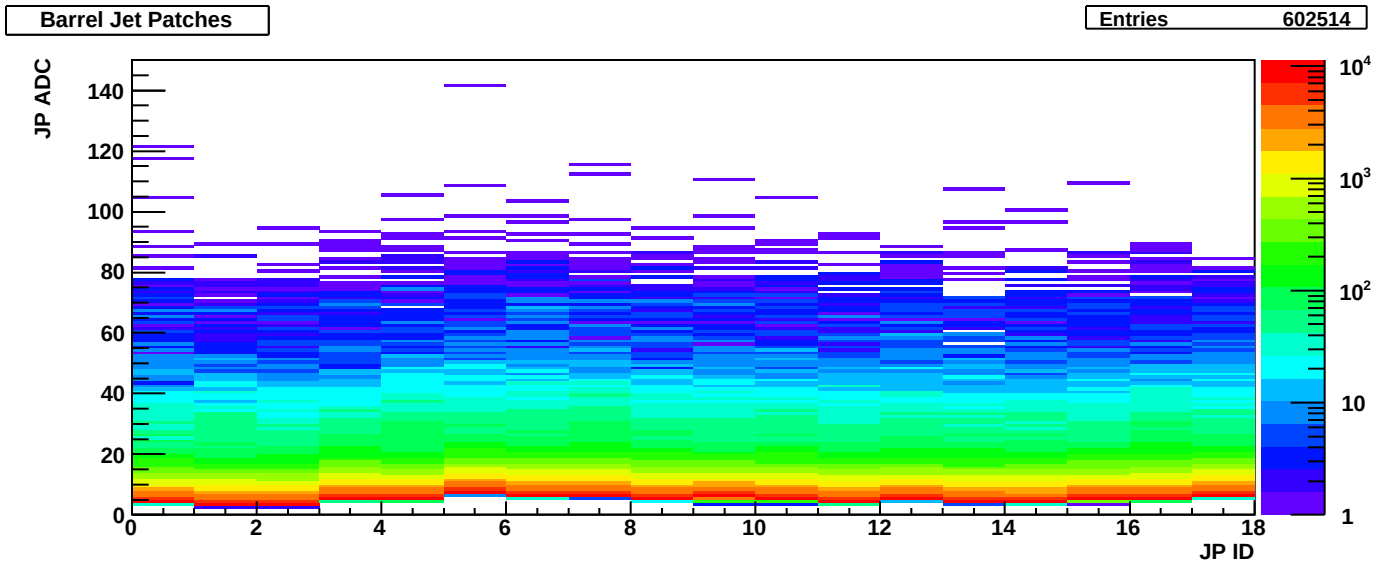

BunchId7Bit (all events)
Entries 33473

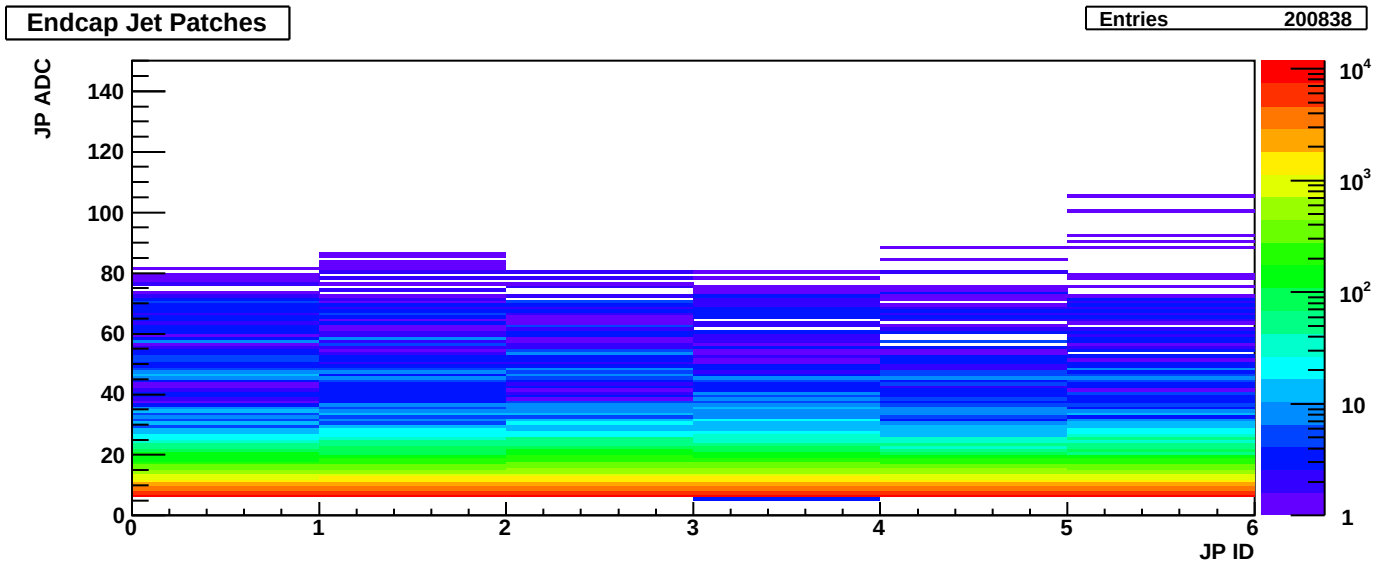

Barrel EMC L0 Input - High Tower
Entries 1.00419e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

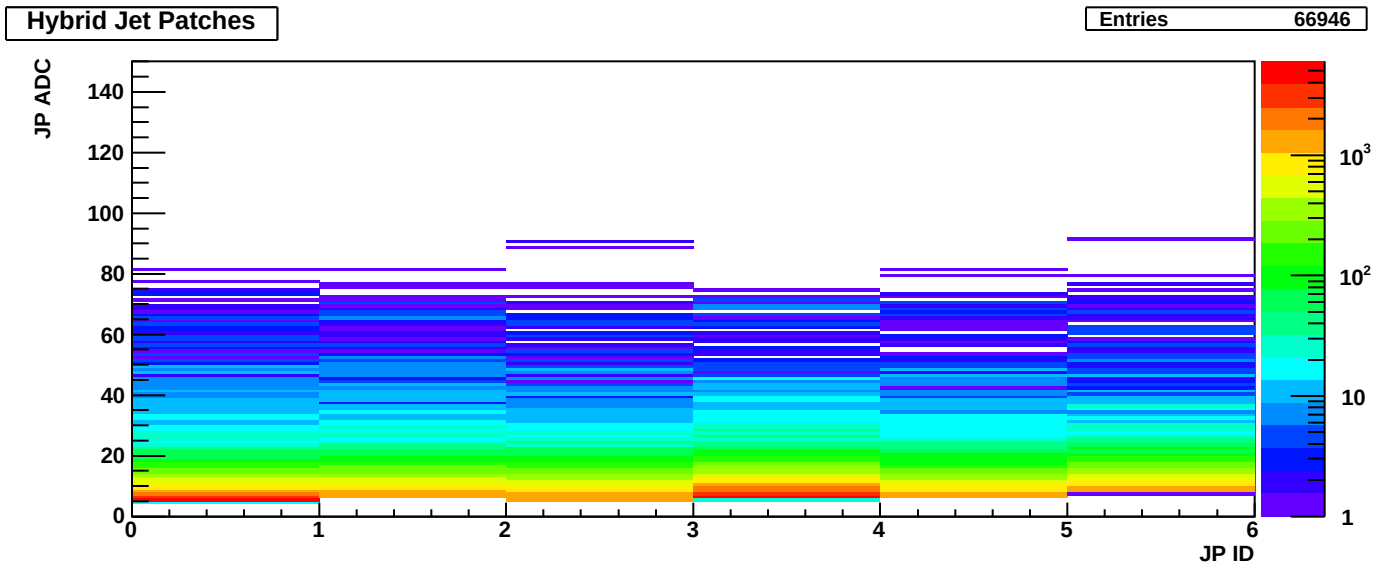

Barrel EMC L0 Input - Patch Sum
Entries 1.00419e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

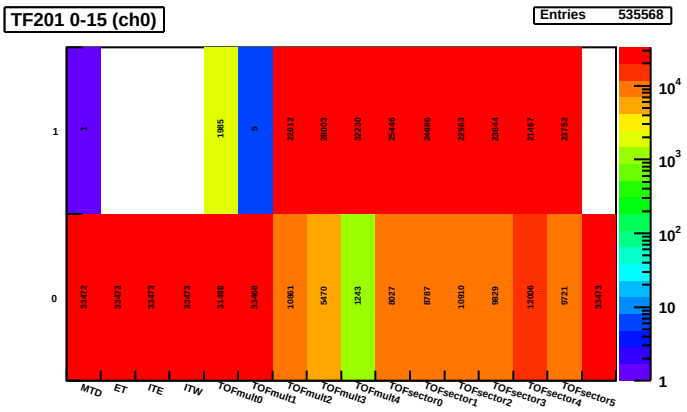

Endcap EMC L0 Input - High Tower
Entries 3012570
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

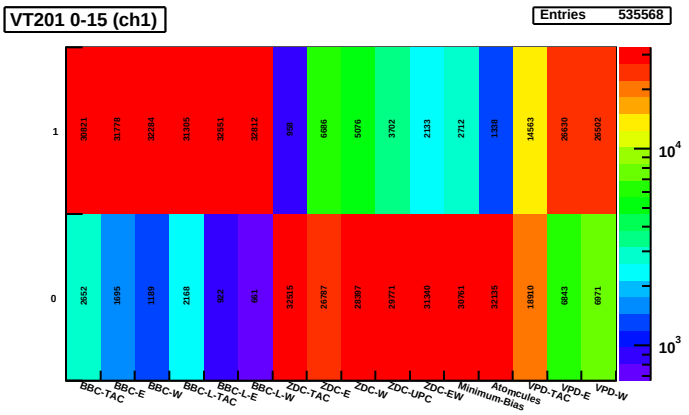

Endcap EMC L0 Input - Patch Sum
Entries 3012570
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

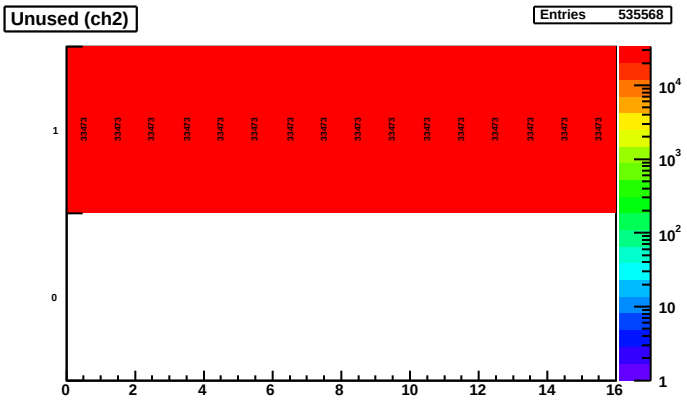

Barrel Jet Patches
Entries 602514
JP ADC
JP ID

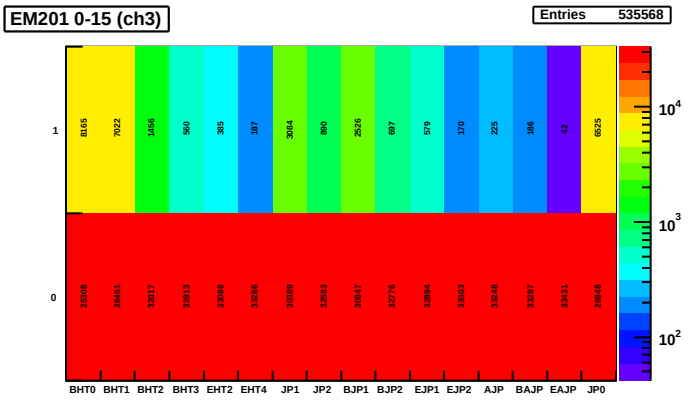

Endcap Jet Patches
Entries 200838
JP ADC
JP ID

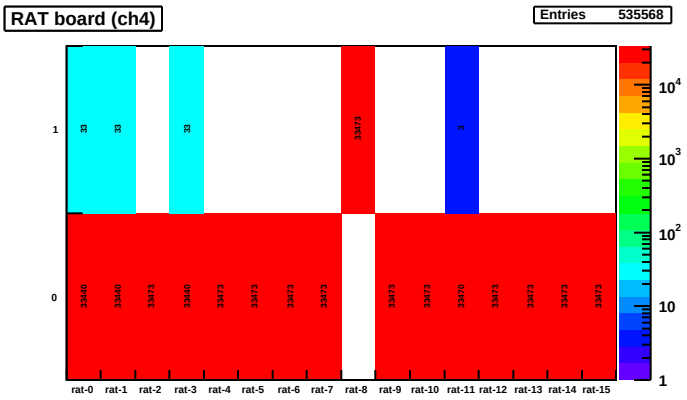

Hybrid Jet Patches
Entries 66946
JP ADC
JP ID

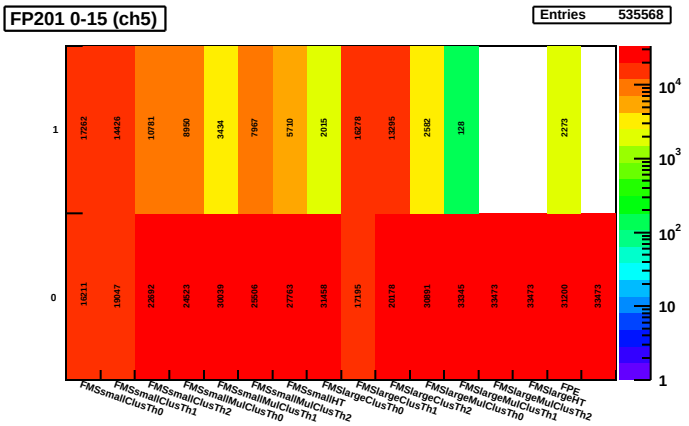

TF201 0-15 (ch0)
Entries 535568
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

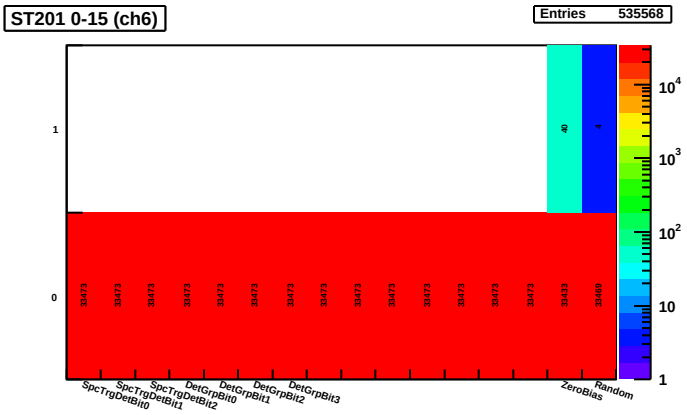

VT201 0-15 (ch1)
Entries 535568
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

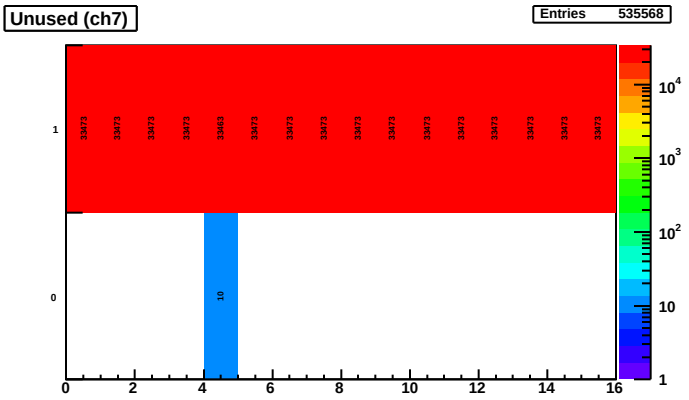

Unused (ch2)
Entries 535568

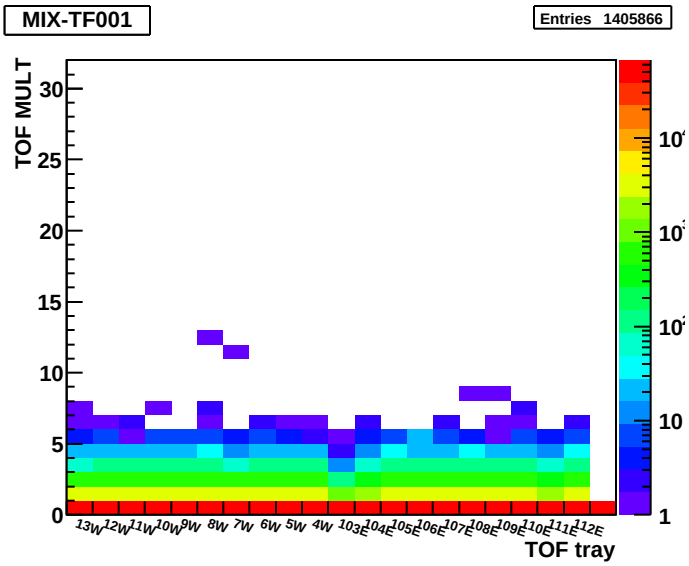

EM201 0-15 (ch3)
Entries 535568
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

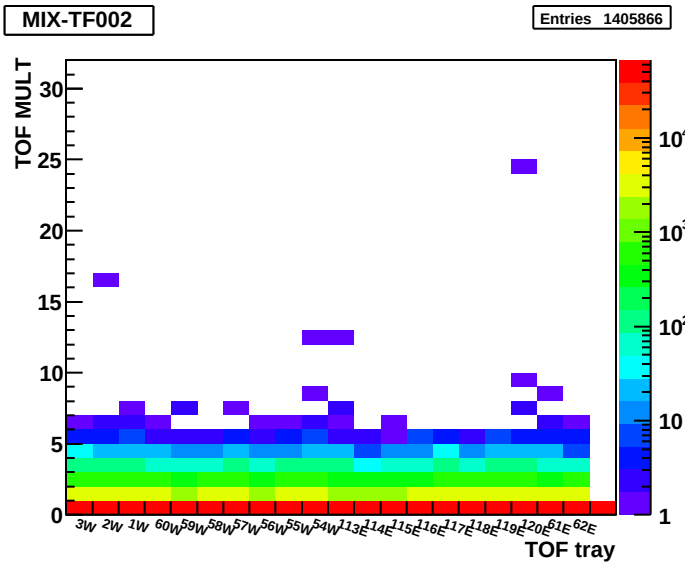

RAT board (ch4)
Entries 535568
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

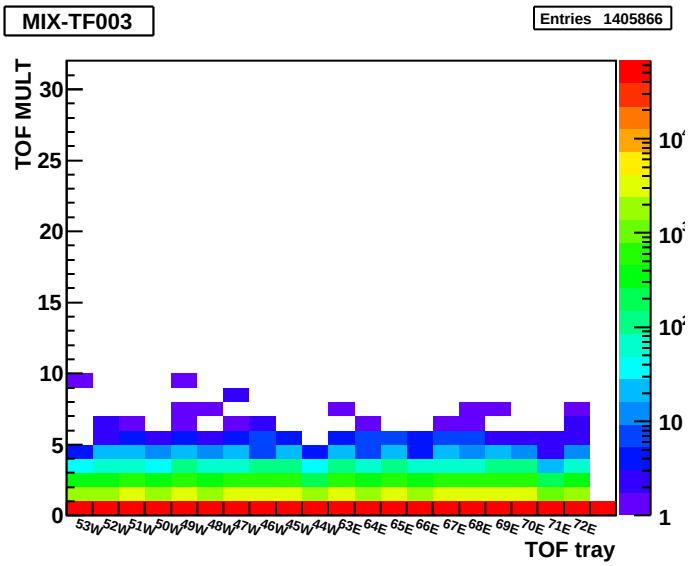

FP201 0-15 (ch5)
Entries 535568
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

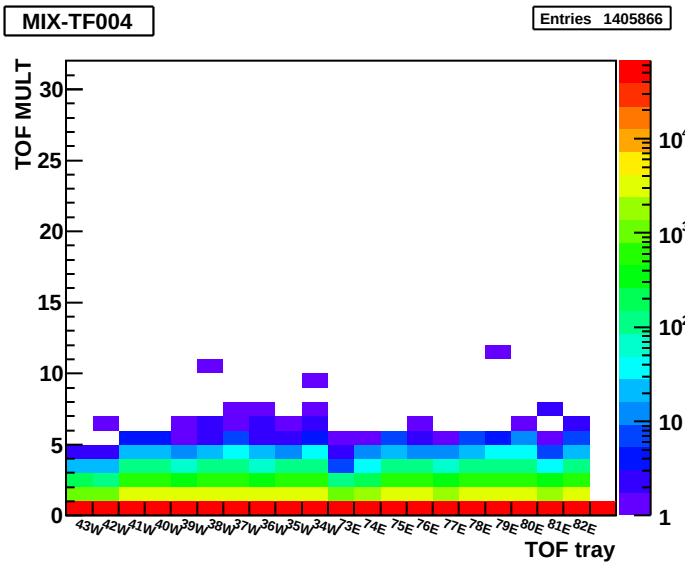

ST201 0-15 (ch6)
Entries 535568
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

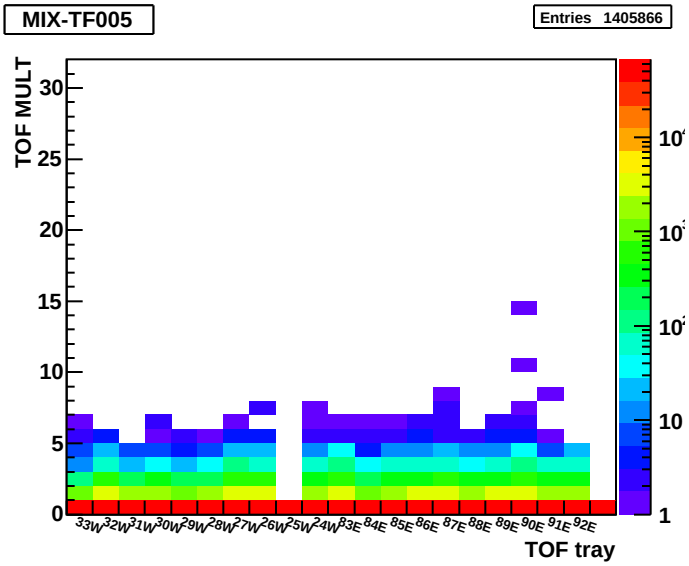

Unused (ch7)
Entries 535568

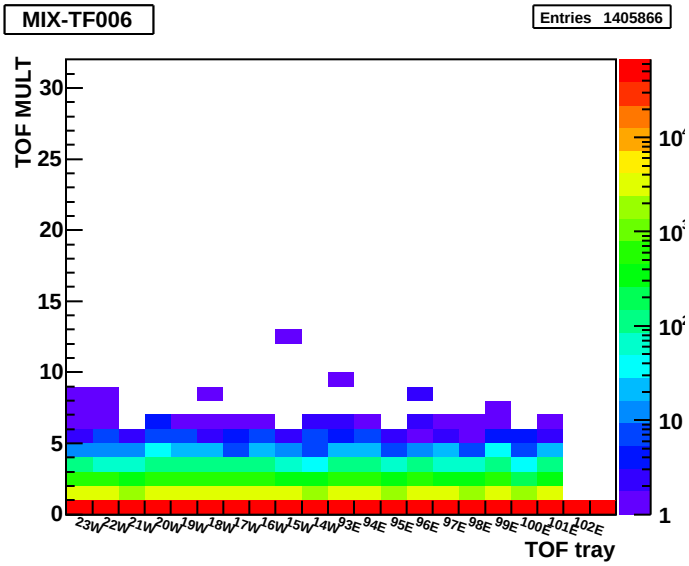

MIX-TF001
Entries 1405866
TOF MULT
TOF tray

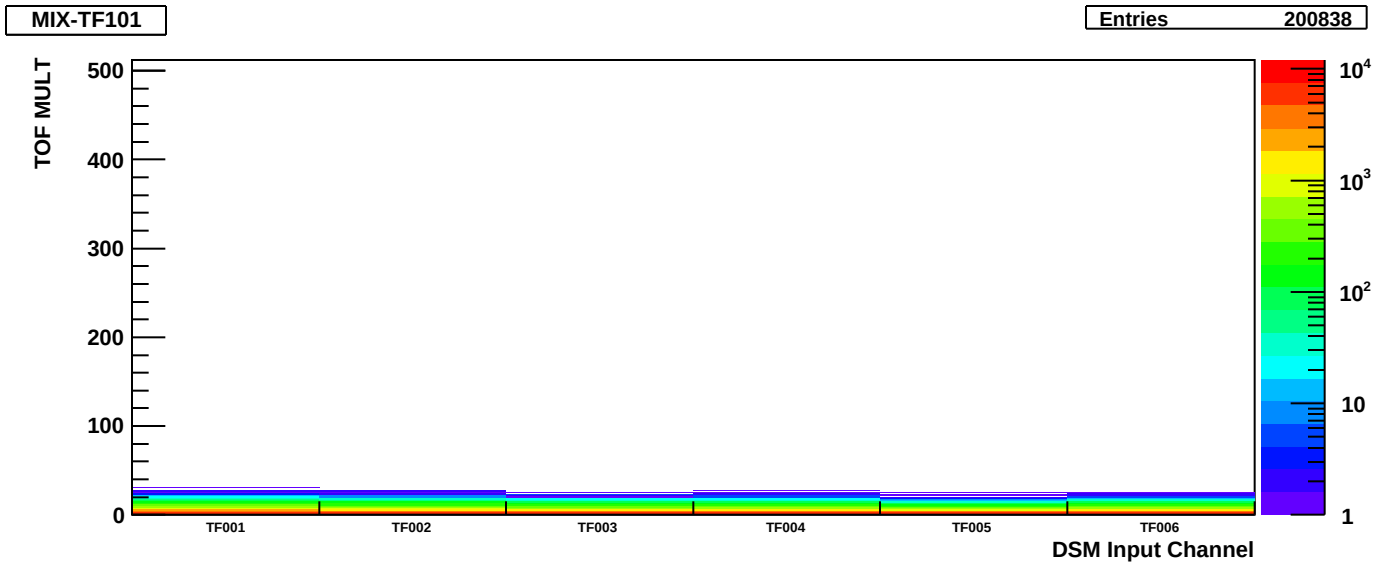

MIX-TF002
Entries 1405866
TOF MULT
TOF tray

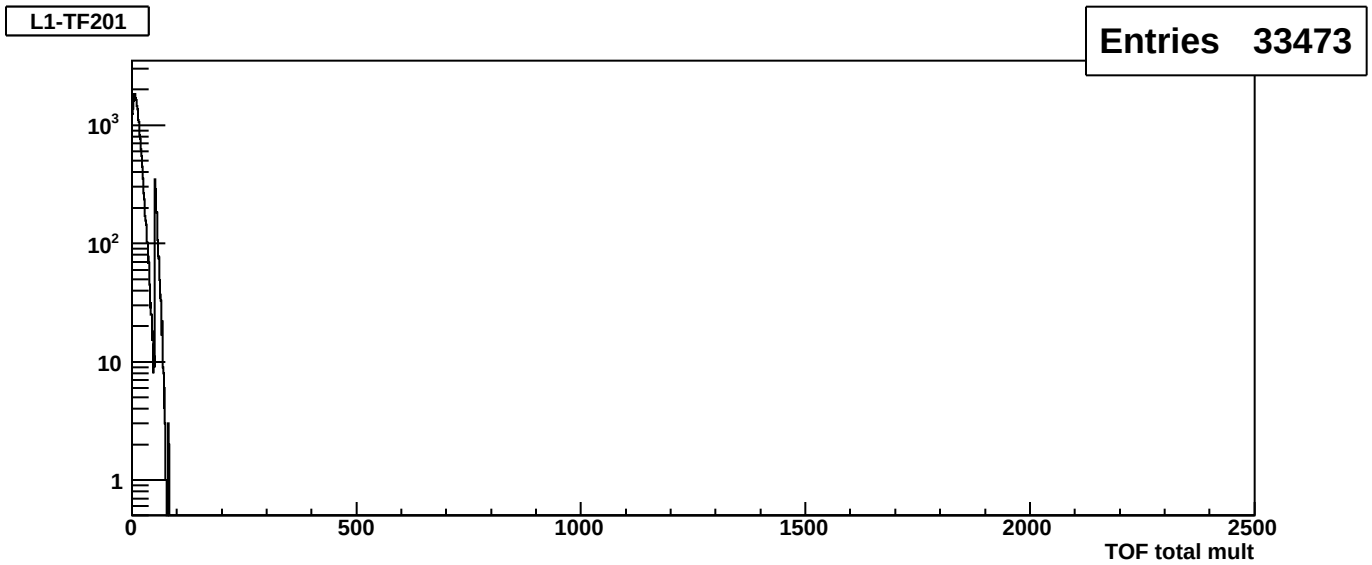

MIX-TF003
Entries 1405866
TOF MULT
TOF tray

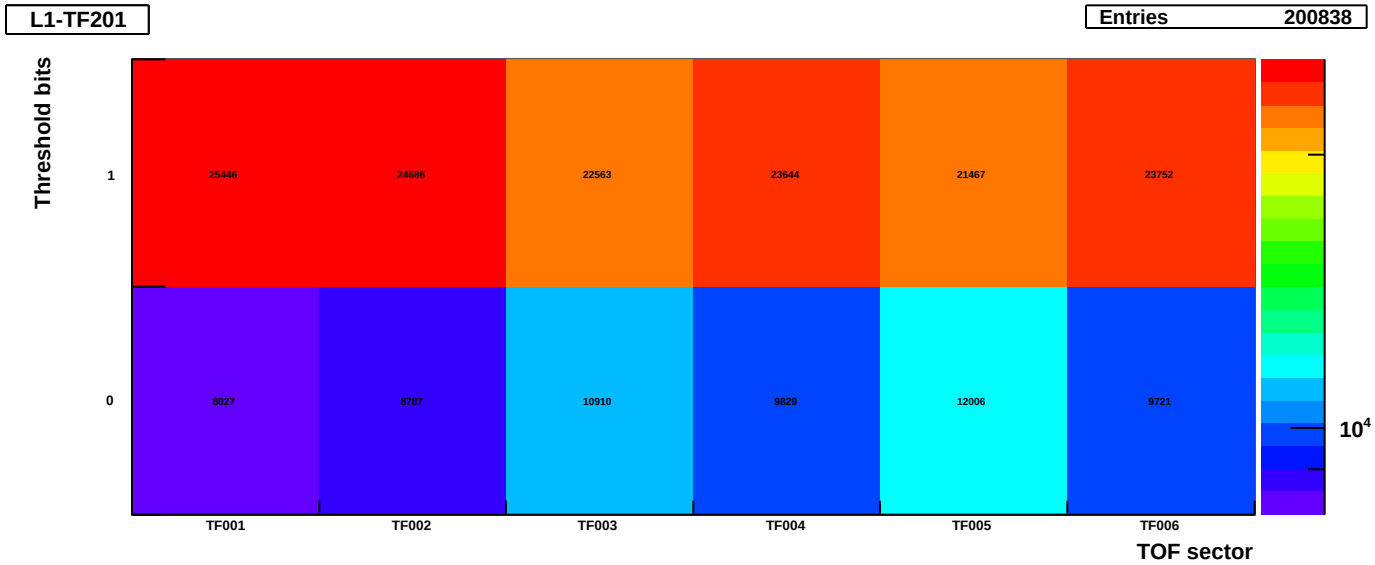

MIX-TF004
Entries 1405866
TOF MULT
TOF tray

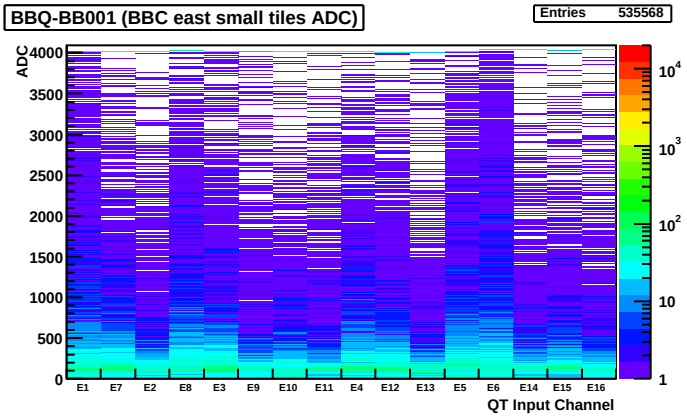

MIX-TF005
Entries 1405866
TOF MULT
TOF tray

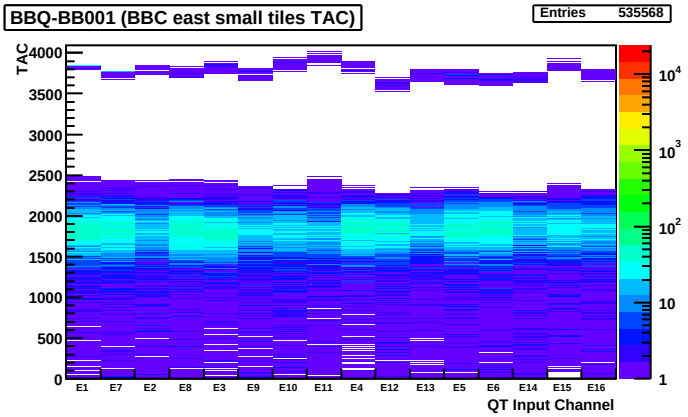

MIX-TF006
Entries 1405866
TOF MULT
TOF tray

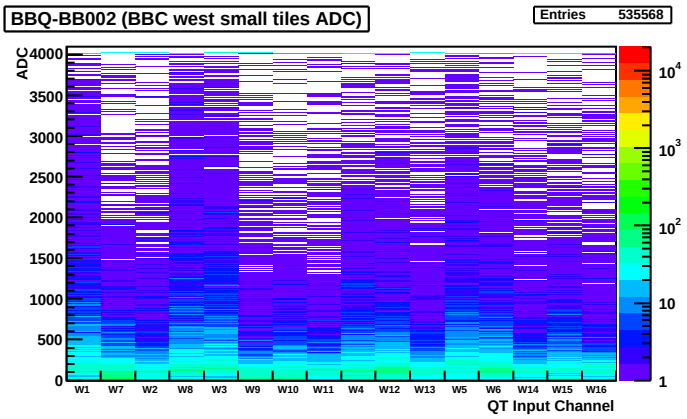

MIX-TF101
Entries 200838
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

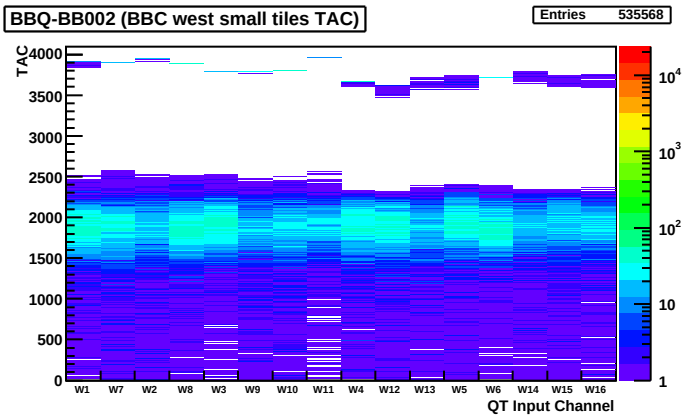

L1-TF201
Entries 33473
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

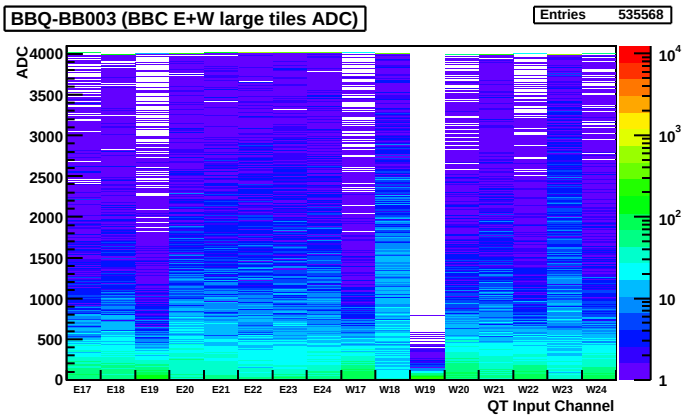

L1-TF201
Entries 200838
Threshold bits
1
0
25446
24686
22563
23644
21467
23752
8027
8787
10910
9829
12006
9721
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

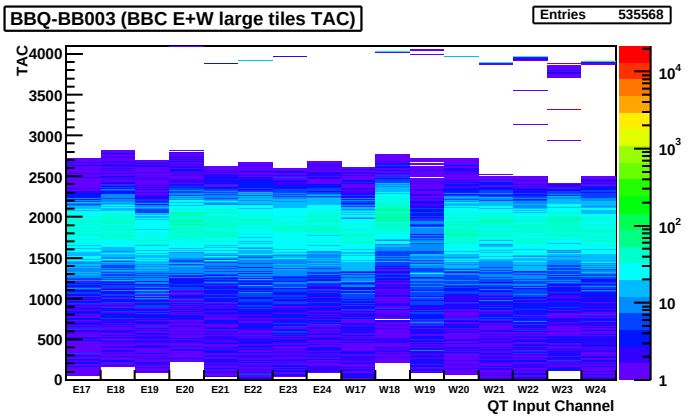

BBQ-BB001 (BBC east small tiles ADC)
Entries 535568
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

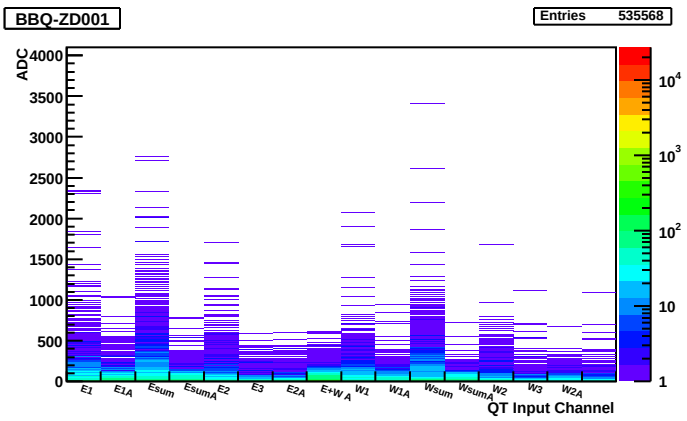

BBQ-BB001 (BBC east small tiles TAC)
Entries 535568
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

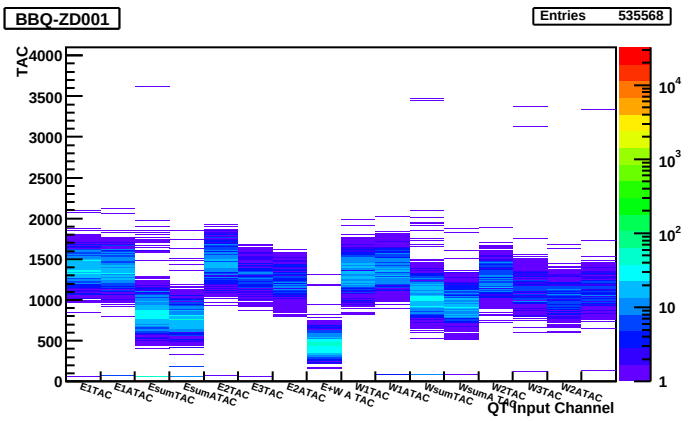

BBQ-BB002 (BBC west small tiles ADC)
Entries 535568
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

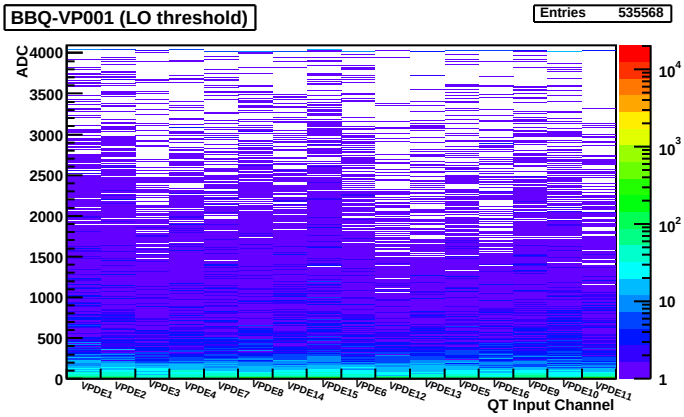

BBQ-BB002 (BBC west small tiles TAC)
Entries 535568
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

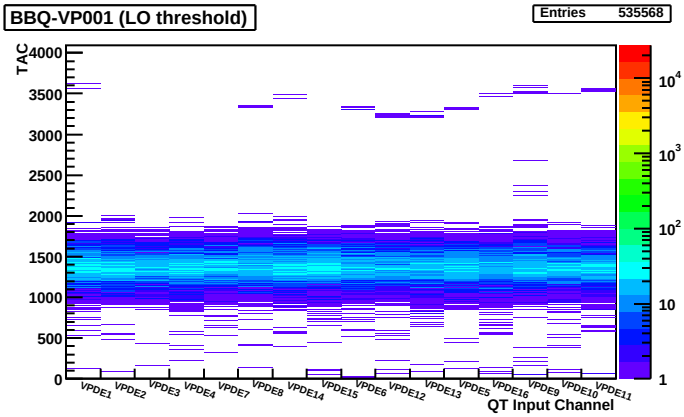

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 535568
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

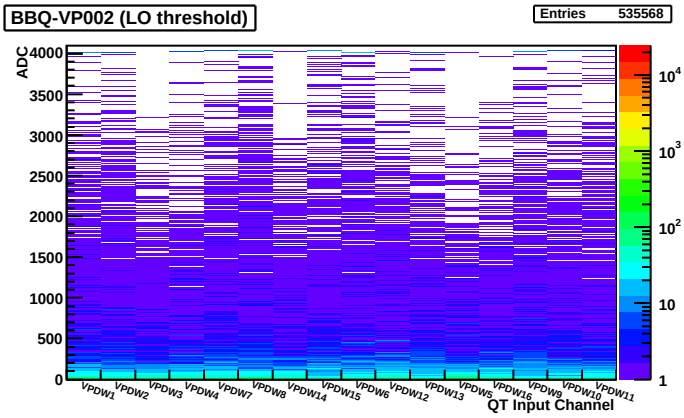

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 535568
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

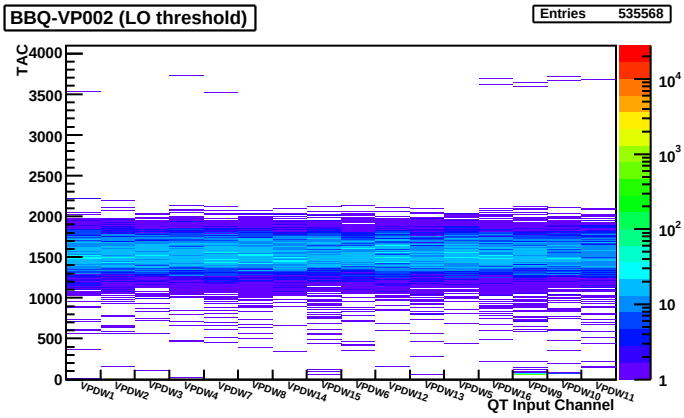

BBQ-ZD001
Entries 535568
ADC
QT Input Channel
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

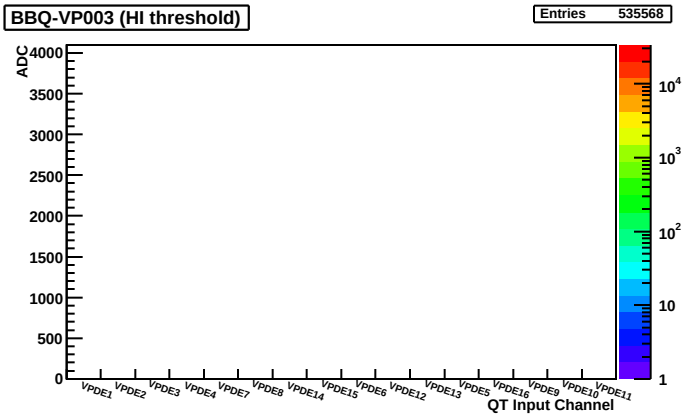

BBQ-ZD001
Entries 535568
TAC
QT Input Channel
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

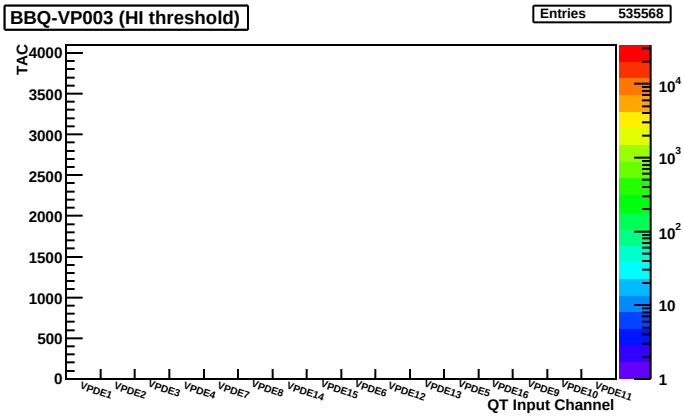

BBQ-VP001 (LO threshold)
Entries 535568
ADC
QT Input Channel

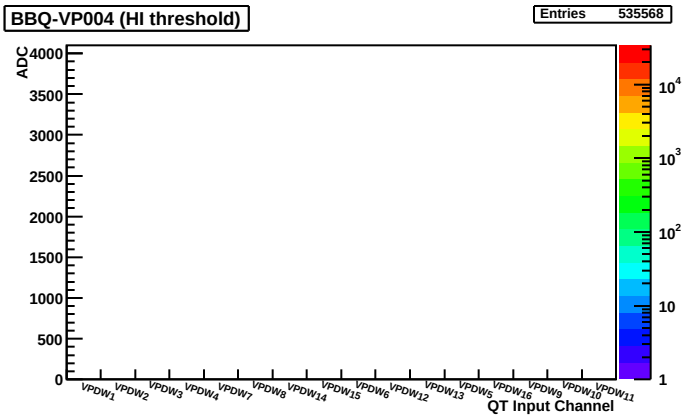

BBQ-VP001 (LO threshold)
Entries 535568
TAC
QT Input Channel

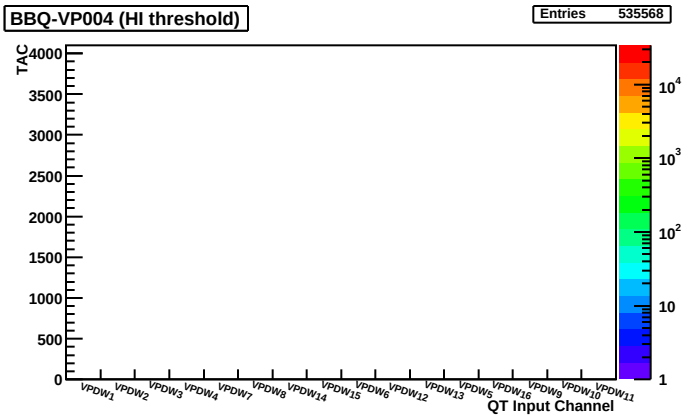

BBQ-VP002 (LO threshold)
Entries 535568
ADC
QT Input Channel

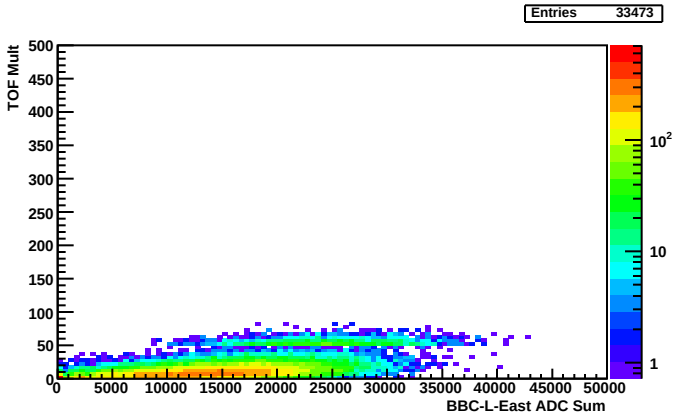

BBQ-VP002 (LO threshold)
Entries 535568
TAC
QT Input Channel

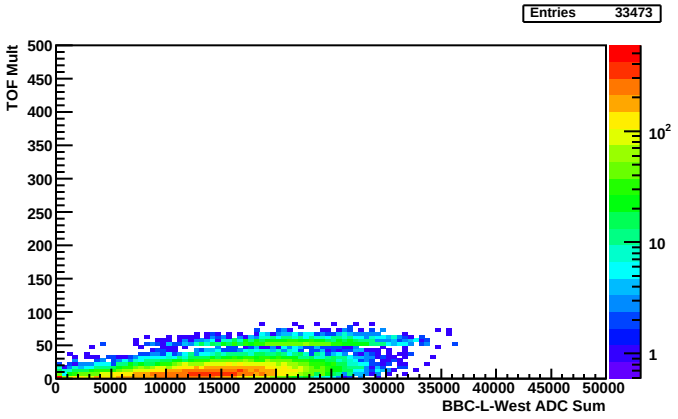

BBQ-VP003 (HI threshold)
Entries 535568
ADC
QT Input Channel

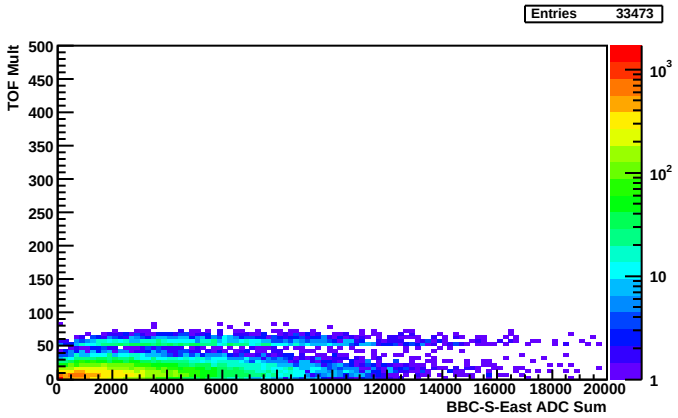

BBQ-VP003 (HI threshold)
Entries 535568
TAC
QT Input Channel

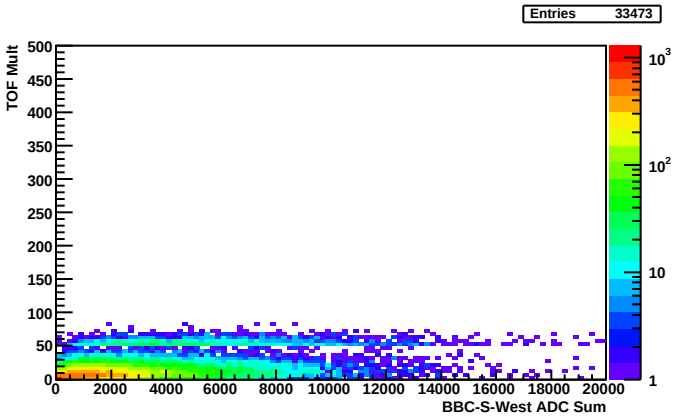

BBQ-VP004 (HI threshold)
Entries 535568
ADC
QT Input Channel

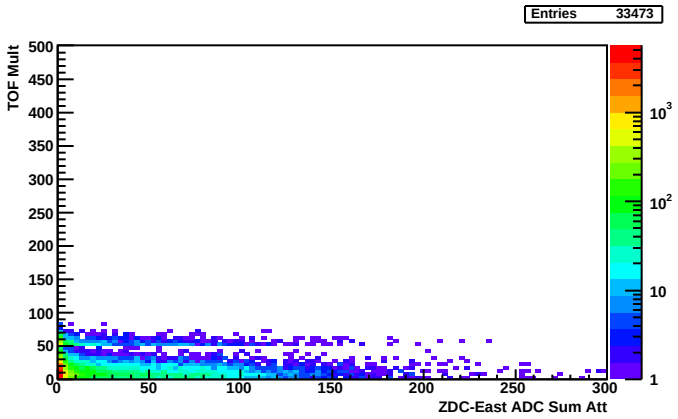

BBQ-VP004 (HI threshold)
Entries 535568
TAC
QT Input Channel

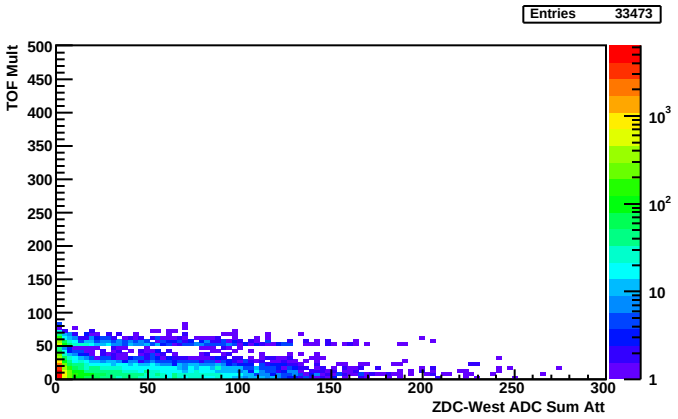

Entries 33473
TOF Mult
BBC-L-East ADC Sum

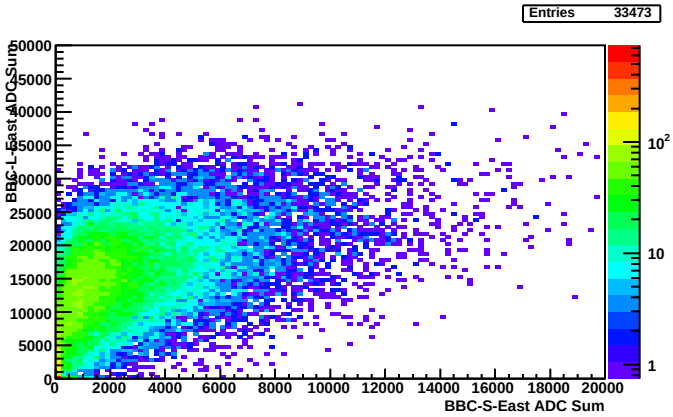

Entries 33473
TOF Mult
BBC-L-West ADC Sum

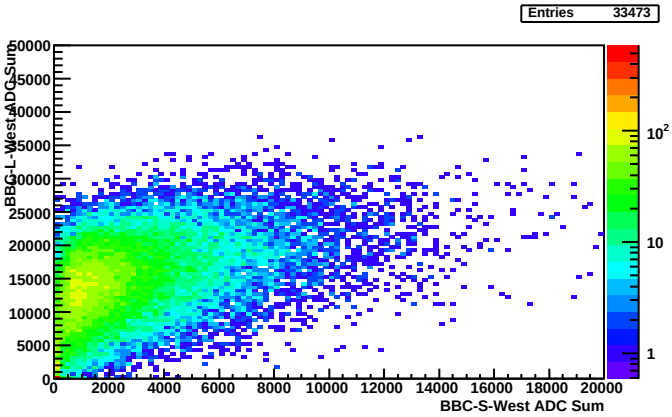

Entries 33473
TOF Mult
BBC-S-East ADC Sum

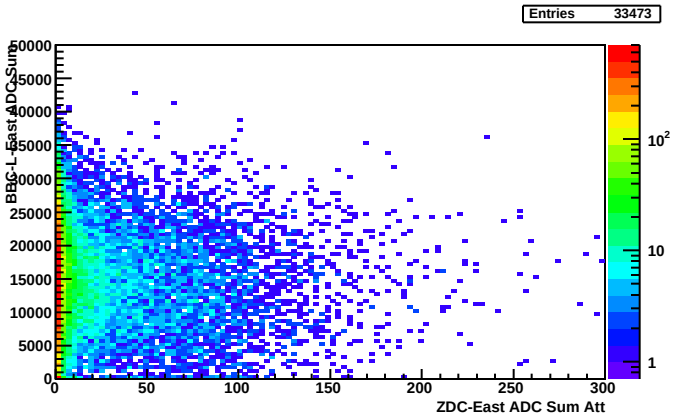

Entries 33473
TOF Mult
BBC-S-West ADC Sum

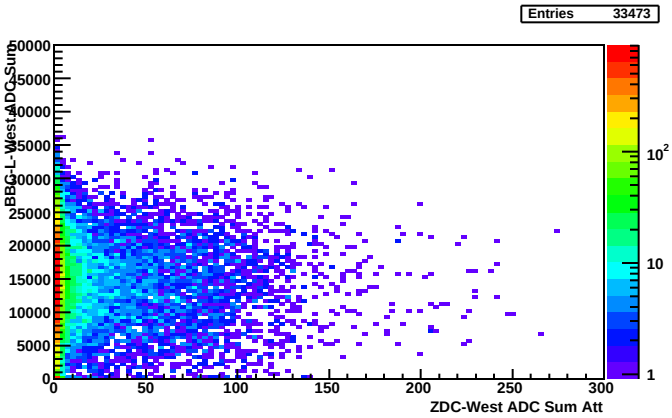

Entries 33473
TOF Mult
ZDC-East ADC Sum Att

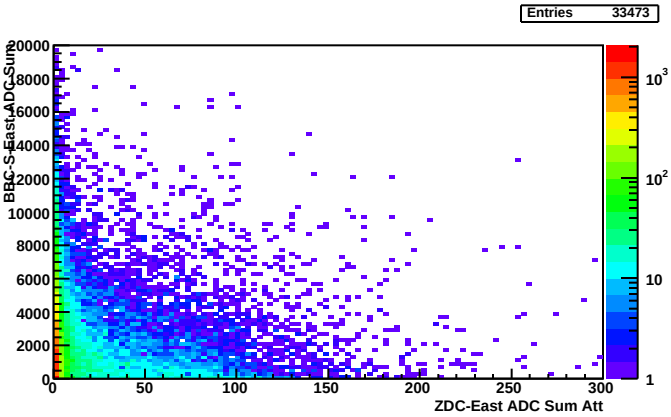

Entries 33473
TOF Mult
ZDC-West ADC Sum Att

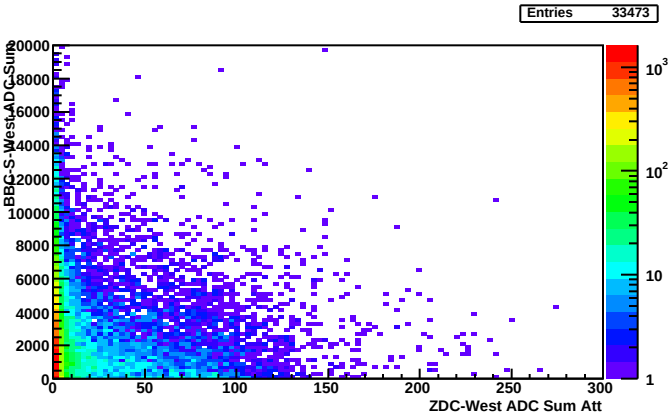

Entries 33473
BBC-L-East ADC Sum
BBC-S-East ADC Sum

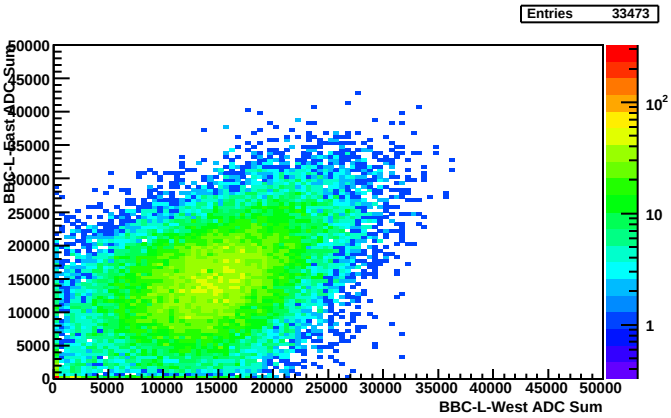

Entries 33473
BBC-L-West ADC Sum
BBC-S-West ADC Sum

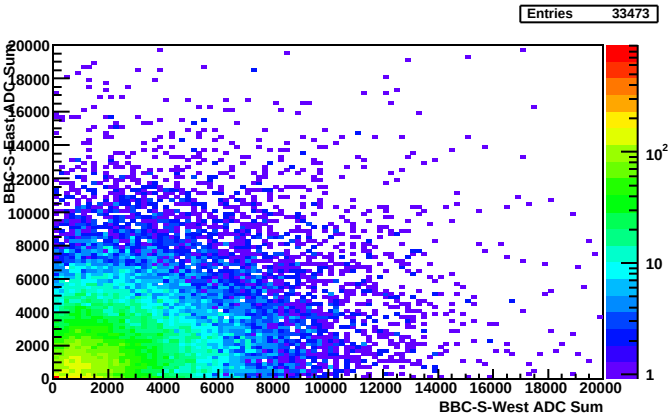

Entries 33473
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

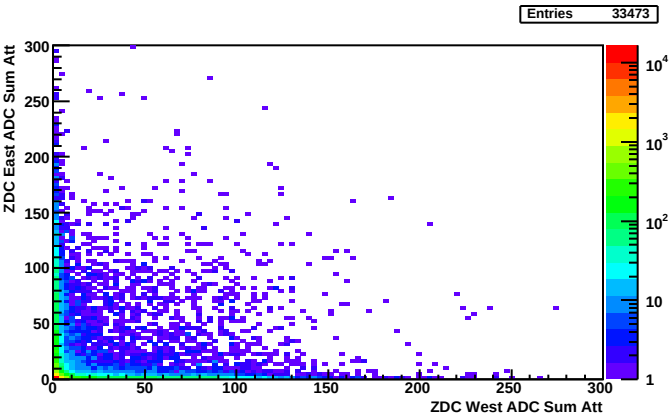

Entries 33473
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

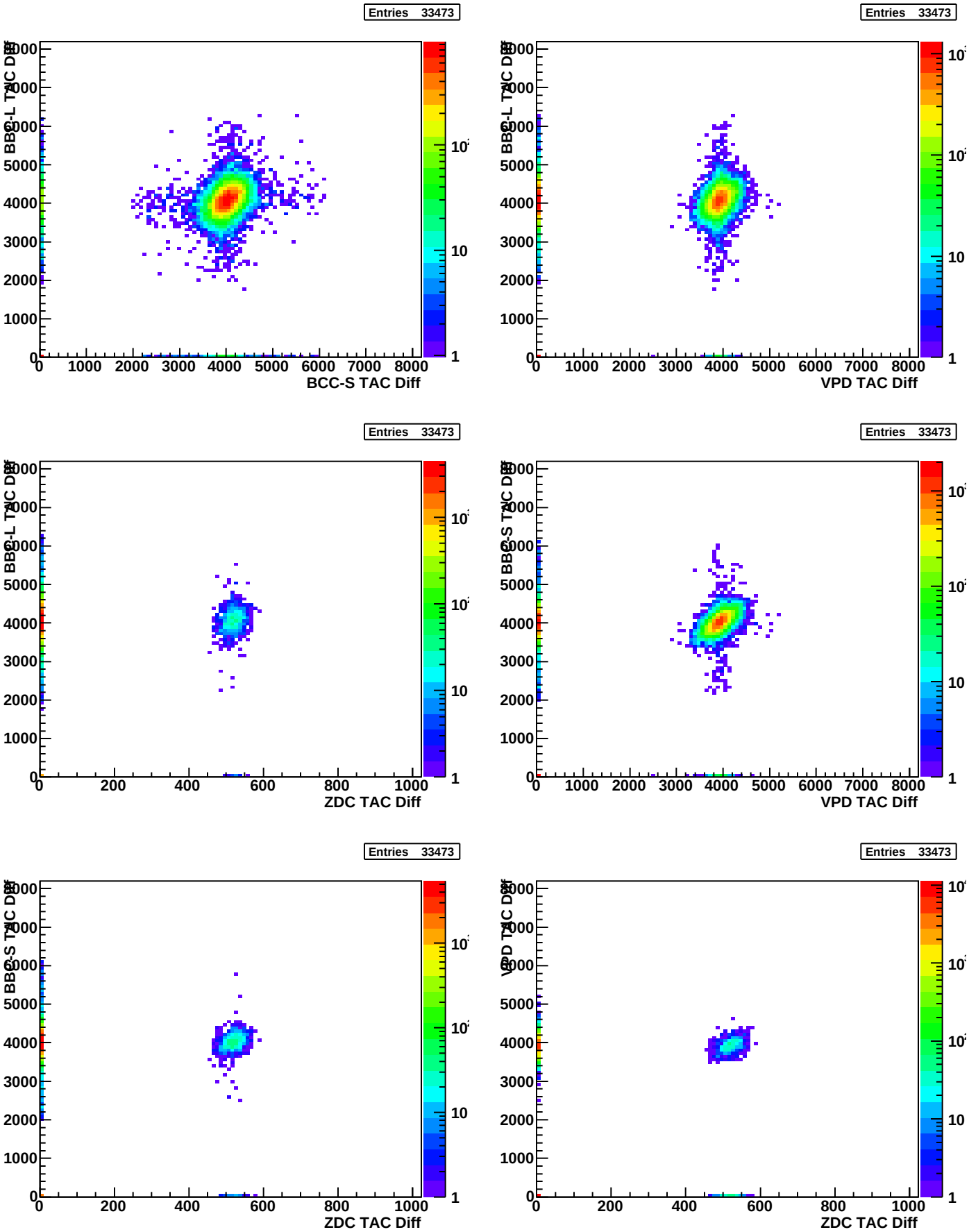

Entries 33473
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

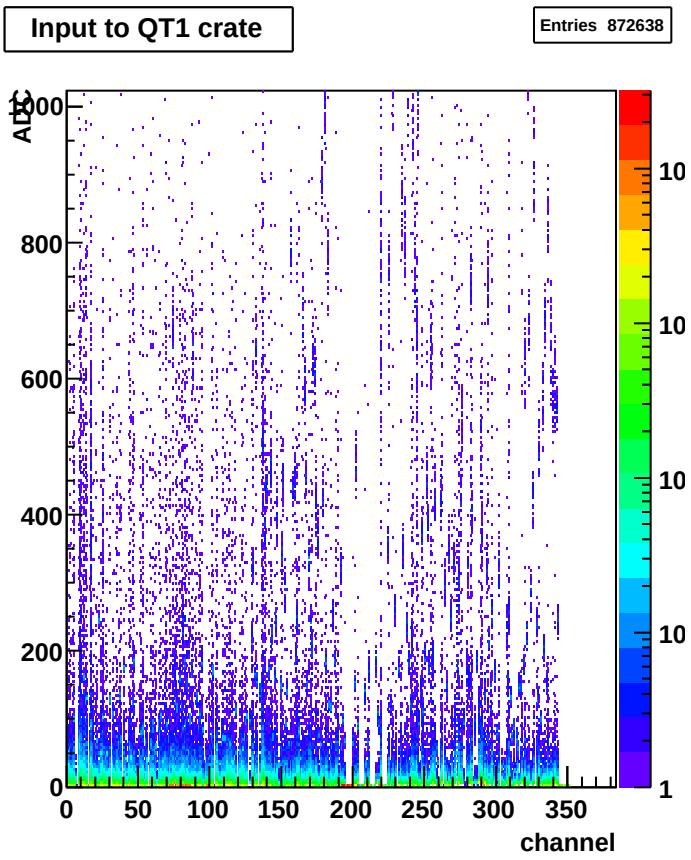

Entries 33473
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

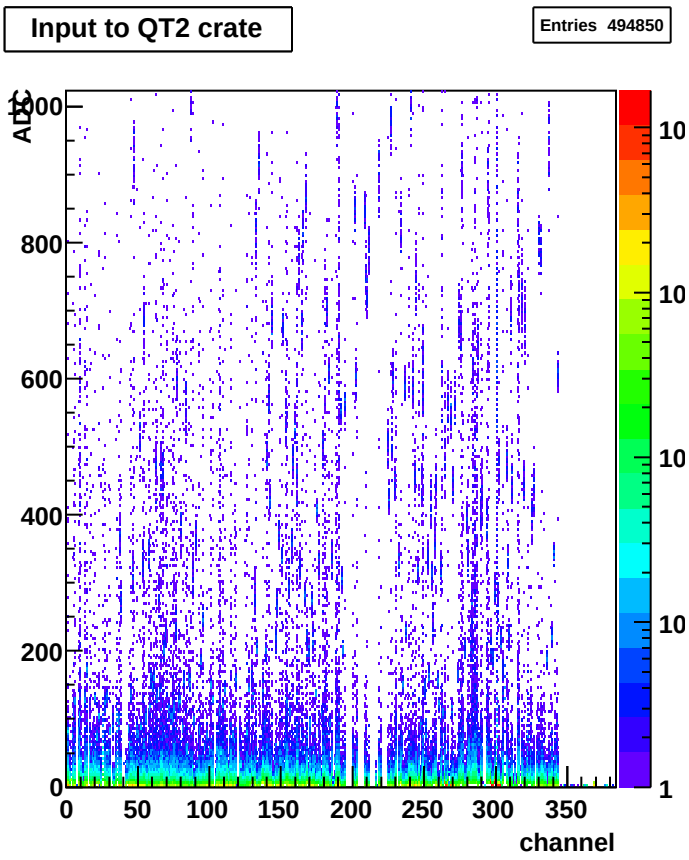

Entries 33473
BBC-L-East ADC Sum
BBC-L-West ADC Sum

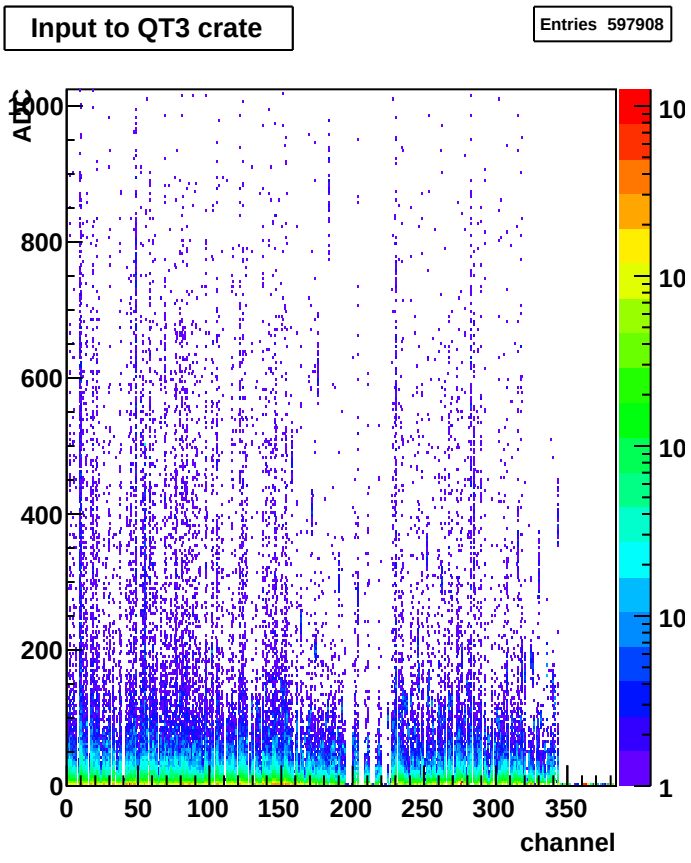

Entries 33473
BBC-S-East ADC Sum
BBC-S-West ADC Sum

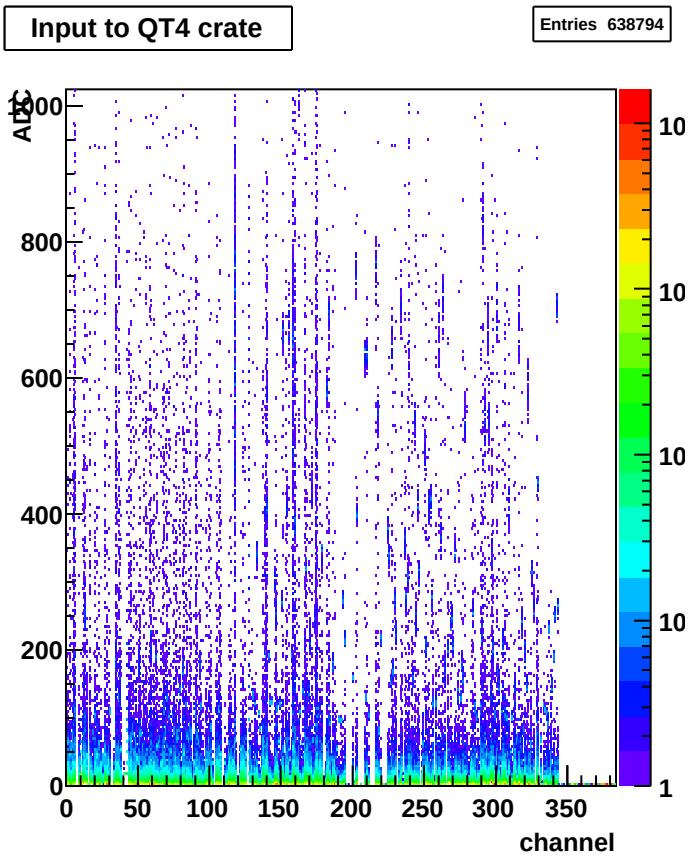

Entries 33473
ZDC East ADC Sum Att
ZDC West ADC Sum Att

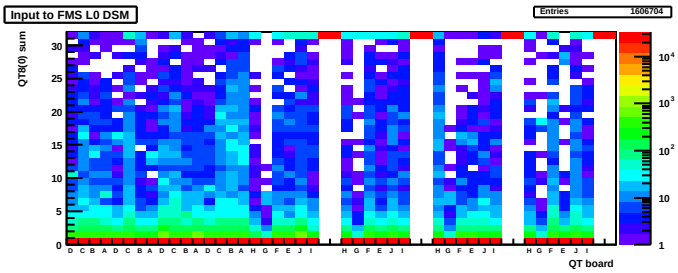

Entries 33473
BBC-L TAC Diff
BCC-S TAC Diff
Entries 33473
BBC-L TAC Diff
VPD TAC Diff
Entries 33473
BBC-L TAC Diff
ZDC TAC Diff
Entries 33473
BBC-S TAC Diff
VPD TAC Diff
Entries 33473
BBC-S TAC Diff
ZDC TAC Diff
Entries 33473
VPD TAC Diff
ZDC TAC Diff

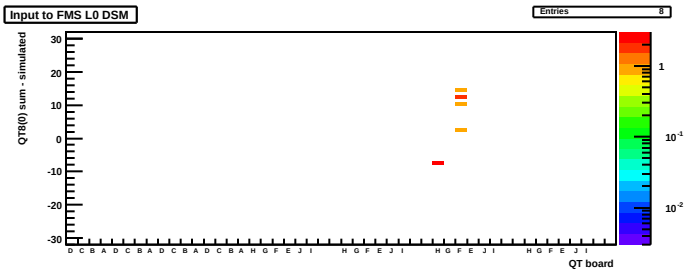

Input to QT1 crate
Entries 872638
ADC
channel

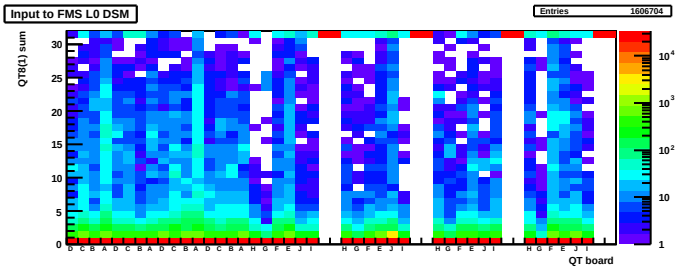

Input to QT2 crate
Entries 494850
ADC
channel

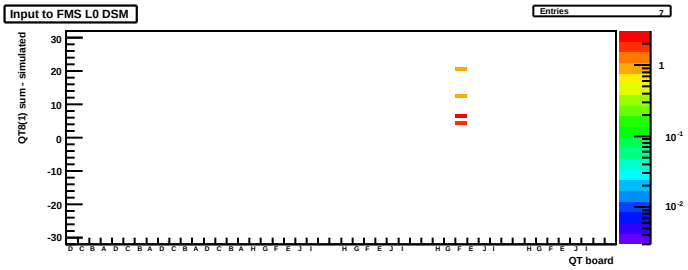

Input to QT3 crate
Entries 597908
ADC
channel

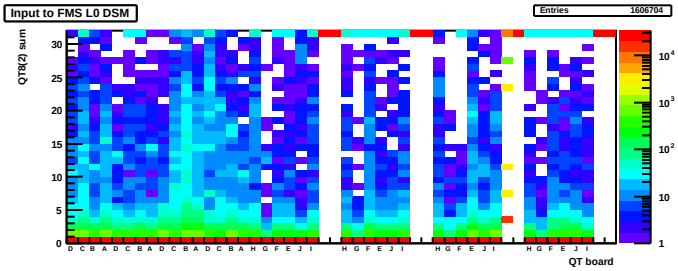

Input to QT4 crate
Entries 638794
ADC
channel

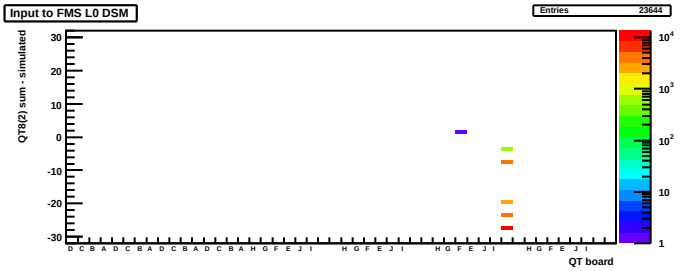

Input to FMS L0 DSM
Entries 1606704
QT8(0) sum
QT board

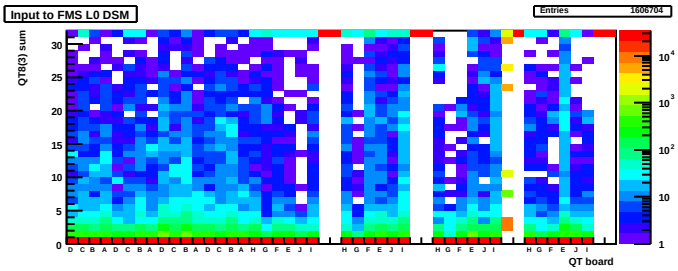

Input to FMS L0 DSM
Entries 8
QT8(0) sum - simulated
QT board

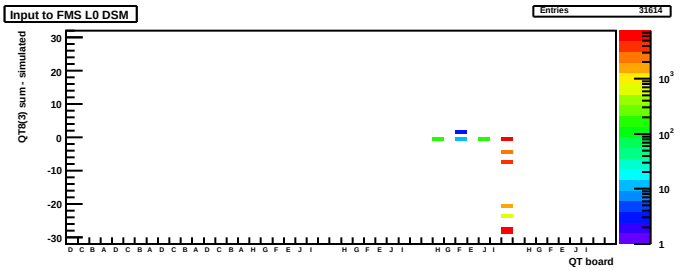

Input to FMS L0 DSM
Entries 1606704
QT8(1) sum
QT board

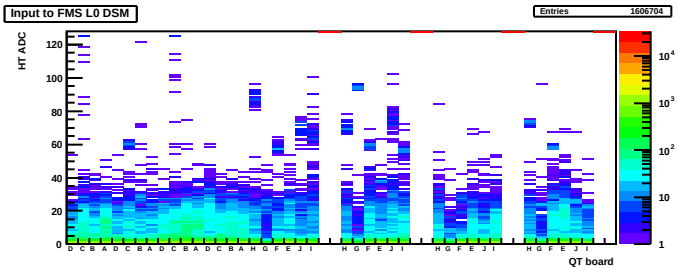

Input to FMS L0 DSM
Entries 7
QT8(1) sum - simulated
QT board

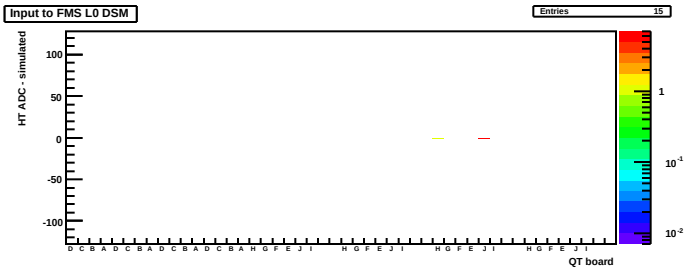

Input to FMS L0 DSM
Entries 1606704
QT8(2) sum
QT board

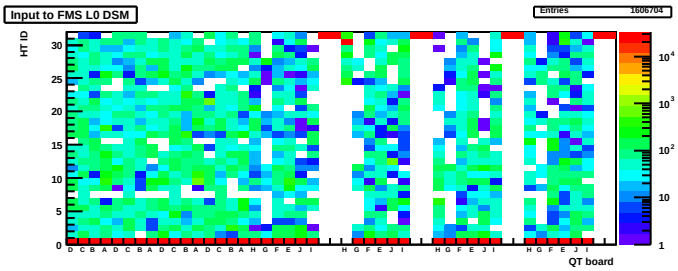

Input to FMS L0 DSM
Entries 23644
QT8(2) sum - simulated
QT board

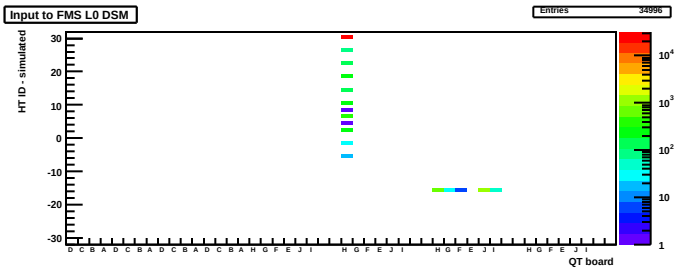

Input to FMS L0 DSM
Entries 1606704
QT8(3) sum
QT board

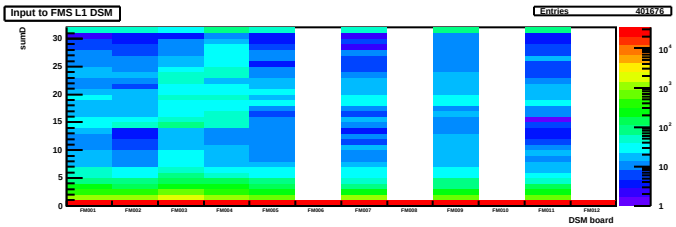

Input to FMS L0 DSM
Entries 31614
QT8(3) sum - simulated
QT board

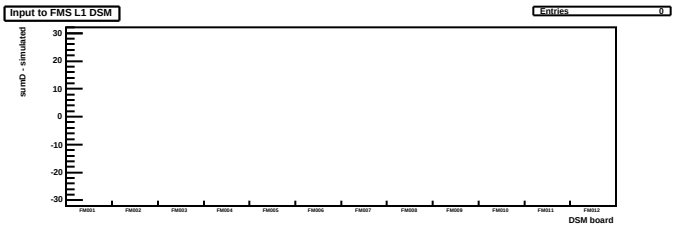

Input to FMS L0 DSM
Entries 1606704
HT ADC
QT board

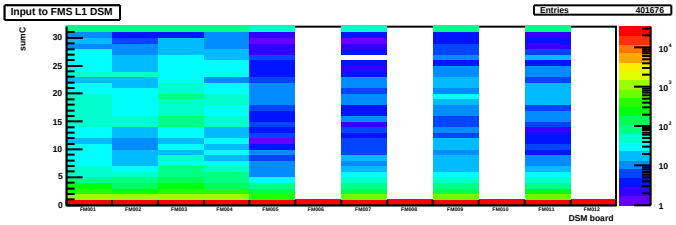

Input to FMS L0 DSM
Entries 15
HT ADC - simulated
QT board

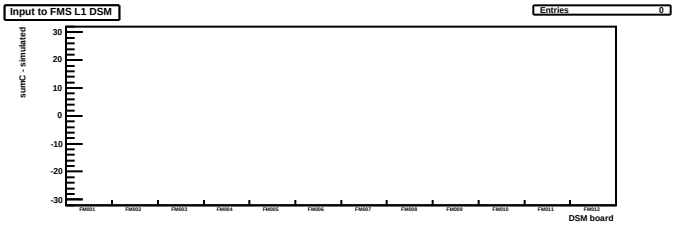

Input to FMS L0 DSM
Entries 1606704
HT ID
QT board

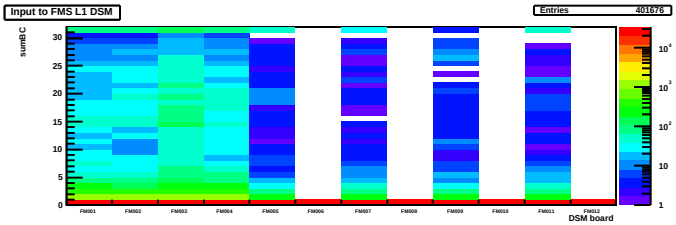

Input to FMS L0 DSM
Entries 34996
HT ID - simulated
QT board

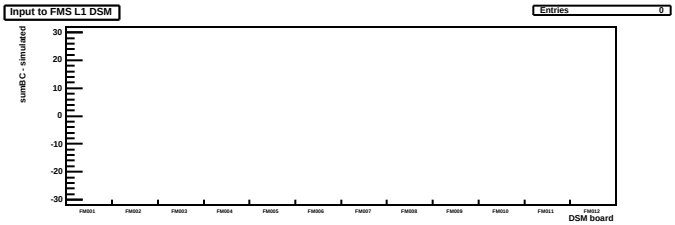

Input to FMS L1 DSM
Entries 401676
sumD
DSM board

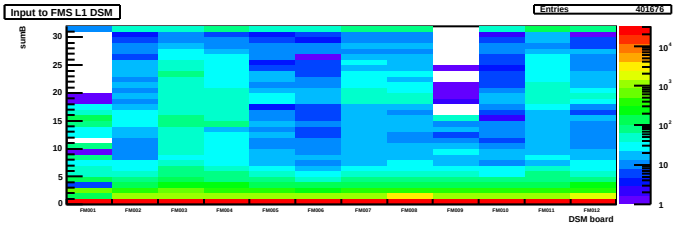

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

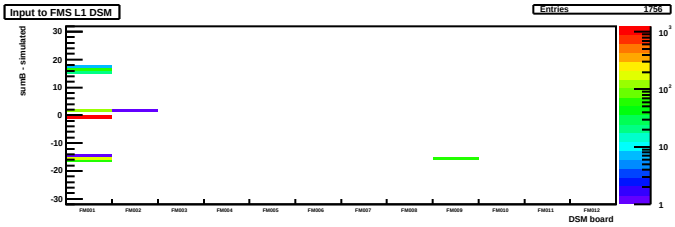

Input to FMS L1 DSM
Entries 401676
sumC
DSM board

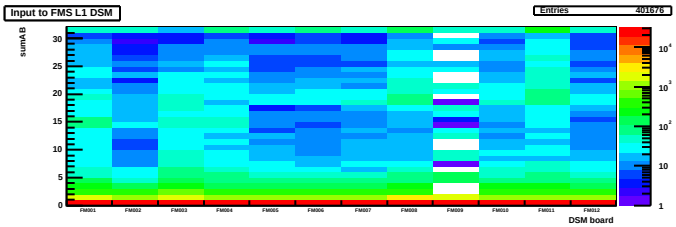

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

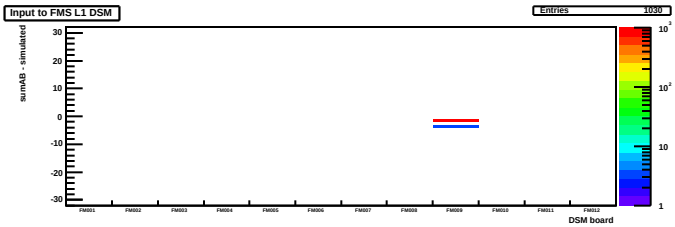

Input to FMS L1 DSM
Entries 401676
sumBC
DSM board

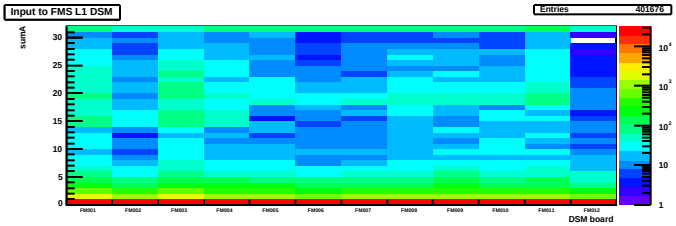

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

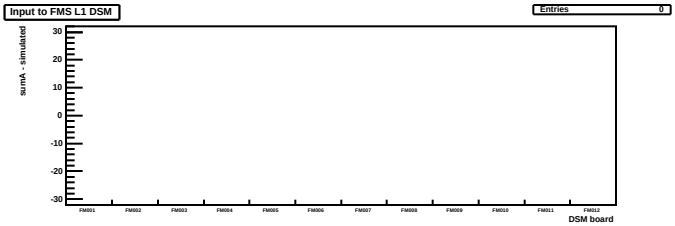

Input to FMS L1 DSM
Entries 401676
sumB
DSM board

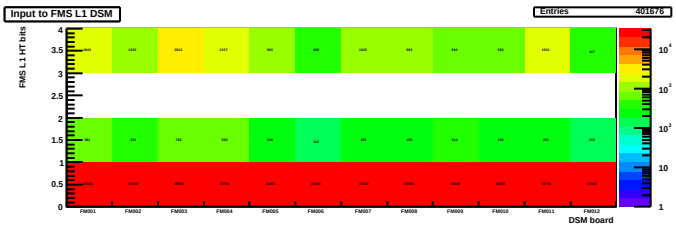

Input to FMS L1 DSM
Entries 1756
sumB - simulated
DSM board

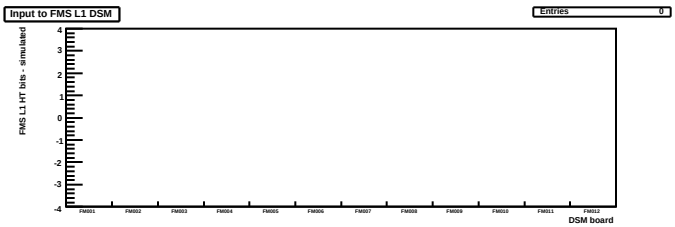

Input to FMS L1 DSM
Entries 401676
sumAB
DSM board

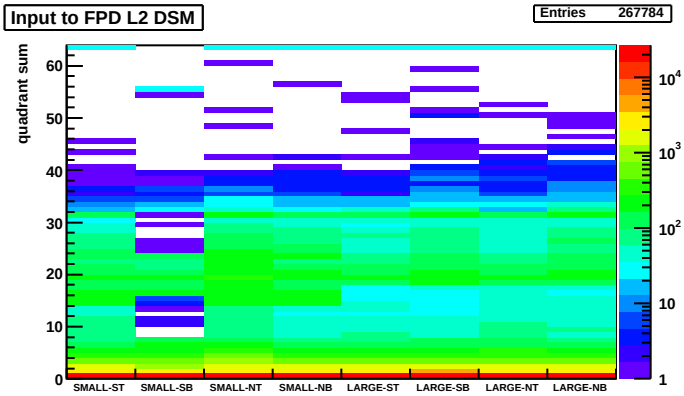

Input to FMS L1 DSM
Entries 1030
sumAB - simulated
DSM board

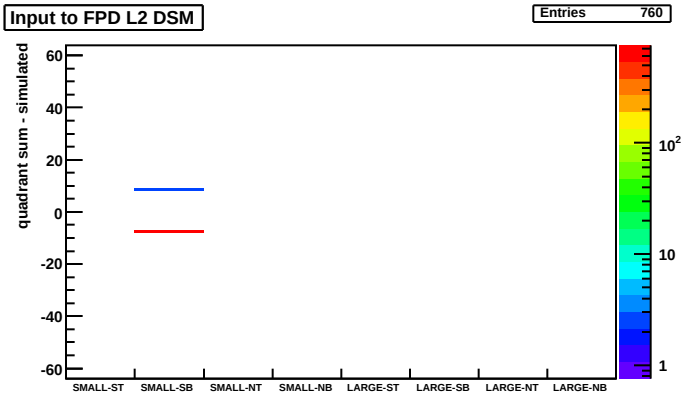

Input to FMS L1 DSM
Entries 401676
sumA
DSM board

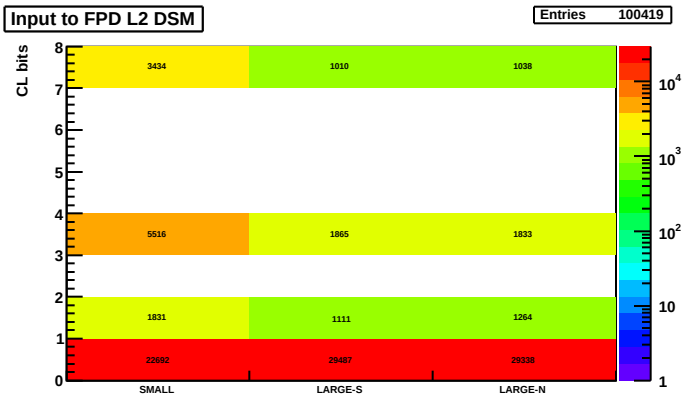

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

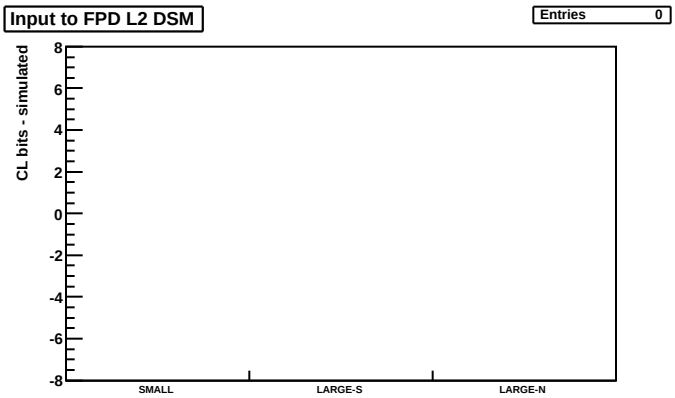

Input to FMS L1 DSM
Entries 401676
FMS L1 HT bits
DSM board

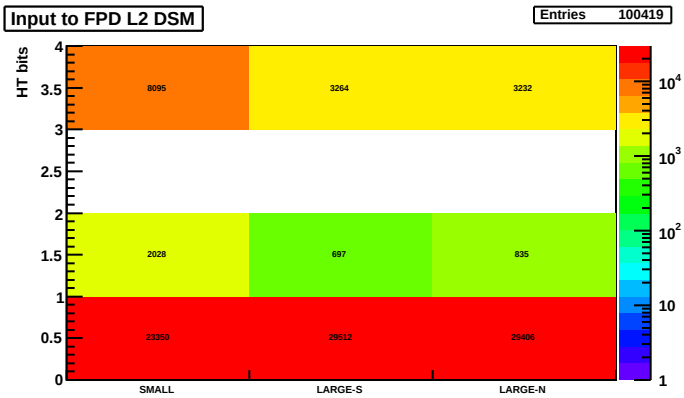

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

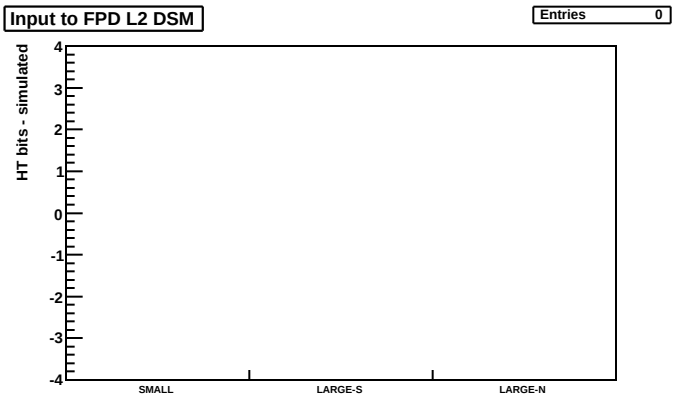

Input to FPD L2 DSM
Entries 267784
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

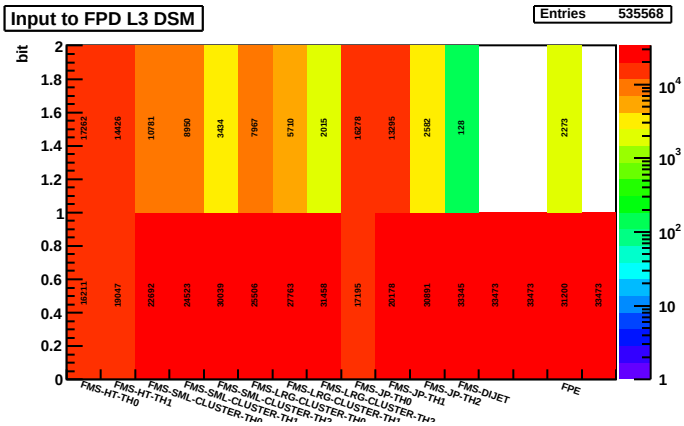

Input to FPD L2 DSM
Entries 760
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

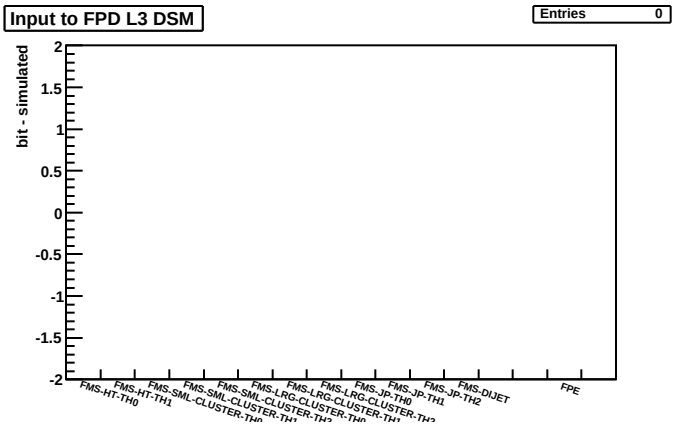

Input to FPD L2 DSM
Entries 100419
CL bits
3434 1010 1038
5516 1865 1833
1831 1111 1264
22692 29487 29338
SMALL LARGE-S LARGE-N

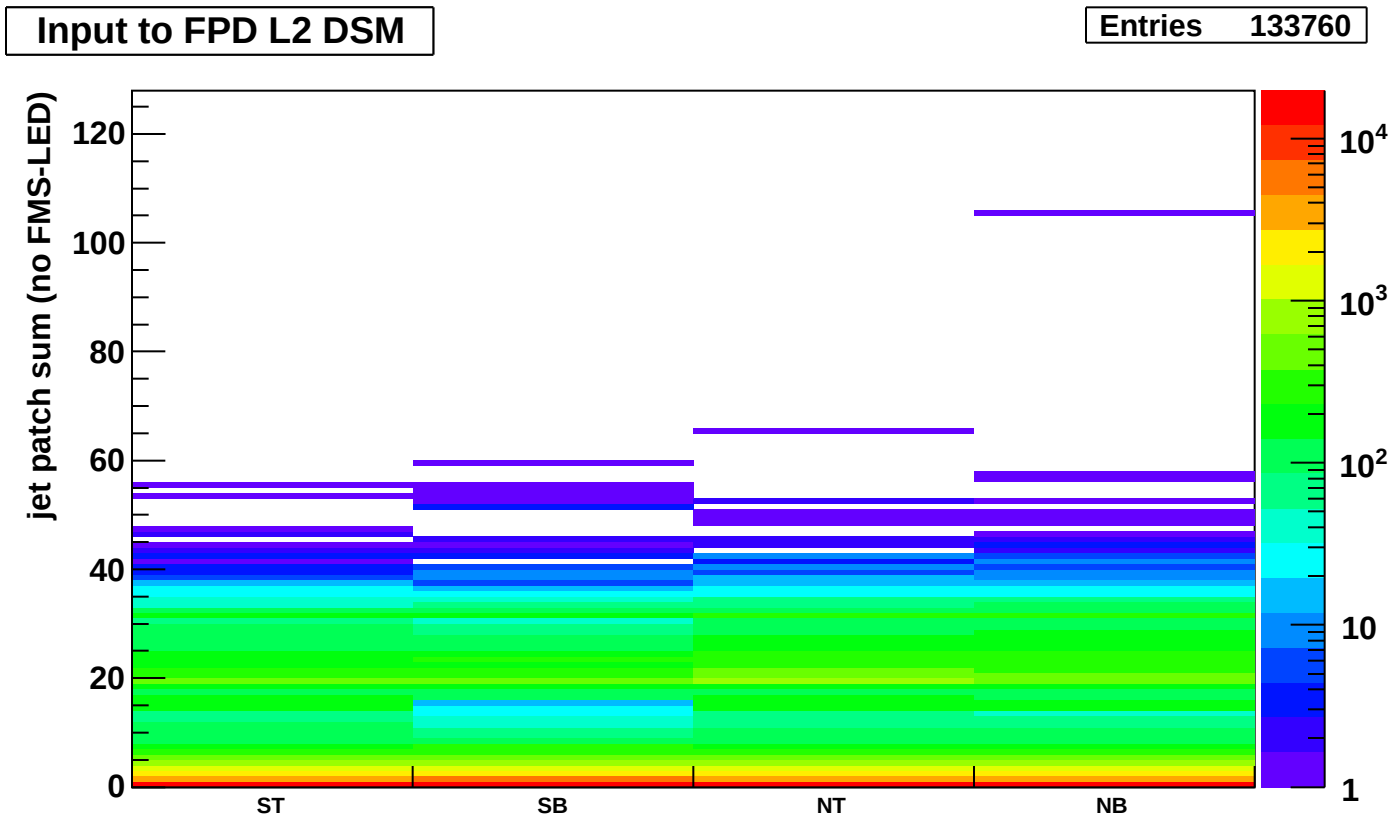

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL LARGE-S LARGE-N

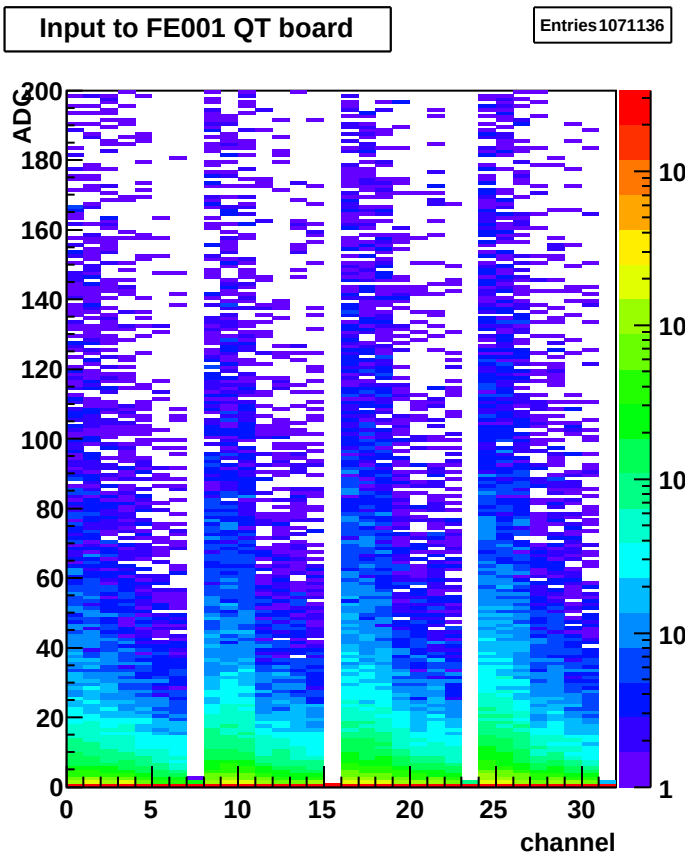

Input to FPD L2 DSM
Entries 100419
HT bits
8095 3264 3232
2028 697 835
23350 29512 29406
SMALL LARGE-S LARGE-N

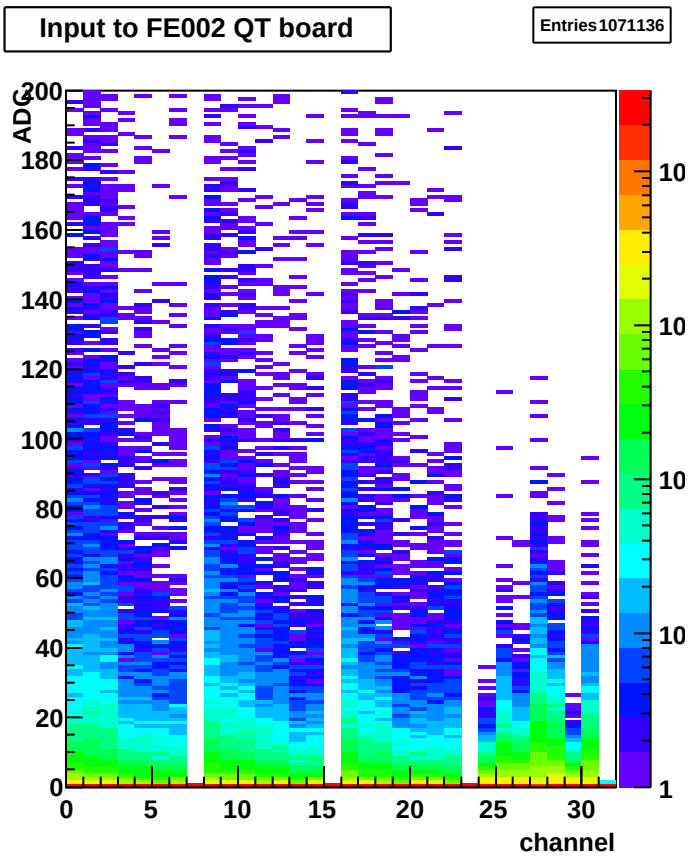

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

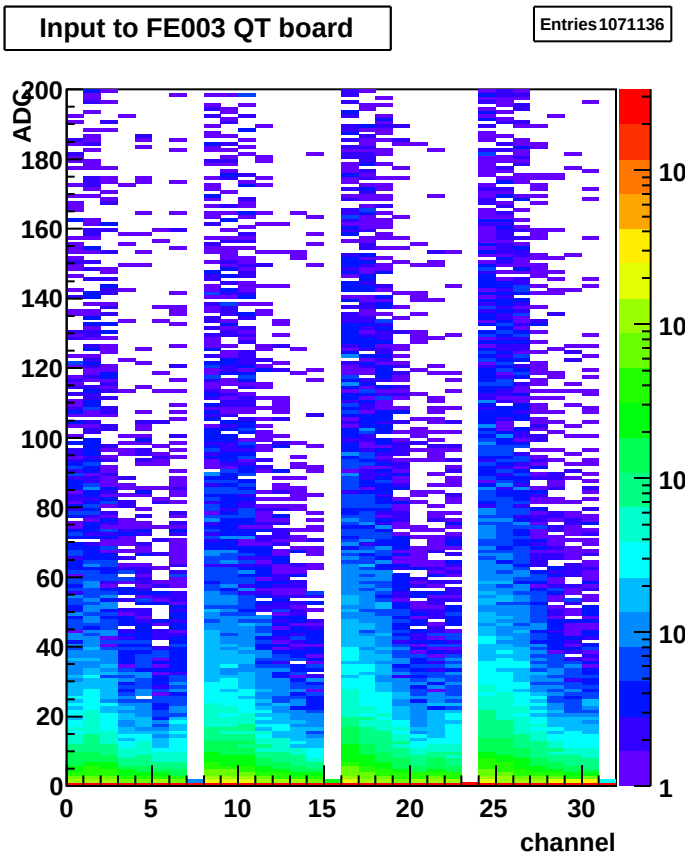

Input to FPD L3 DSM
Entries 535568
bit
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

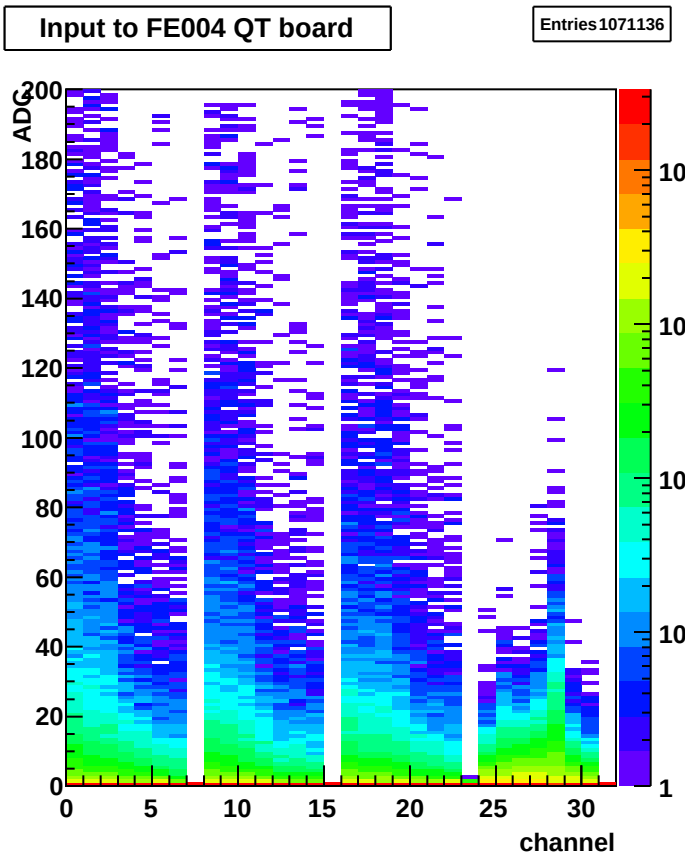

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

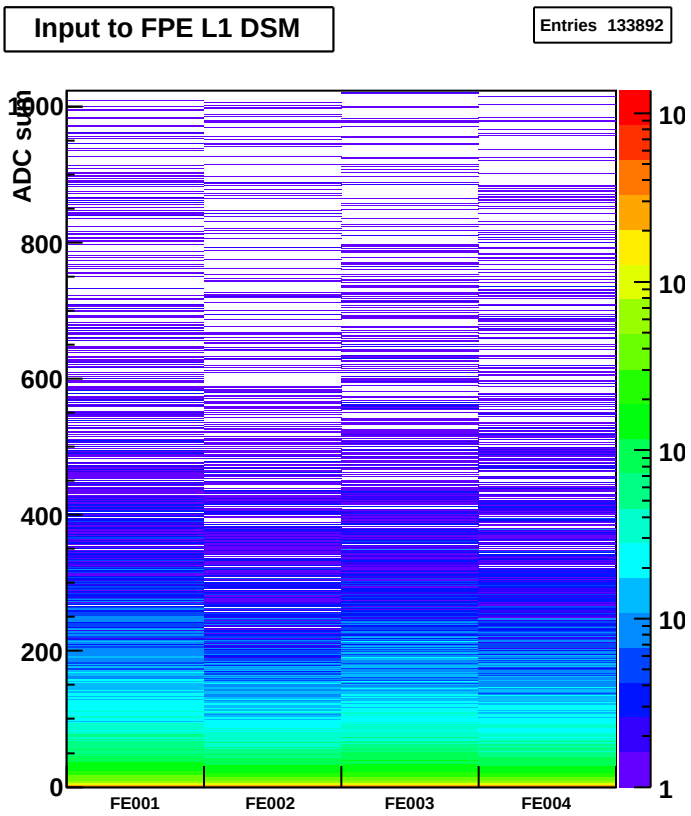

Input to FPD L2 DSM
Entries 133760
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

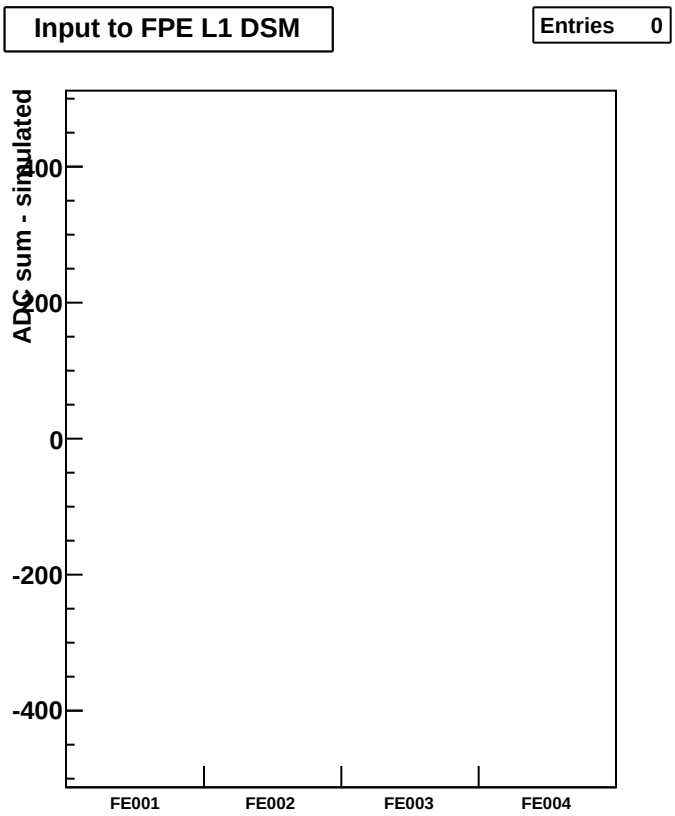

Input to FE001 QT board
Entries 1071136
ADC
channel

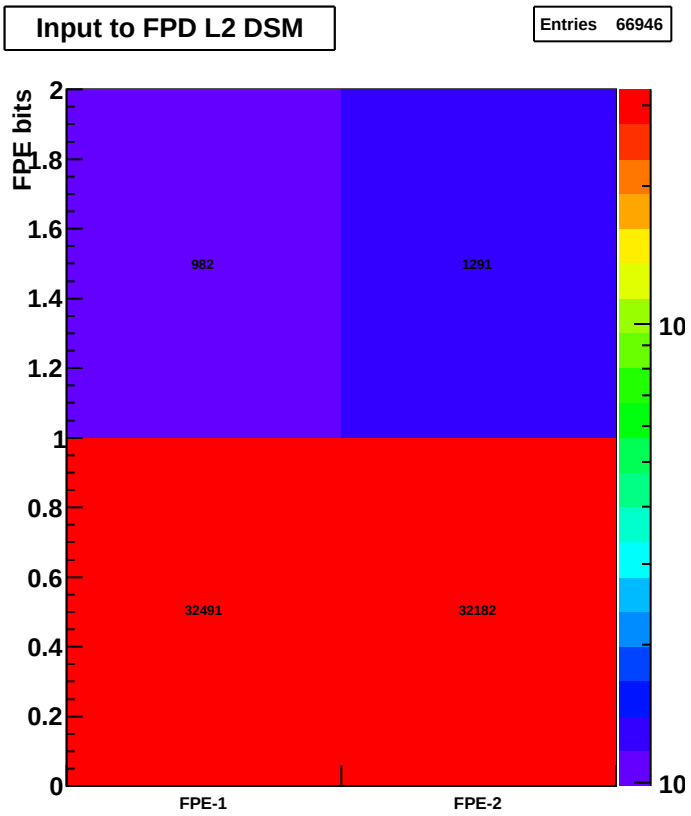

Input to FE002 QT board
Entries 1071136
ADC
channel

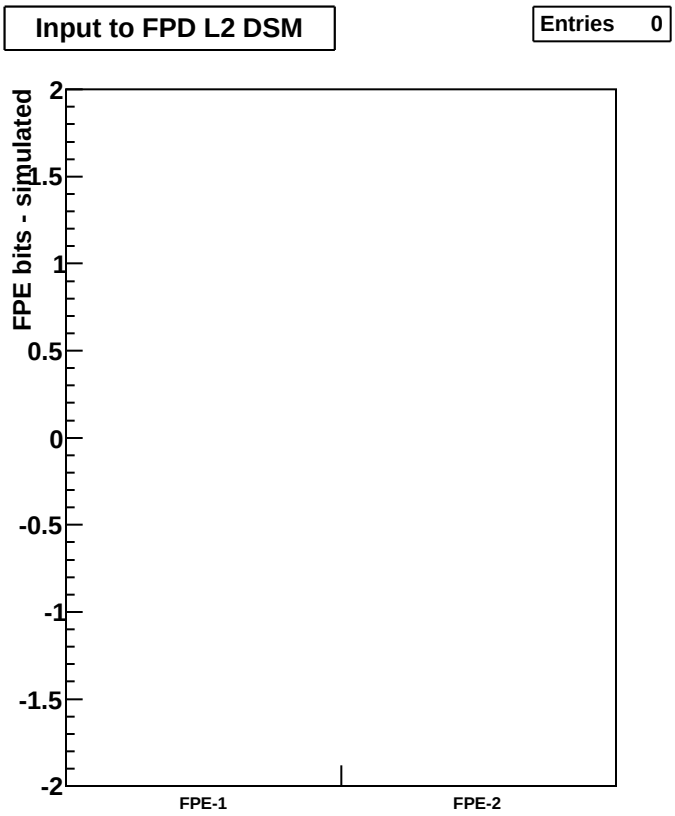

Input to FE003 QT board
Entries 1071136
ADC
channel


Input to FE004 QT board
Entries 1071136
ADC
channel


Input to FPE L1 DSM
Entries 133892
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 66946
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
982
1291
32491
32182
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2