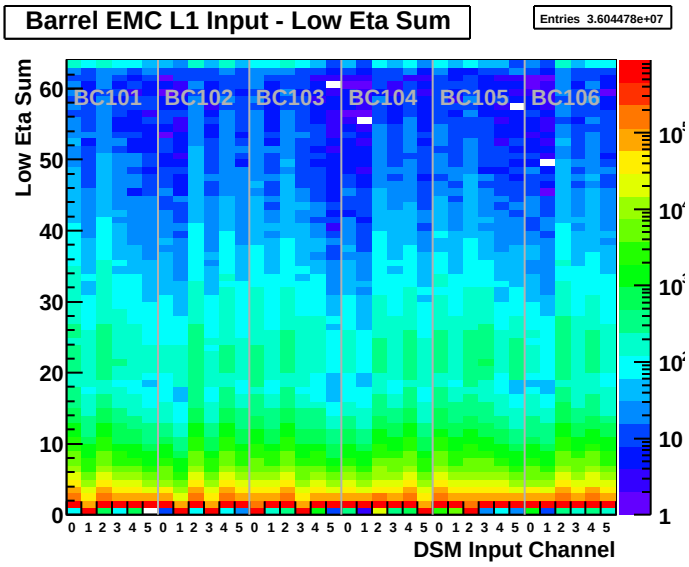
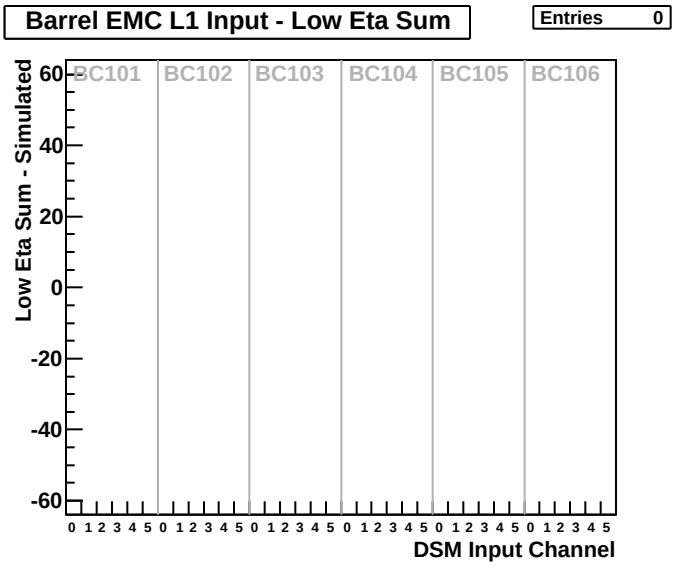
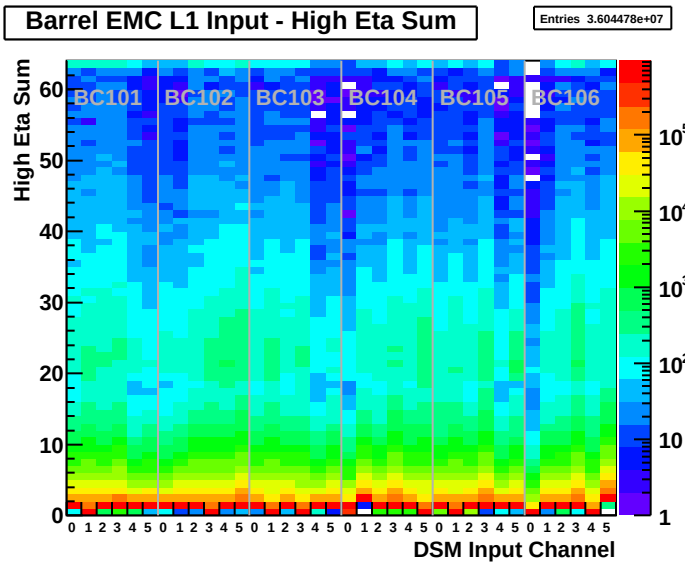
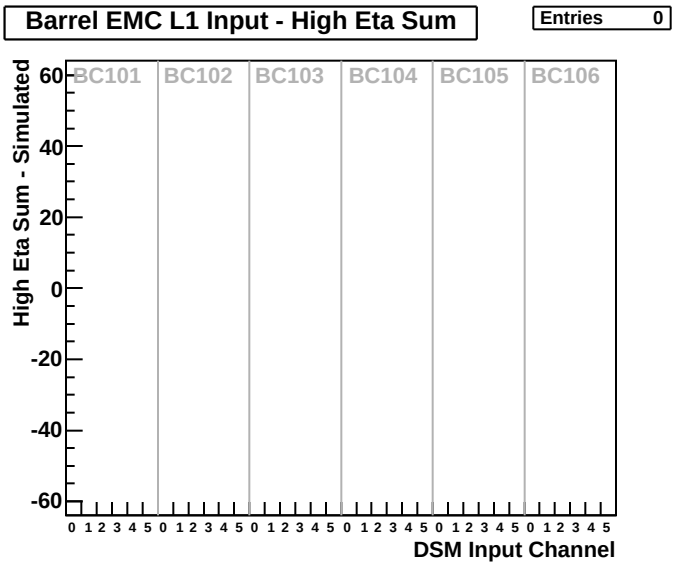
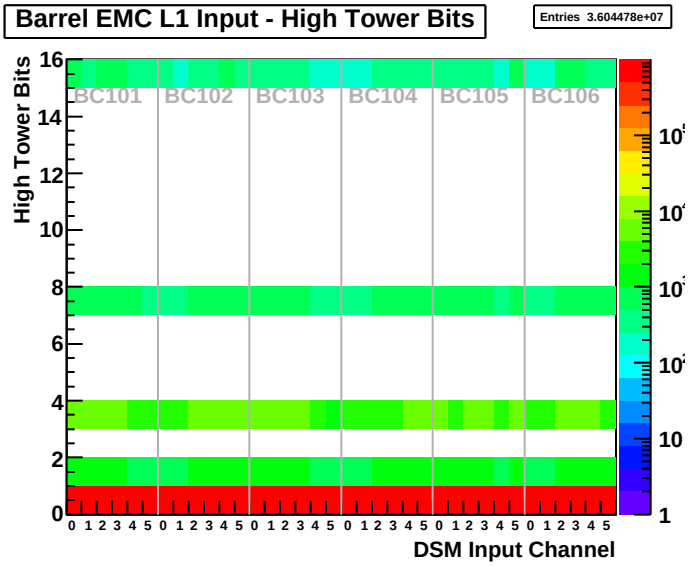
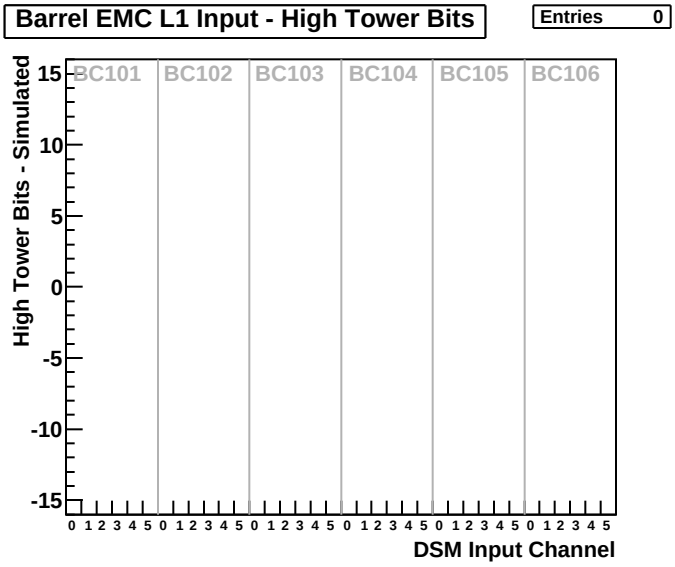

Barrel EMC L1 Input - Low Eta Sum
Entries 3.604478e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

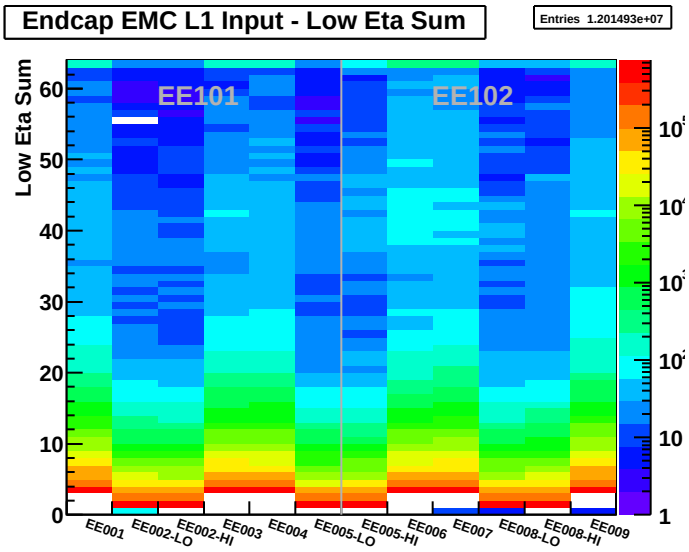

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

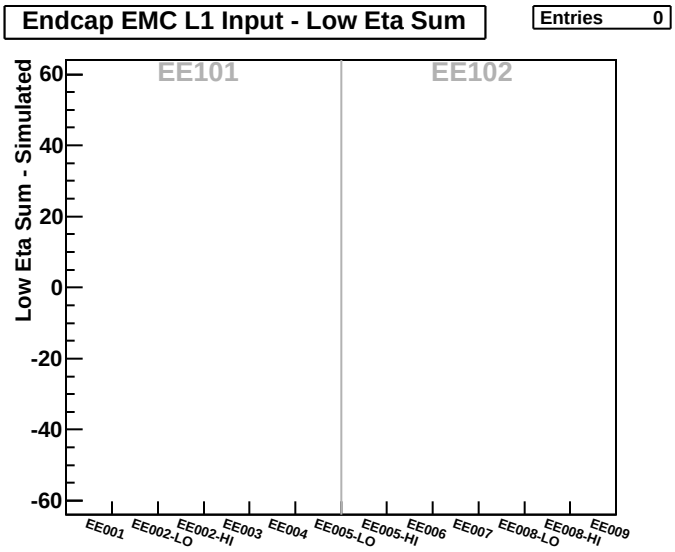

Barrel EMC L1 Input - High Eta Sum
Entries 3.604478e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

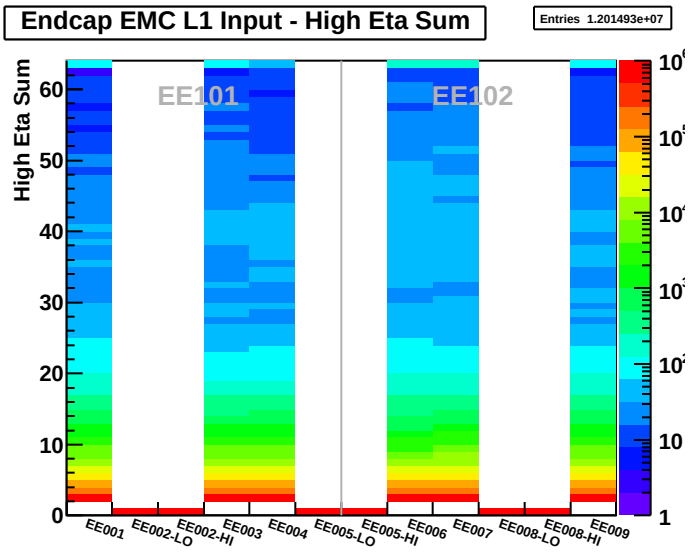

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

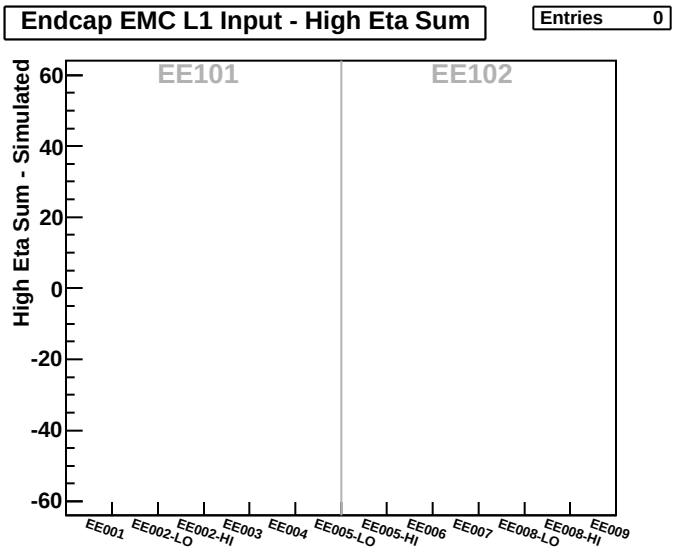

Barrel EMC L1 Input - High Tower Bits
Entries 3.604478e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

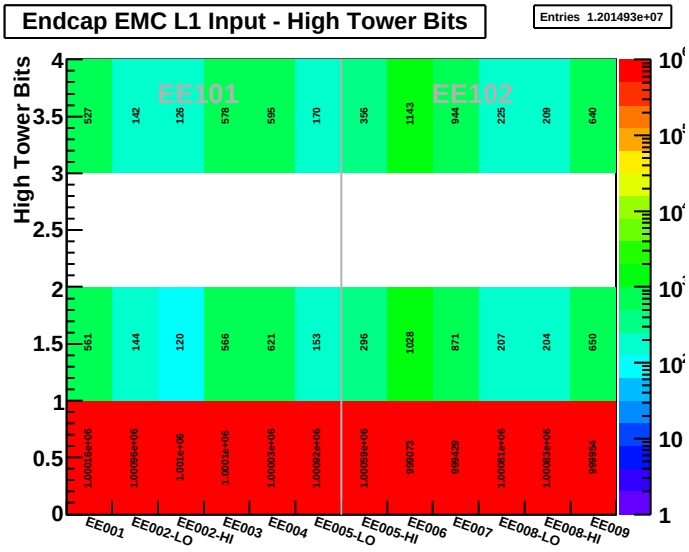

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

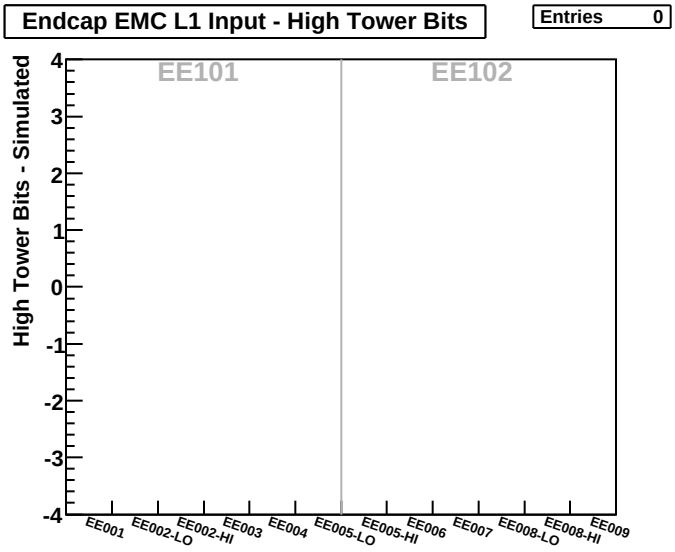

Endcap EMC L1 Input - Low Eta Sum
Entries 1.201493e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

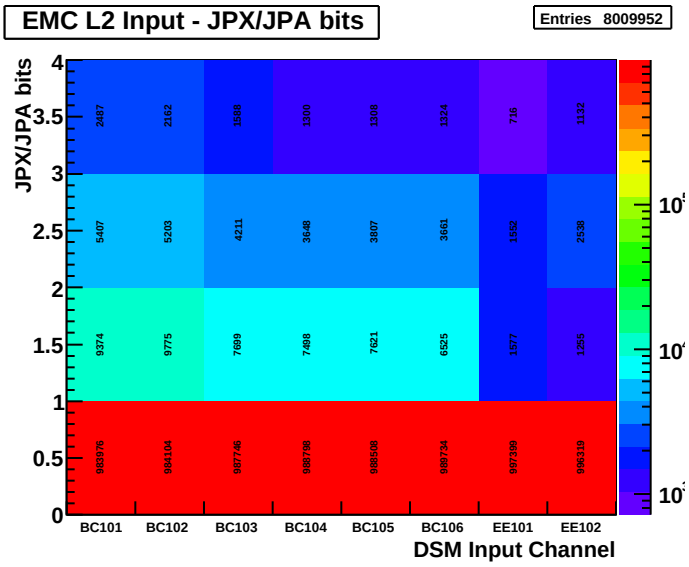

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

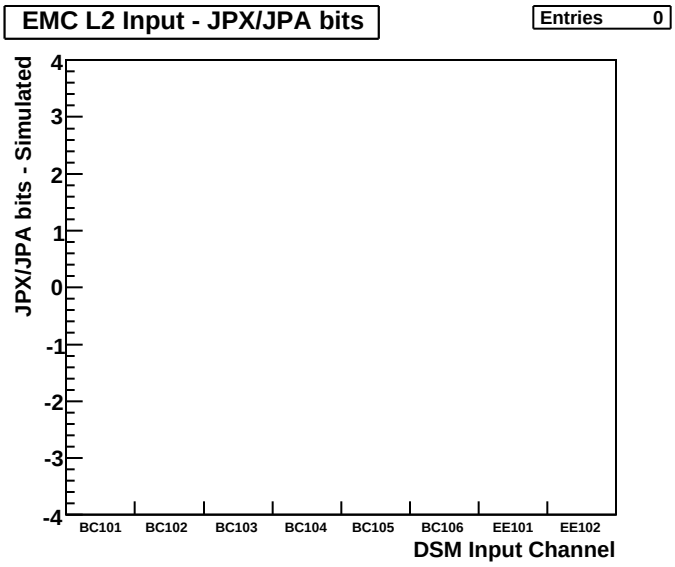

Endcap EMC L1 Input - High Eta Sum
Entries 1.201493e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

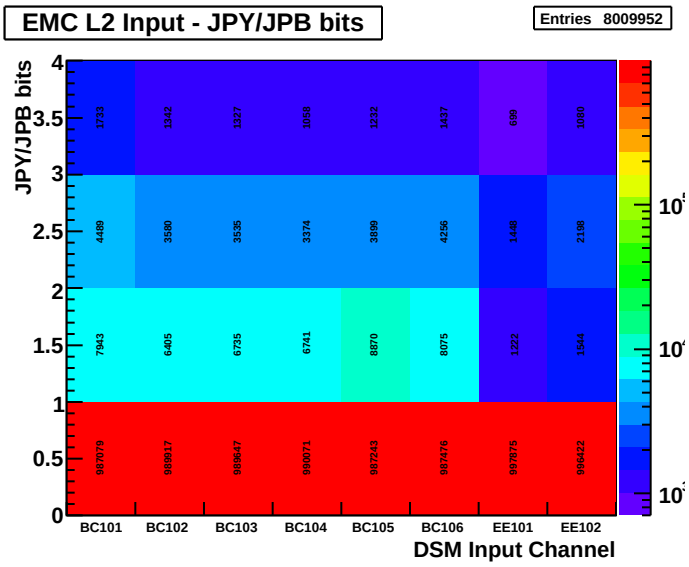

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

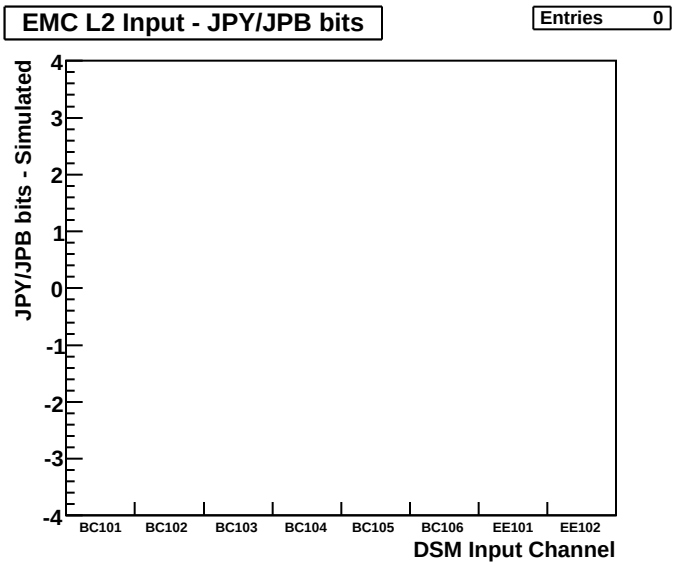

Endcap EMC L1 Input - High Tower Bits
Entries 1.201493e+07
High Tower Bits
EE101
EE102
527 142 126 578 595 170 356 1143 944 225 209 640
561 144 120 566 621 153 296 1028 871 207 204 650
1.00015e+06 1.00096e+06 1.001e+06 1.0001e+06 1.00003e+06 1.00092e+06 1.00059e+06 999073 999429 1.00081e+06 1.00083e+06 999954
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

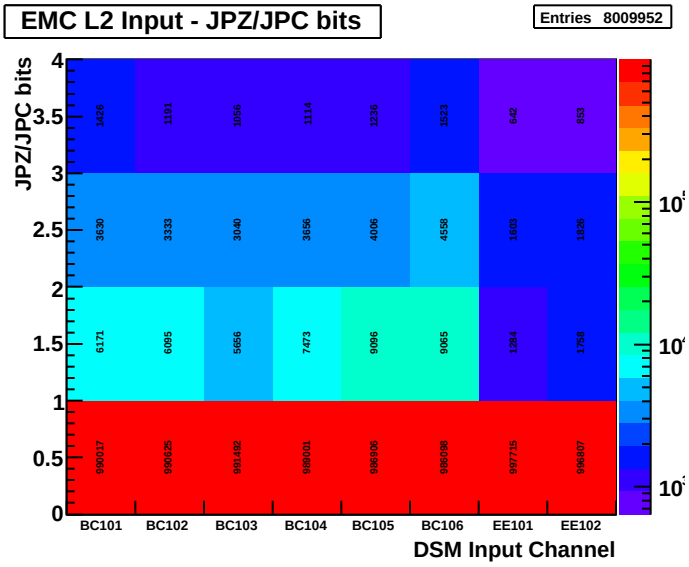

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

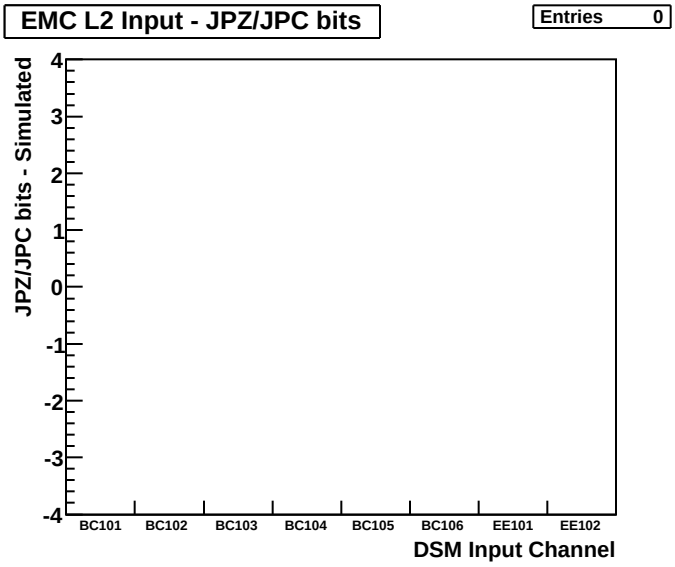

EMC L2 Input - JPX/JPA bits
Entries 8009952
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2487 2162 1588 1300 1308 1324 716 1132
5407 5203 4211 3648 3807 3661 1582 2538
9374 9775 7699 7498 7621 6525 1577 1255
983976 984104 987746 988798 988508 988734 997399 998319

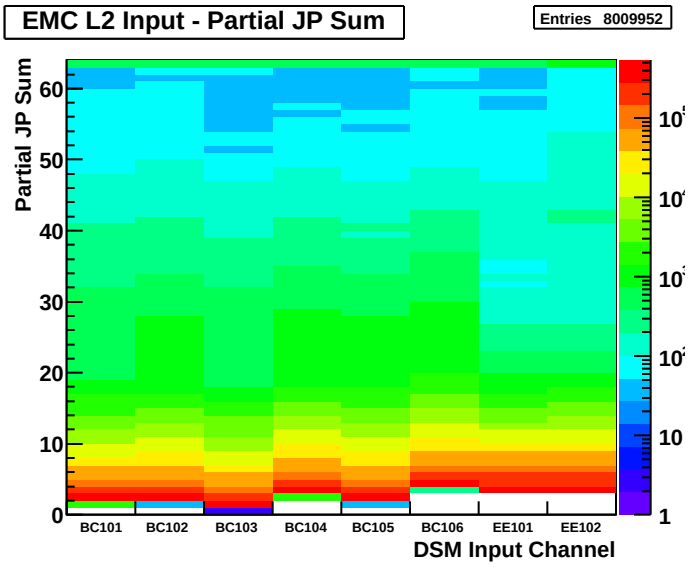

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

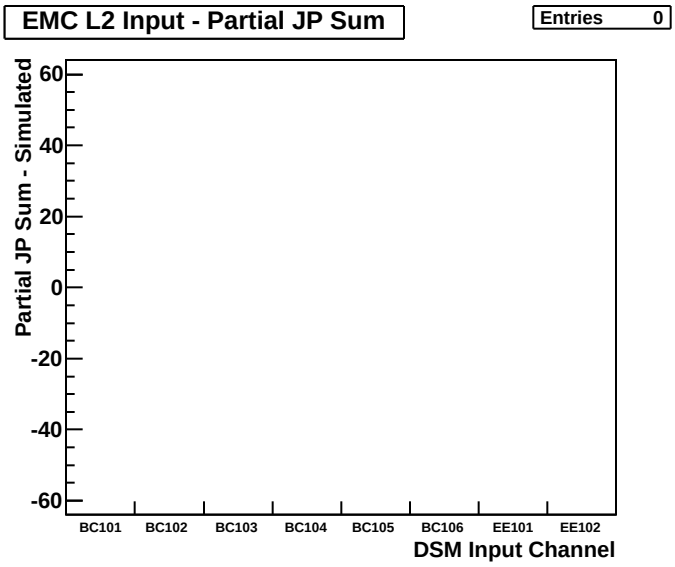

EMC L2 Input - JPY/JPB bits
Entries 8009952
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1733 1342 1327 1058 1232 1437 699 1080
4489 3580 3535 3374 3899 4256 1448 2198
7943 6405 6735 6741 8970 8075 1222 1544
987079 988917 988647 990071 987243 987476 997875 996422

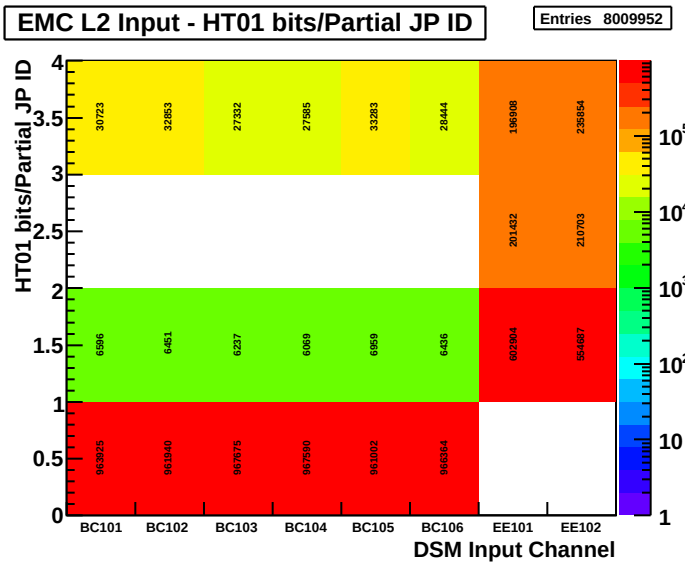

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

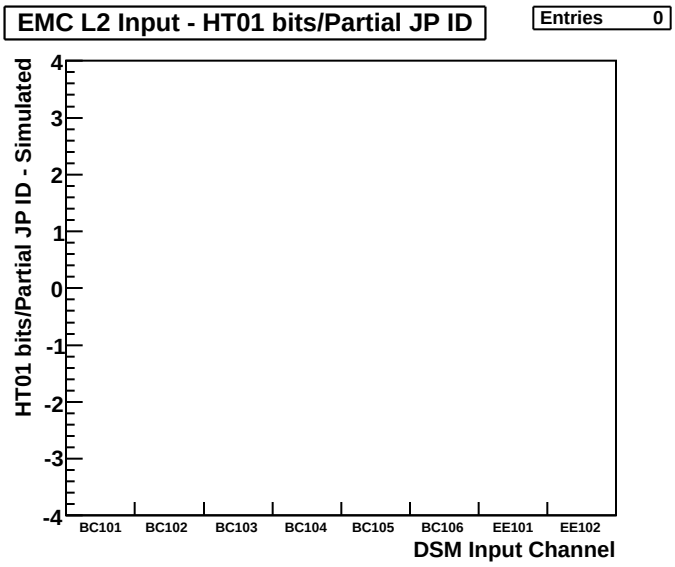

EMC L2 Input - JPZ/JPC bits
Entries 8009952
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1426 1191 1056 1114 1236 1523 642 853
3630 3333 3040 3656 4006 4598 1603 1826
6171 6095 5656 7473 9096 9065 1284 1758
990017 990625 991492 989001 986906 986098 997715 996807

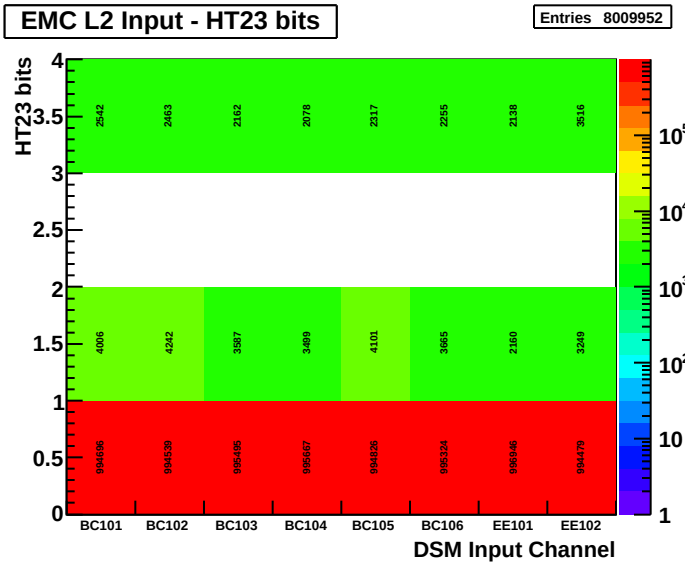

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

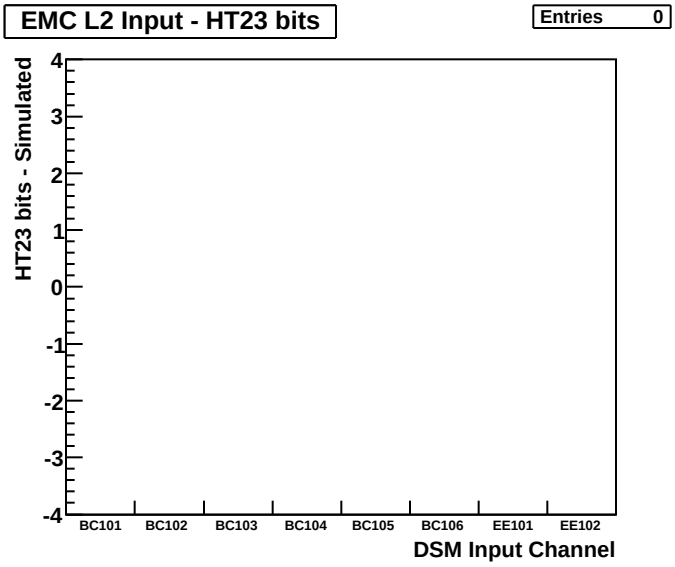

EMC L2 Input - Partial JP Sum
Entries 8009952
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

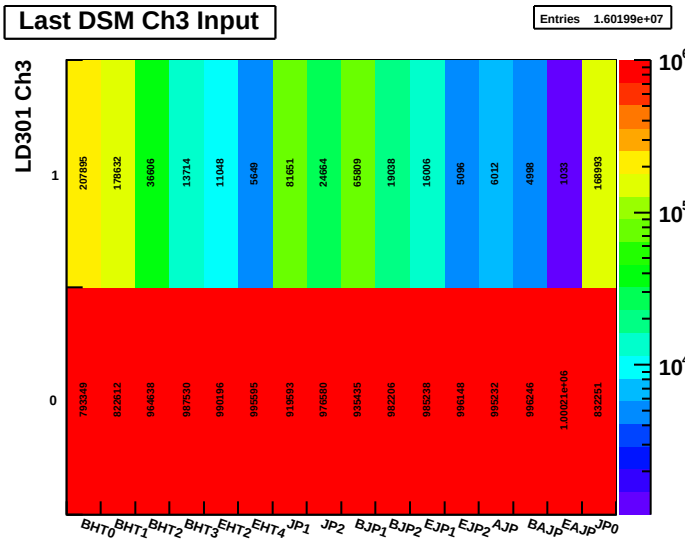

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

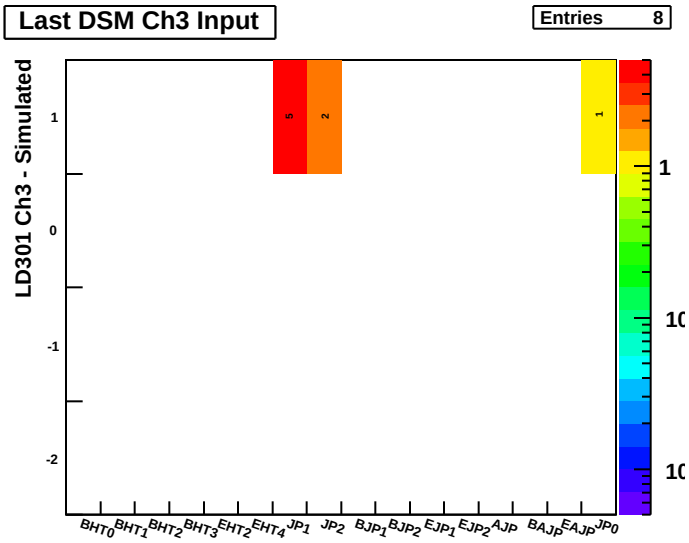

EMC L2 Input - HT01 bits/Partial JP ID
Entries 8009952
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

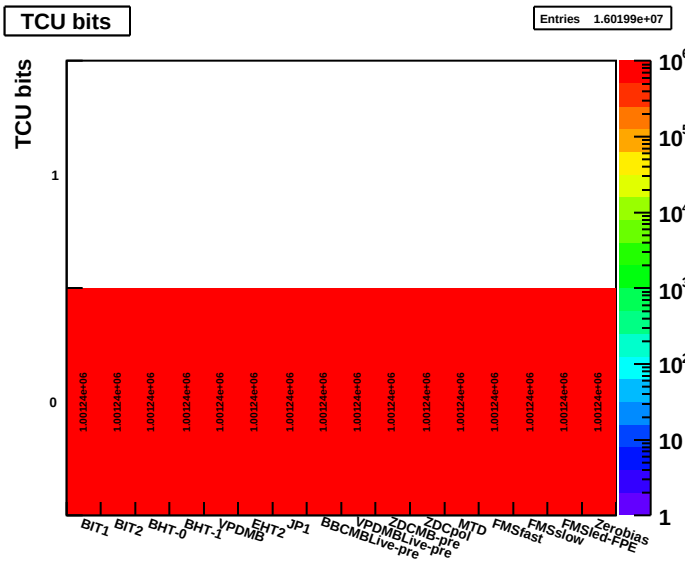

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

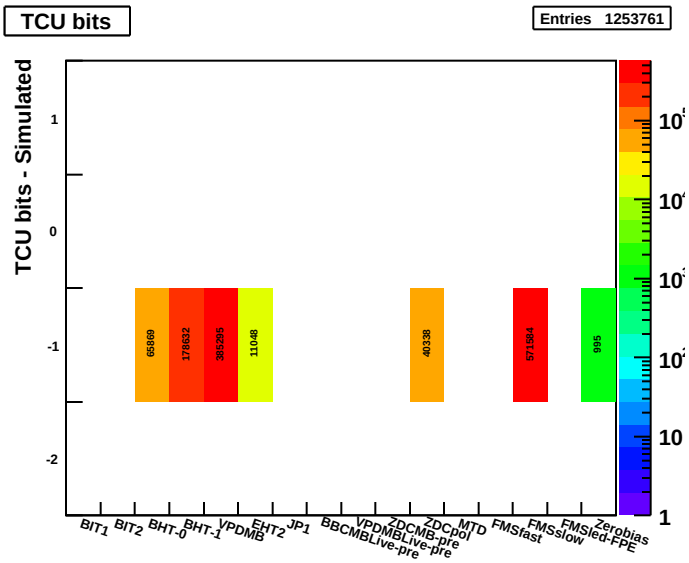

EMC L2 Input - HT23 bits
Entries 8009952
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

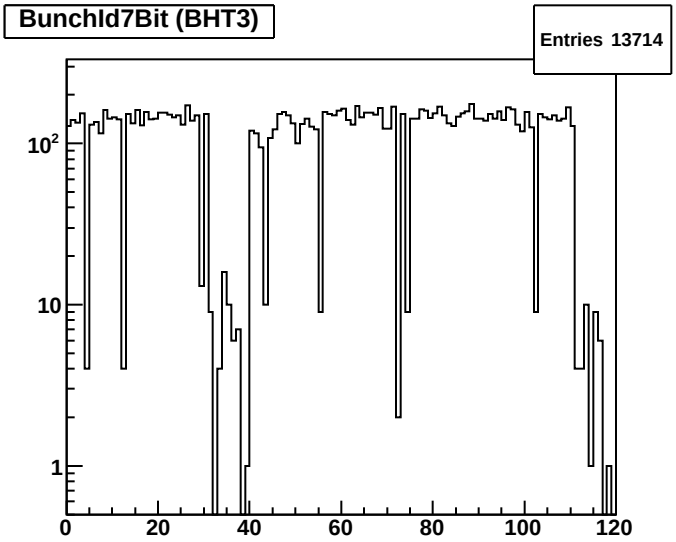

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

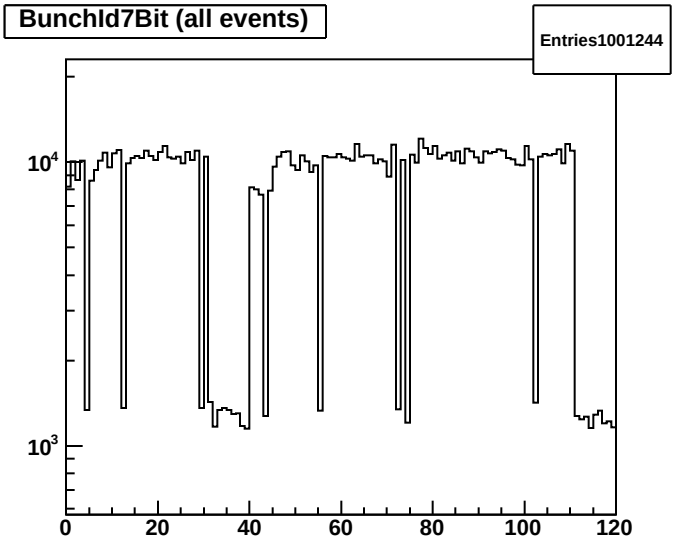

Last DSM Ch3 Input
Entries 1.60199e+07
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

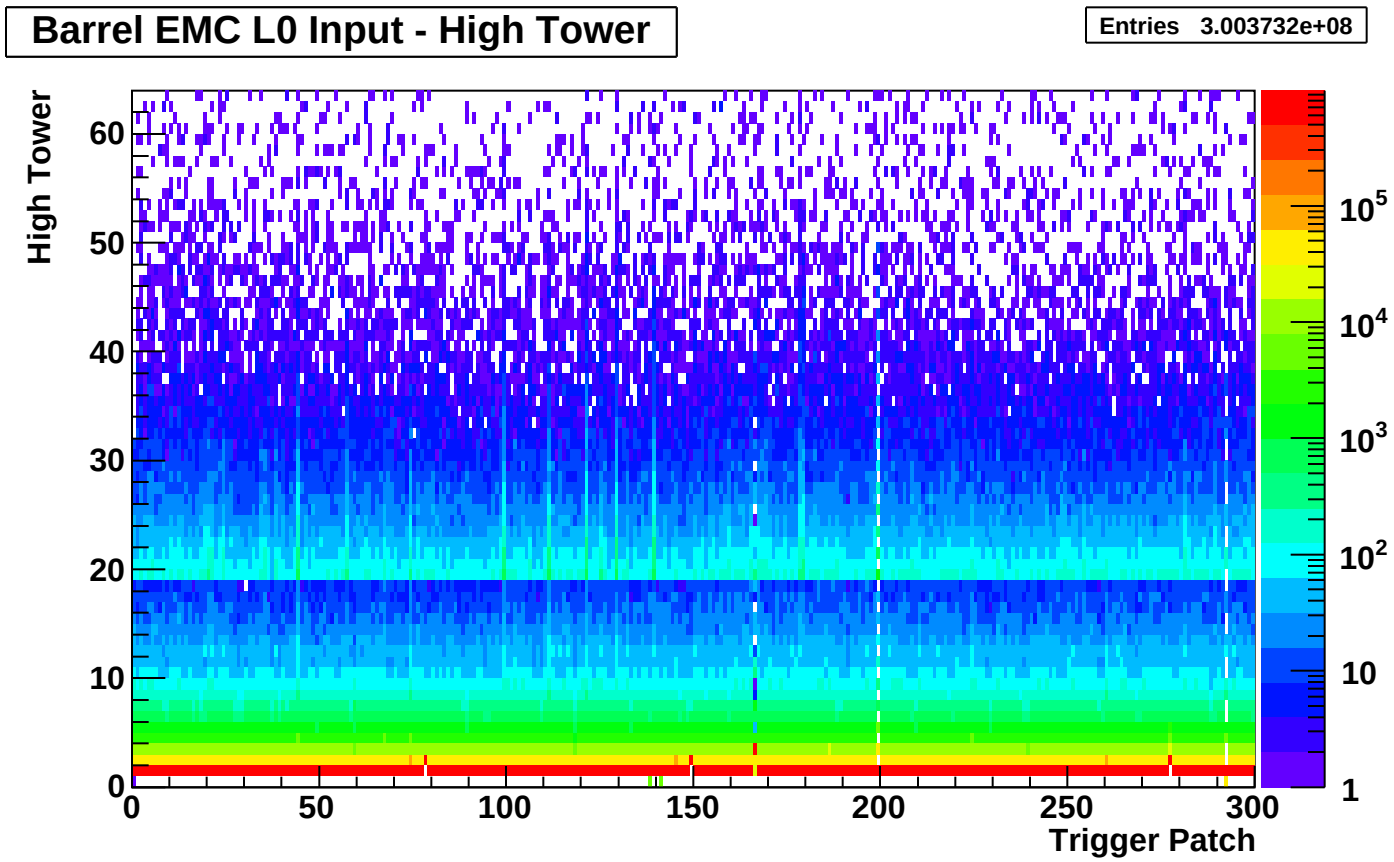

Last DSM Ch3 Input
Entries 8
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

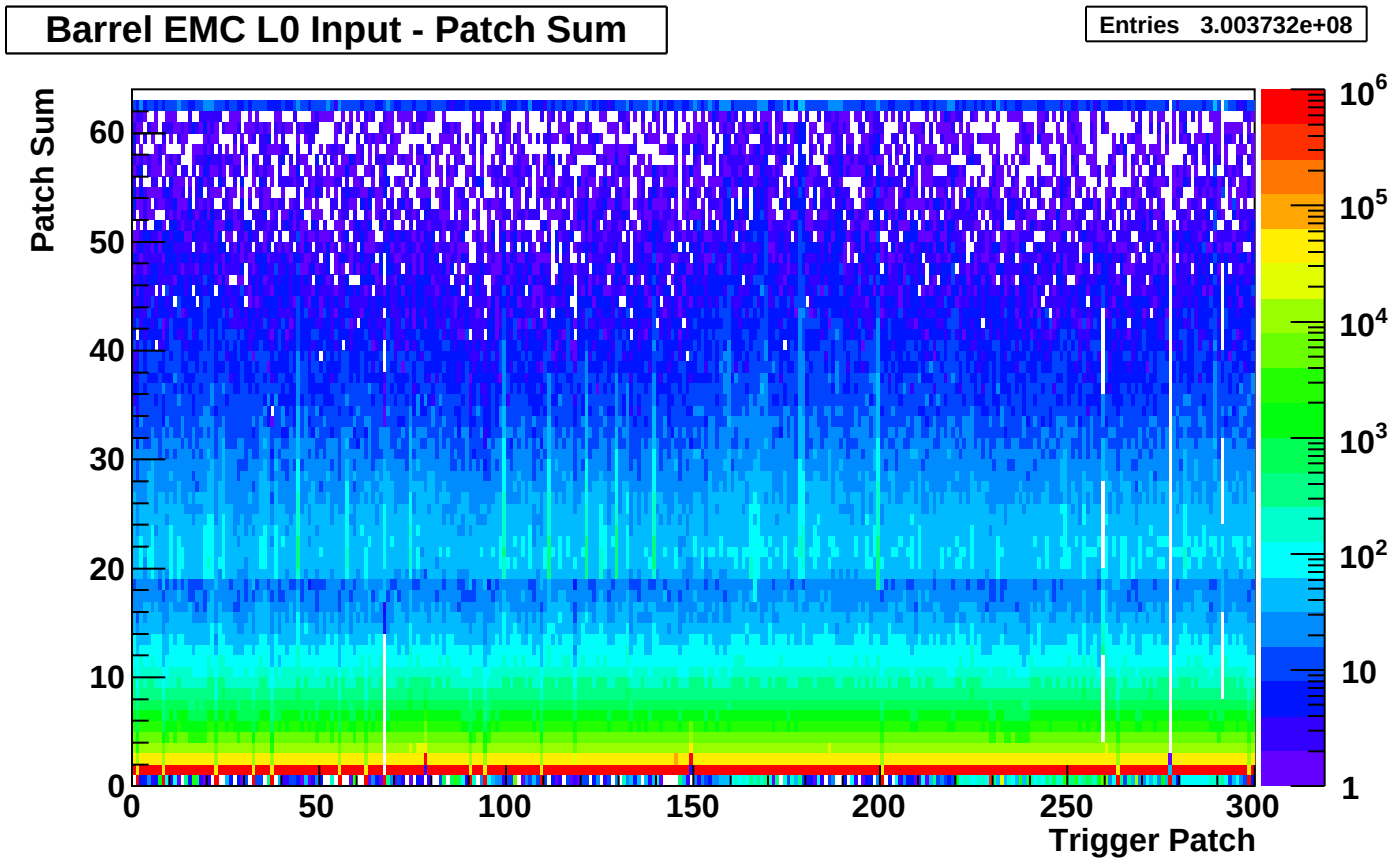

TCU bits
Entries 1.60199e+07
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

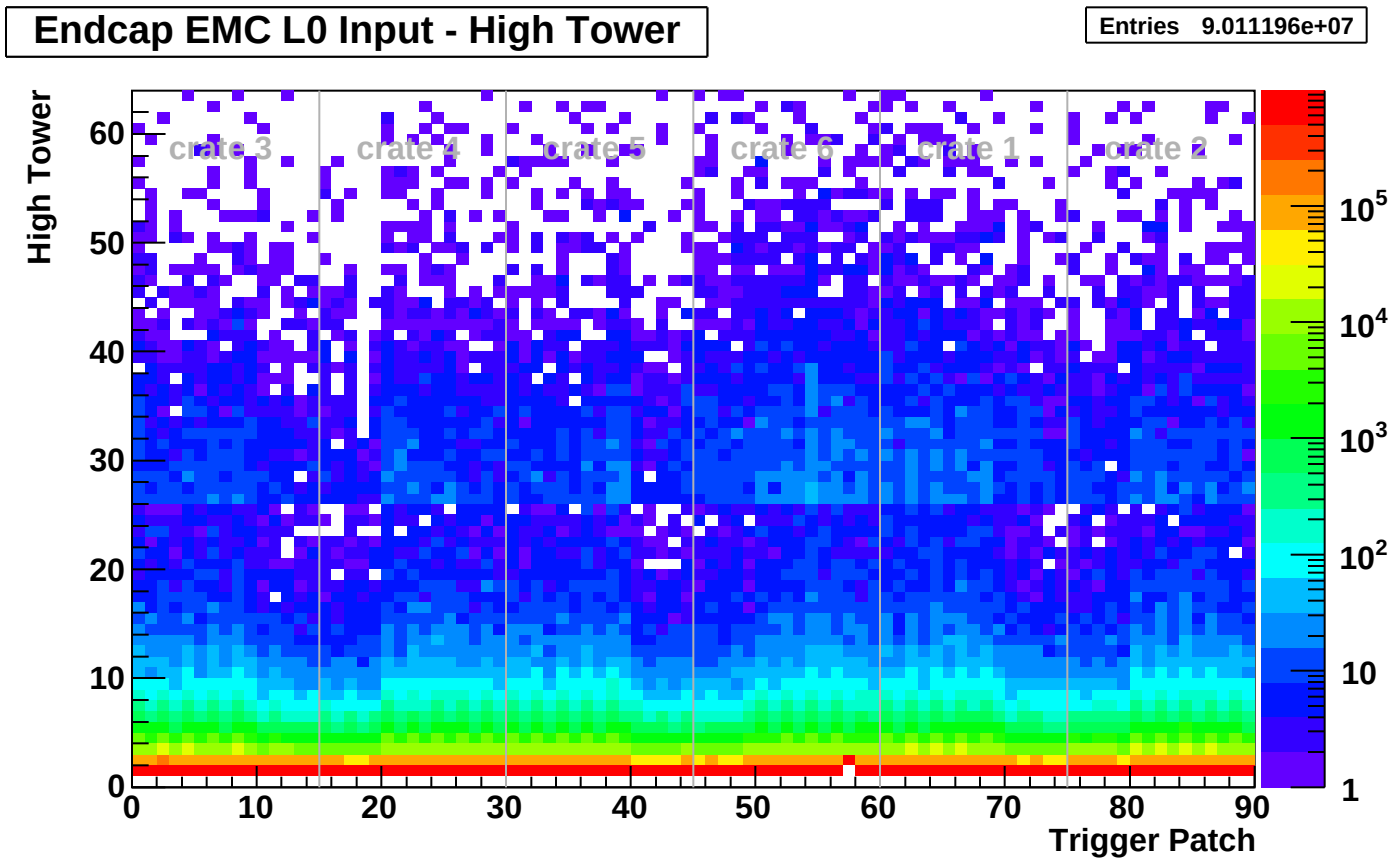

TCU bits
Entries 1253761
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

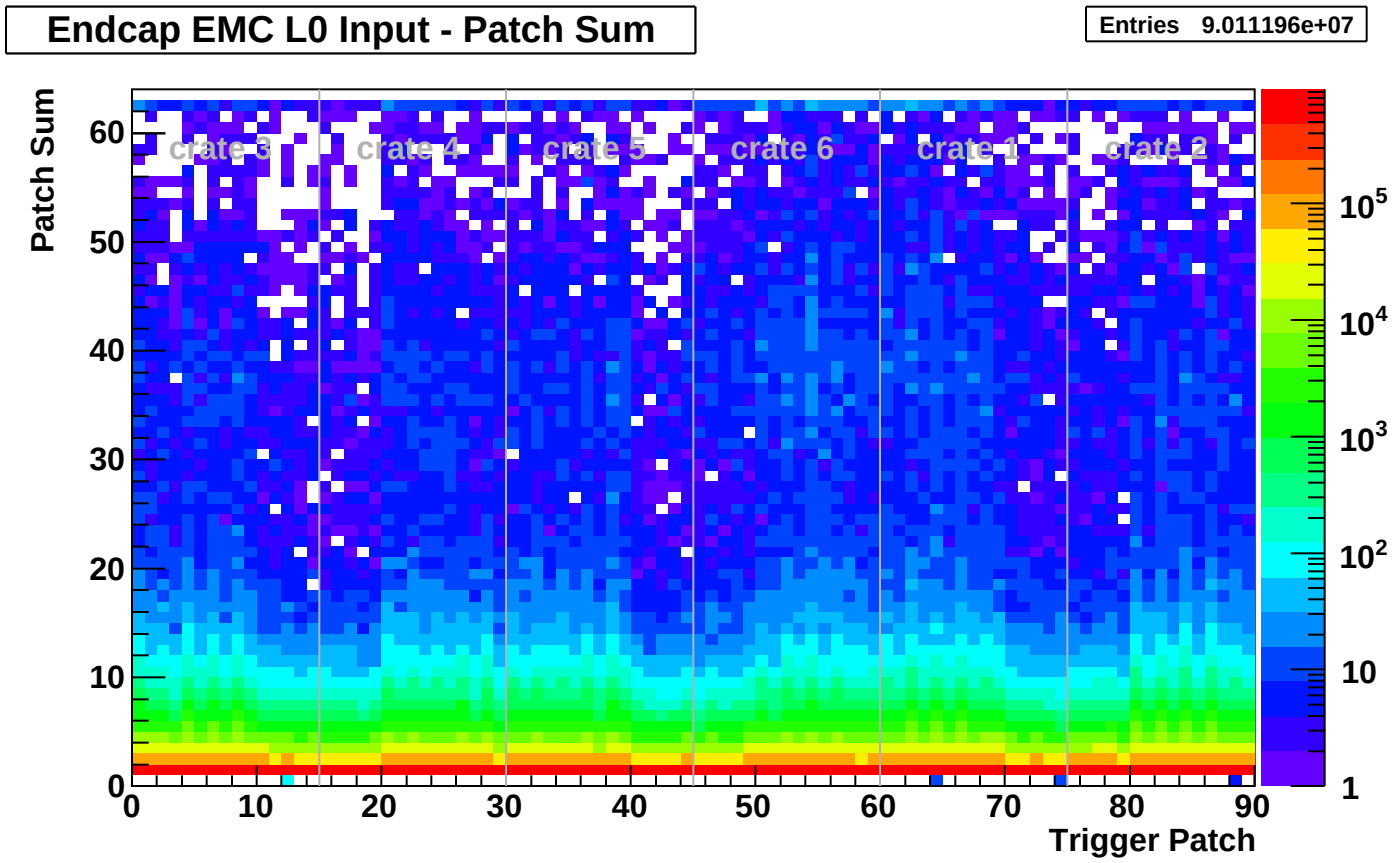

BunchId7Bit (BHT3)
Entries 13714

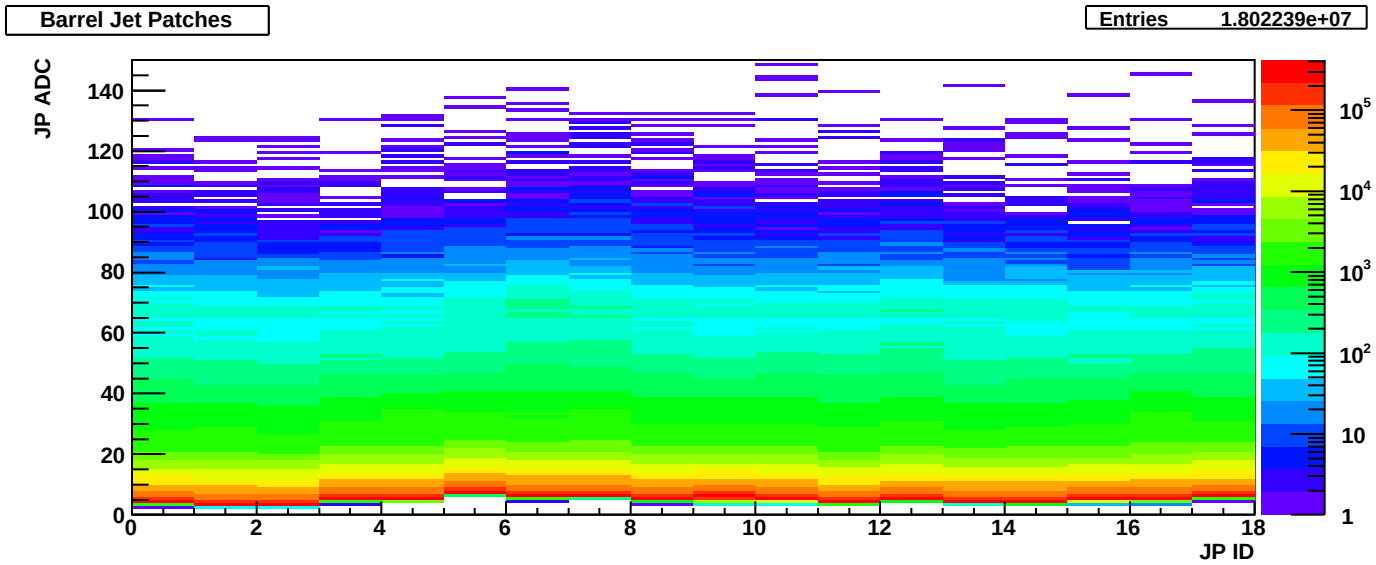

BunchId7Bit (all events)
Entries1001244

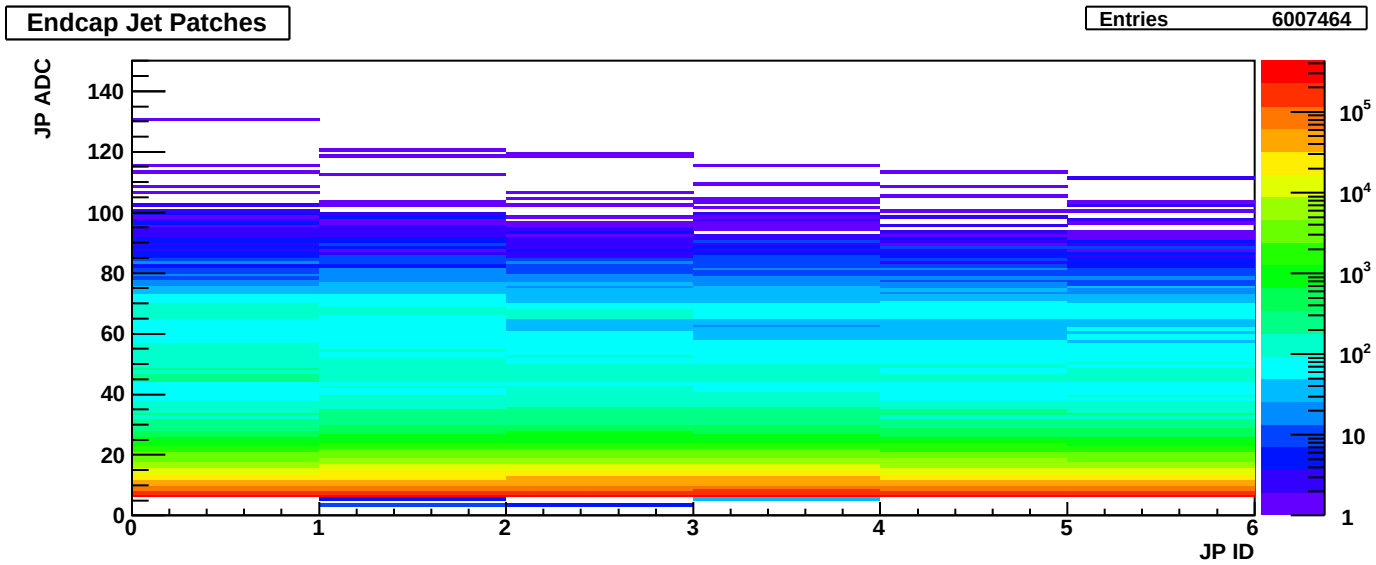

Barrel EMC L0 Input - High Tower
Entries 3.003732e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

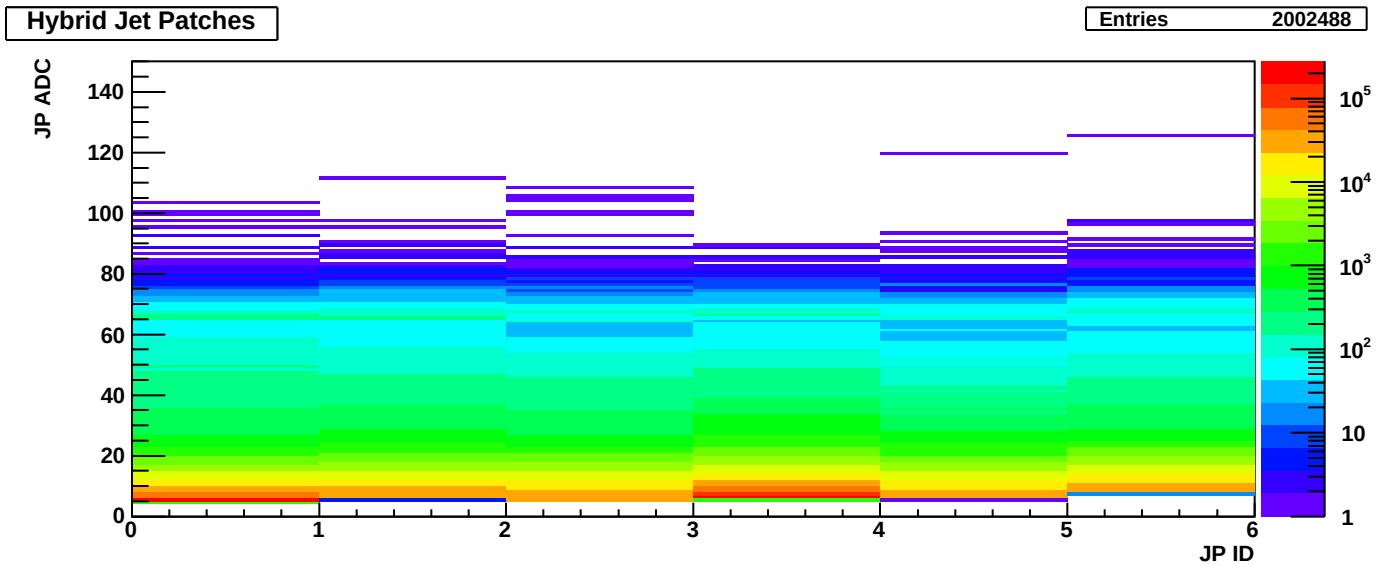

Barrel EMC L0 Input - Patch Sum
Entries 3.003732e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

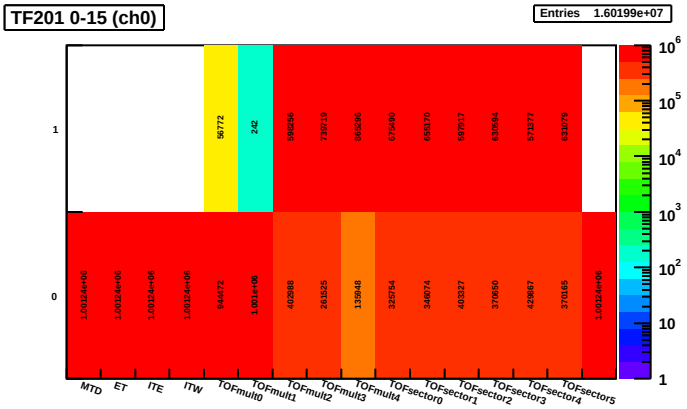

Endcap EMC L0 Input - High Tower
Entries 9.011196e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

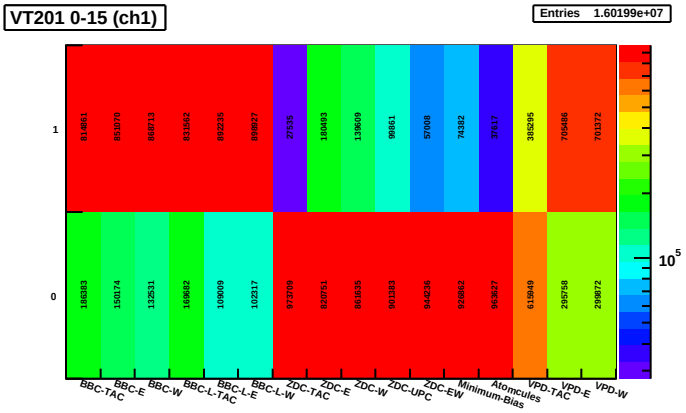

Endcap EMC L0 Input - Patch Sum
Entries 9.011196e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

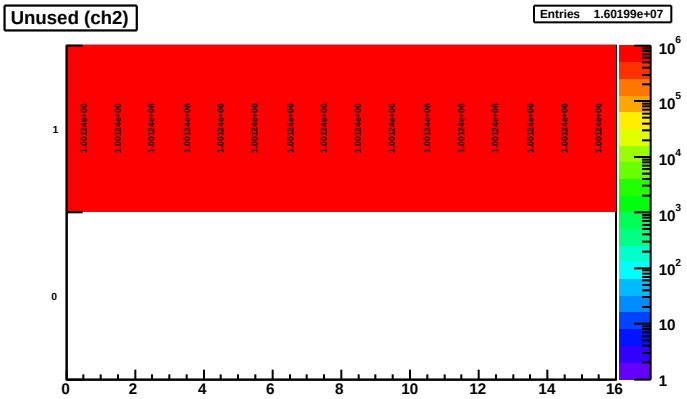

Barrel Jet Patches
Entries 1.802239e+07
JP ADC
JP ID

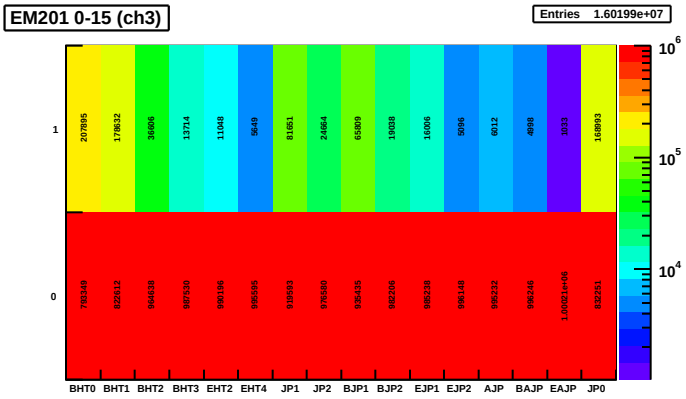

Endcap Jet Patches
Entries 6007464
JP ADC
JP ID

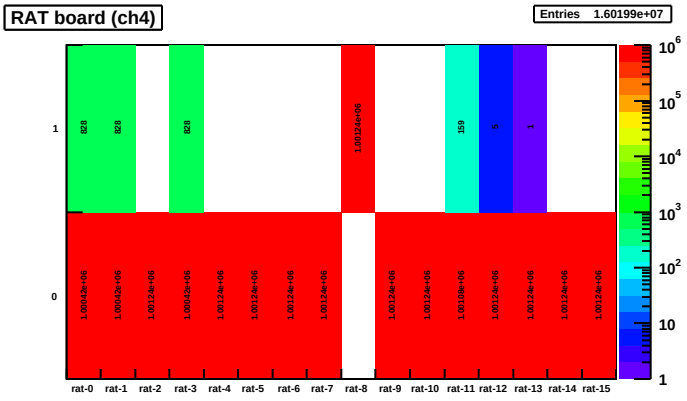

Hybrid Jet Patches
Entries 2002488
JP ADC
JP ID

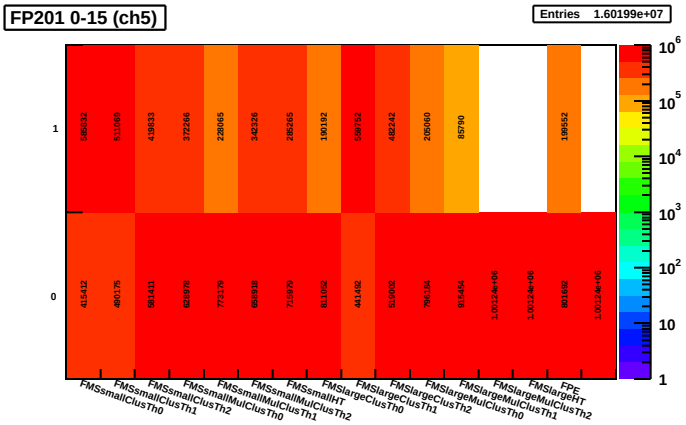

TF201 0-15 (ch0)
Entries 1.60199e+07
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

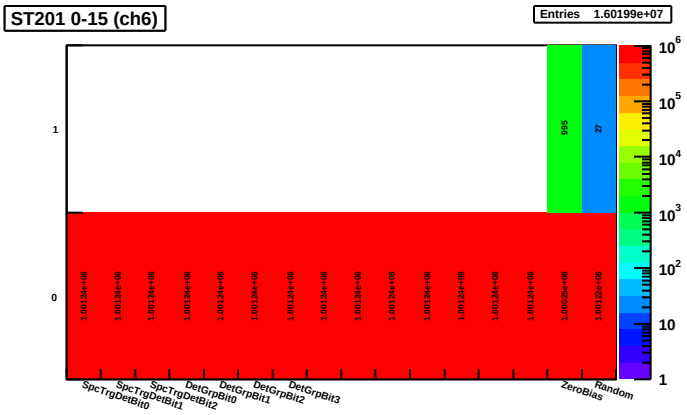

VT201 0-15 (ch1)
Entries 1.60199e+07
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

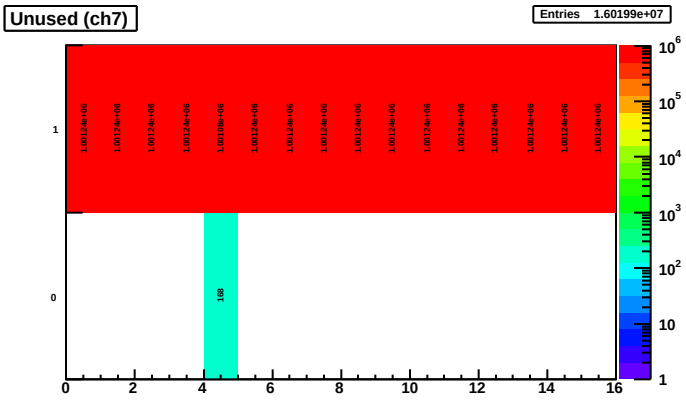

Unused (ch2)
Entries 1.60199e+07

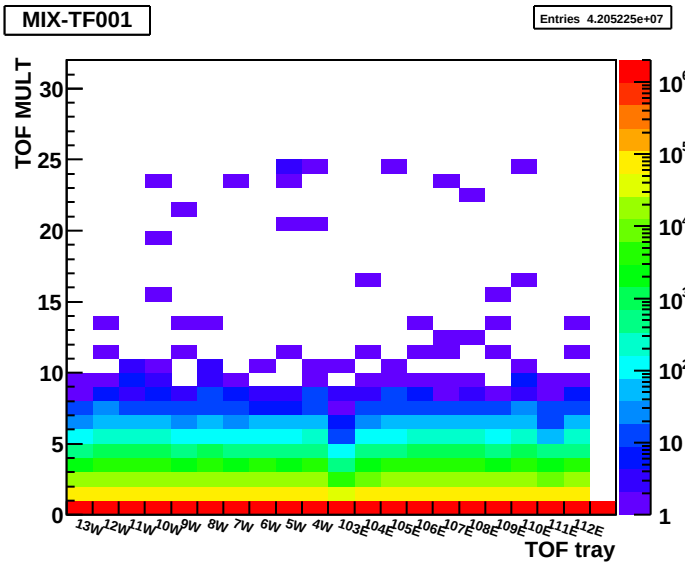

EM201 0-15 (ch3)
Entries 1.60199e+07
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

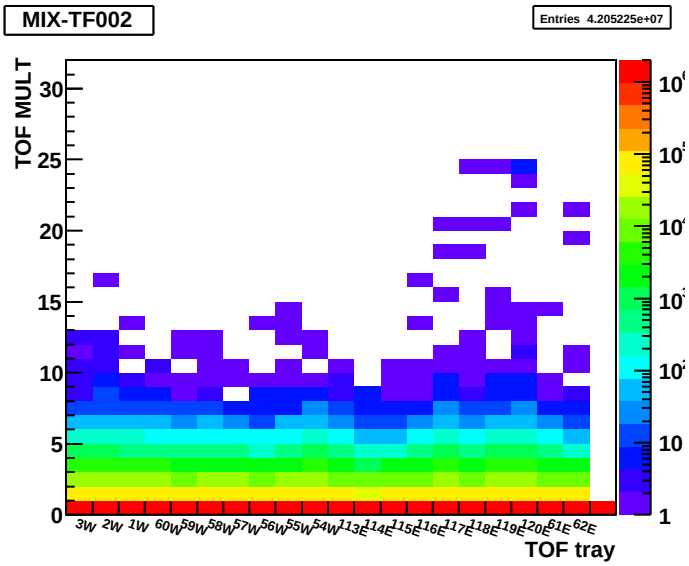

RAT board (ch4)
Entries 1.60199e+07
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

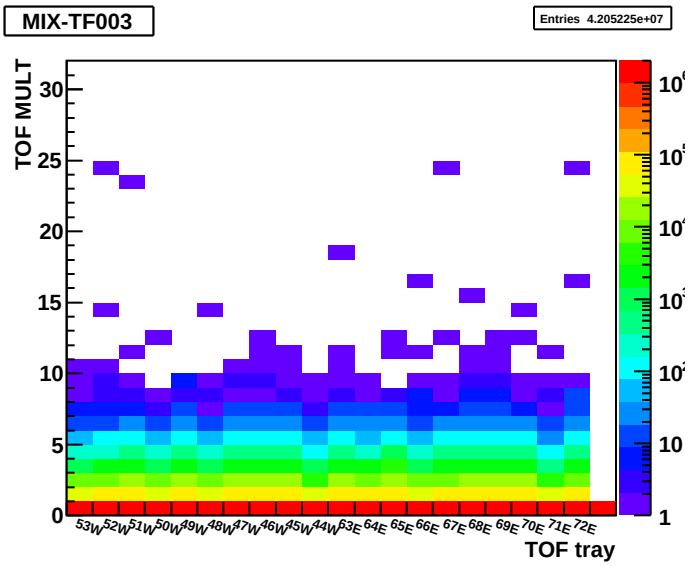

FP201 0-15 (ch5)
Entries 1.60199e+07
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

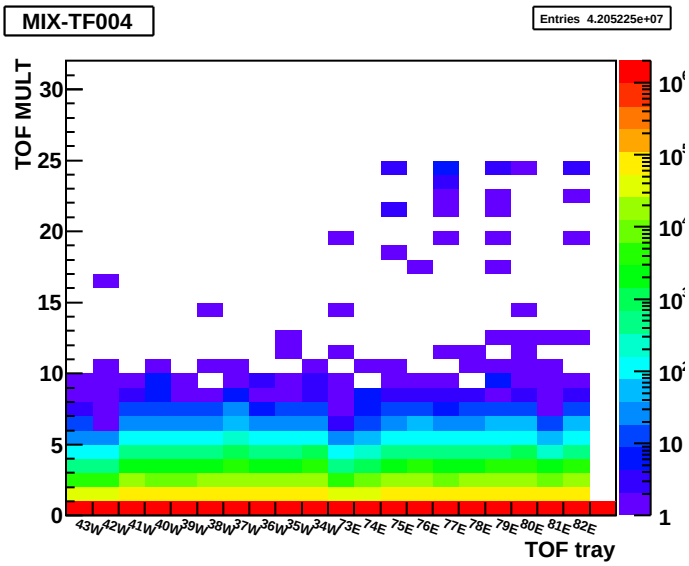

ST201 0-15 (ch6)
Entries 1.60199e+07
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

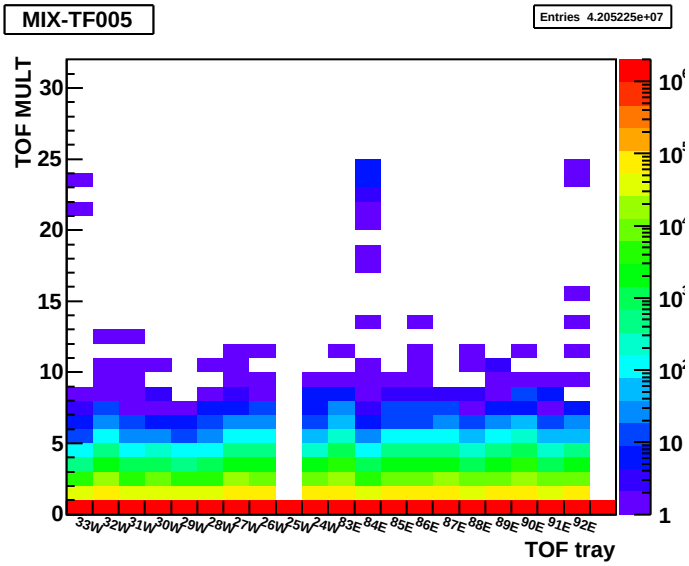

Unused (ch7)
Entries 1.60199e+07

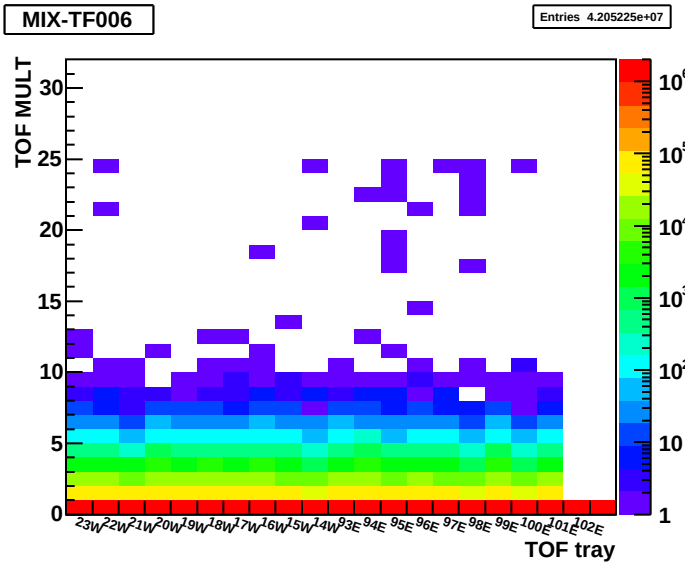

MIX-TF001
Entries 4.205225e+07
TOF MULT
TOF tray

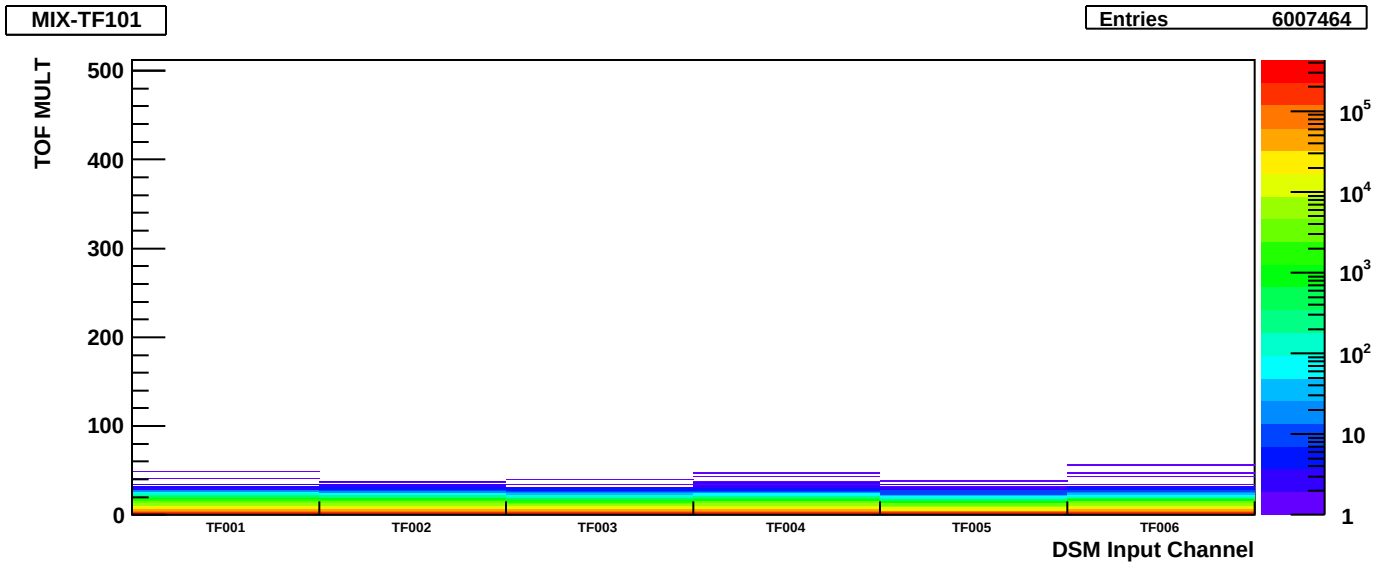

MIX-TF002
Entries 4.205225e+07
TOF MULT
TOF tray

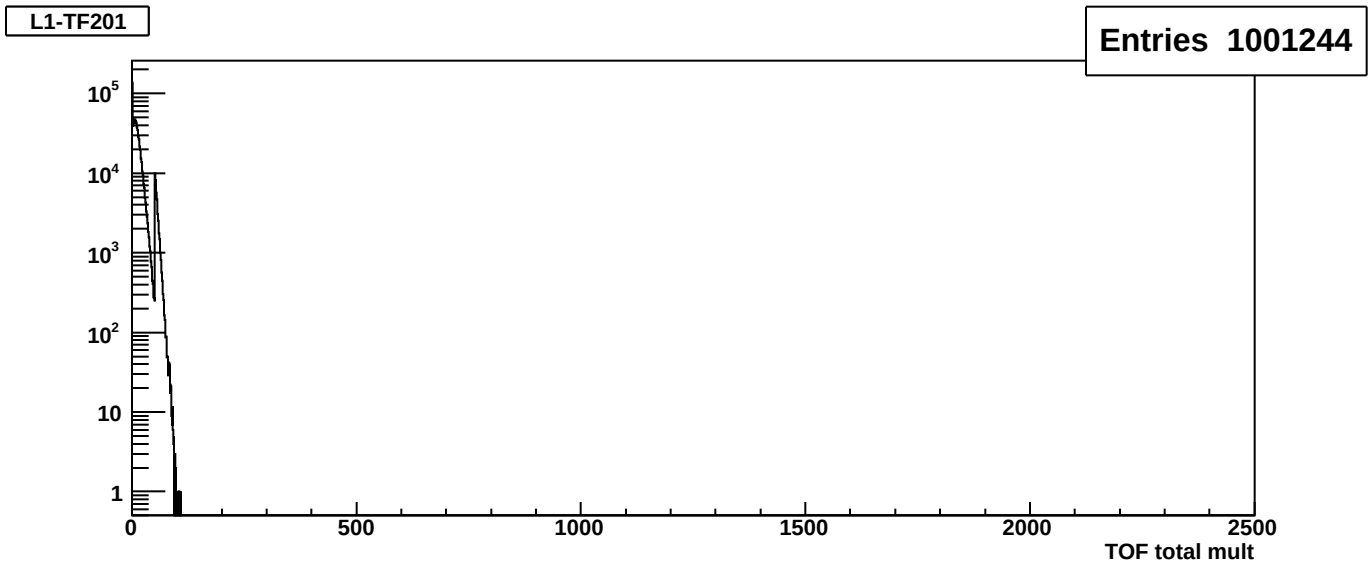

MIX-TF003
Entries 4.205225e+07
TOF MULT
TOF tray

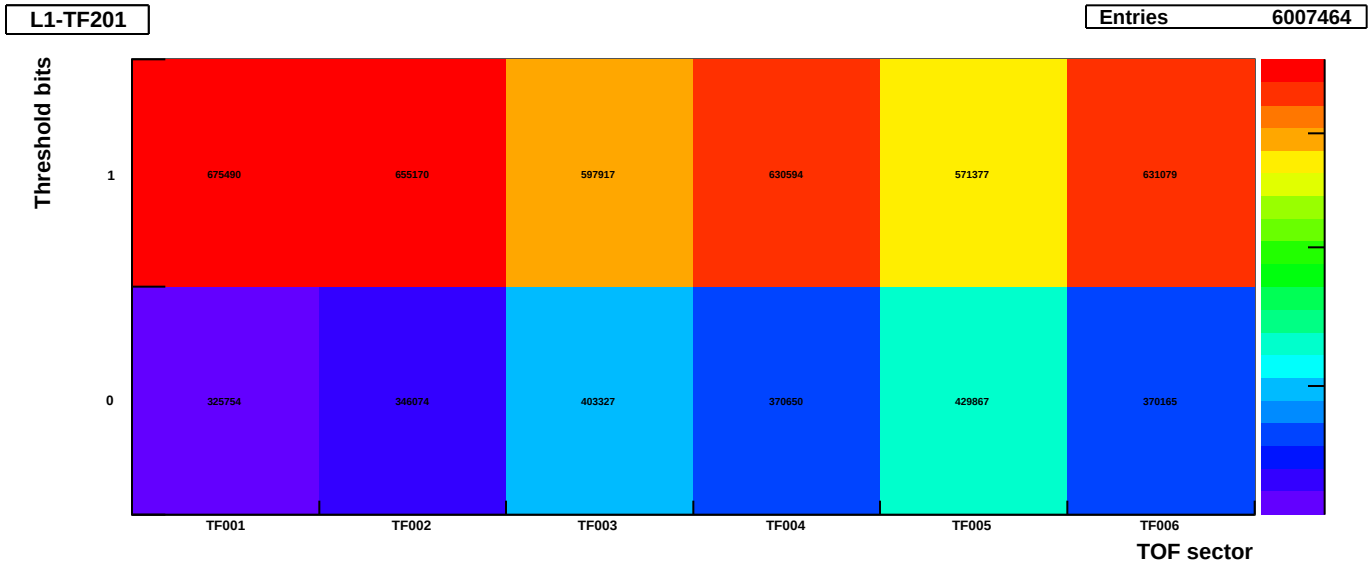

MIX-TF004
Entries 4.205225e+07
TOF MULT
TOF tray

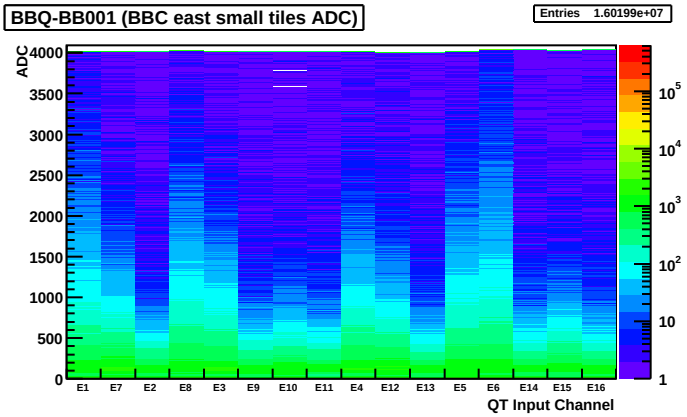

MIX-TF005
Entries 4.205225e+07
TOF MULT
TOF tray

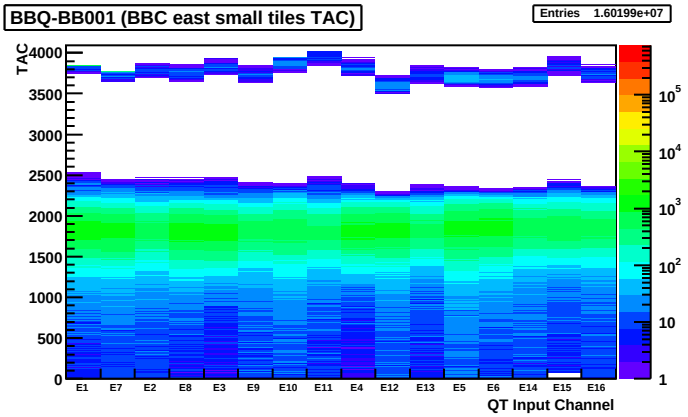

MIX-TF006
Entries 4.205225e+07
TOF MULT
TOF tray

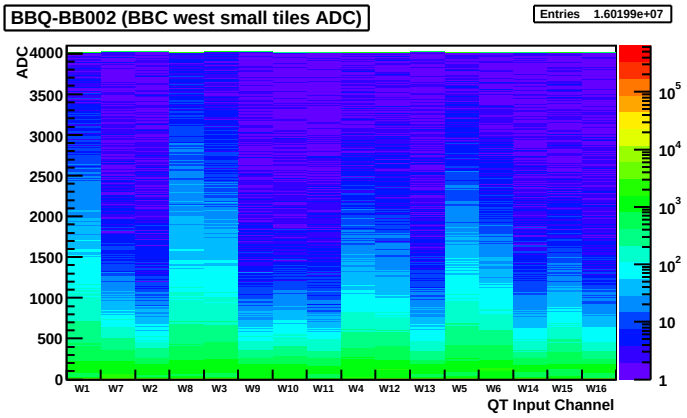

MIX-TF101
Entries 6007464
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

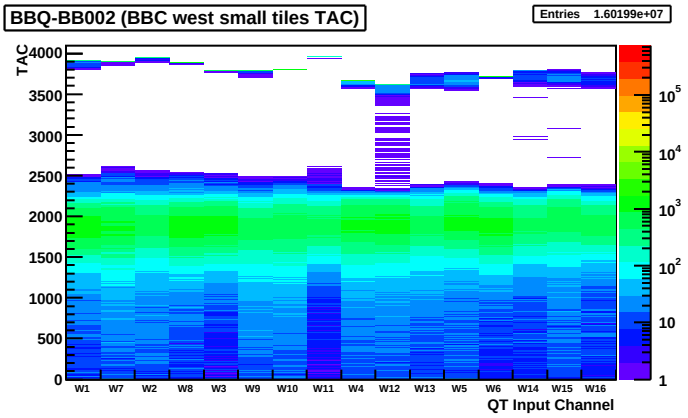

L1-TF201
Entries 1001244
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

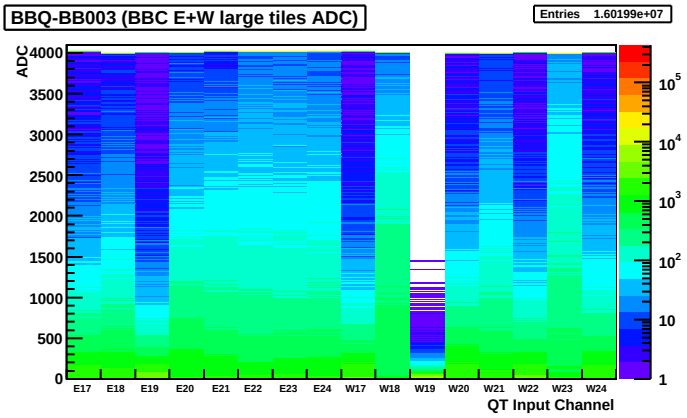

L1-TF201
Entries 6007464
Threshold bits
1
0
675490
655170
597917
630594
571377
631079
325754
346074
403327
370650
429867
370165
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

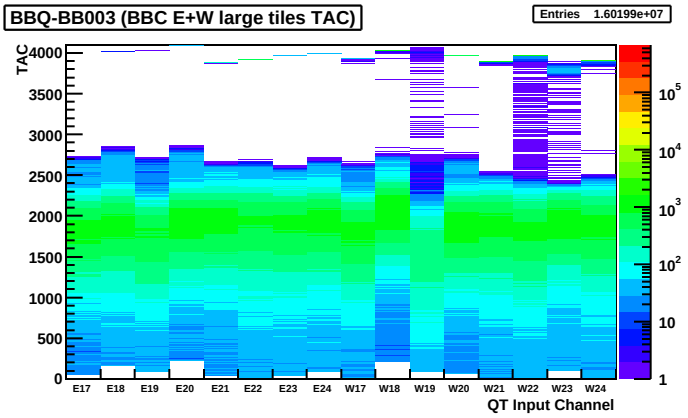

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.60199e+07
ADC
QT Input Channel

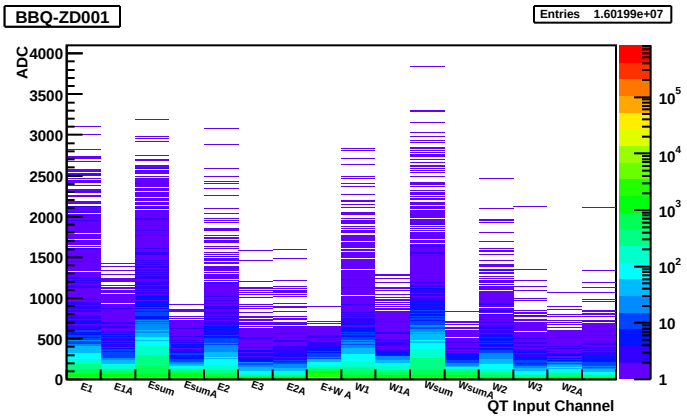

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.60199e+07
TAC
QT Input Channel

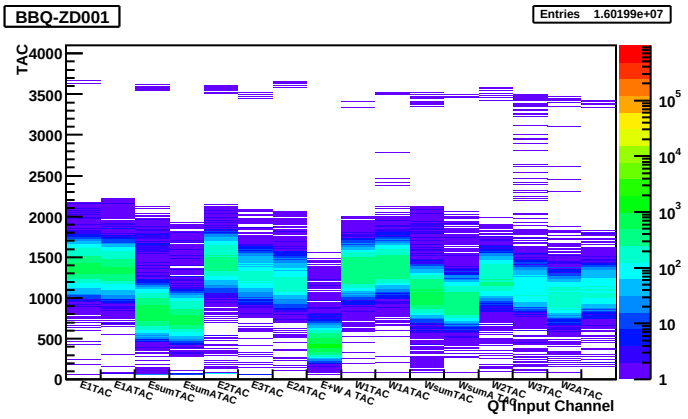

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.60199e+07
ADC
QT Input Channel

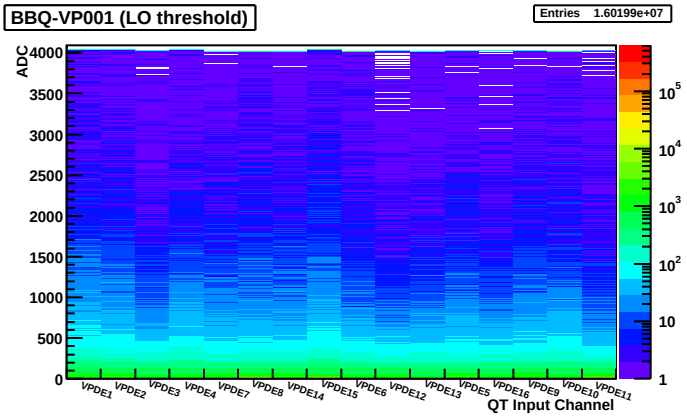

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.60199e+07
TAC
QT Input Channel

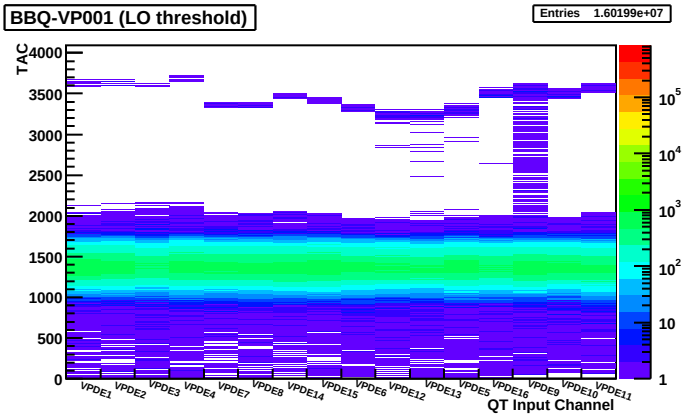

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.60199e+07
ADC
QT Input Channel

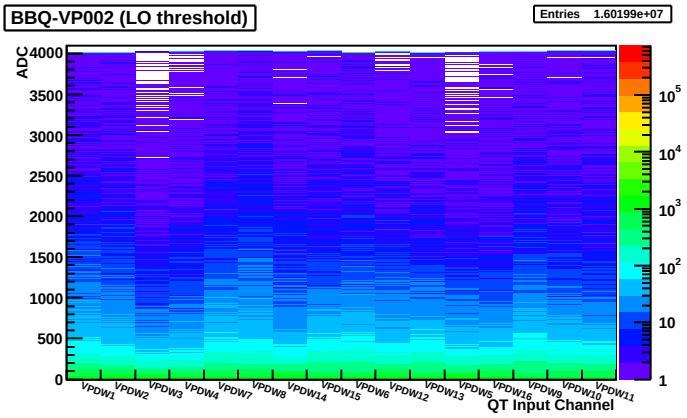

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.60199e+07
TAC
QT Input Channel

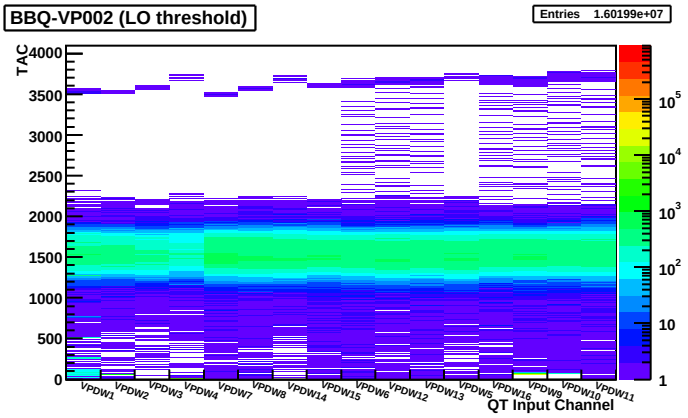

BBQ-ZD001
Entries 1.60199e+07
ADC
QT Input Channel

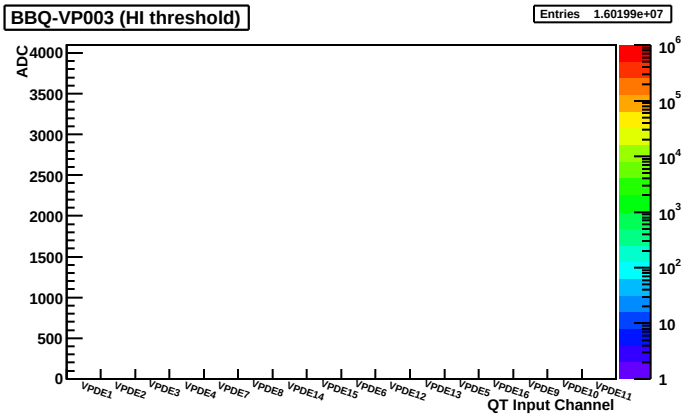

BBQ-ZD001
Entries 1.60199e+07
TAC
QT Input Channel

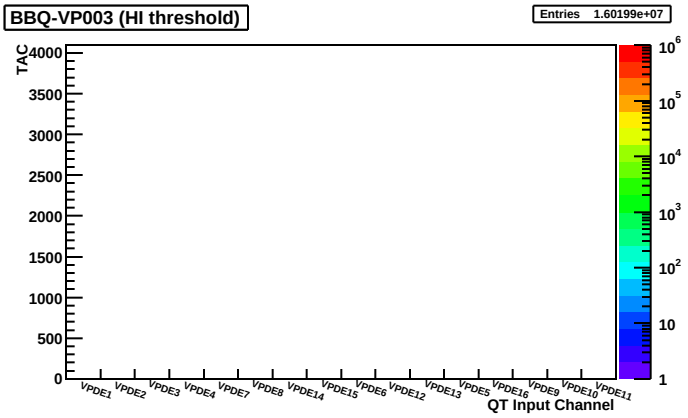

BBQ-VP001 (LO threshold)
Entries 1.60199e+07
ADC
QT Input Channel

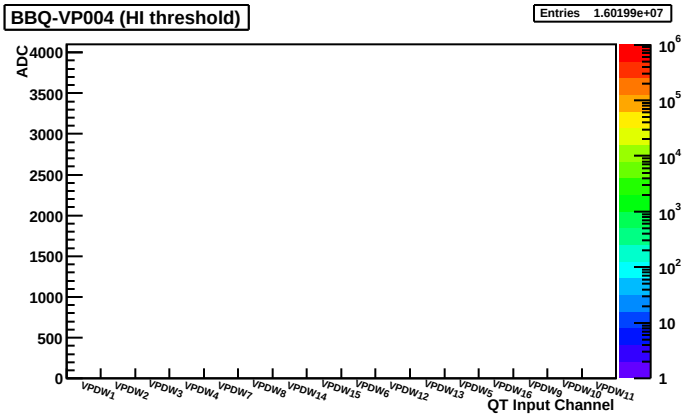

BBQ-VP001 (LO threshold)
Entries 1.60199e+07
TAC
QT Input Channel

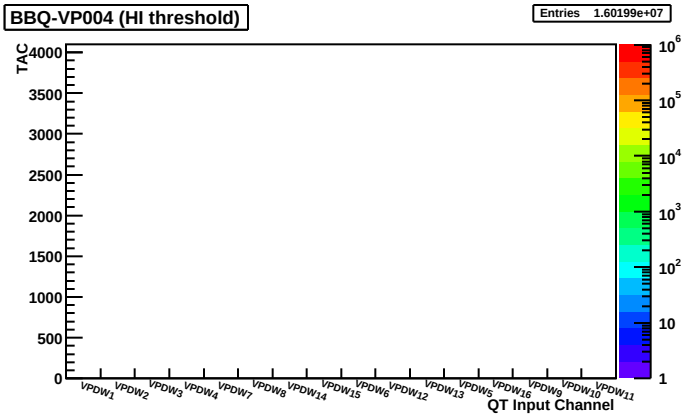

BBQ-VP002 (LO threshold)
Entries 1.60199e+07
ADC
QT Input Channel

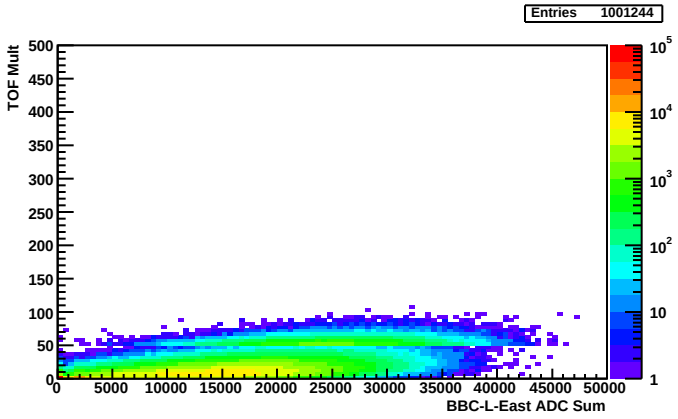

BBQ-VP002 (LO threshold)
Entries 1.60199e+07
TAC
QT Input Channel

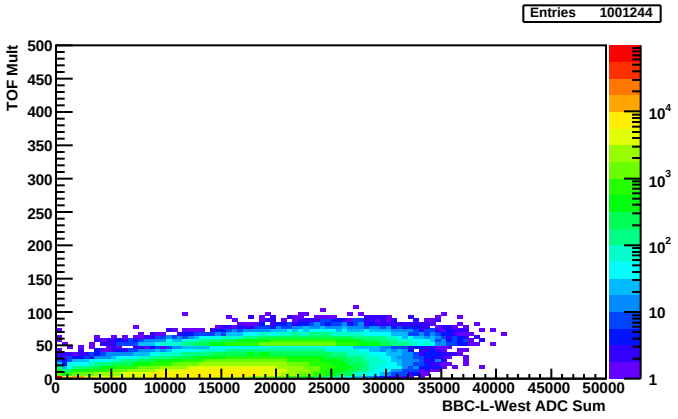

BBQ-VP003 (HI threshold)
Entries 1.60199e+07
ADC
QT Input Channel

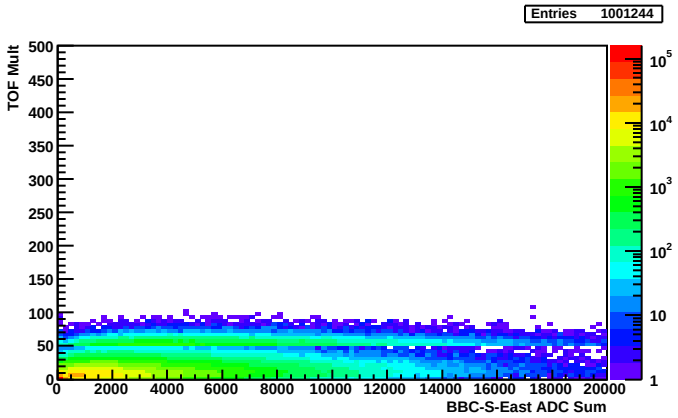

BBQ-VP003 (HI threshold)
Entries 1.60199e+07
TAC
QT Input Channel

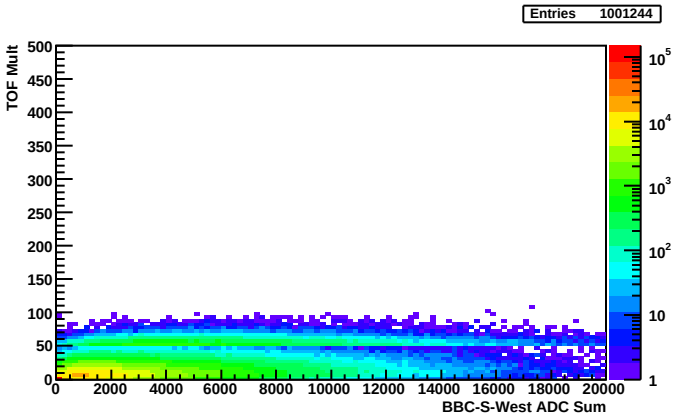

BBQ-VP004 (HI threshold)
Entries 1.60199e+07
ADC
QT Input Channel

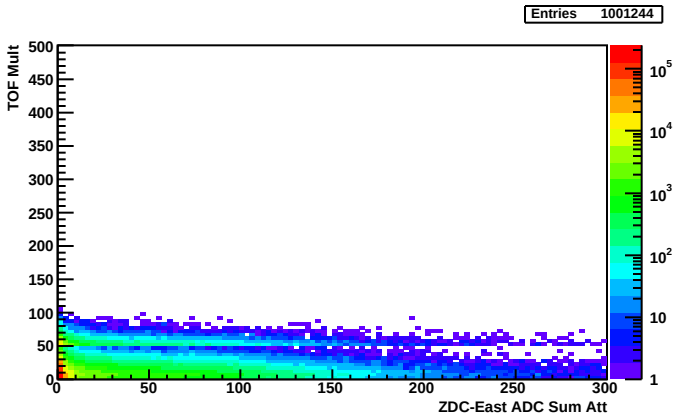

BBQ-VP004 (HI threshold)
Entries 1.60199e+07
TAC
QT Input Channel

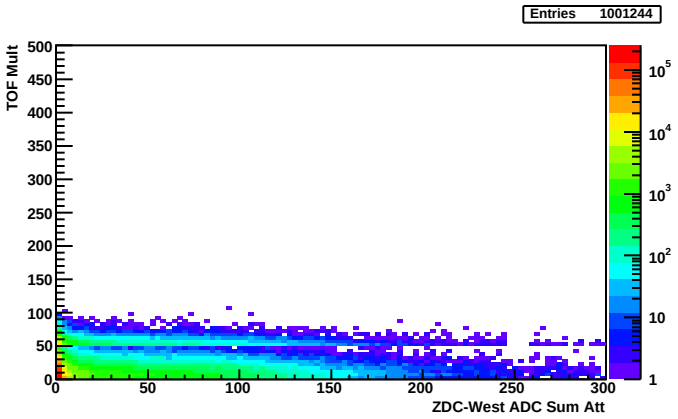

Entries 1001244
TOF Mult
BBC-L-East ADC Sum

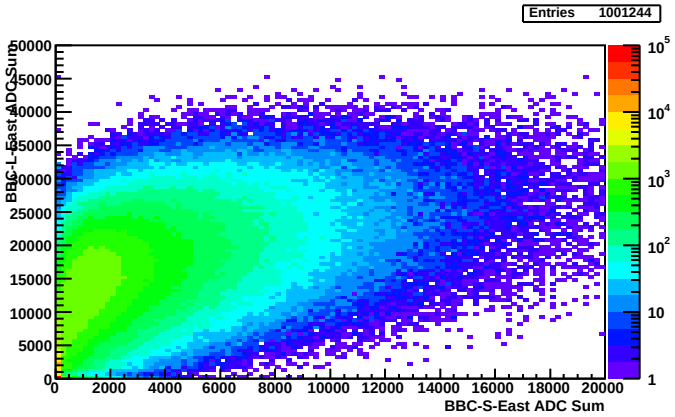

Entries 1001244
TOF Mult
BBC-L-West ADC Sum

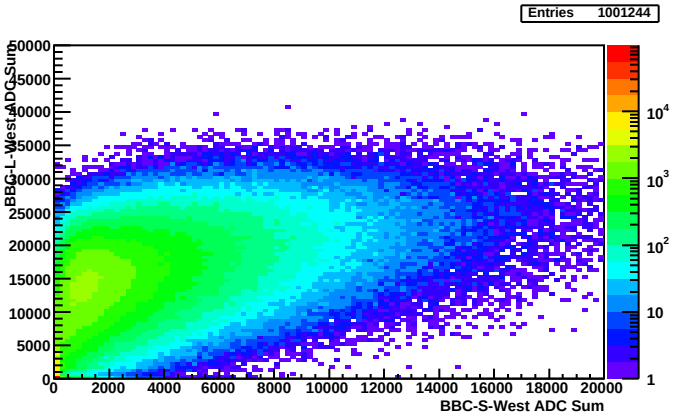

Entries 1001244
TOF Mult
BBC-S-East ADC Sum

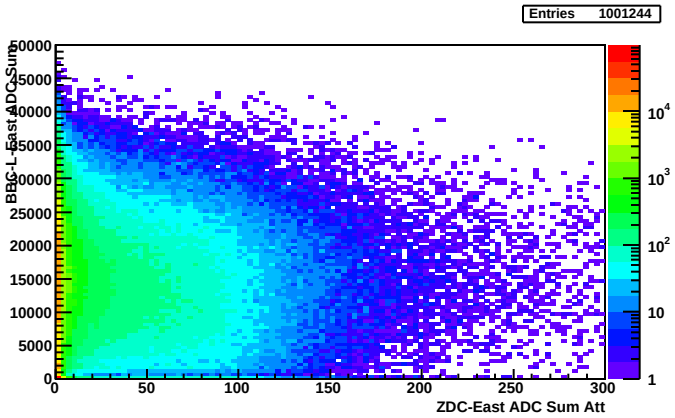

Entries 1001244
TOF Mult
BBC-S-West ADC Sum

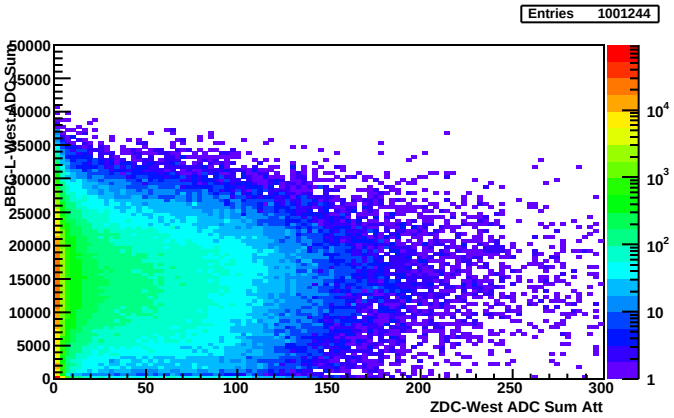

Entries 1001244
TOF Mult
ZDC-East ADC Sum Att

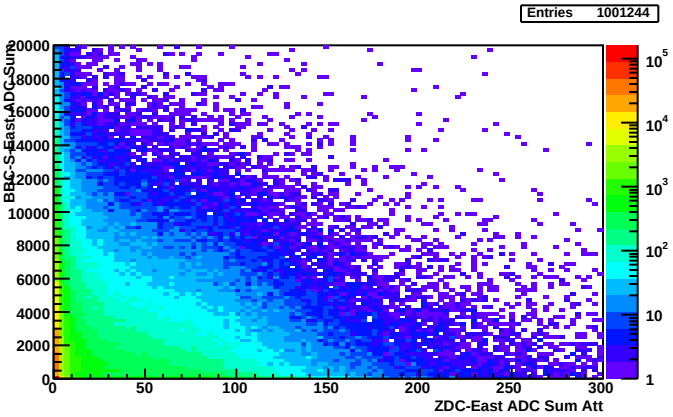

Entries 1001244
TOF Mult
ZDC-West ADC Sum Att

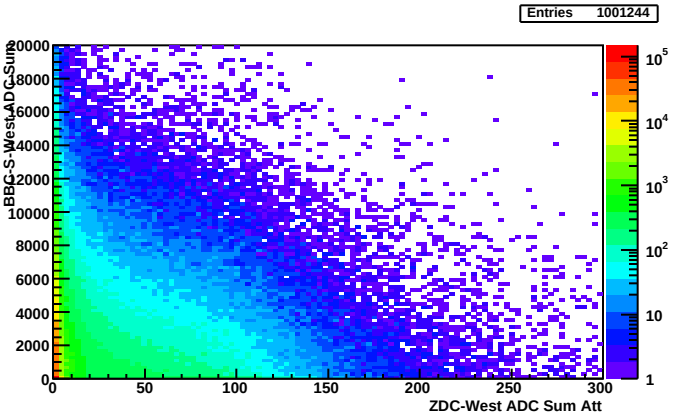

Entries 1001244
BBC-L-East ADC Sum
BBC-S-East ADC Sum

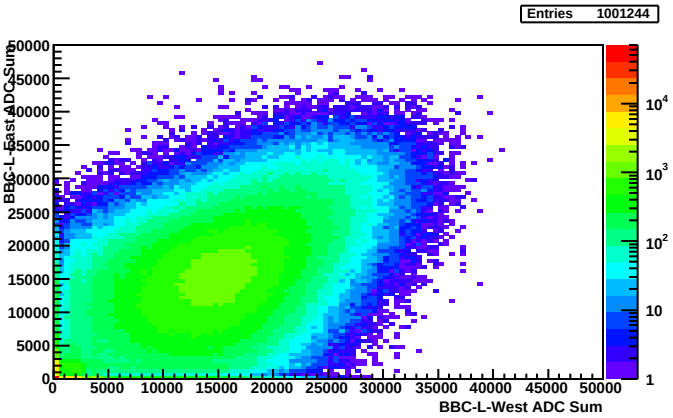

Entries 1001244
BBC-L-West ADC Sum
BBC-S-West ADC Sum

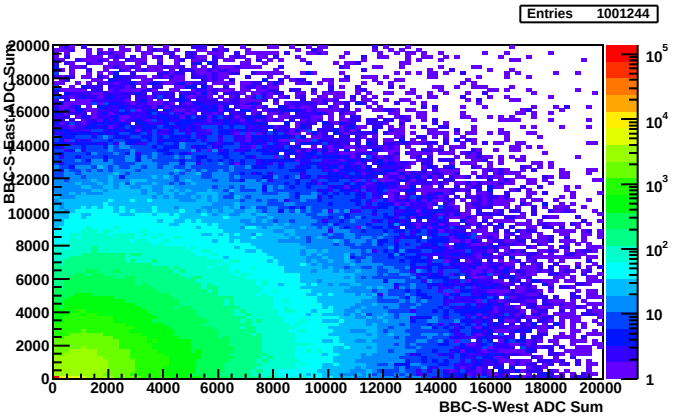

Entries 1001244
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

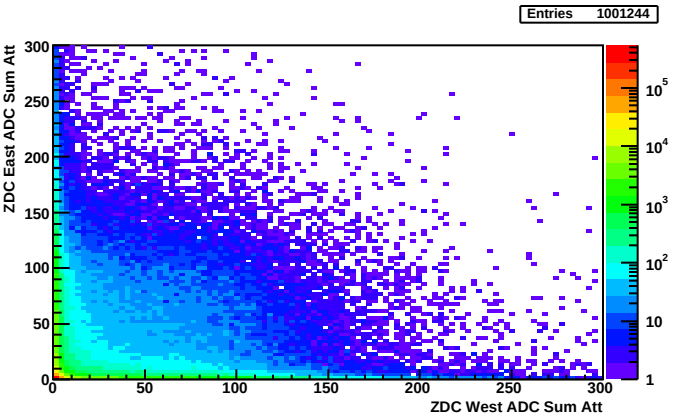

Entries 1001244
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

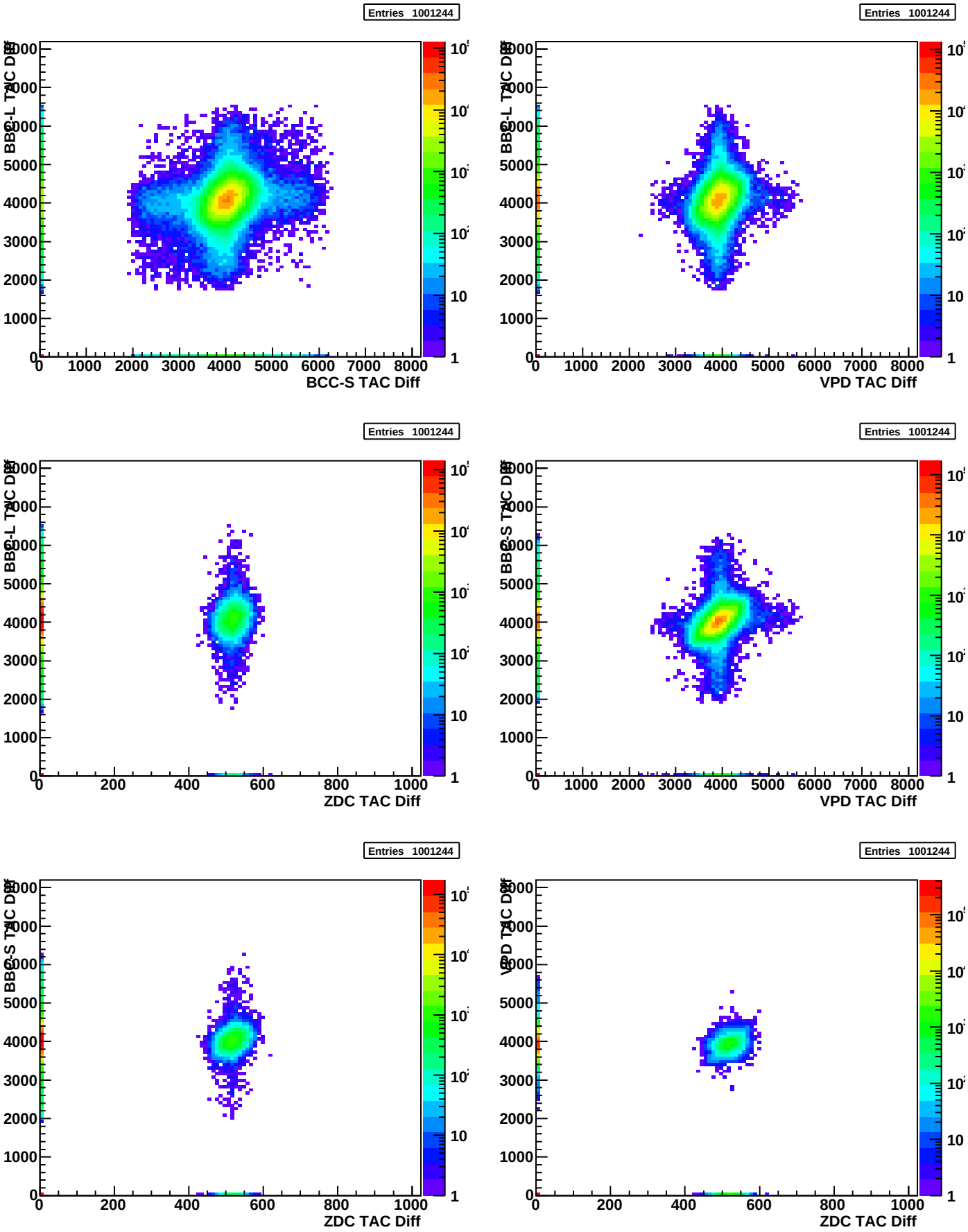

Entries 1001244
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

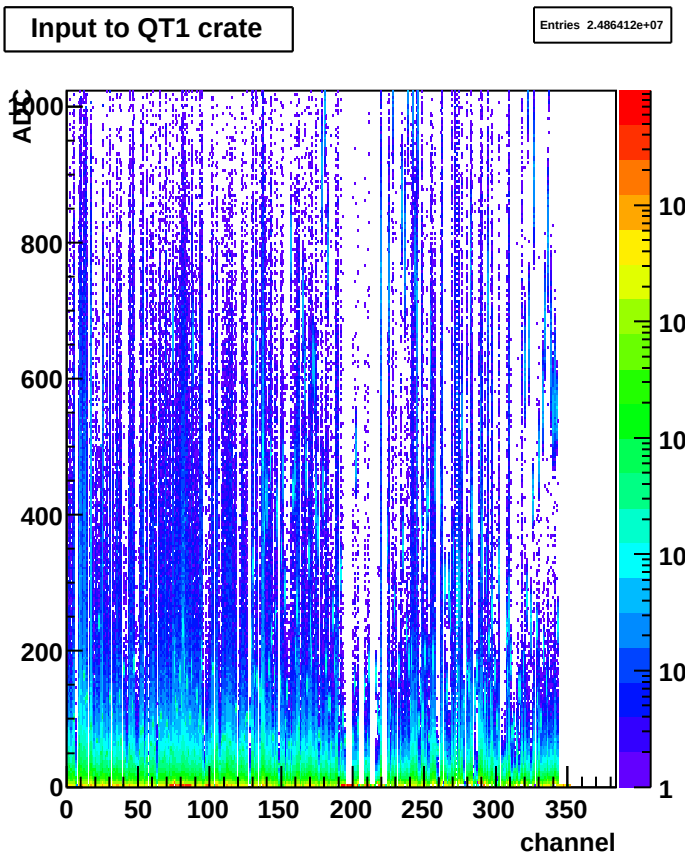

Entries 1001244
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

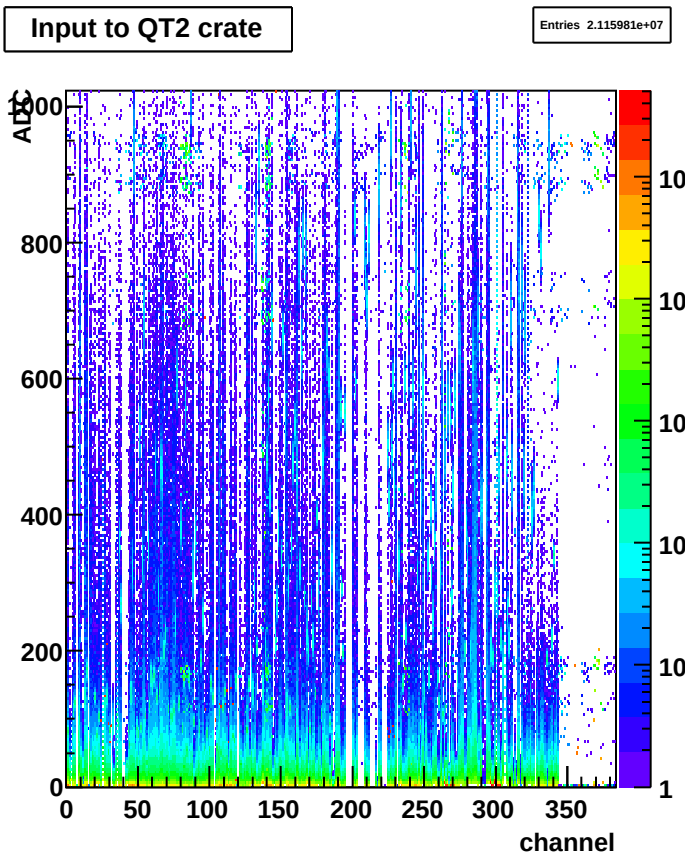

Entries 1001244
BBC-L-East ADC Sum
BBC-L-West ADC Sum

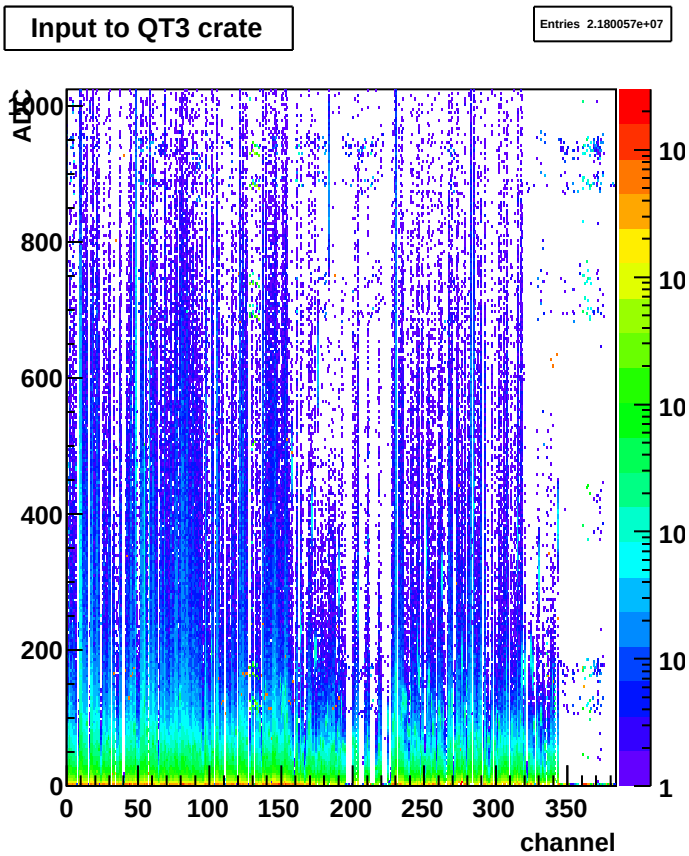

Entries 1001244
BBC-S-East ADC Sum
BBC-S-West ADC Sum

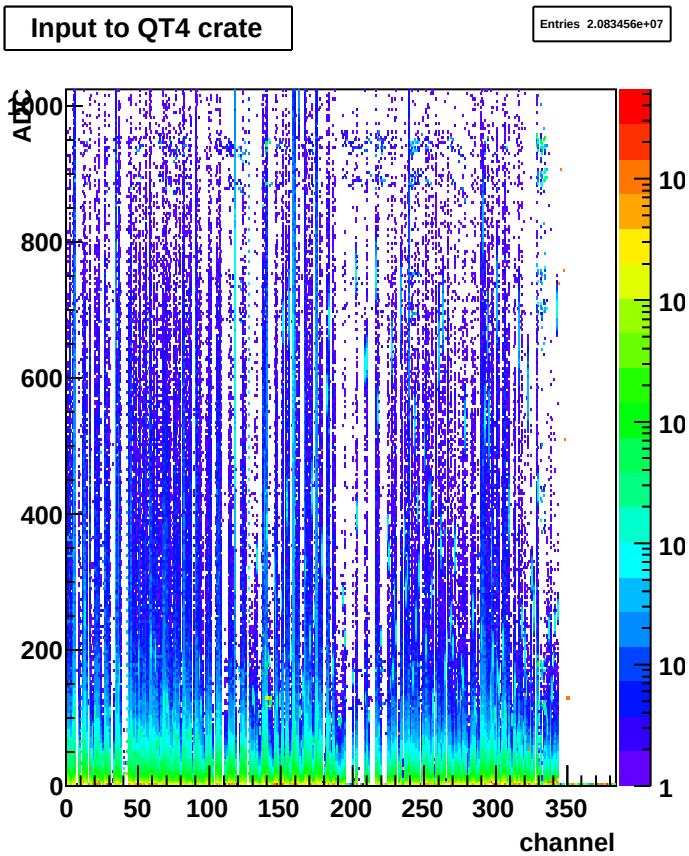

Entries 1001244
ZDC East ADC Sum Att
ZDC West ADC Sum Att

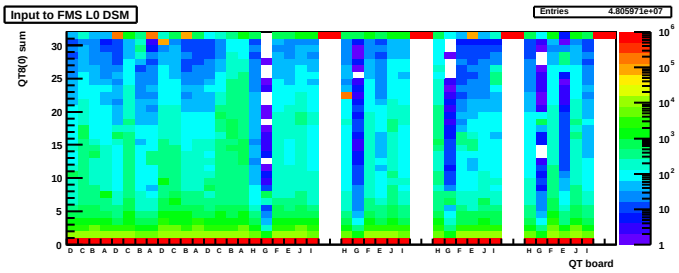

Entries 1001244
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1001244
BBC-L TAC Diff
VPD TAC Diff
Entries 1001244
BBC-L TAC Diff
ZDC TAC Diff
Entries 1001244
BBC-S TAC Diff
VPD TAC Diff
Entries 1001244
BBC-S TAC Diff
ZDC TAC Diff
Entries 1001244
VPD TAC Diff
ZDC TAC Diff

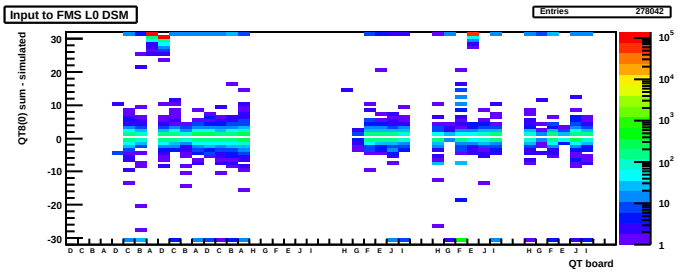

Input to QT1 crate
Entries 2.486412e+07
ADC
channel

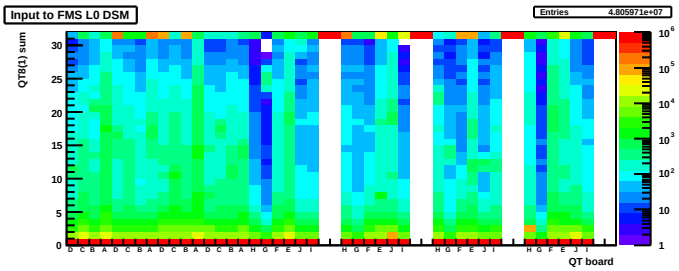

Input to QT2 crate
Entries 2.115981e+07
ADC
channel

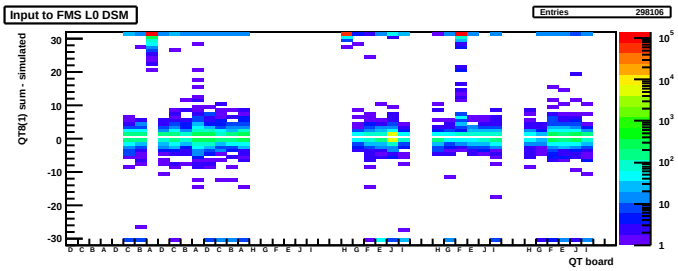

Input to QT3 crate
Entries 2.180057e+07
ADC
channel

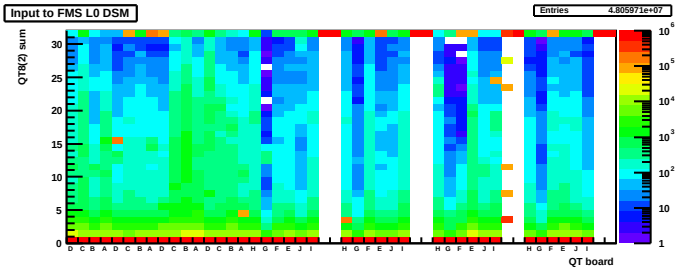

Input to QT4 crate
Entries 2.083456e+07
ADC
channel

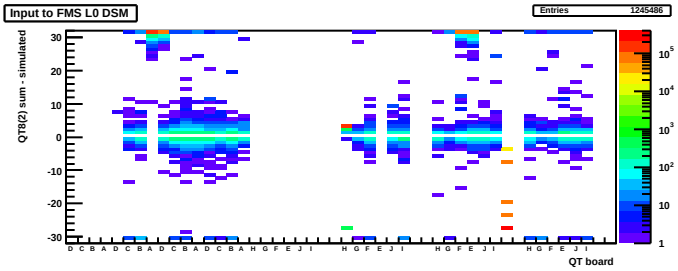

Input to FMS L0 DSM
Entries 4.805971e+07
QT8(0) sum
QT board

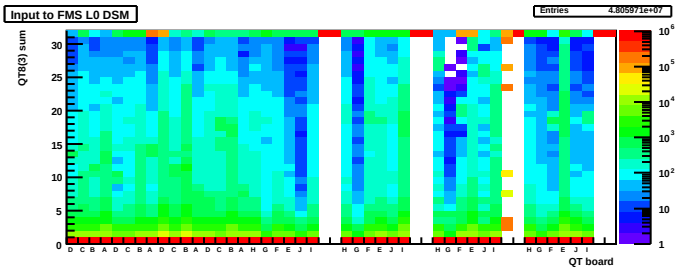

Input to FMS L0 DSM
Entries 278042
QT8(0) sum - simulated
QT board

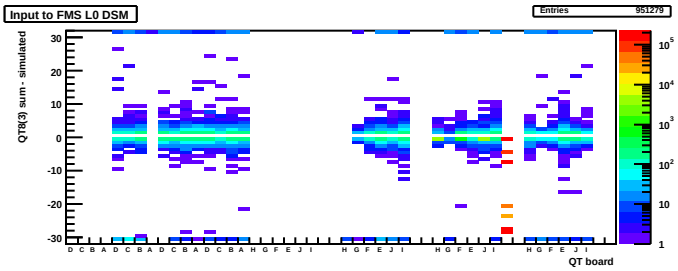

Input to FMS L0 DSM
Entries 4.805971e+07
QT8(1) sum
QT board

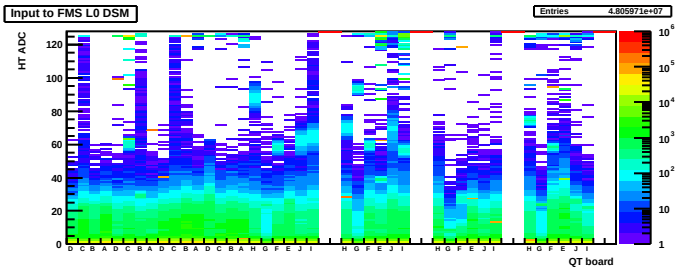

Input to FMS L0 DSM
Entries 298106
QT8(1) sum - simulated
QT board

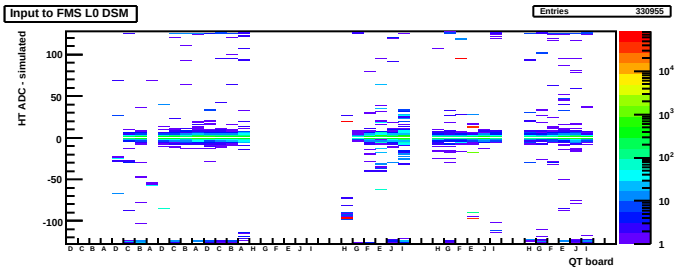

Input to FMS L0 DSM
Entries 4.805971e+07
QT8(2) sum
QT board

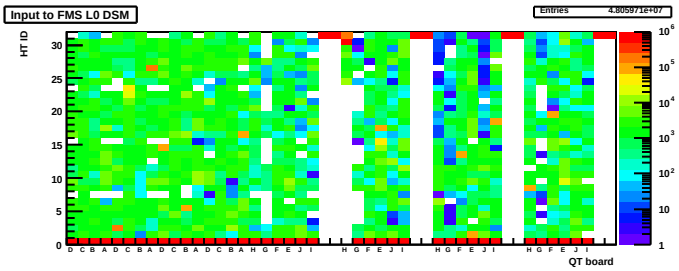

Input to FMS L0 DSM
Entries 1245486
QT8(2) sum - simulated
QT board

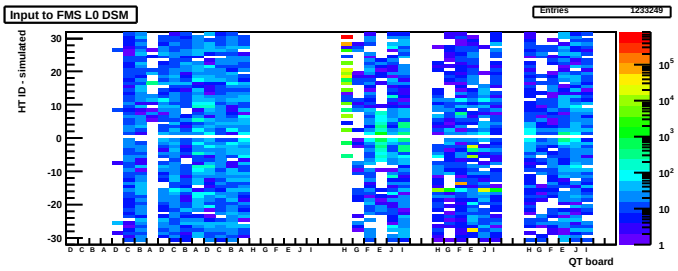

Input to FMS L0 DSM
Entries 4.805971e+07
QT8(3) sum
QT board

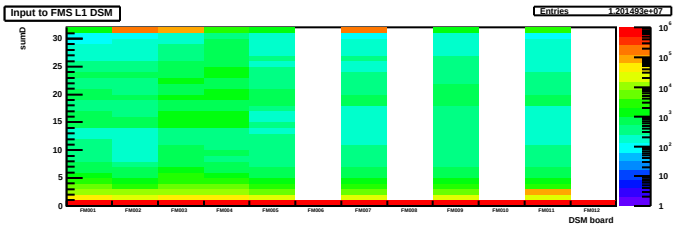

Input to FMS L0 DSM
Entries 951279
QT8(3) sum - simulated
QT board

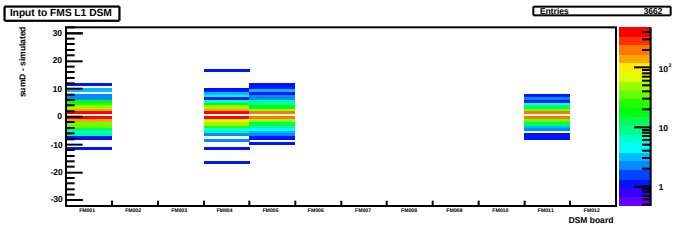

Input to FMS L0 DSM
Entries 4.805971e+07
HT ADC
QT board

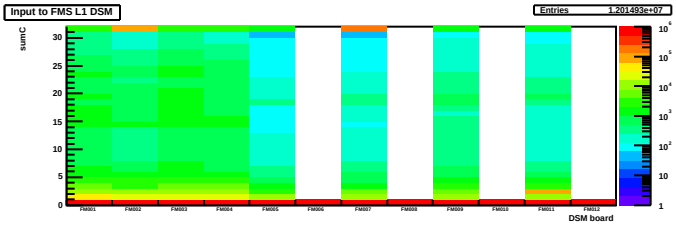

Input to FMS L0 DSM
Entries 330955
HT ADC - simulated
QT board

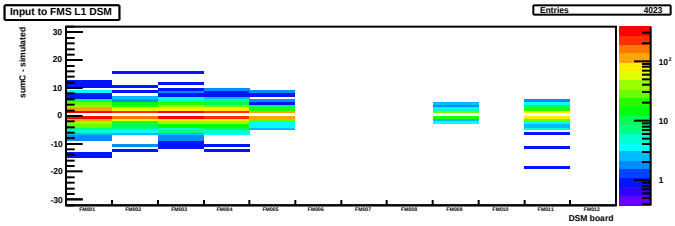

Input to FMS L0 DSM
Entries 4.805971e+07
HT ID
QT board

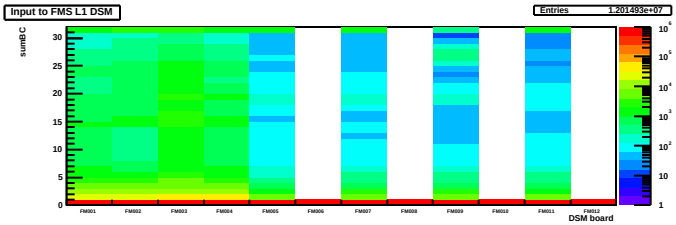

Input to FMS L0 DSM
Entries 1233249
HT ID - simulated
QT board

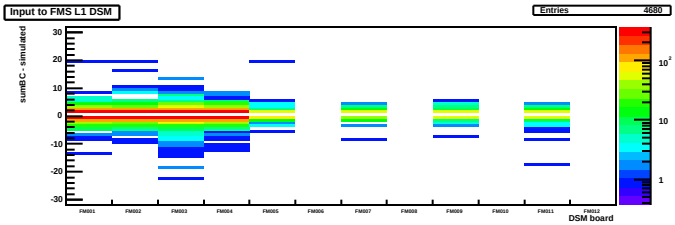

Input to FMS L1 DSM
Entries 1.201493e+07
sumD
DSM board

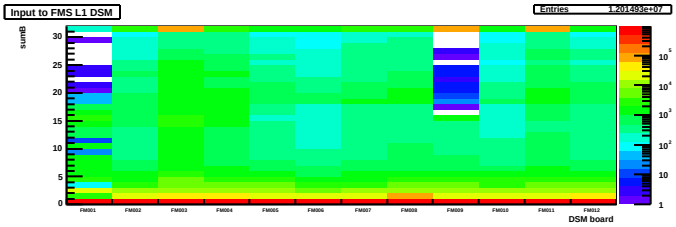

Input to FMS L1 DSM
Entries 3662
sumD - simulated
DSM board

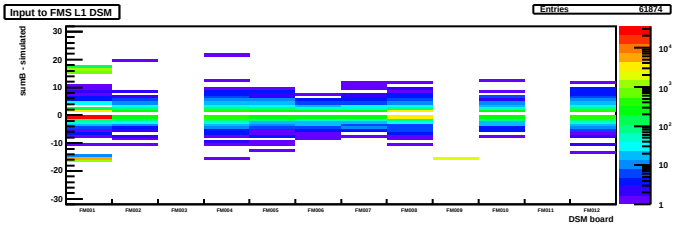

Input to FMS L1 DSM
Entries 1.201493e+07
sumC
DSM board

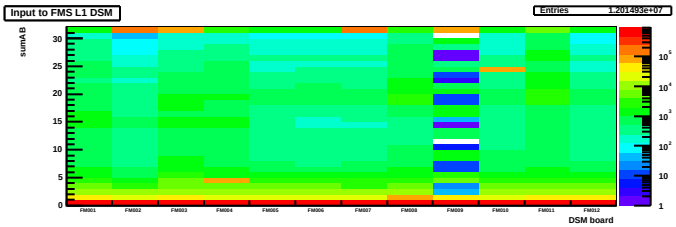

Input to FMS L1 DSM
Entries 4023
sumC - simulated
DSM board

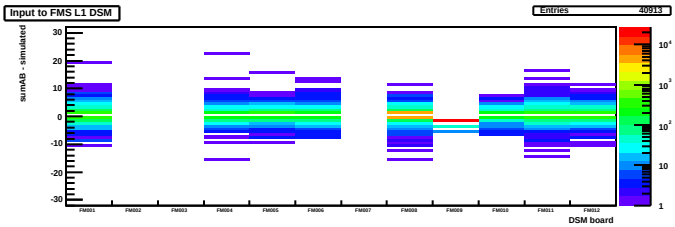

Input to FMS L1 DSM
Entries 1.201493e+07
sumBC
DSM board

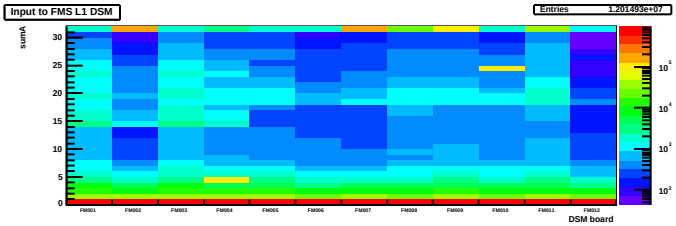

Input to FMS L1 DSM
Entries 4680
sumBC - simulated
DSM board

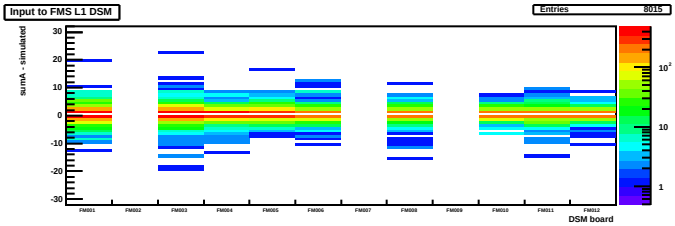

Input to FMS L1 DSM
Entries 1.201493e+07
sumB
DSM board

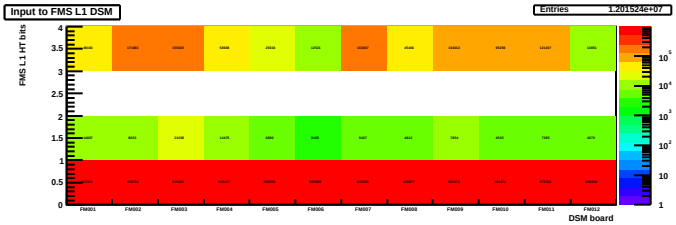

Input to FMS L1 DSM
Entries 61874
sumB - simulated
DSM board

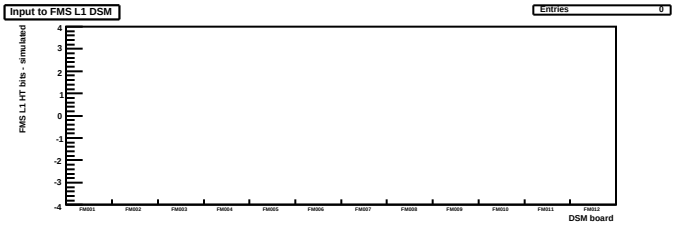

Input to FMS L1 DSM
Entries 1.201493e+07
sumAB
DSM board

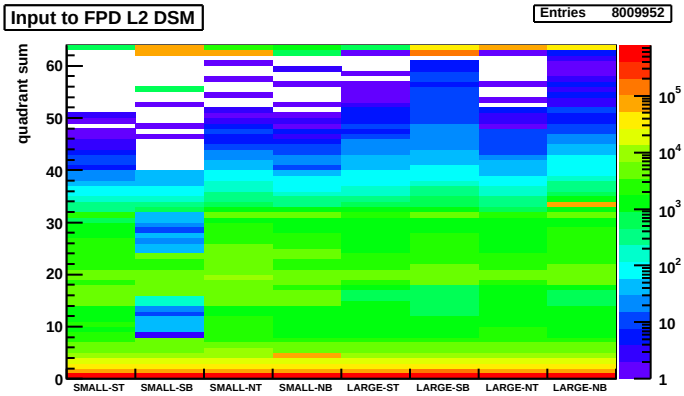

Input to FMS L1 DSM
Entries 40913
sumAB - simulated
DSM board

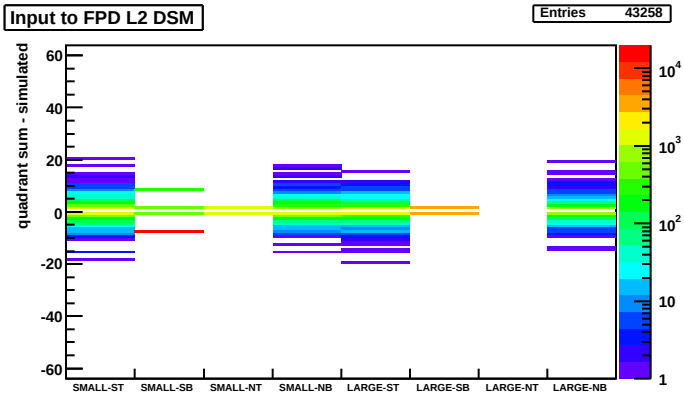

Input to FMS L1 DSM
Entries 1.201493e+07
sumA
DSM board

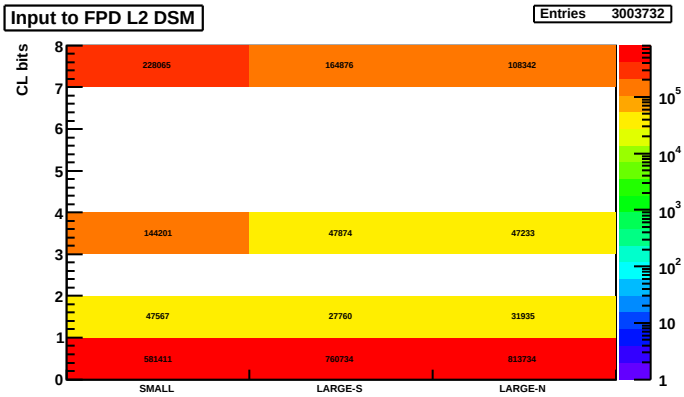

Input to FMS L1 DSM
Entries 8015
sumA - simulated
DSM board

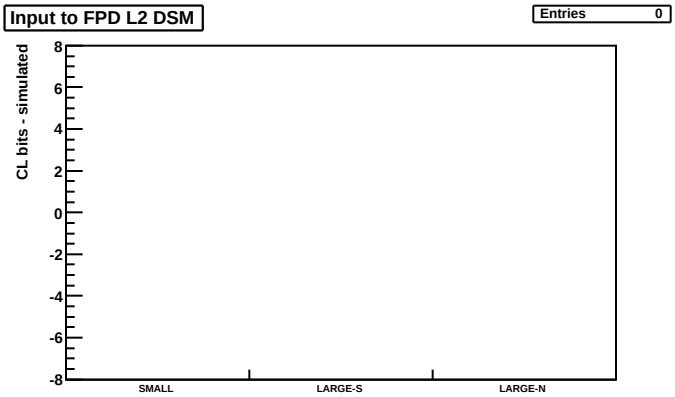

Input to FMS L1 DSM
Entries 1.201524e+07
FMS L1 HT bits
DSM board

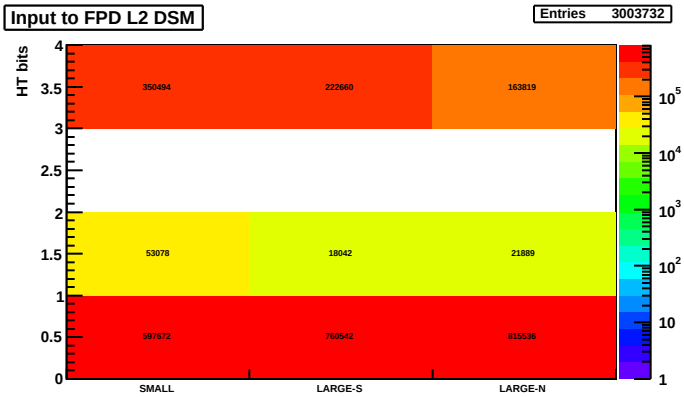

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

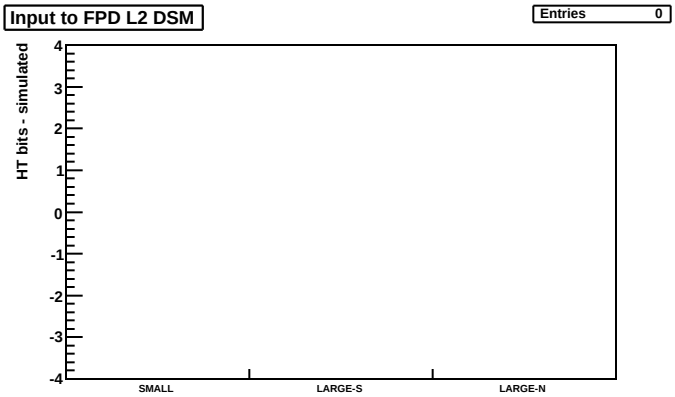

Input to FPD L2 DSM
Entries 8009952
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

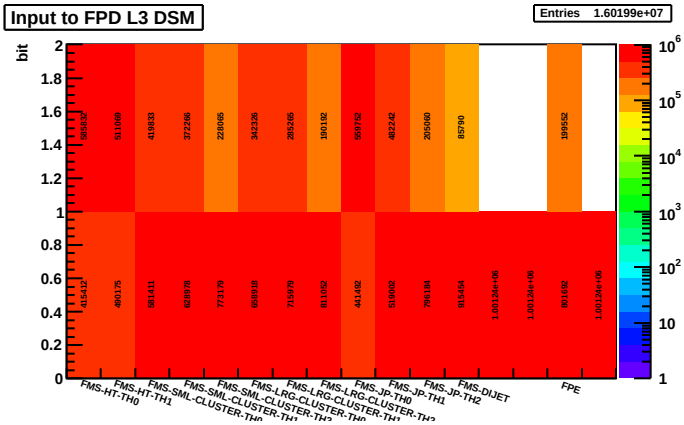

Input to FPD L2 DSM
Entries 43258
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

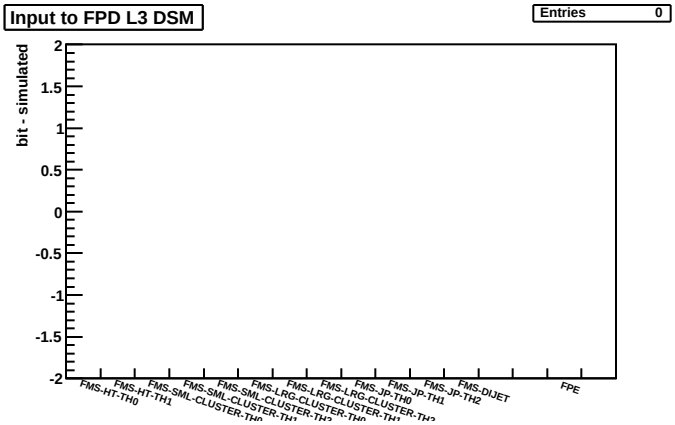

Input to FPD L2 DSM
Entries 3003732
CL bits
228065
164876
108342
144201
47874
47233
47567
27760
31935
581411
760734
813734
SMALL
LARGE-S
LARGE-N

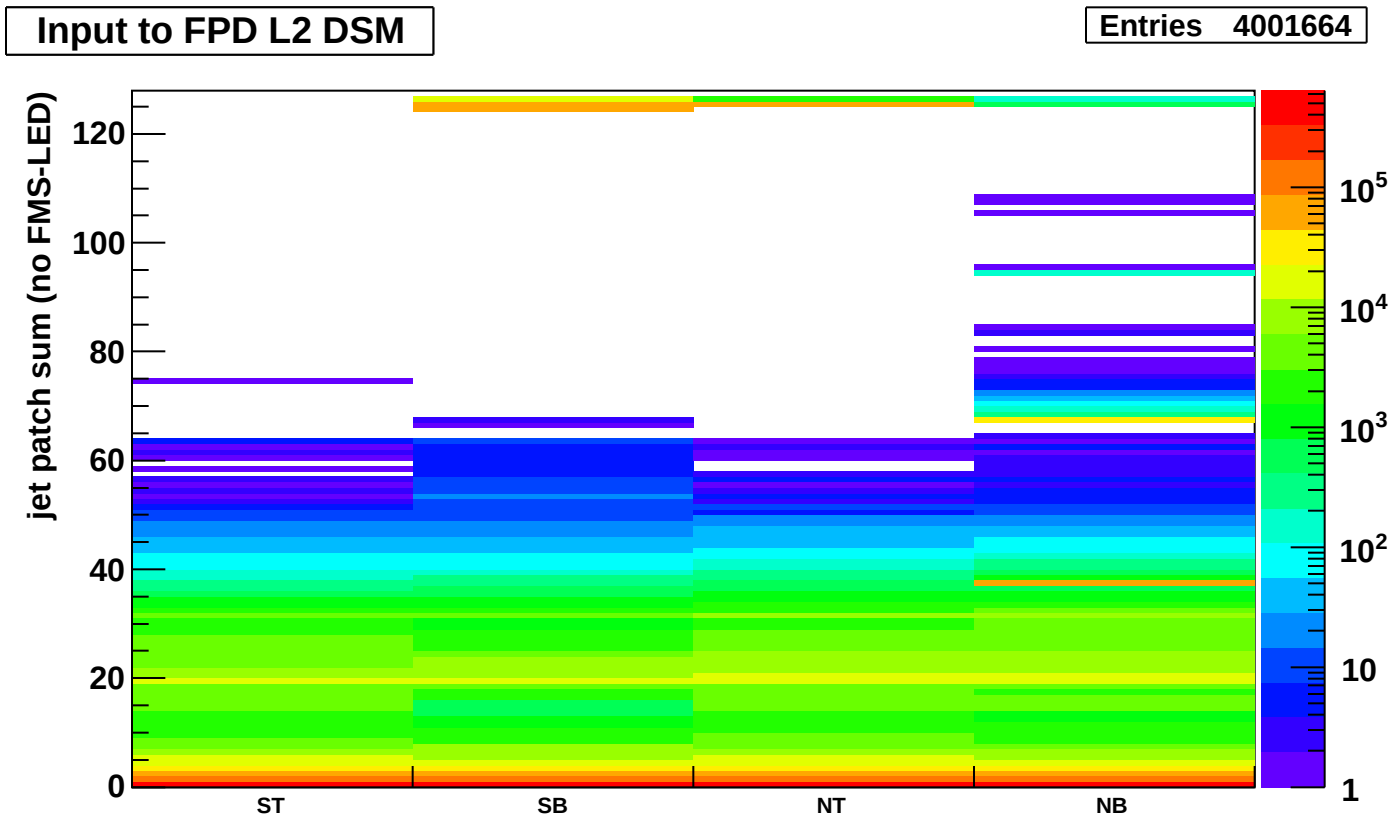

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

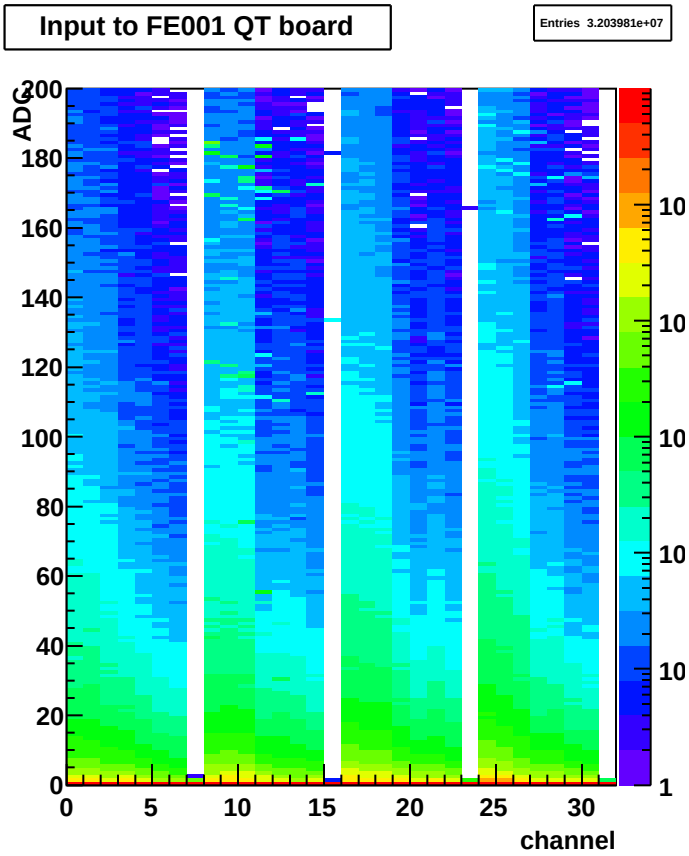

Input to FPD L2 DSM
Entries 3003732
HT bits
350494
222660
163819
53078
18042
21889
597672
760542
815536
SMALL
LARGE-S
LARGE-N

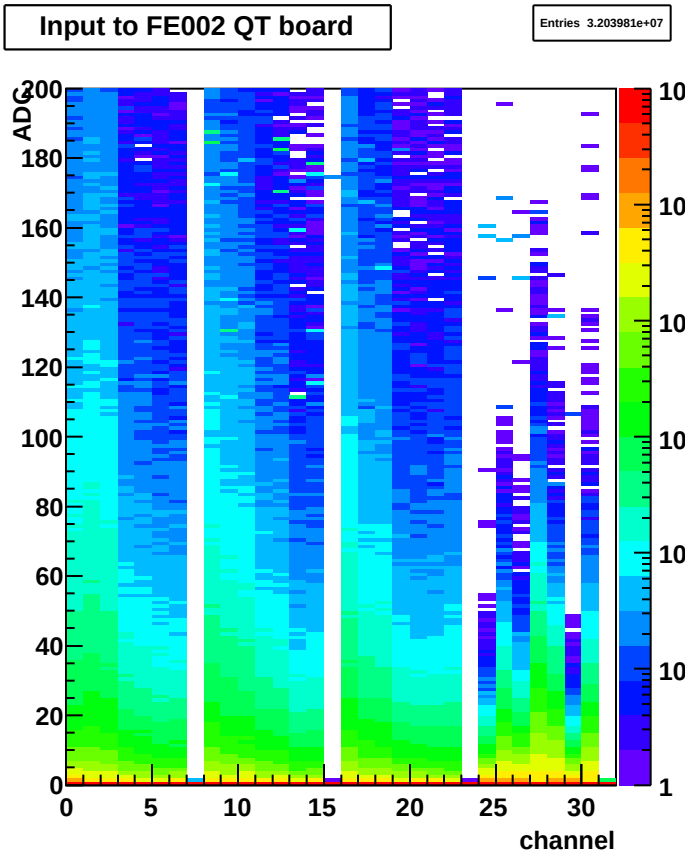

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

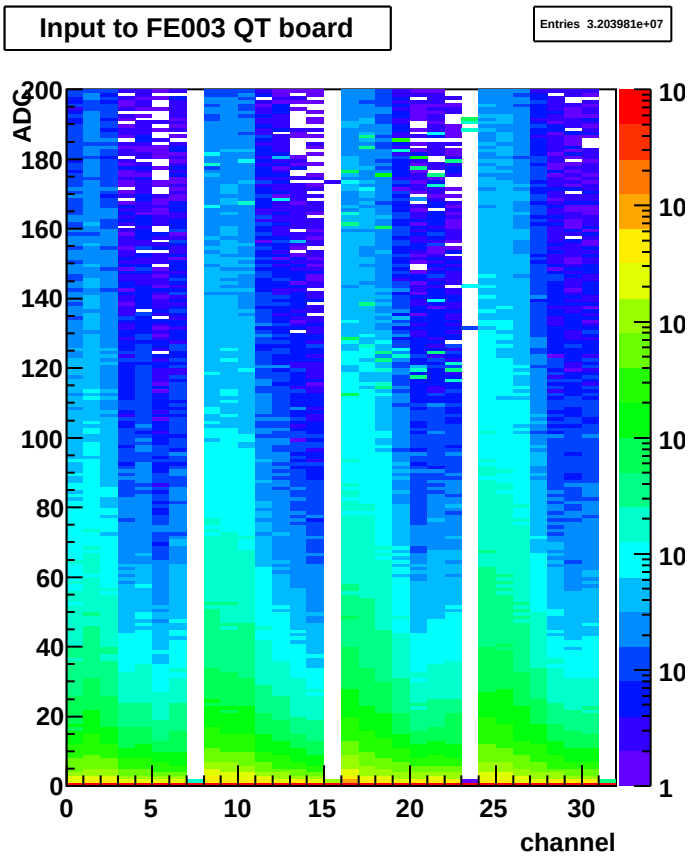

Input to FPD L3 DSM
Entries 1.60199e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

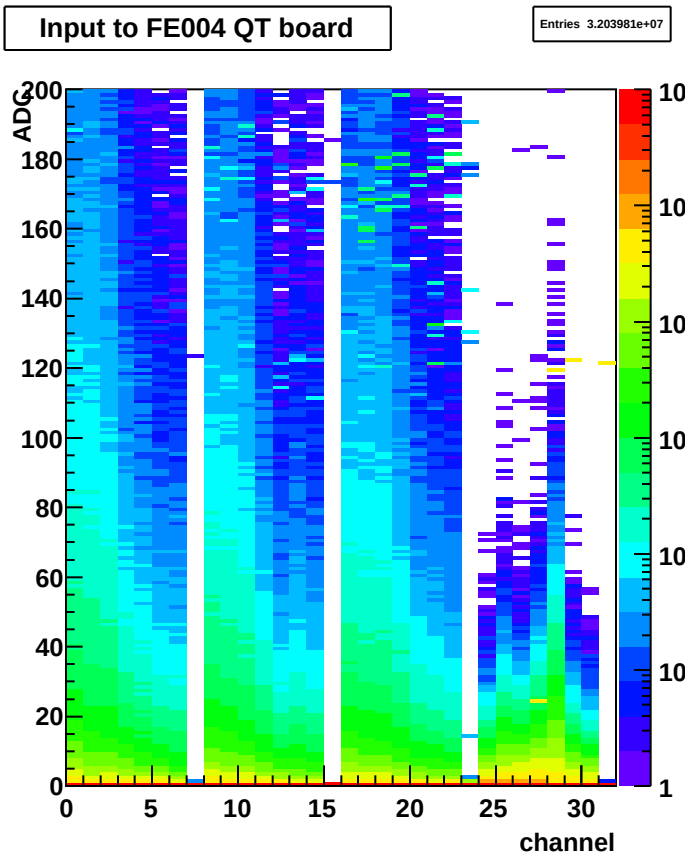

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

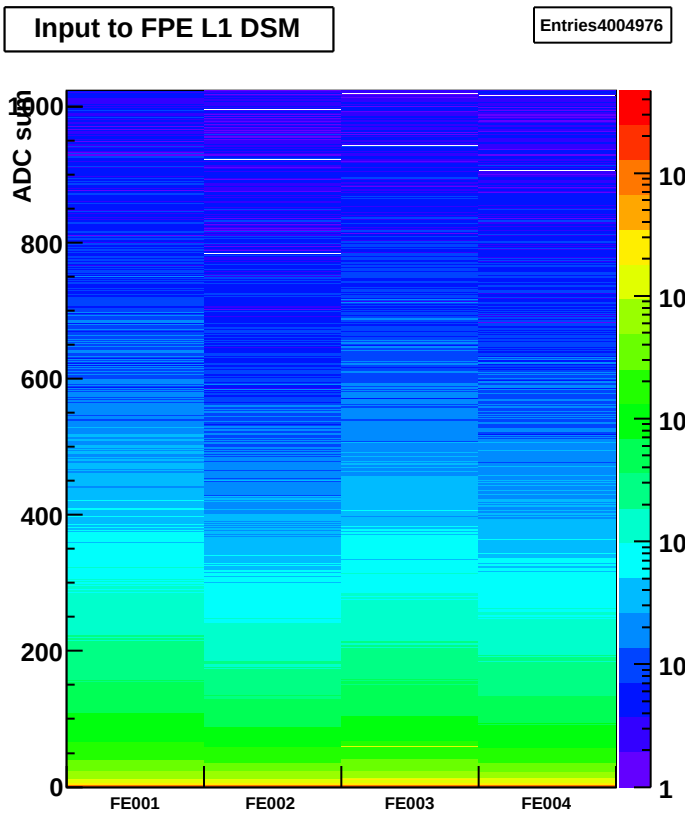

Input to FPD L2 DSM
Entries 4001664
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

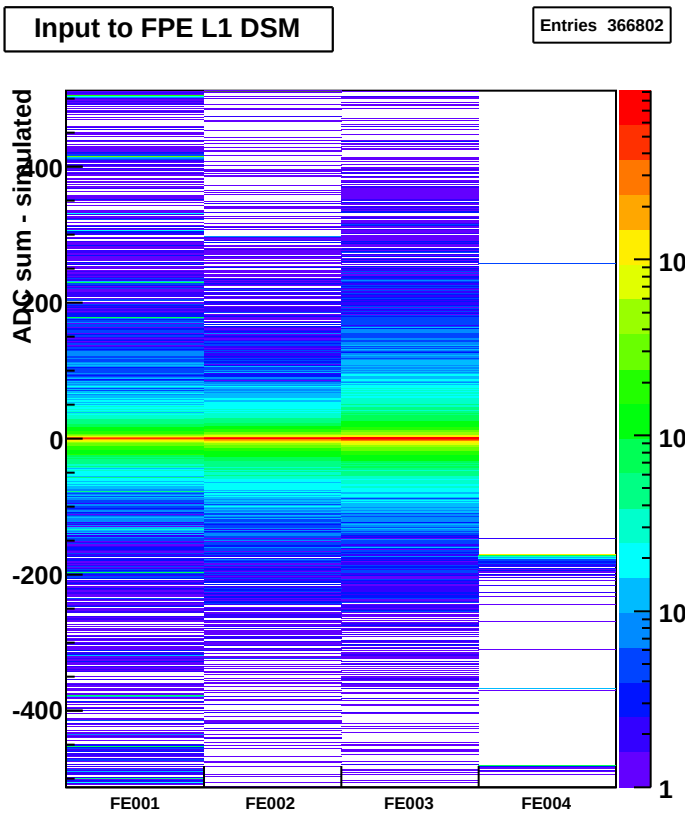

Input to FE001 QT board
Entries 3.203981e+07
ADC
channel

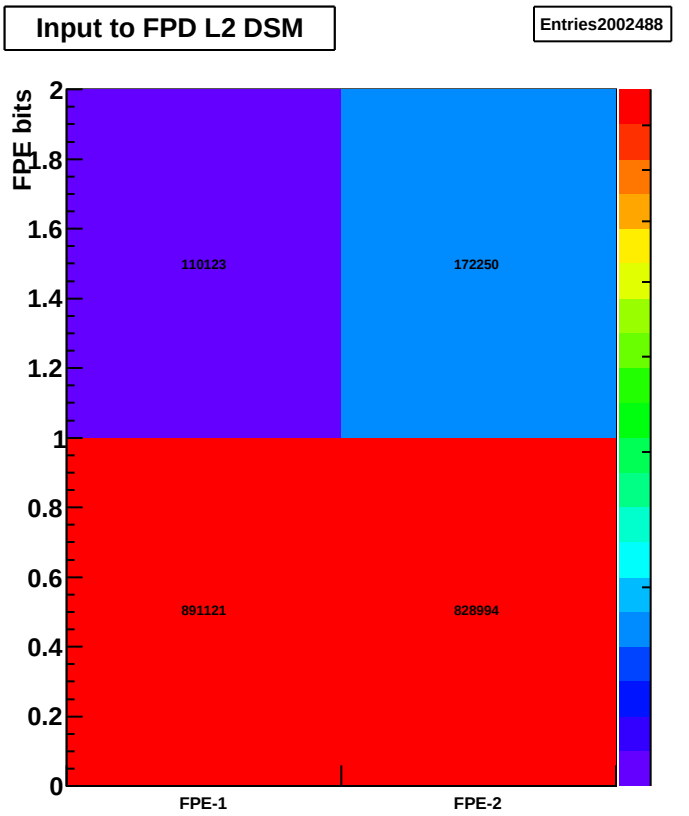

Input to FE002 QT board
Entries 3.203981e+07
ADC
channel

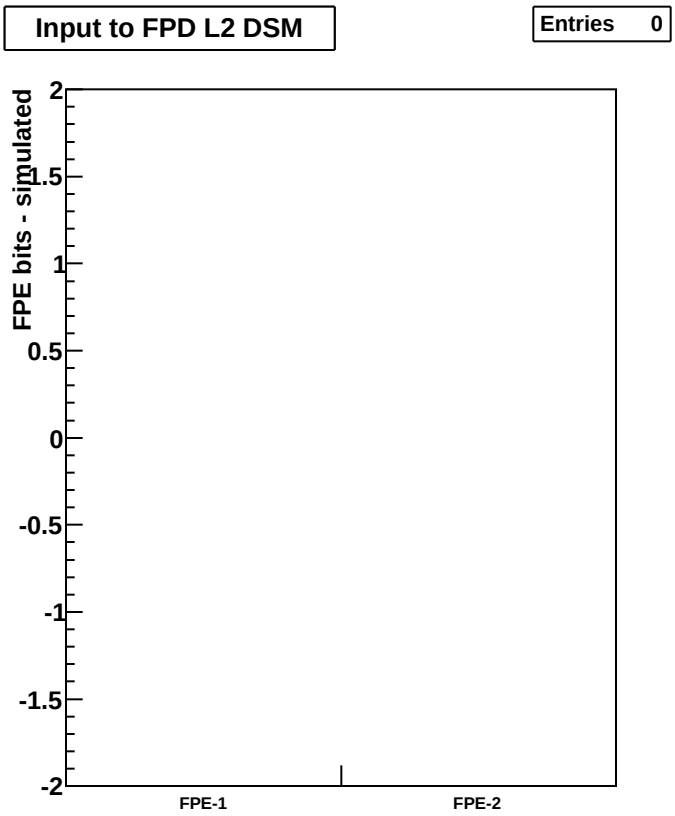

Input to FE003 QT board
Entries 3.203981e+07
ADC
channel


Input to FE004 QT board
Entries 3.203981e+07
ADC
channel


Input to FPE L1 DSM
Entries4004976
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 366802
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPD L2 DSM
Entries2002488
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
110123
172250
891121
828994
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2