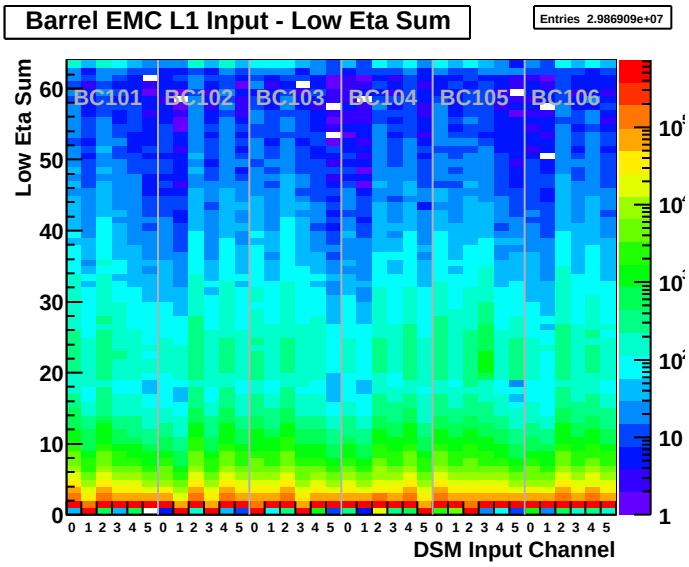
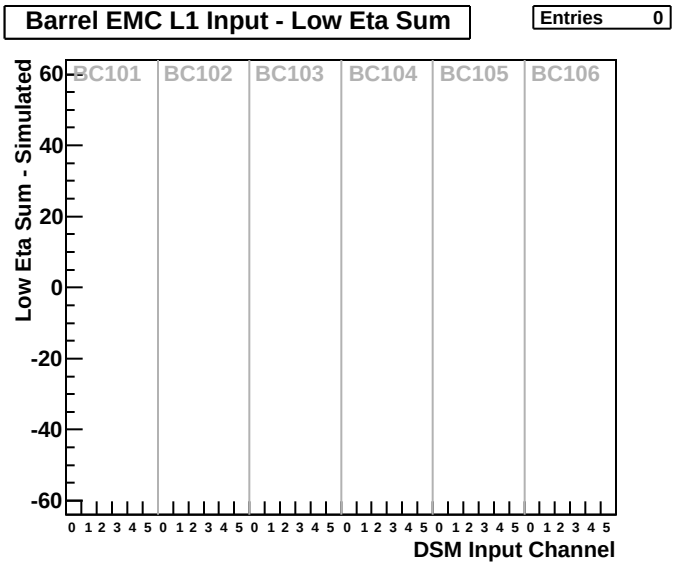
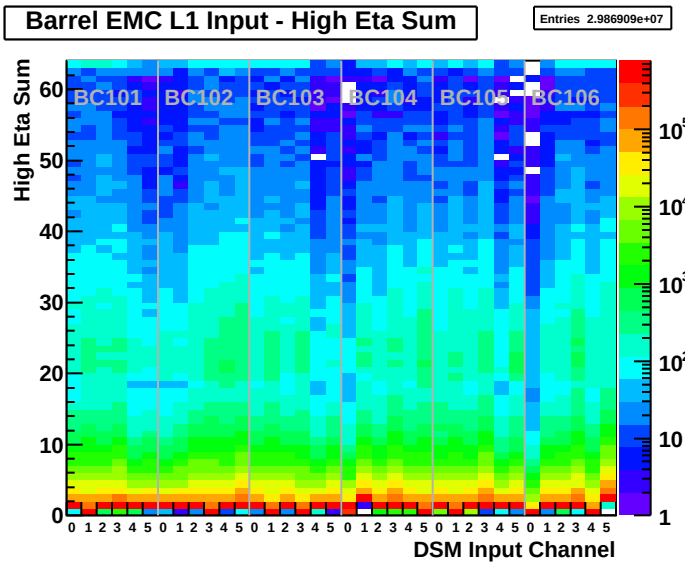
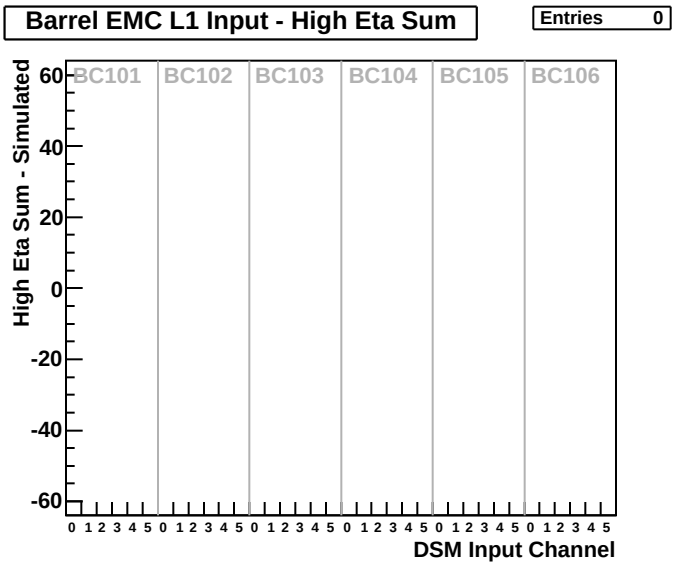
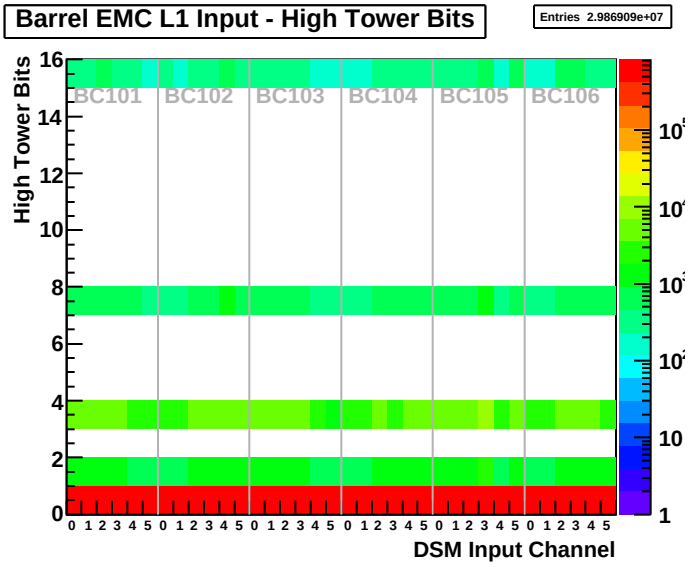
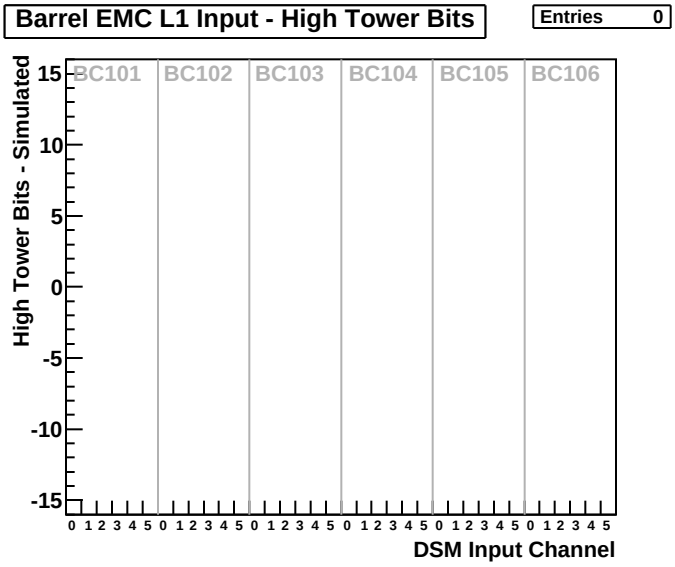

Barrel EMC L1 Input - Low Eta Sum
Entries 2.986909e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

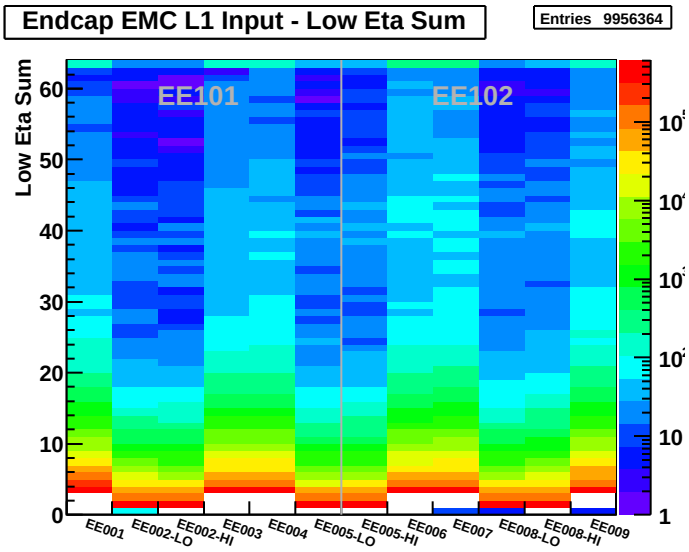

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

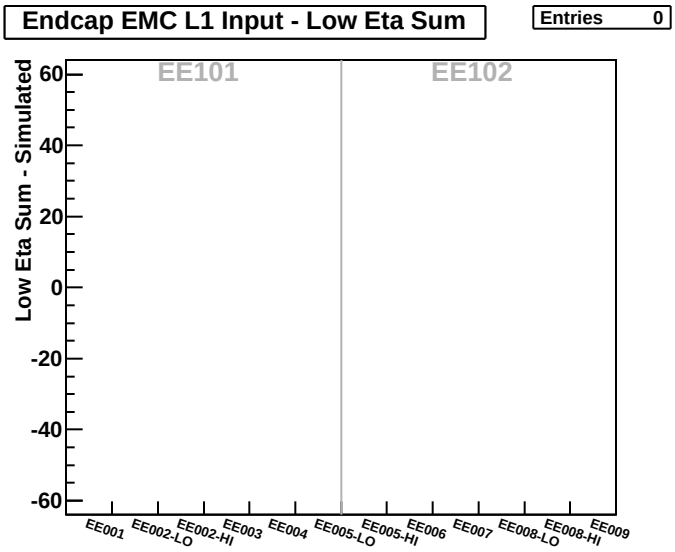

Barrel EMC L1 Input - High Eta Sum
Entries 2.986909e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

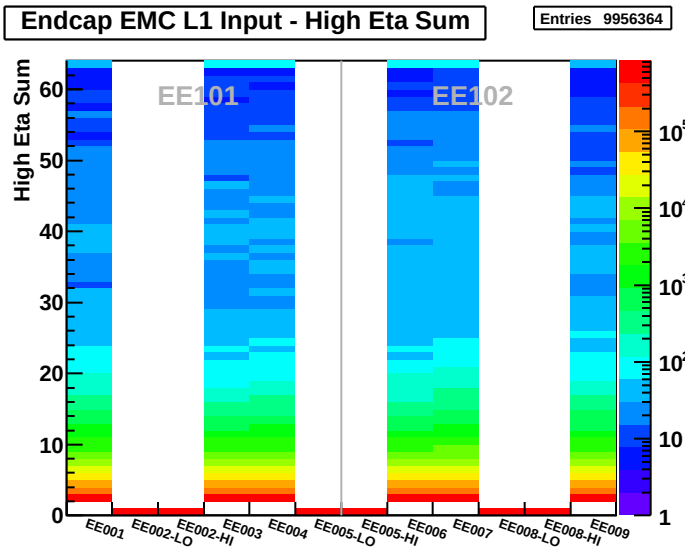

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

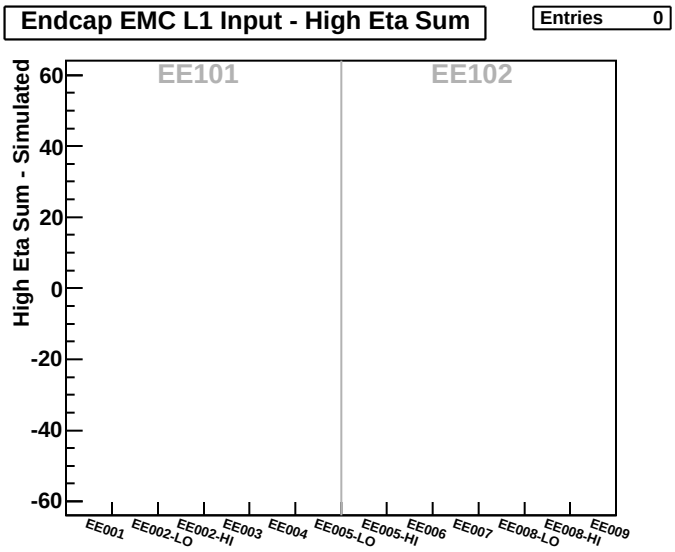

Barrel EMC L1 Input - High Tower Bits
Entries 2.986909e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

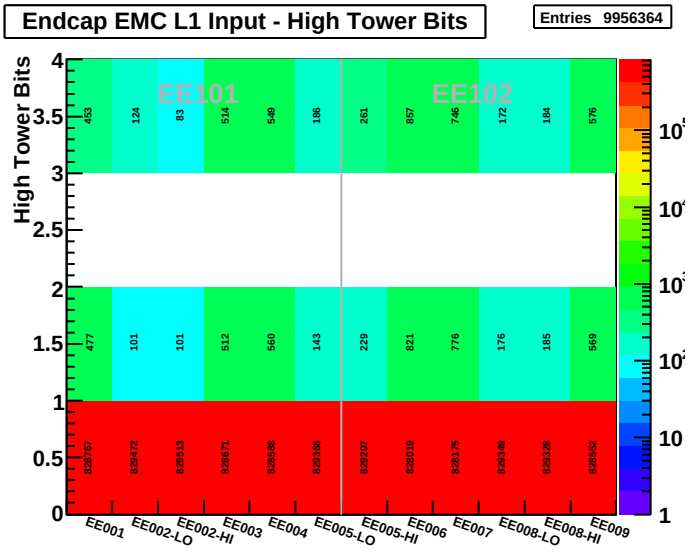

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

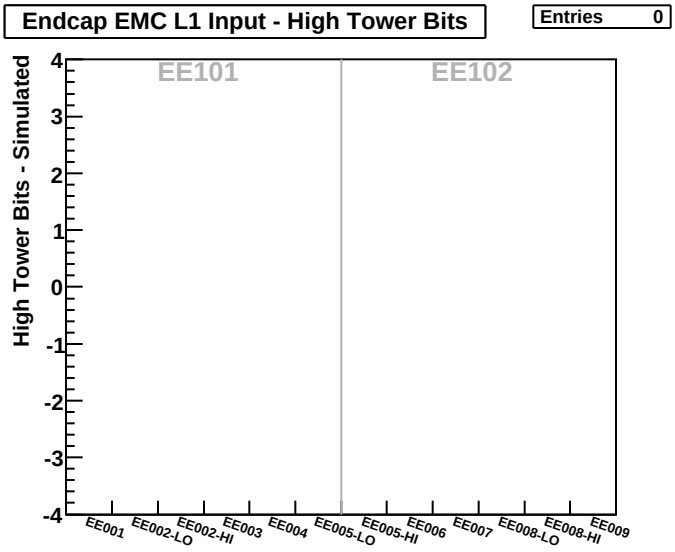

Endcap EMC L1 Input - Low Eta Sum
Entries 9956364
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

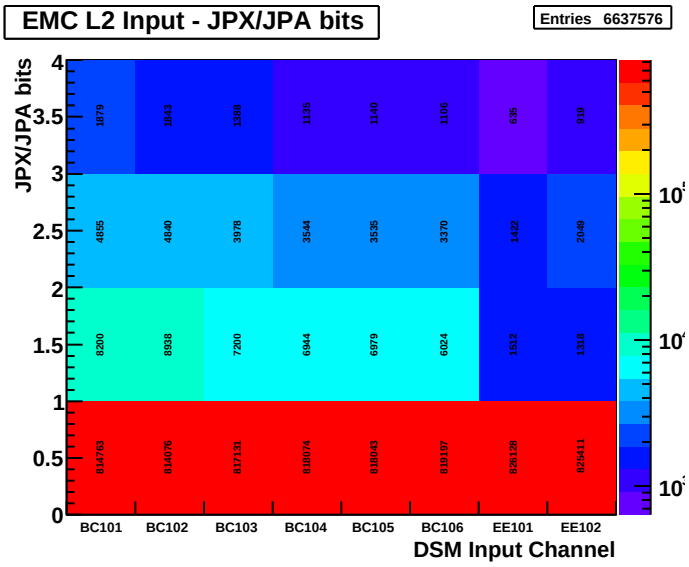

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

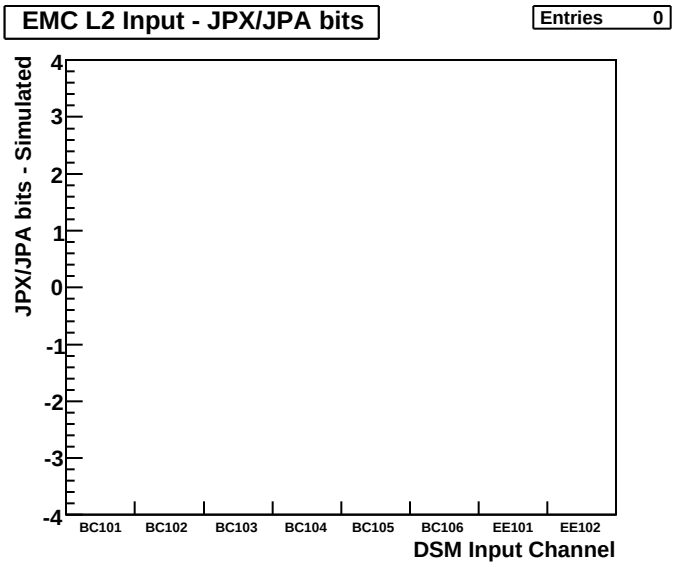

Endcap EMC L1 Input - High Eta Sum
Entries 9956364
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

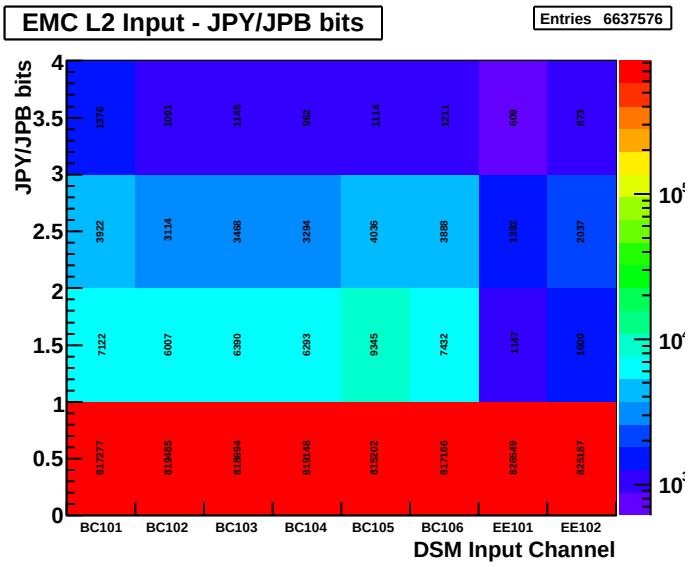

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

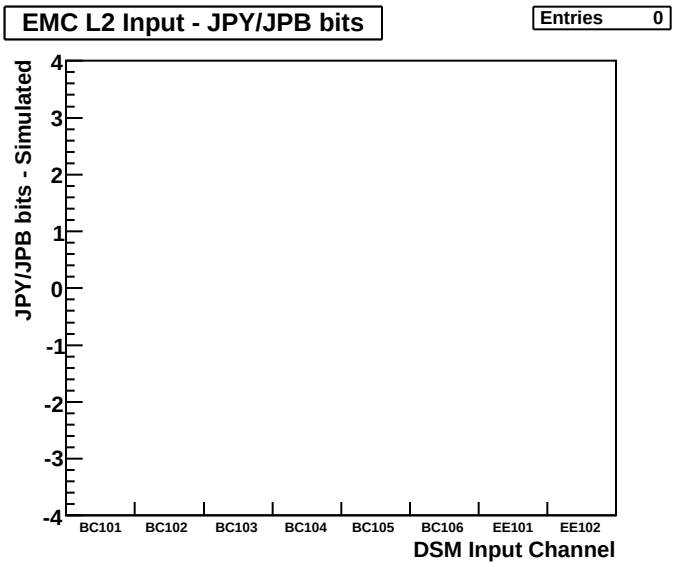

Endcap EMC L1 Input - High Tower Bits
Entries 9956364
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

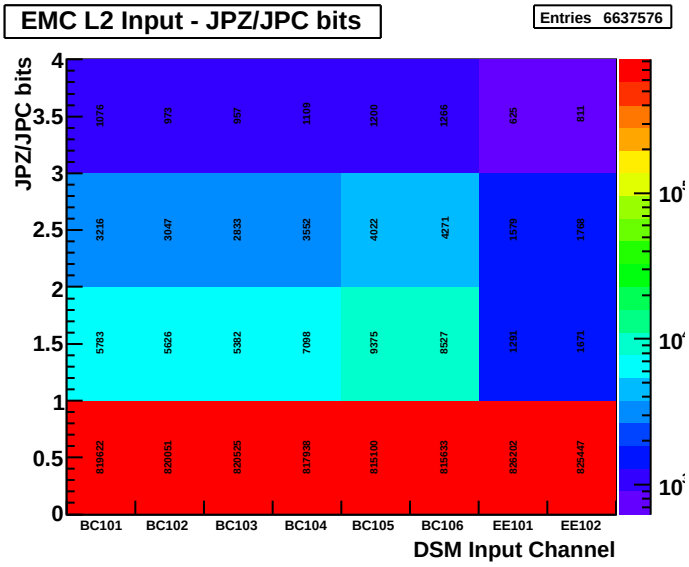

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

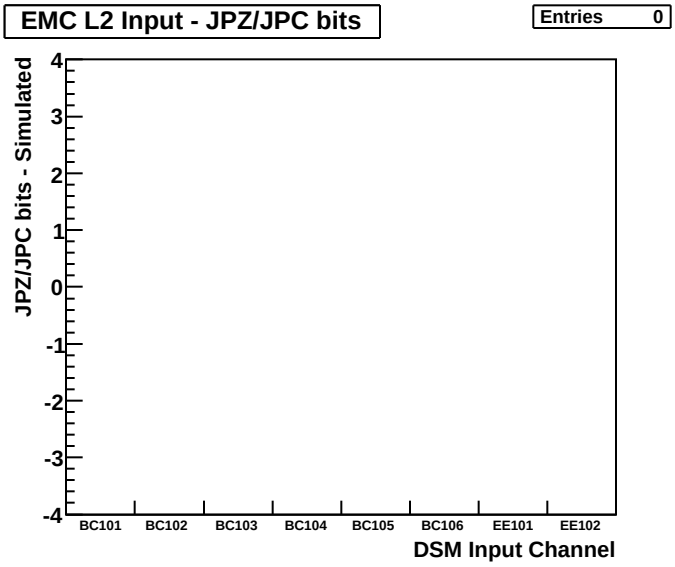

EMC L2 Input - JPX/JPA bits
Entries 6637576
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1879 1843 1288 1135 1140 1106 635 919
4855 4840 3978 3544 3535 3370 1422 2049
8200 8938 7200 6944 6979 6024 1512 1318
814763 814076 817121 818074 818043 819197 826128 825411

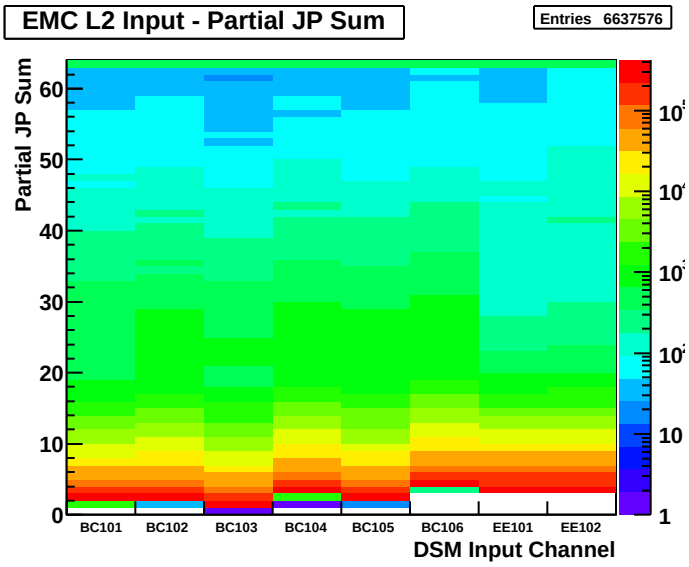

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

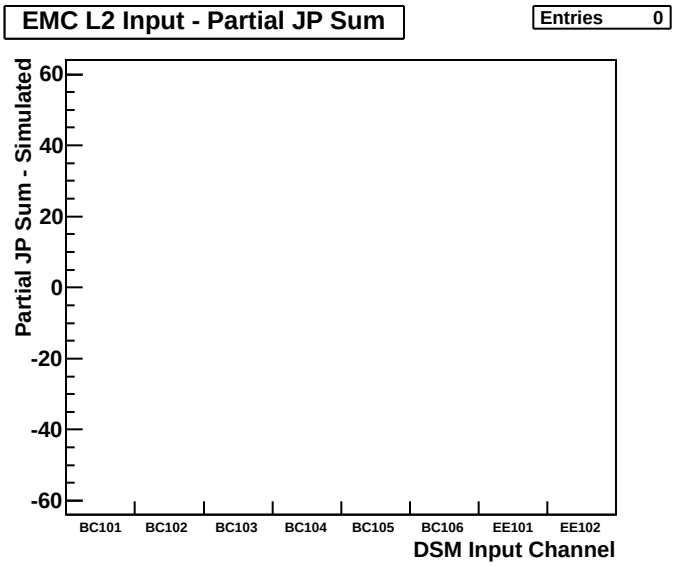

EMC L2 Input - JPY/JPB bits
Entries 6637576
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1376 1091 1145 982 1114 1211 609 873
3922 3114 3468 3294 4036 3888 1392 2037
7122 6007 6390 6293 9345 7432 1147 1600
817277 819485 818694 819148 815202 817166 826549 825187

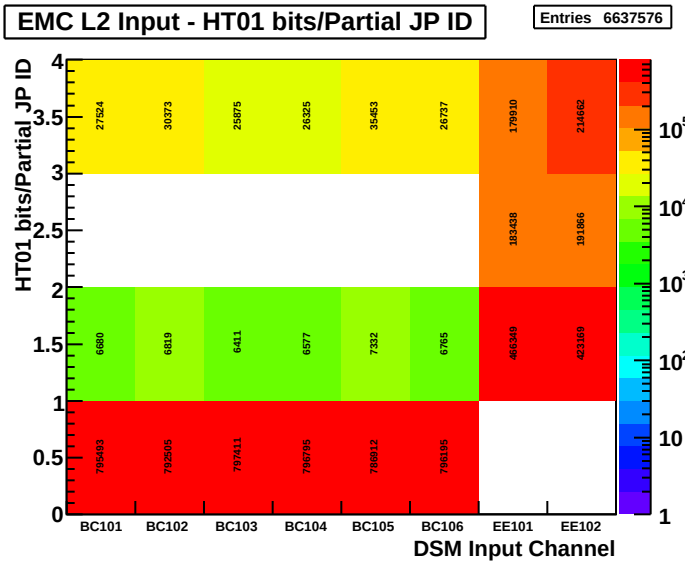

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

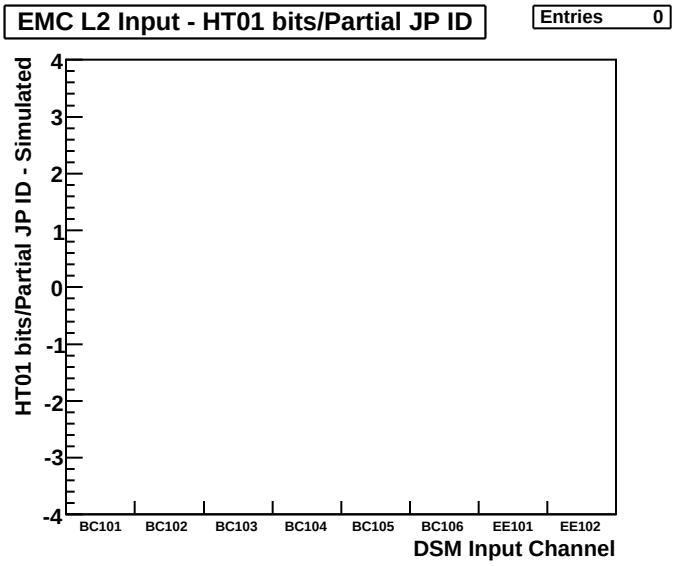

EMC L2 Input - JPZ/JPC bits
Entries 6637576
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1076 973 957 1109 1200 1266 625 811
3216 3047 2833 3552 4022 4271 1579 1768
5783 5626 5382 7098 9375 8527 1291 1671
819522 820051 820325 817938 815100 815533 826202 825447

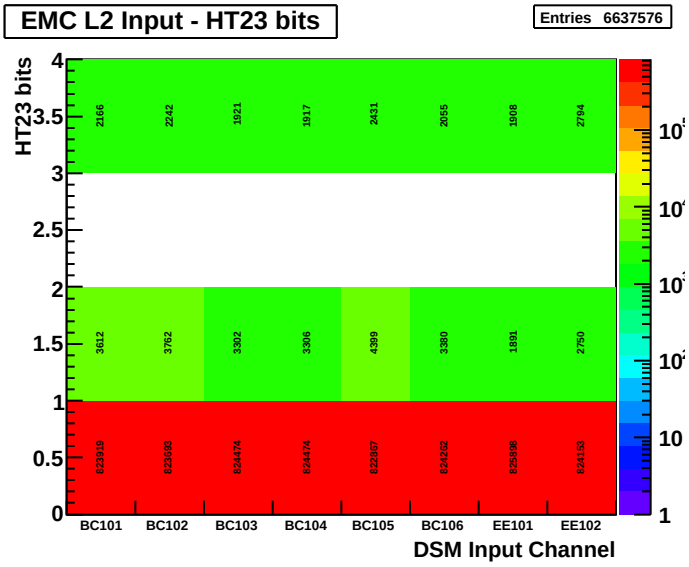

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

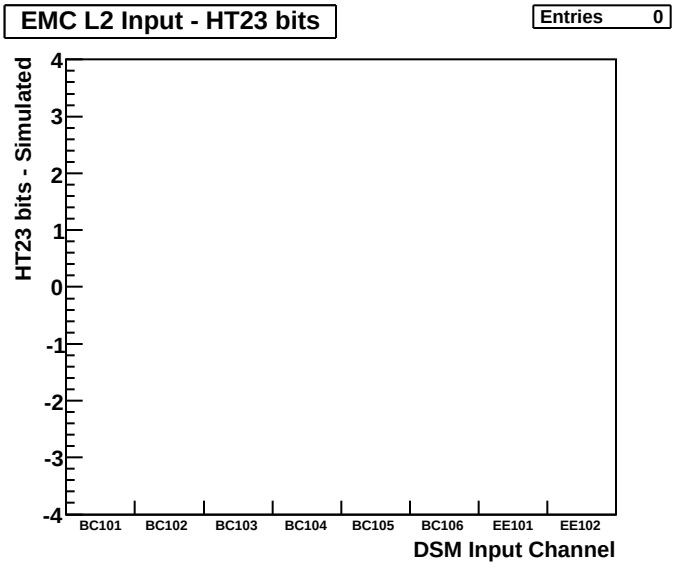

EMC L2 Input - Partial JP Sum
Entries 6637576
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

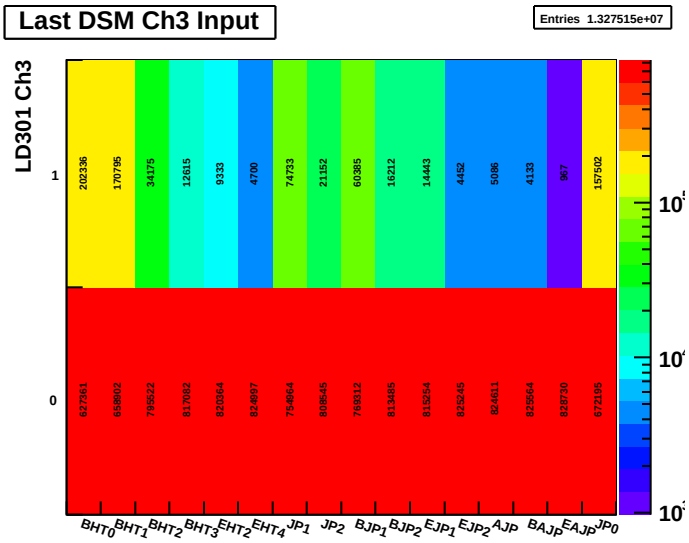

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

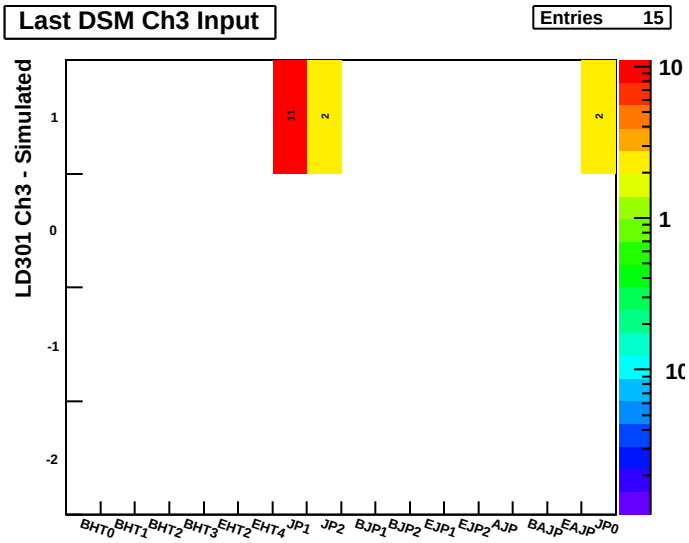

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6637576
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

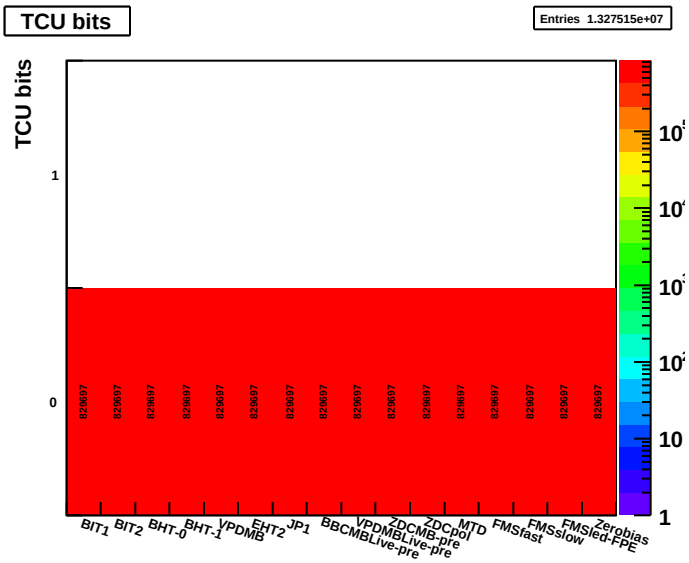

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

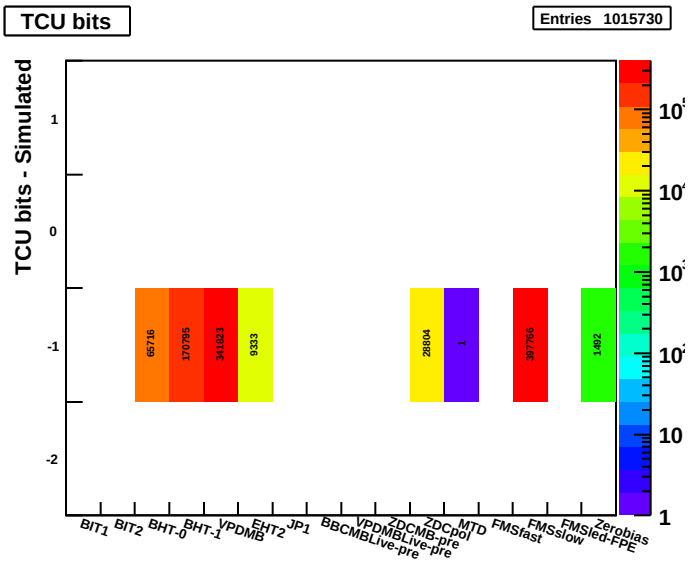

EMC L2 Input - HT23 bits
Entries 6637576
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

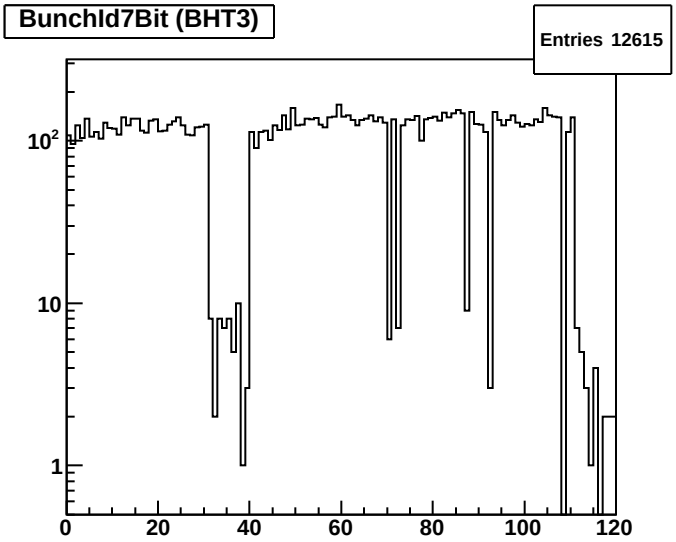

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

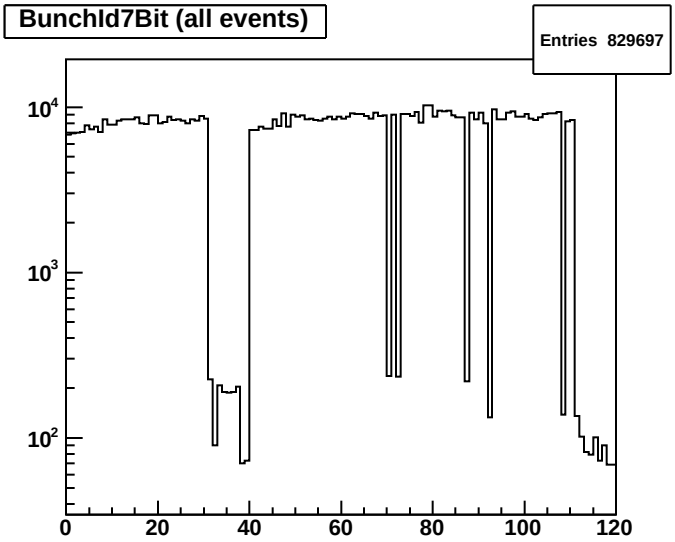

Last DSM Ch3 Input
Entries 1.327515e+07
LD301 Ch3
202336 170795 34175 12615 9333 4700 74733 21152 60385 16212 14443 4452 5086 4133 967 157502
627361 658902 795522 817082 820364 824997 754964 808545 768312 813485 815254 825245 824611 825564 828730 672195
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

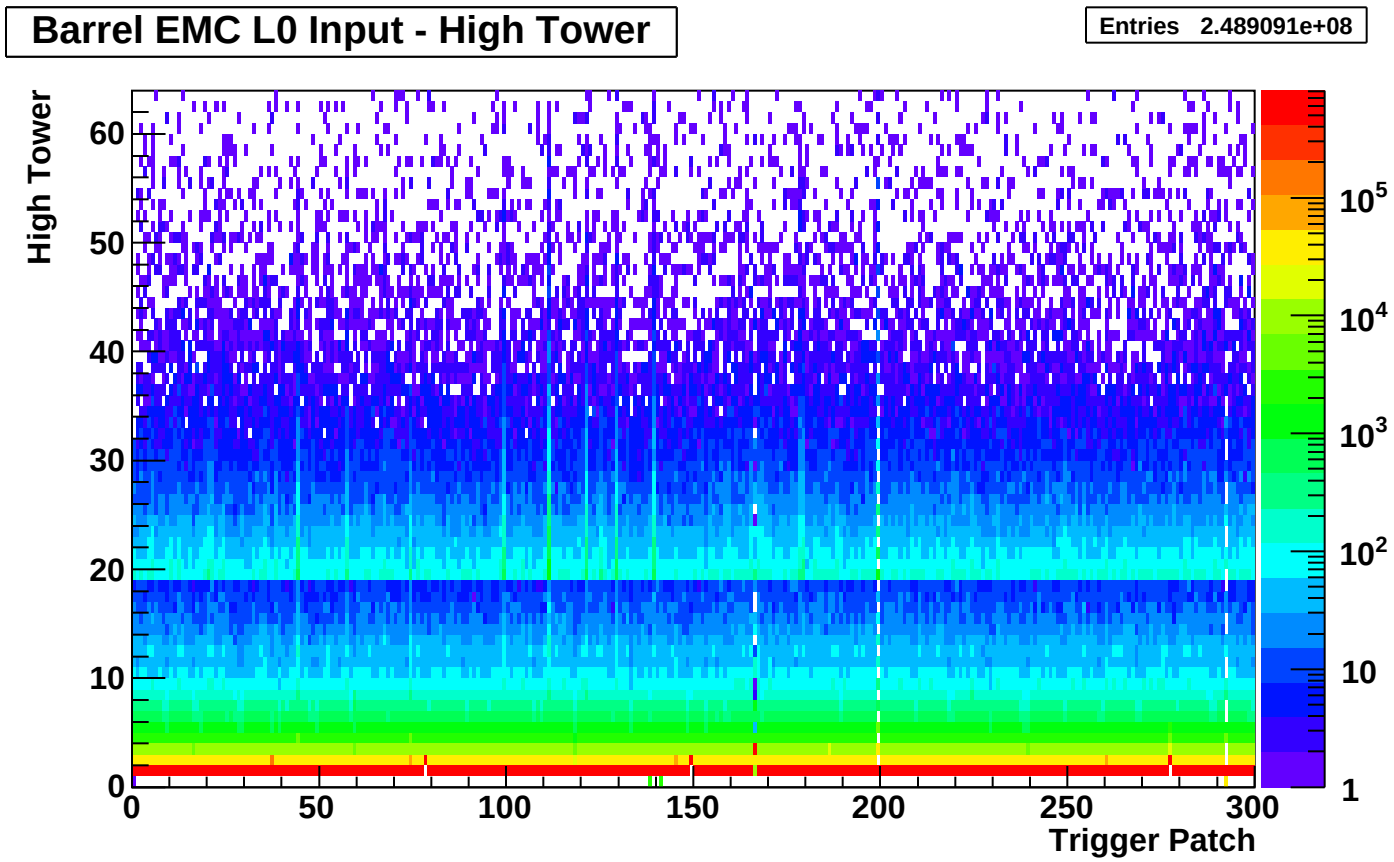

Last DSM Ch3 Input
Entries 15
LD301 Ch3 - Simulated
11 2 2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

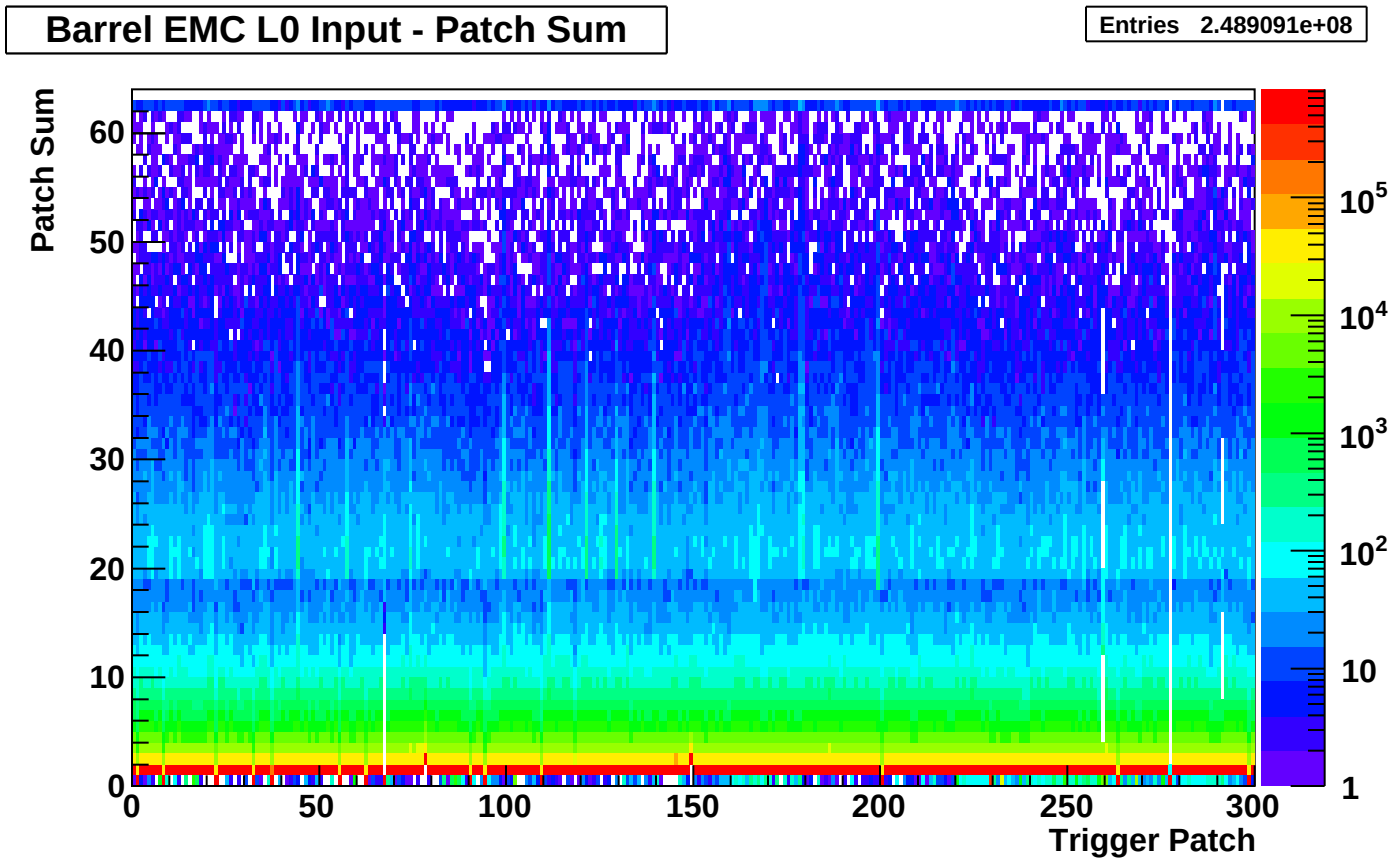

TCU bits
Entries 1.327515e+07
TCU bits
829697
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

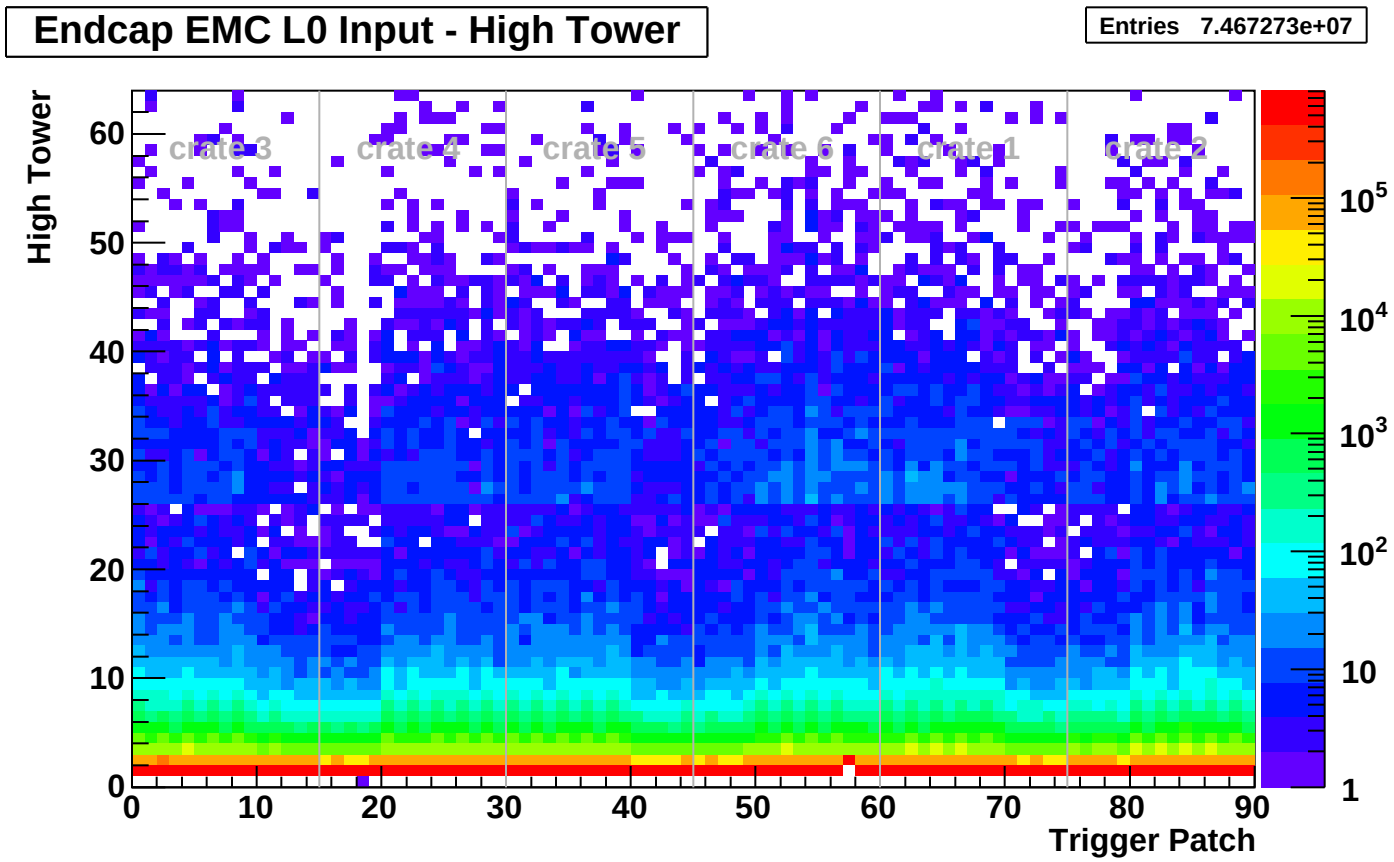

TCU bits
Entries 1015730
TCU bits - Simulated
65716 170795 341823 9333 28804 1 397766 1492
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

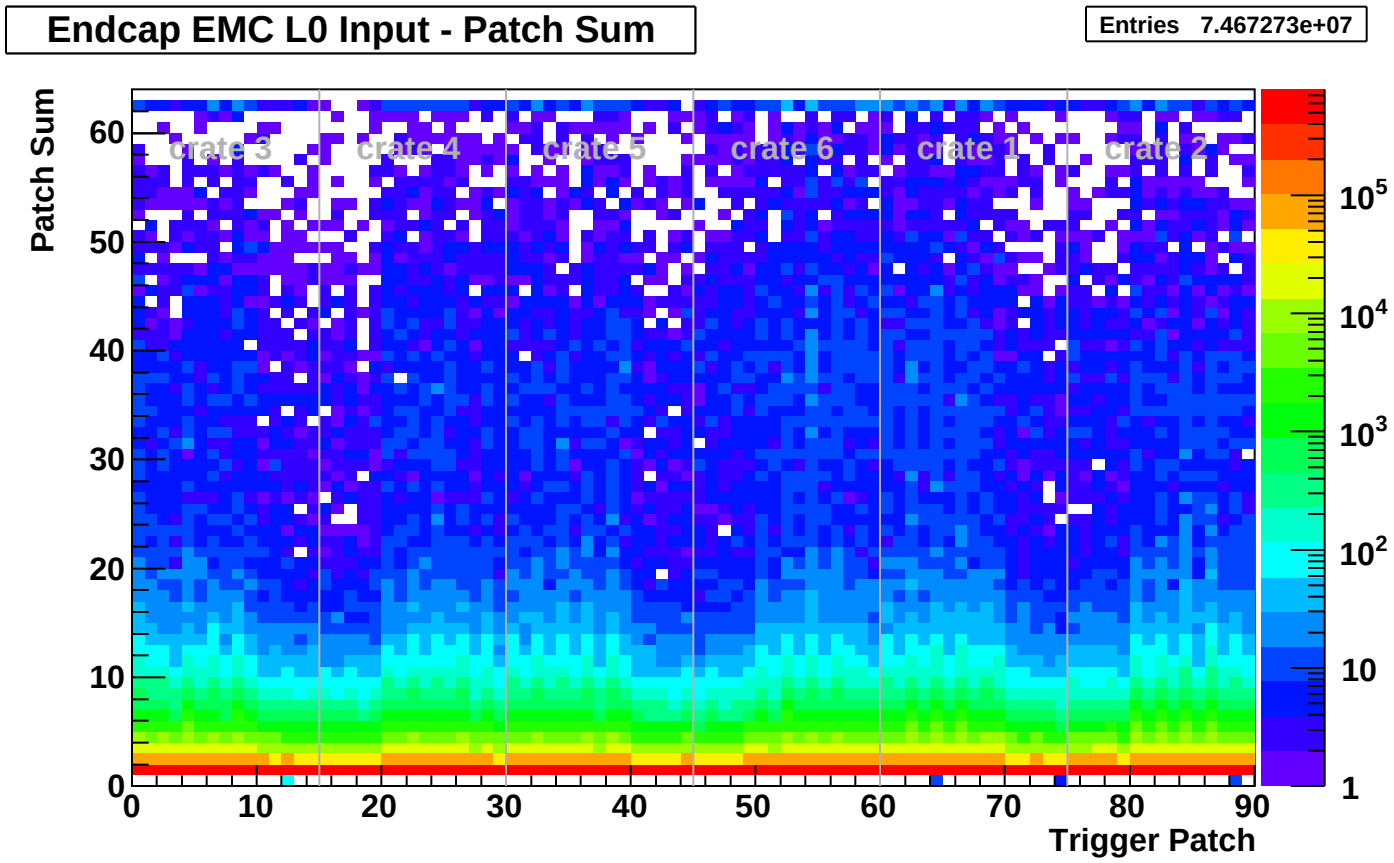

BunchId7Bit (BHT3)
Entries 12615

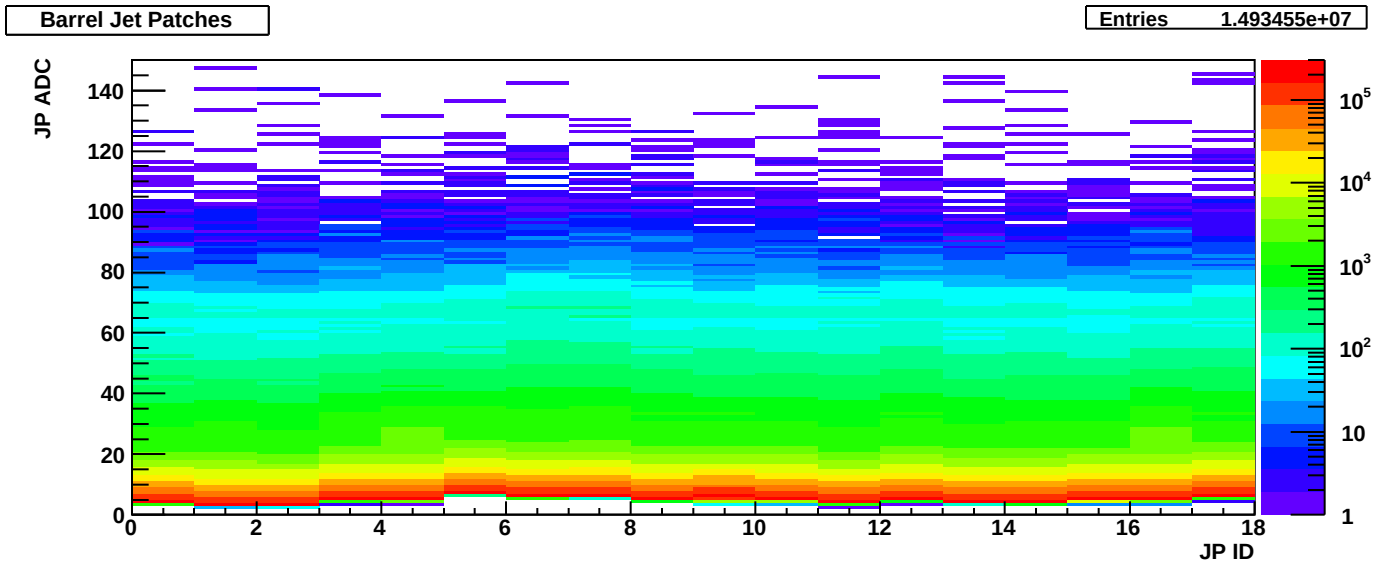

BunchId7Bit (all events)
Entries 829697

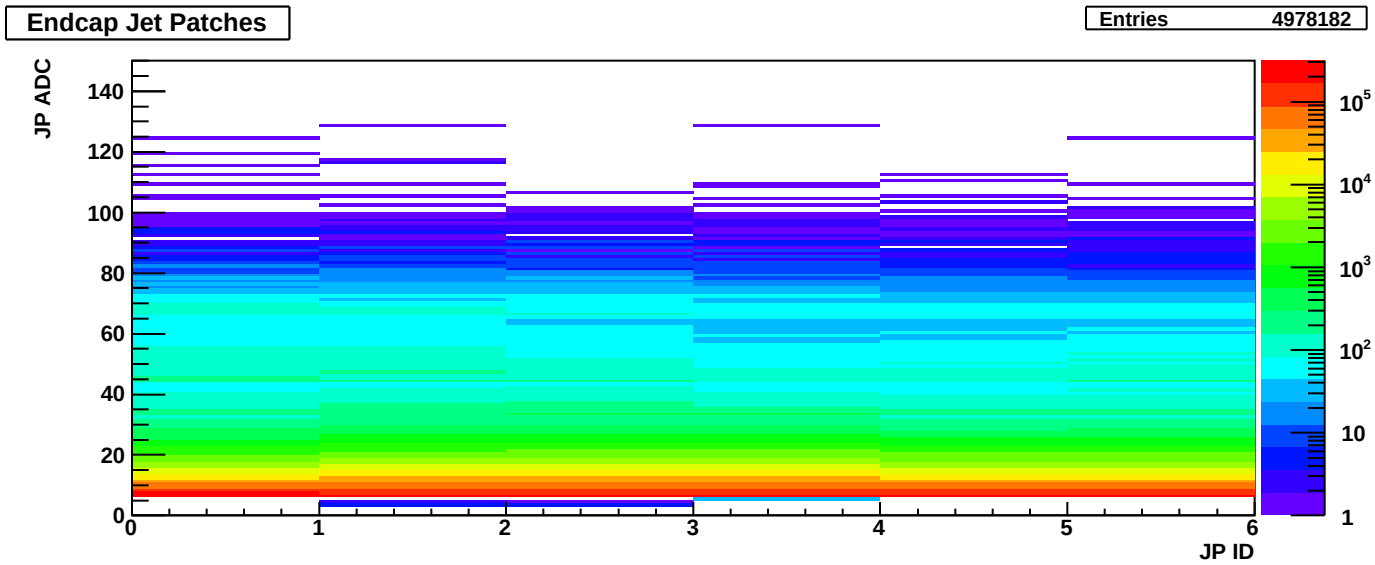

Barrel EMC L0 Input - High Tower
Entries 2.489091e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

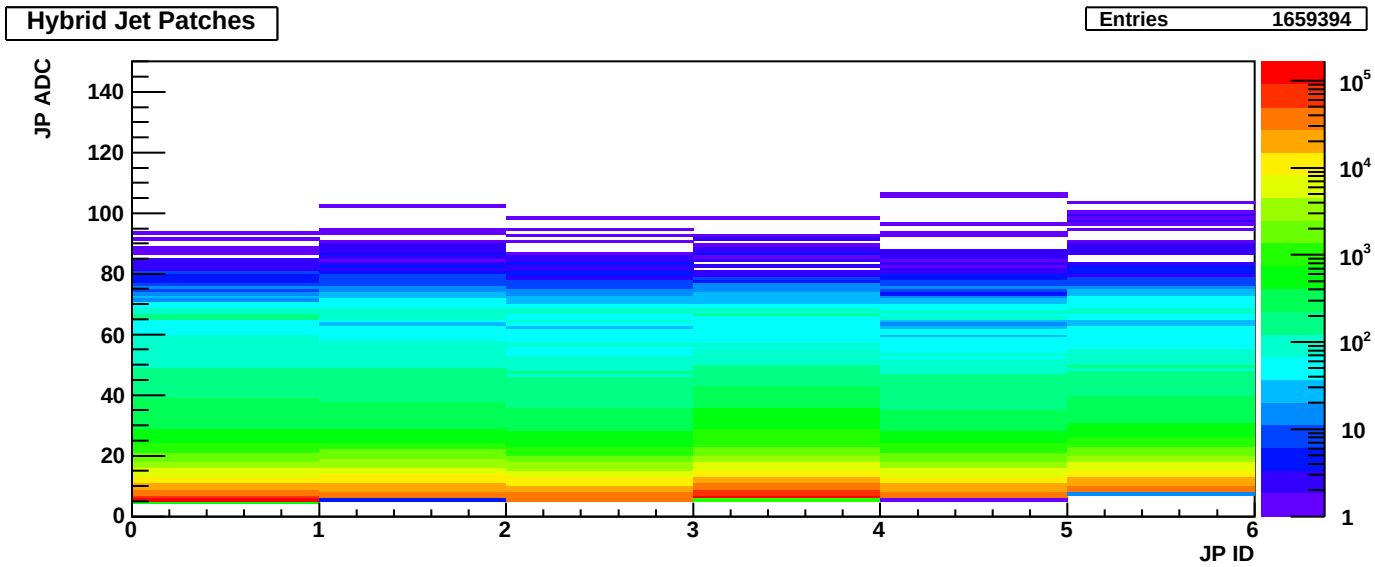

Barrel EMC L0 Input - Patch Sum
Entries 2.489091e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

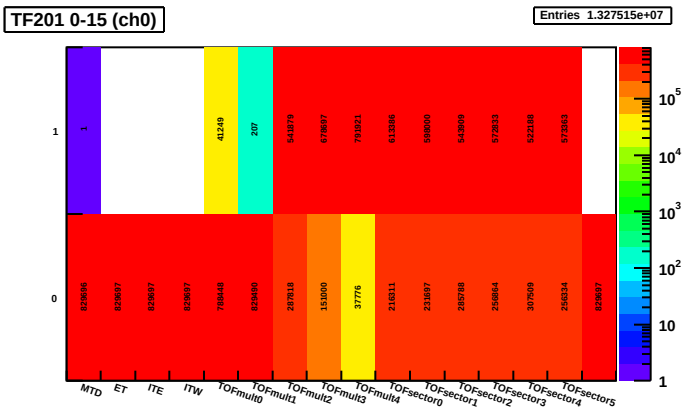

Endcap EMC L0 Input - High Tower
Entries 7.467273e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

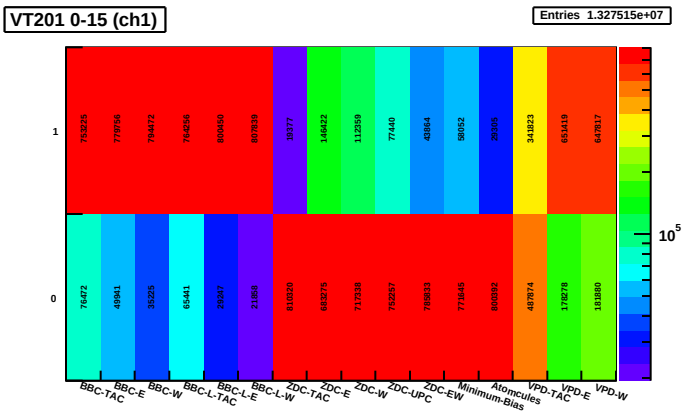

Endcap EMC L0 Input - Patch Sum
Entries 7.467273e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

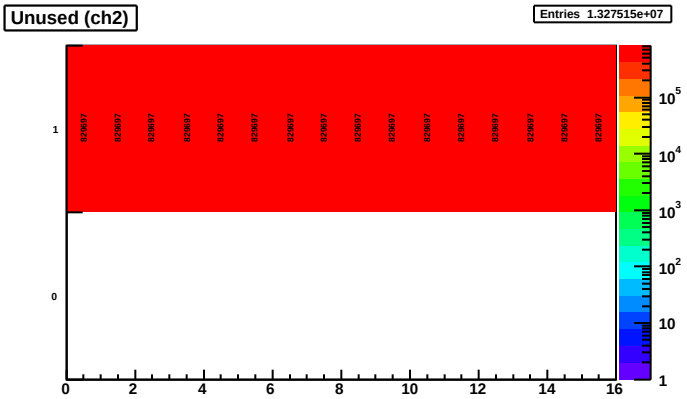

Barrel Jet Patches
Entries 1.493455e+07
JP ADC
JP ID

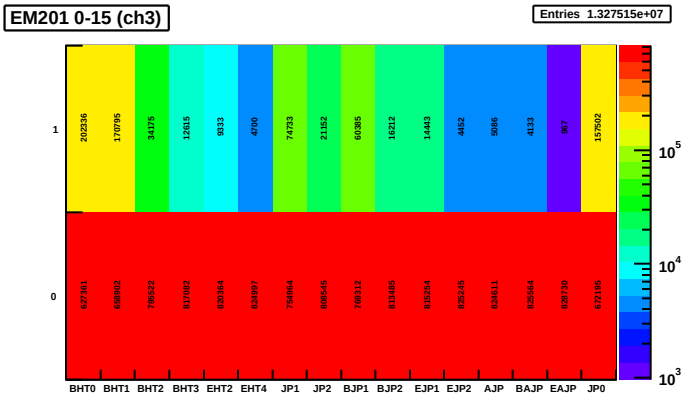

Endcap Jet Patches
Entries 4978182
JP ADC
JP ID

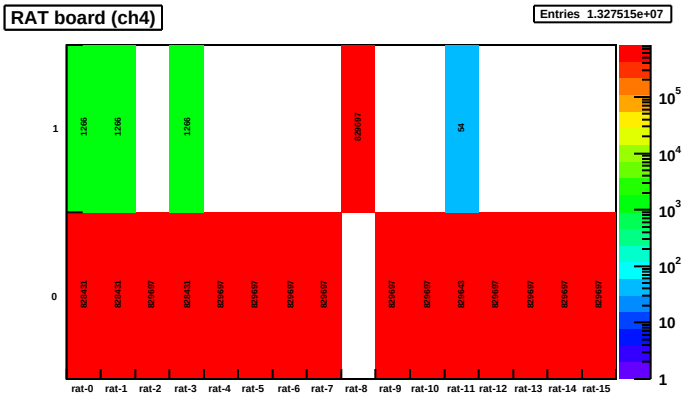

Hybrid Jet Patches
Entries 1659394
JP ADC
JP ID

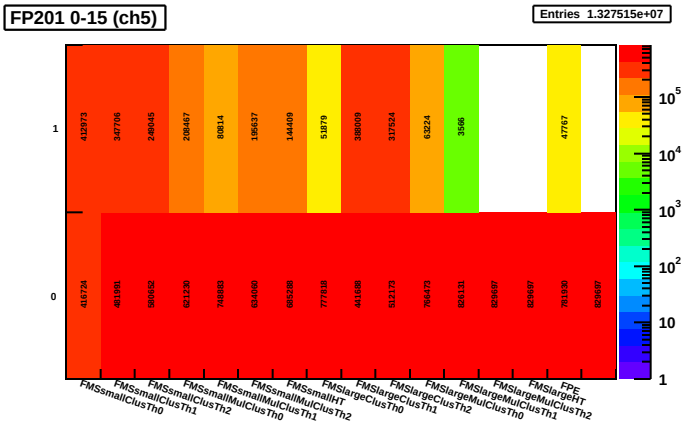

TF201 0-15 (ch0)
Entries 1.327515e+07
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

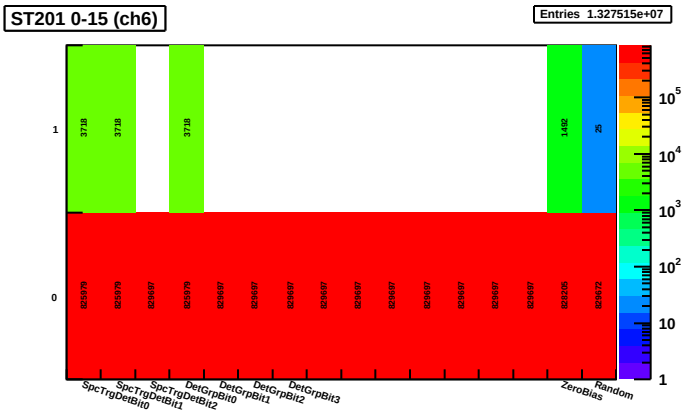

VT201 0-15 (ch1)
Entries 1.327515e+07
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

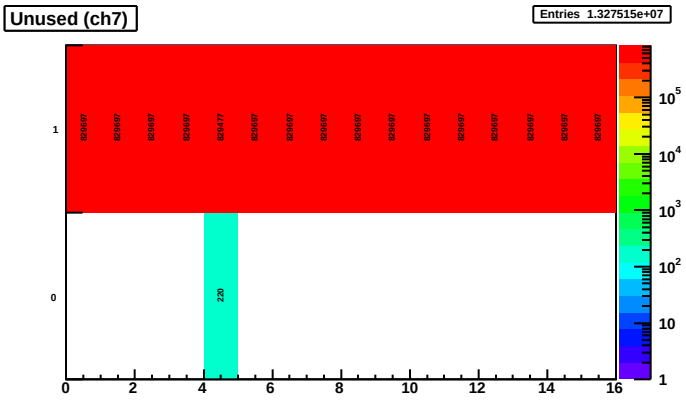

Unused (ch2)
Entries 1.327515e+07

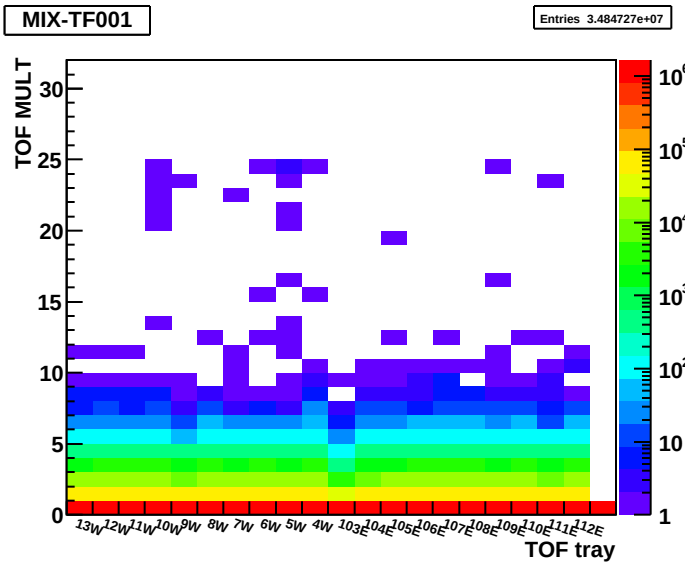

EM201 0-15 (ch3)
Entries 1.327515e+07
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

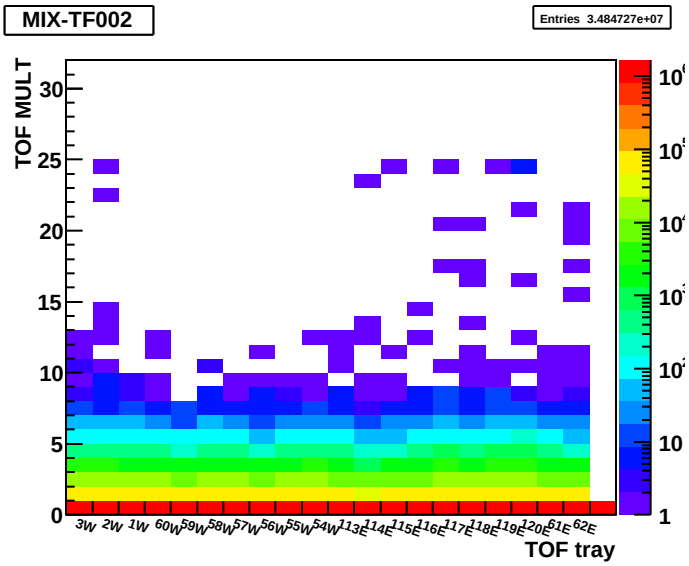

RAT board (ch4)
Entries 1.327515e+07
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

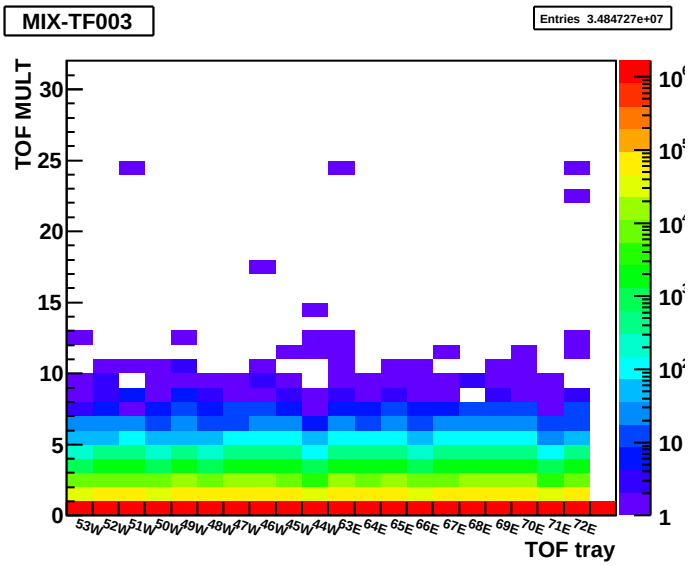

FP201 0-15 (ch5)
Entries 1.327515e+07
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

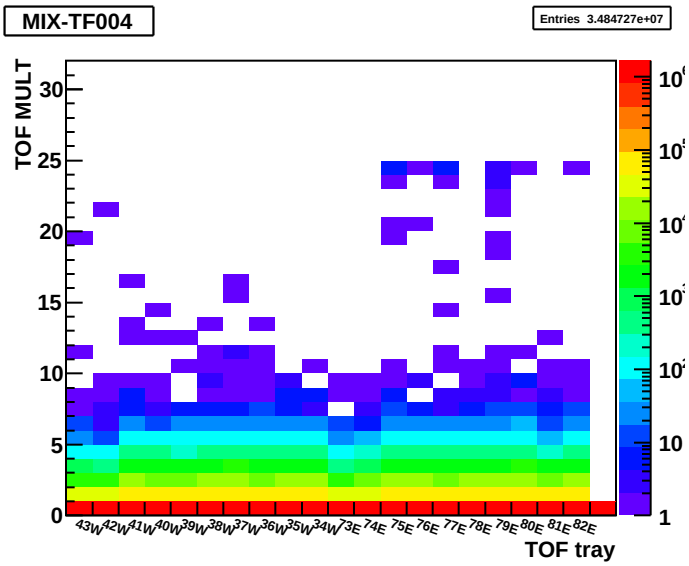

ST201 0-15 (ch6)
Entries 1.327515e+07
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

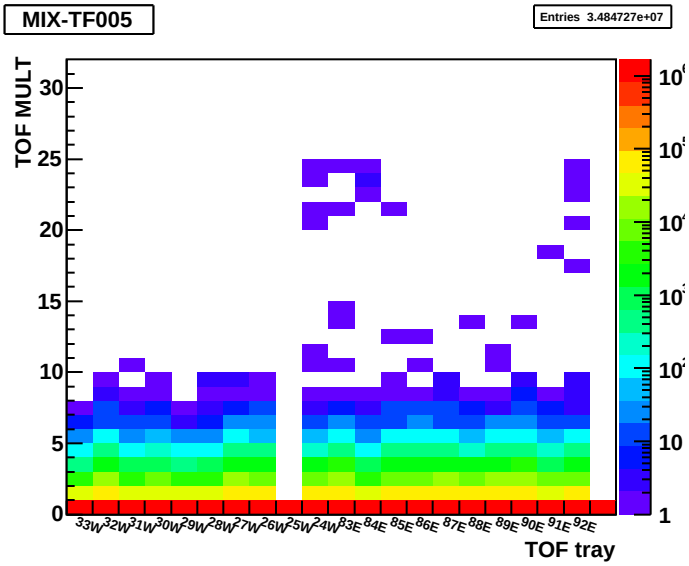

Unused (ch7)
Entries 1.327515e+07

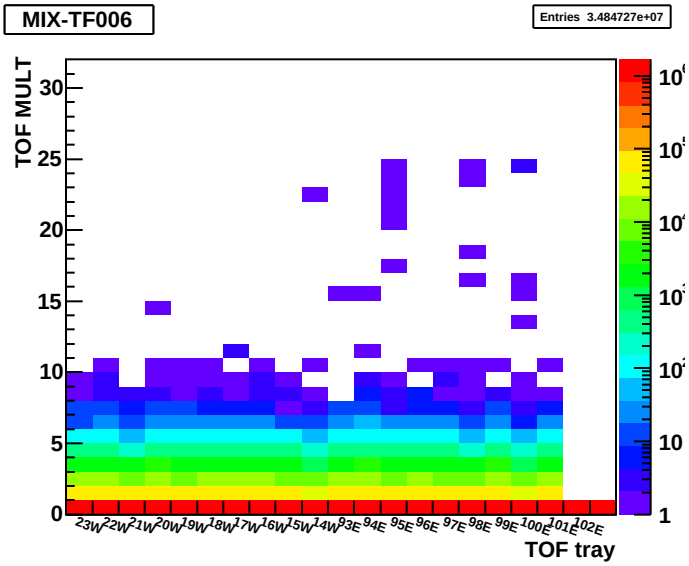

MIX-TF001
Entries 3.484727e+07
TOF MULT
TOF tray

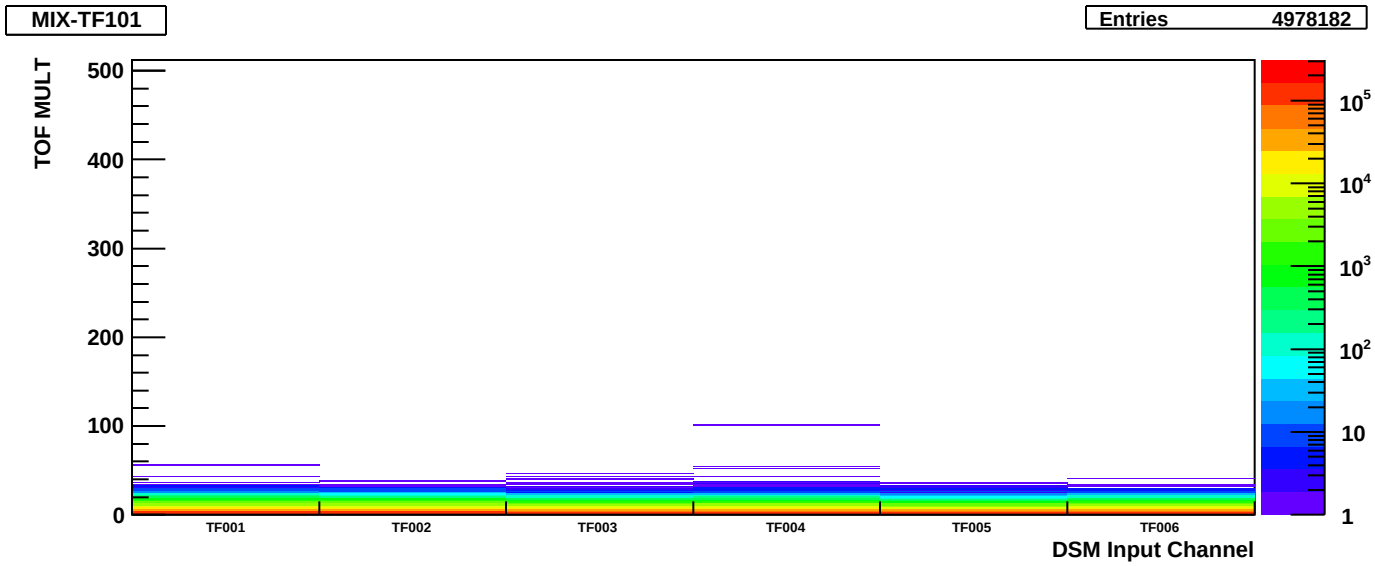

MIX-TF002
Entries 3.484727e+07
TOF MULT
TOF tray

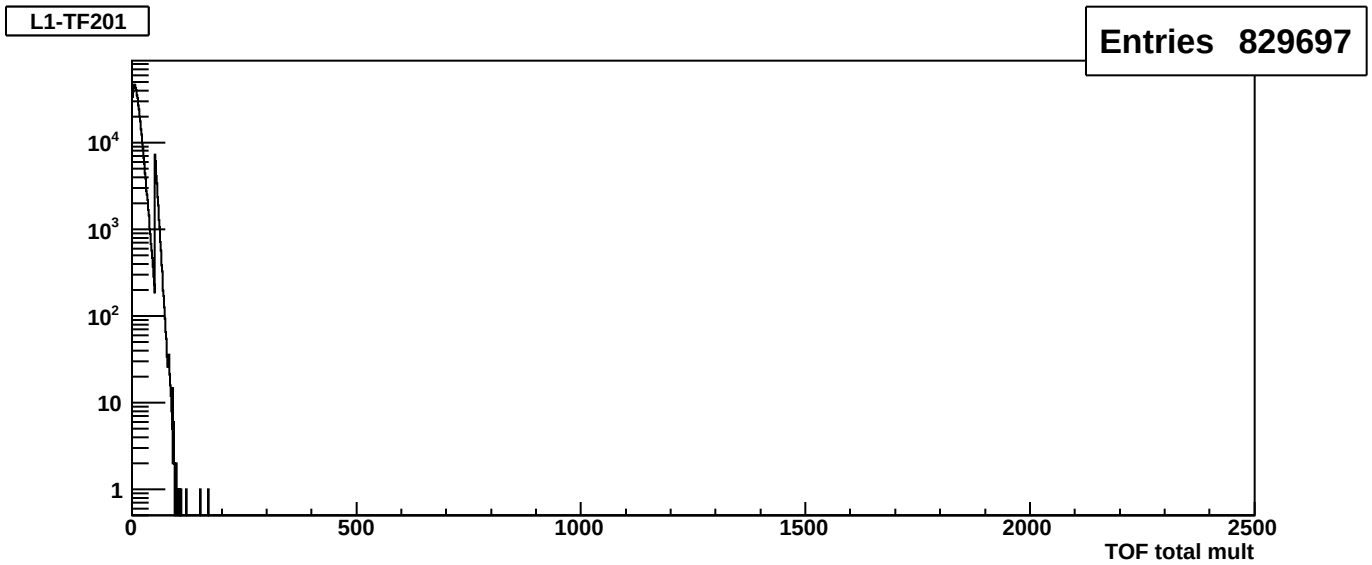

MIX-TF003
Entries 3.484727e+07
TOF MULT
TOF tray

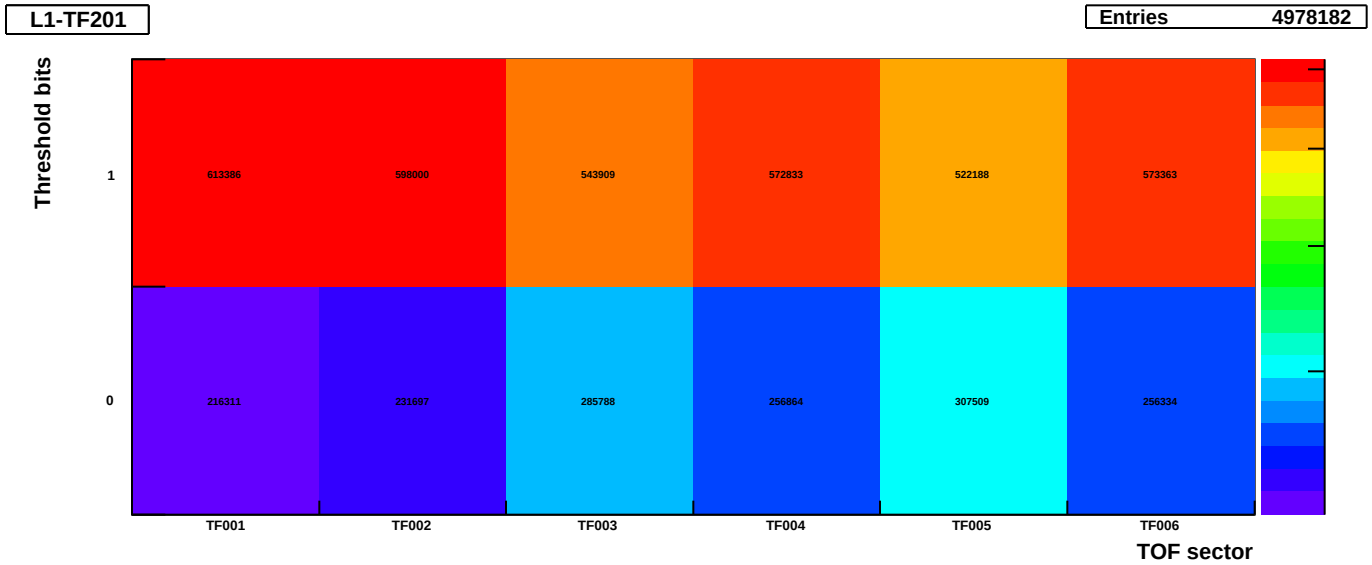

MIX-TF004
Entries 3.484727e+07
TOF MULT
TOF tray

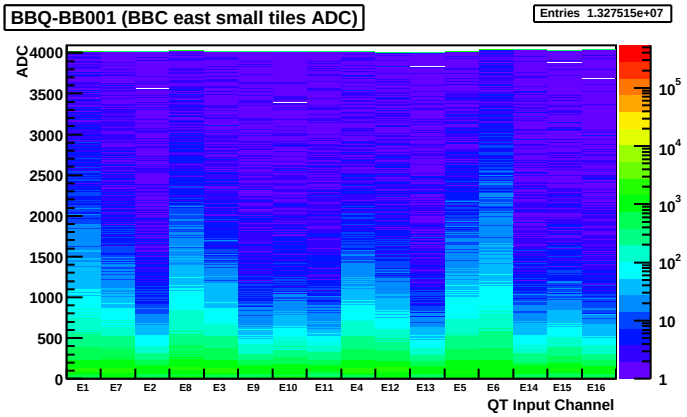

MIX-TF005
Entries 3.484727e+07
TOF MULT
TOF tray

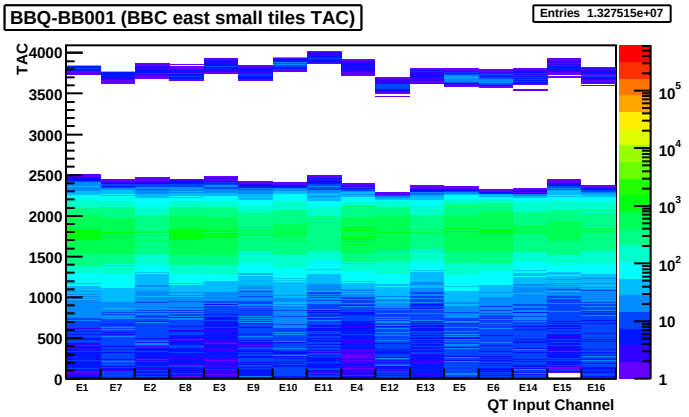

MIX-TF006
Entries 3.484727e+07
TOF MULT
TOF tray

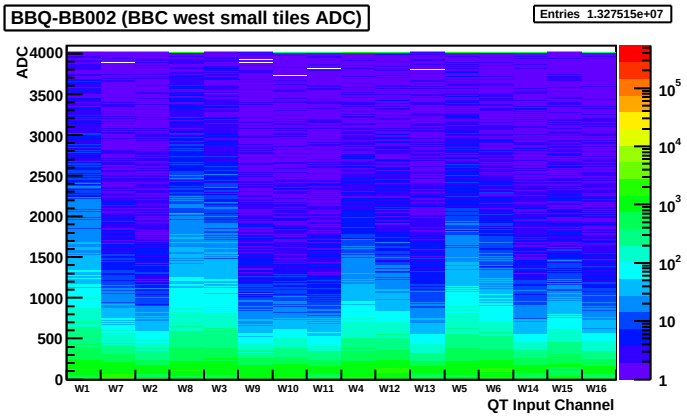

MIX-TF101
Entries 4978182
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

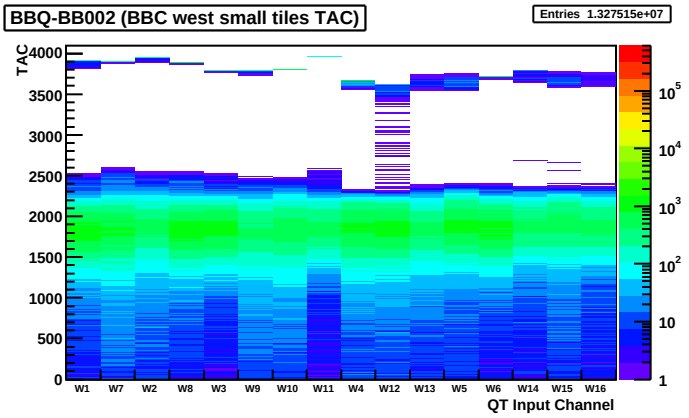

L1-TF201
Entries 829697
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

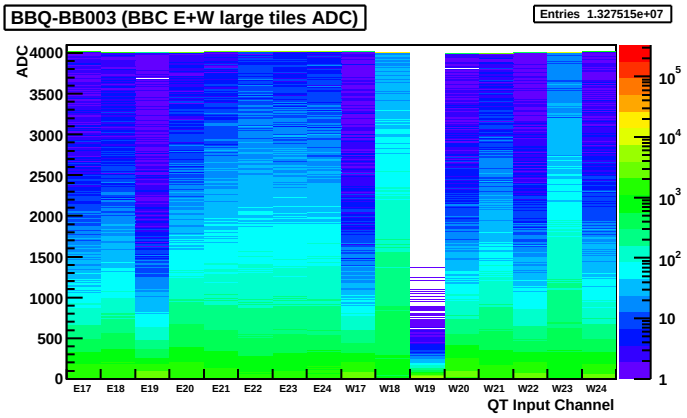

L1-TF201
Entries 4978182
Threshold bits
1
0
613386
598000
543909
572833
522188
573363
216311
231697
285788
256864
307509
256334
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

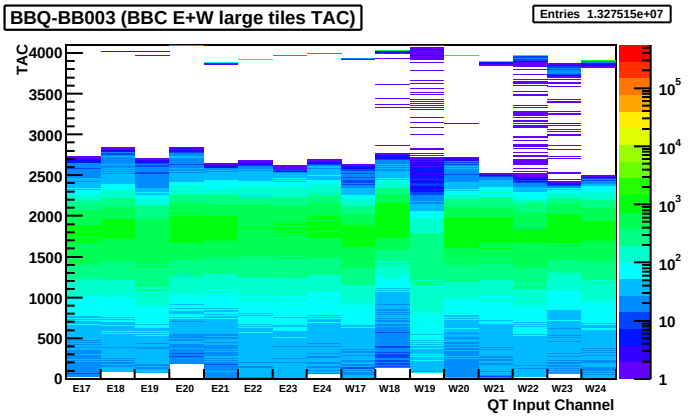

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.327515e+07
ADC
QT Input Channel

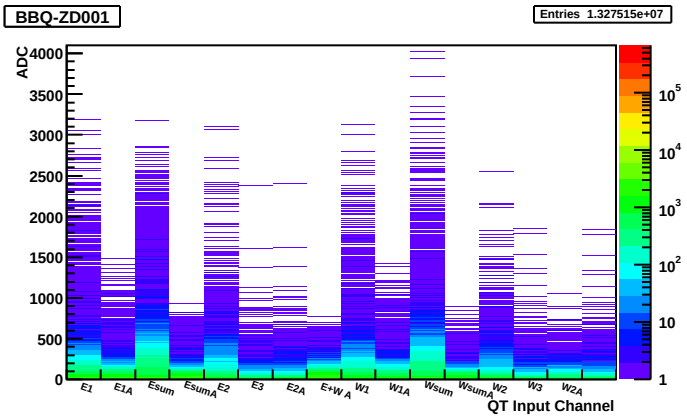

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.327515e+07
TAC
QT Input Channel

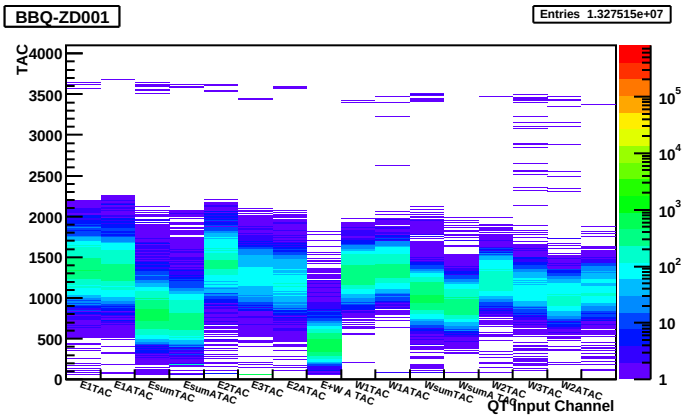

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.327515e+07
ADC
QT Input Channel

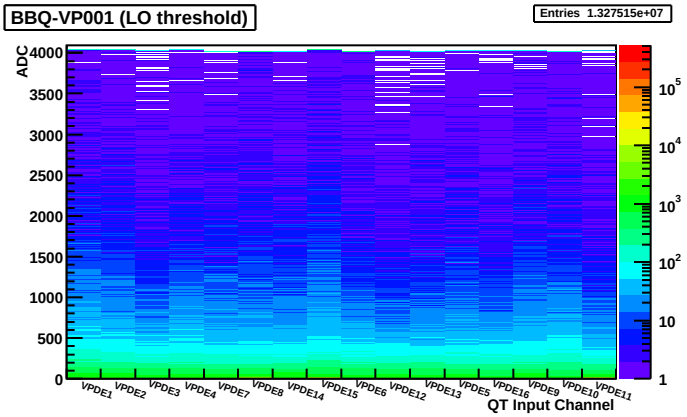

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.327515e+07
TAC
QT Input Channel

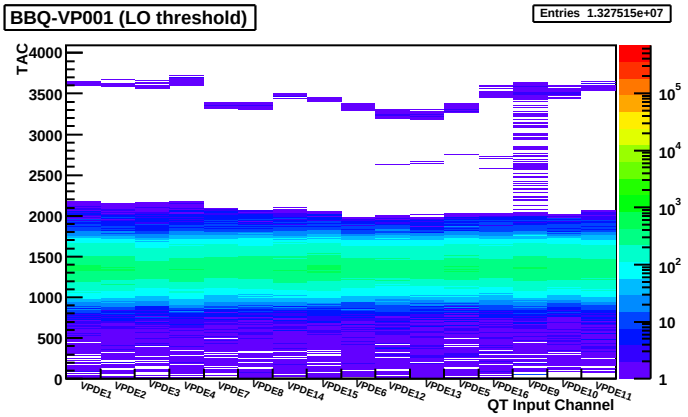

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.327515e+07
ADC
QT Input Channel

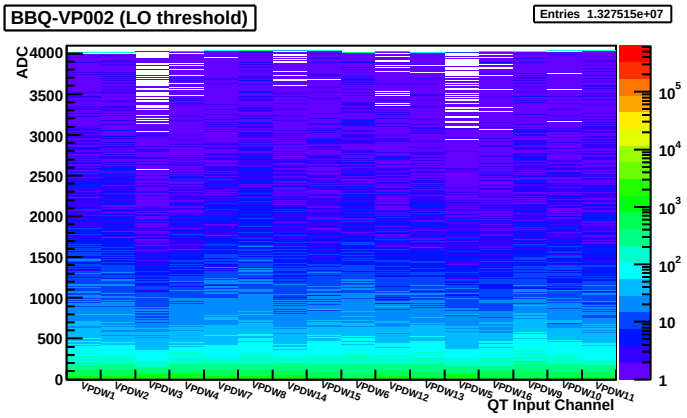

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.327515e+07
TAC
QT Input Channel

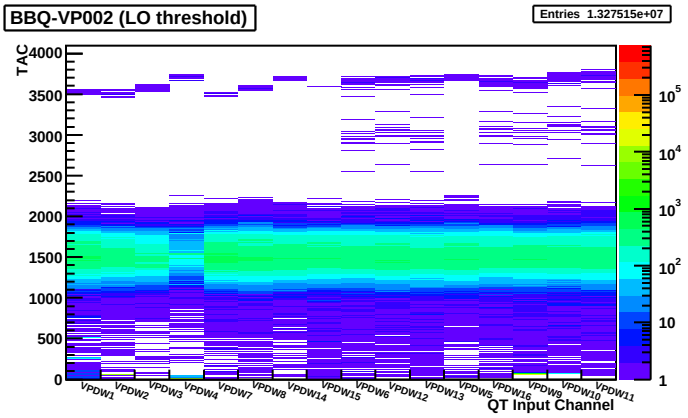

BBQ-ZD001
Entries 1.327515e+07
ADC
QT Input Channel

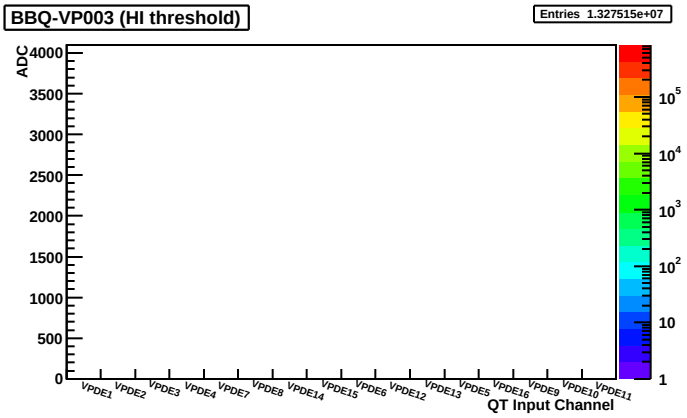

BBQ-ZD001
Entries 1.327515e+07
TAC
QT Input Channel

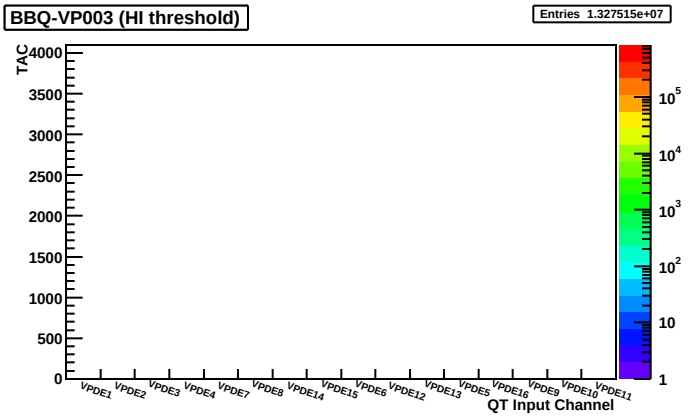

BBQ-VP001 (LO threshold)
Entries 1.327515e+07
ADC
QT Input Channel

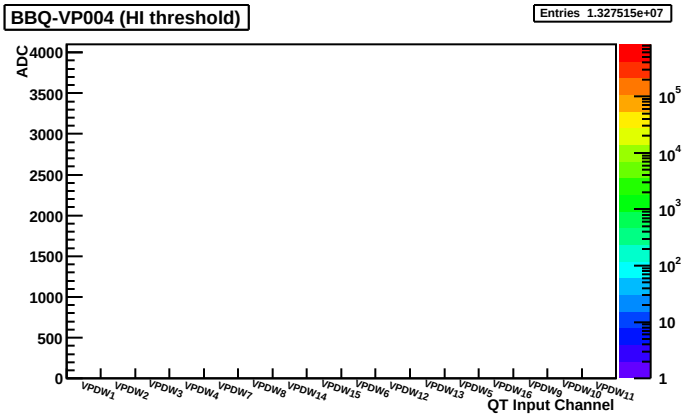

BBQ-VP001 (LO threshold)
Entries 1.327515e+07
TAC
QT Input Channel

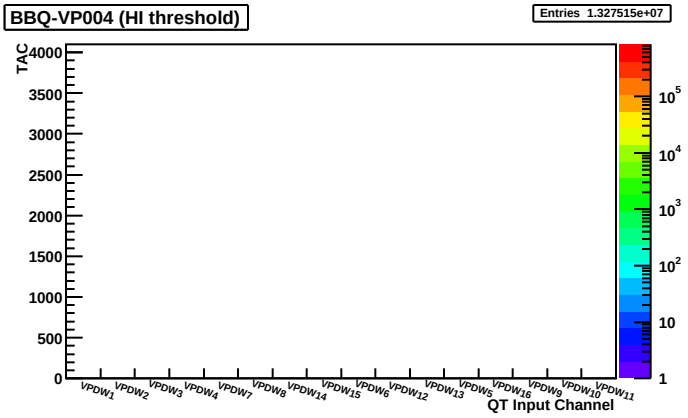

BBQ-VP002 (LO threshold)
Entries 1.327515e+07
ADC
QT Input Channel

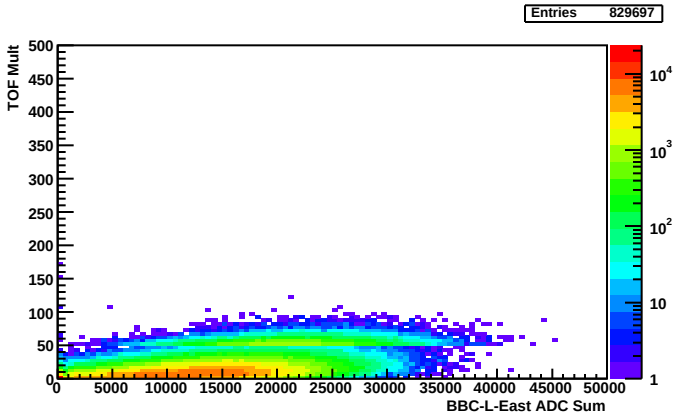

BBQ-VP002 (LO threshold)
Entries 1.327515e+07
TAC
QT Input Channel

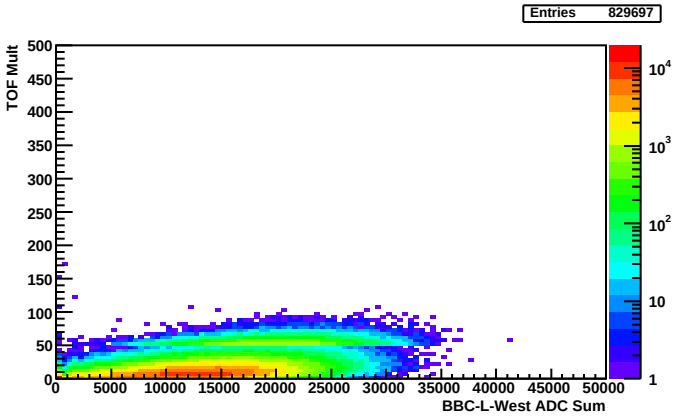

BBQ-VP003 (HI threshold)
Entries 1.327515e+07
ADC
QT Input Channel

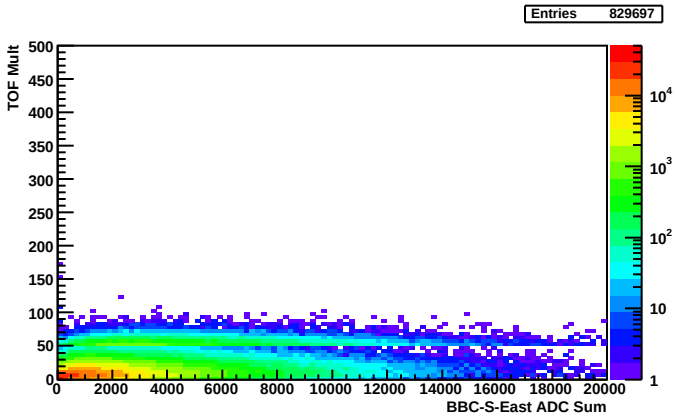

BBQ-VP003 (HI threshold)
Entries 1.327515e+07
TAC
QT Input Channel

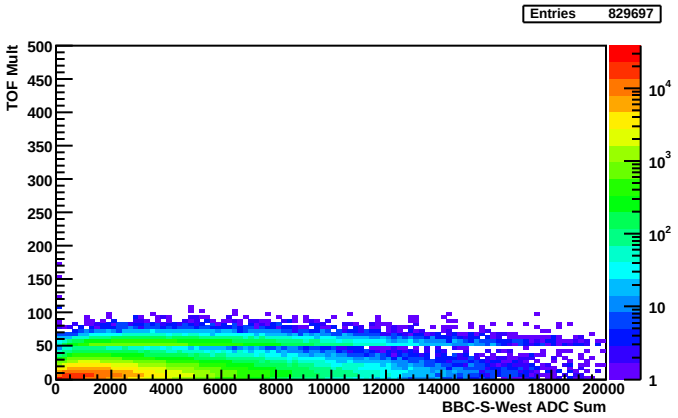

BBQ-VP004 (HI threshold)
Entries 1.327515e+07
ADC
QT Input Channel

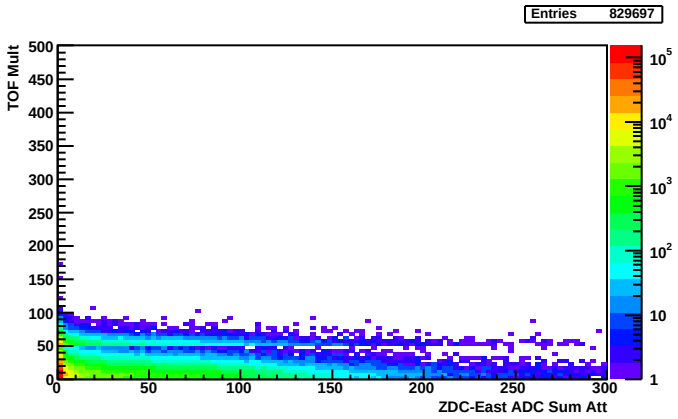

BBQ-VP004 (HI threshold)
Entries 1.327515e+07
TAC
QT Input Channel

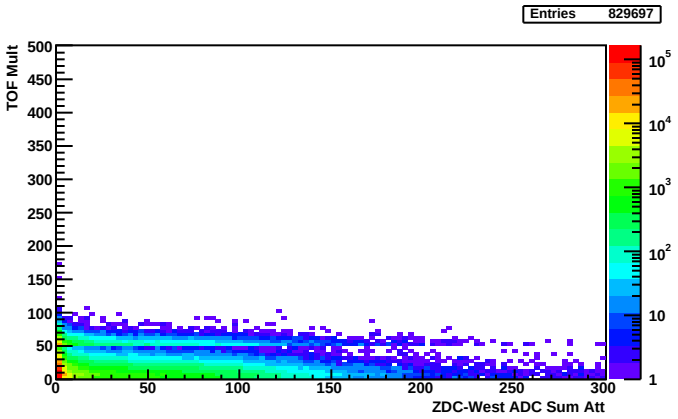

Entries 829697
TOF Mult
BBC-L-East ADC Sum

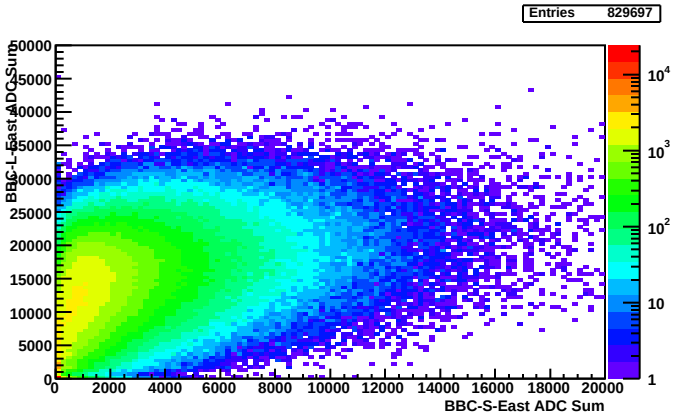

Entries 829697
TOF Mult
BBC-L-West ADC Sum

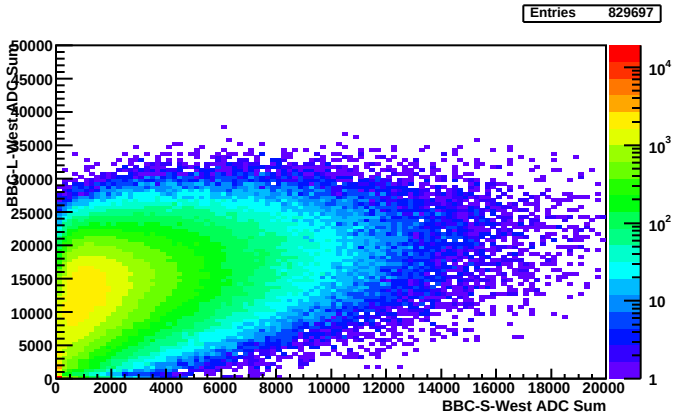

Entries 829697
TOF Mult
BBC-S-East ADC Sum

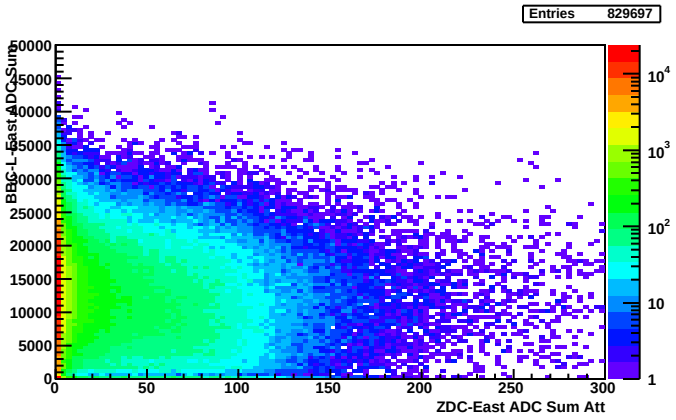

Entries 829697
TOF Mult
BBC-S-West ADC Sum

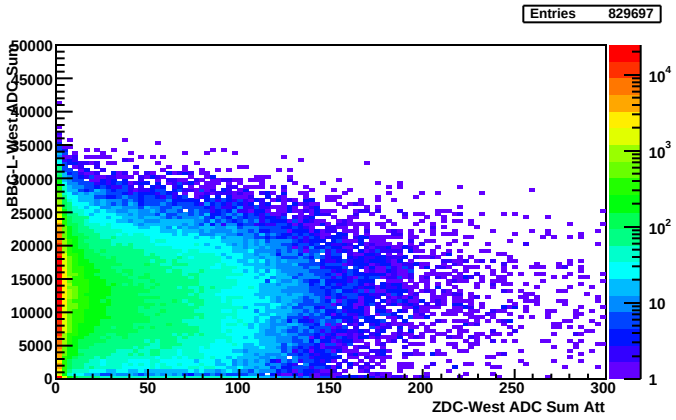

Entries 829697
TOF Mult
ZDC-East ADC Sum Att

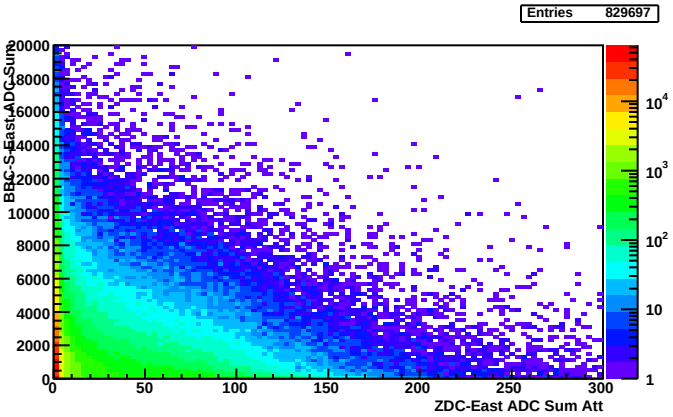

Entries 829697
TOF Mult
ZDC-West ADC Sum Att

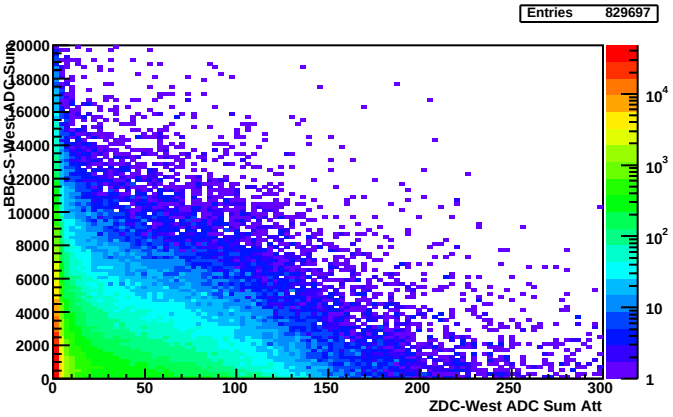

Entries 829697
BBC-L-East ADC Sum
BBC-S-East ADC Sum

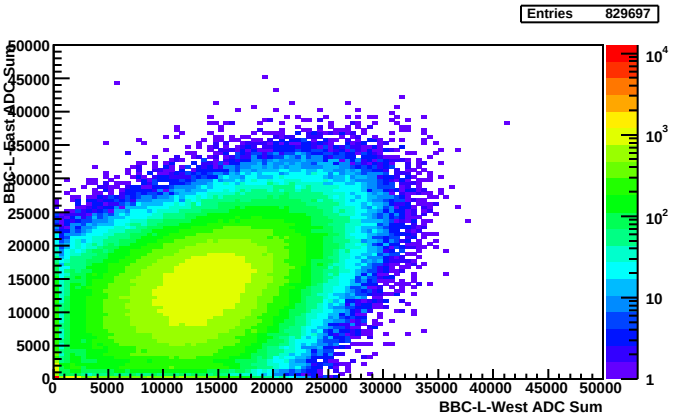

Entries 829697
BBC-L-West ADC Sum
BBC-S-West ADC Sum

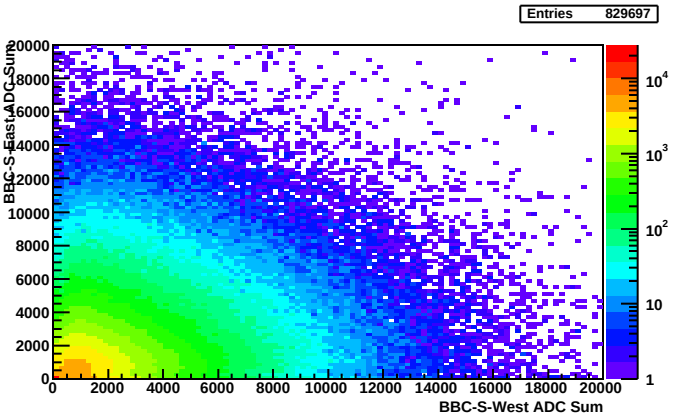

Entries 829697
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

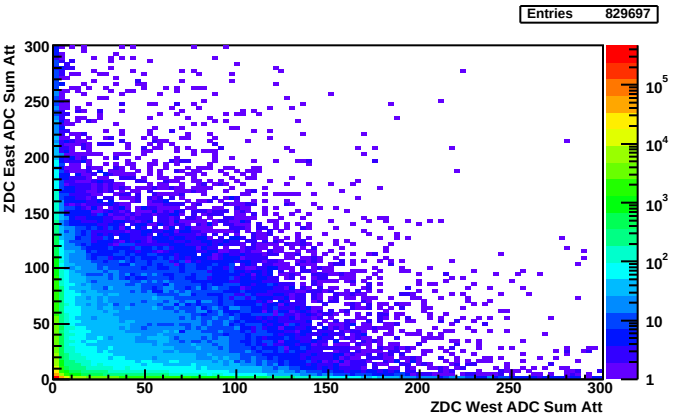

Entries 829697
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

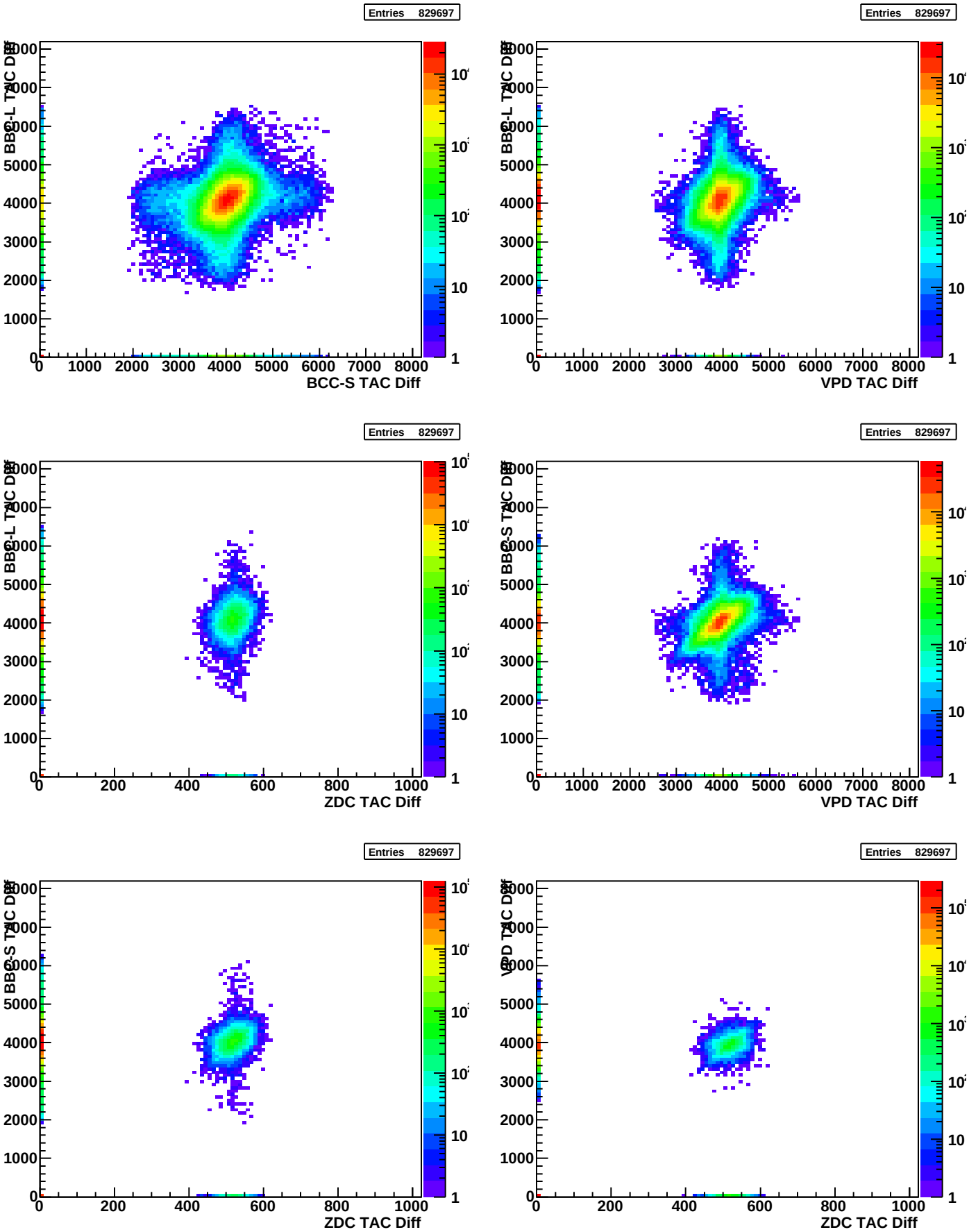

Entries 829697
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

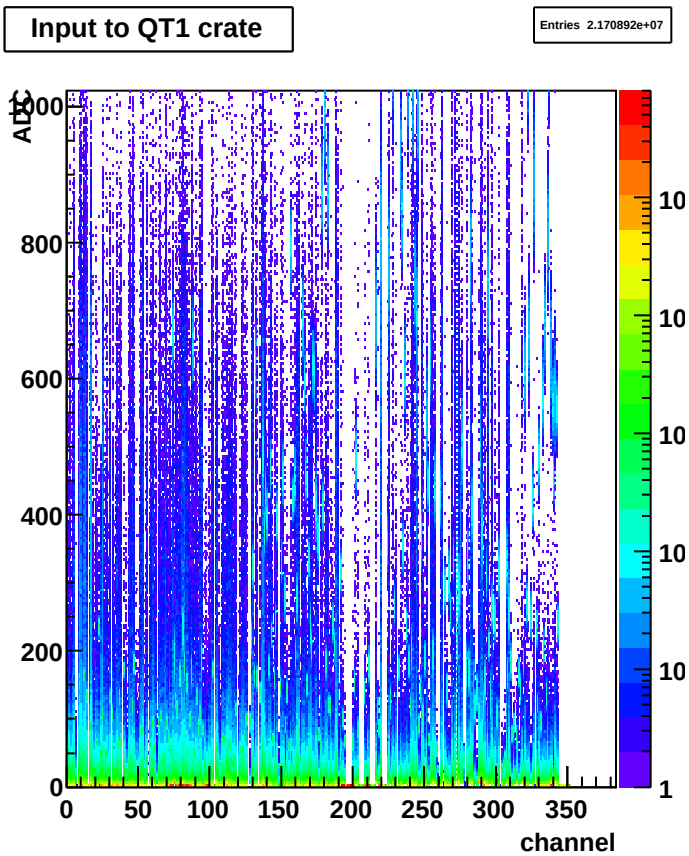

Entries 829697
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

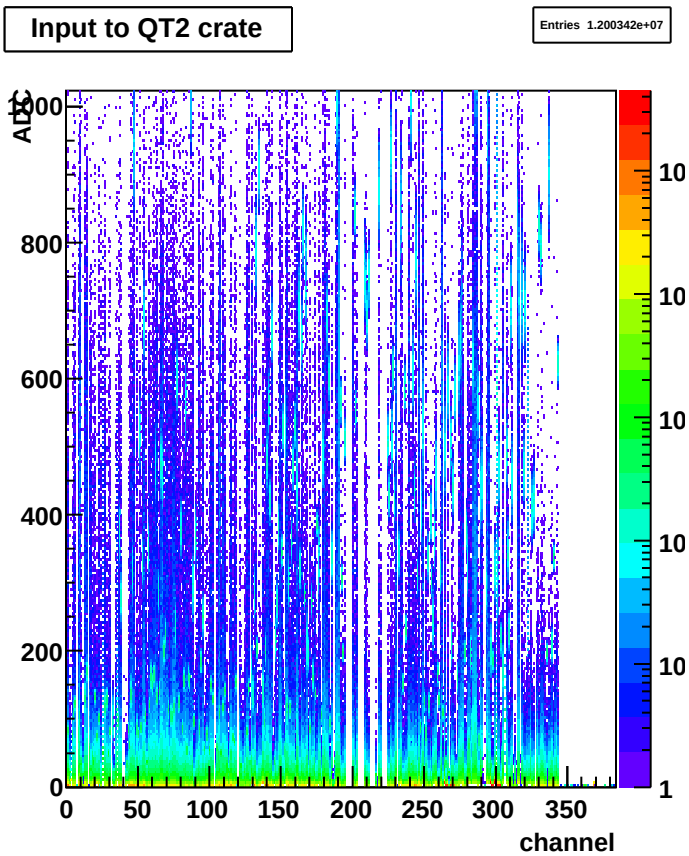

Entries 829697
BBC-L-East ADC Sum
BBC-L-West ADC Sum

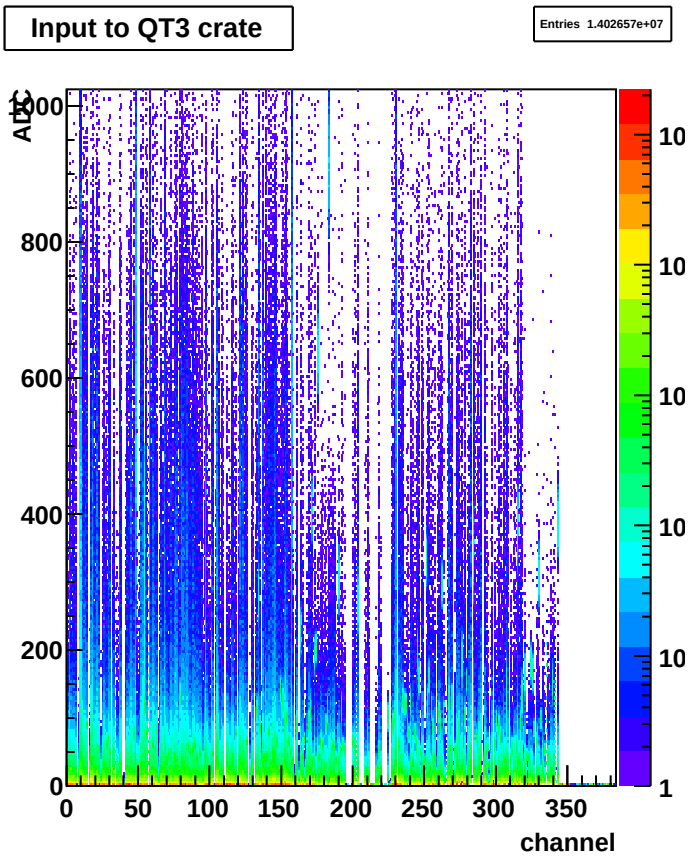

Entries 829697
BBC-S-East ADC Sum
BBC-S-West ADC Sum

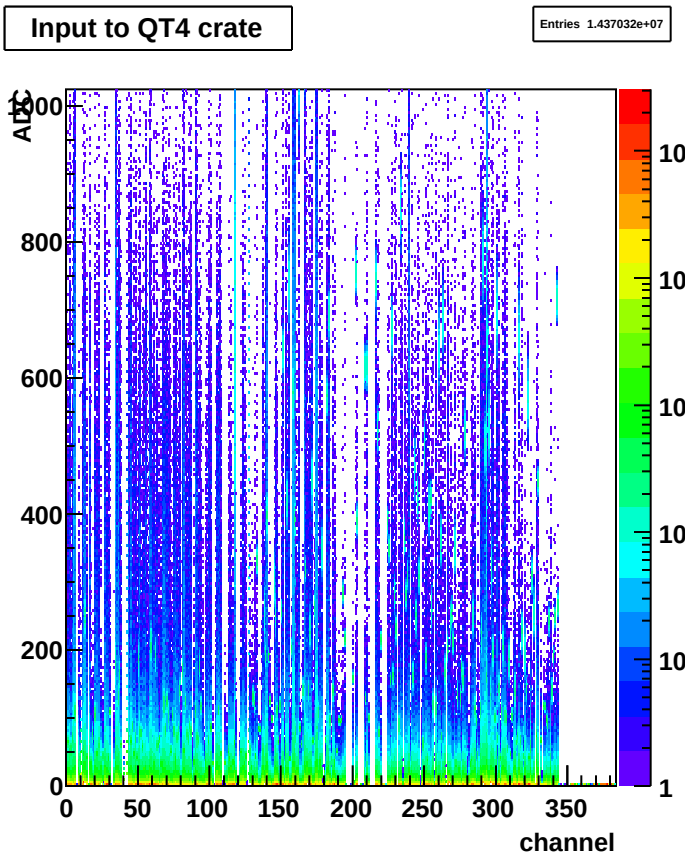

Entries 829697
ZDC East ADC Sum Att
ZDC West ADC Sum Att

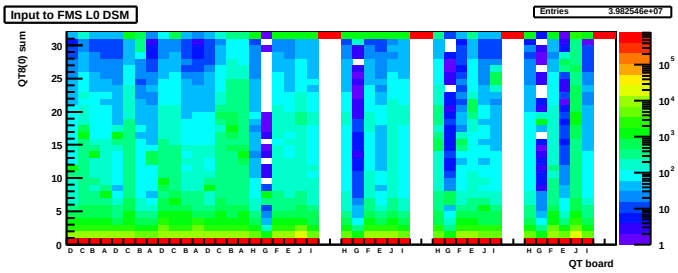

Entries 829697
BBC-L TAC Diff
BCC-S TAC Diff
Entries 829697
BBC-L TAC Diff
VPD TAC Diff
Entries 829697
BBC-L TAC Diff
ZDC TAC Diff
Entries 829697
BBC-S TAC Diff
VPD TAC Diff
Entries 829697
BBC-S TAC Diff
ZDC TAC Diff
Entries 829697
VPD TAC Diff
ZDC TAC Diff

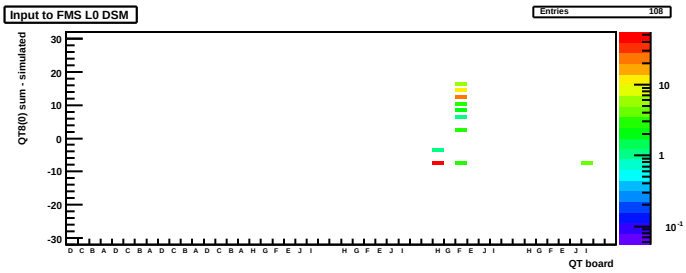

Input to QT1 crate
Entries 2.170892e+07
ADC
channel

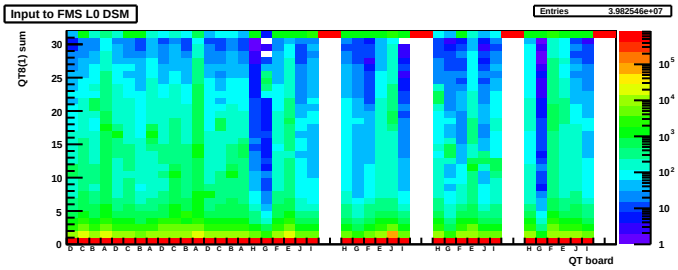

Input to QT2 crate
Entries 1.200342e+07
ADC
channel

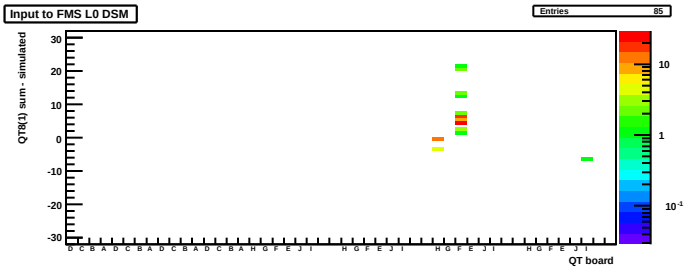

Input to QT3 crate
Entries 1.402657e+07
ADC
channel

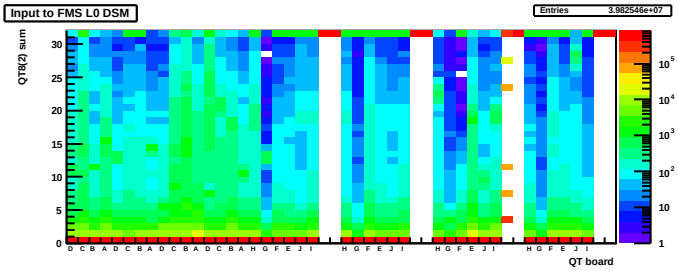

Input to QT4 crate
Entries 1.437032e+07
ADC
channel

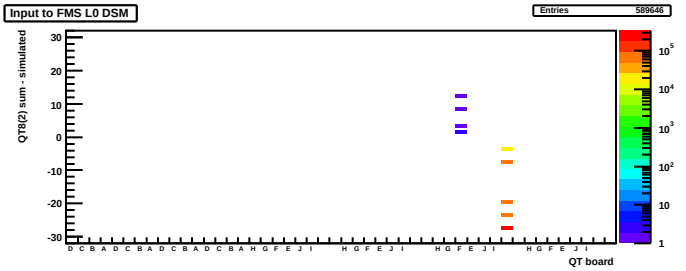

Input to FMS L0 DSM
Entries 3.982546e+07
QT8(0) sum
QT board

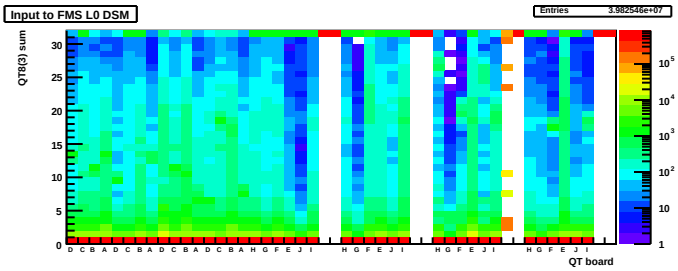

Input to FMS L0 DSM
Entries 108
QT8(0) sum - simulated
QT board

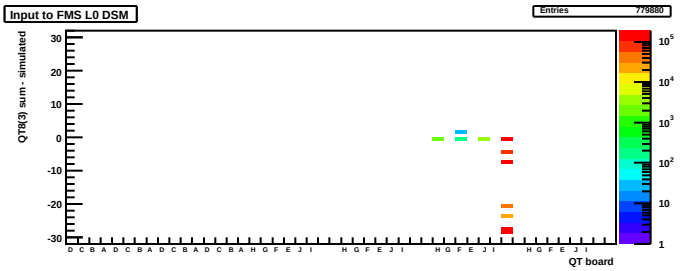

Input to FMS L0 DSM
Entries 3.982546e+07
QT8(1) sum
QT board

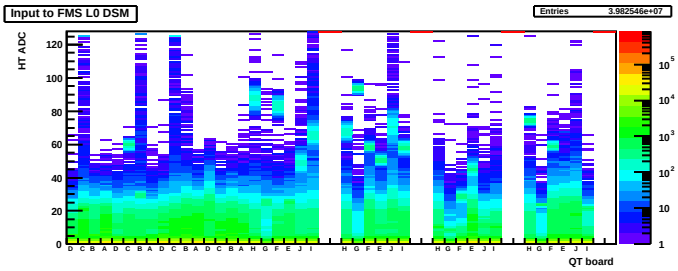

Input to FMS L0 DSM
Entries 85
QT8(1) sum - simulated
QT board

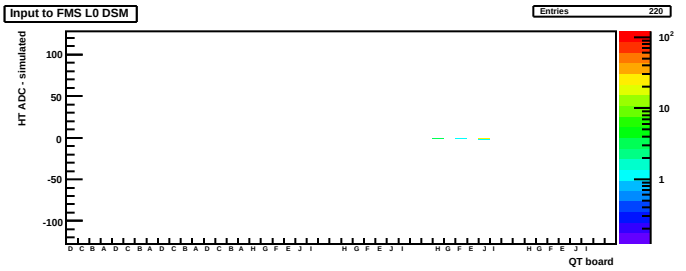

Input to FMS L0 DSM
Entries 3.982546e+07
QT8(2) sum
QT board

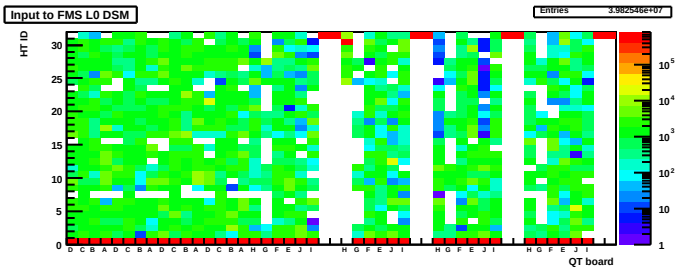

Input to FMS L0 DSM
Entries 589646
QT8(2) sum - simulated
QT board

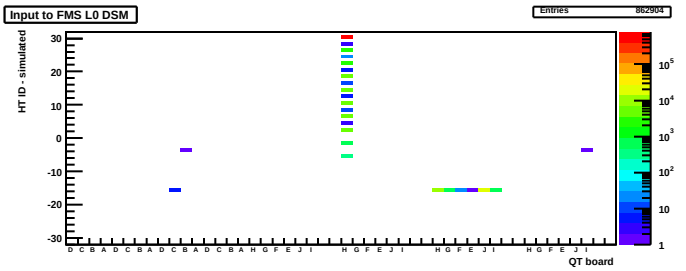

Input to FMS L0 DSM
Entries 3.982546e+07
QT8(3) sum
QT board

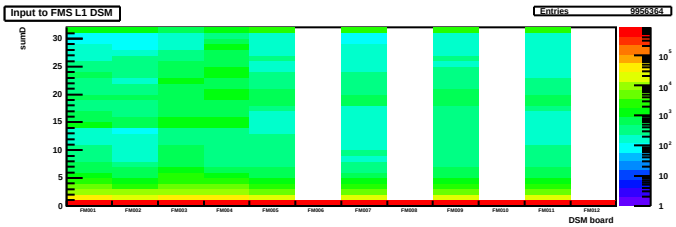

Input to FMS L0 DSM
Entries 779880
QT8(3) sum - simulated
QT board

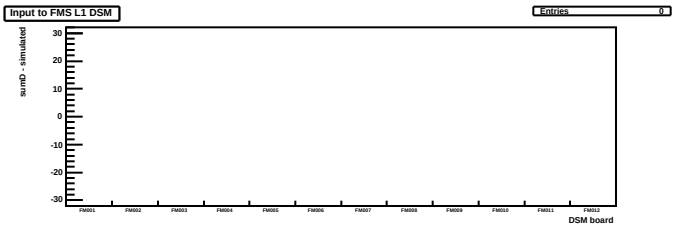

Input to FMS L0 DSM
Entries 3.982546e+07
HT ADC
QT board

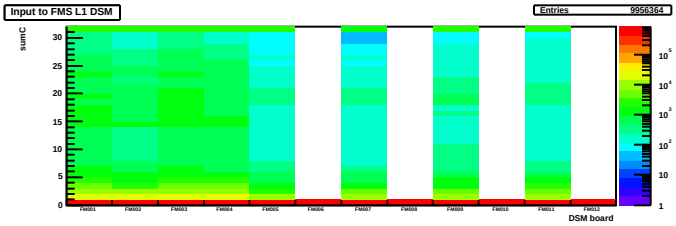

Input to FMS L0 DSM
Entries 220
HT ADC - simulated
QT board

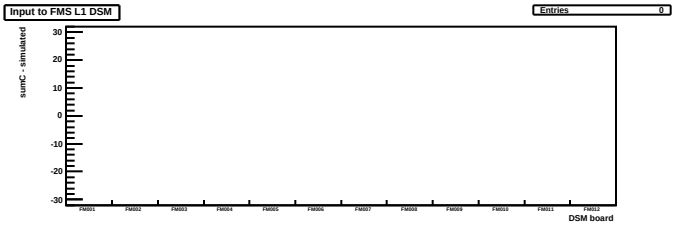

Input to FMS L0 DSM
Entries 3.982546e+07
HT ID
QT board

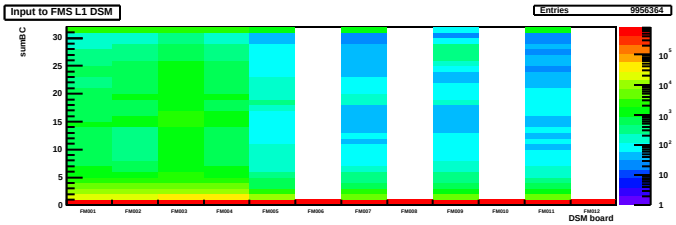

Input to FMS L0 DSM
Entries 862904
HT ID - simulated
QT board

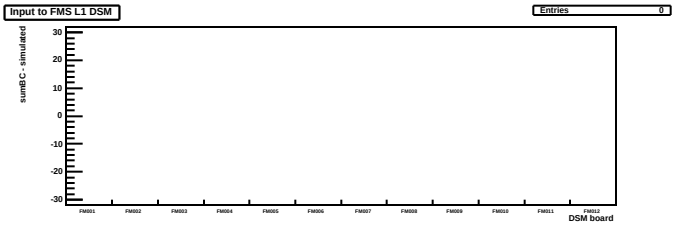

Input to FMS L1 DSM
Entries 9956364
sumD
DSM board

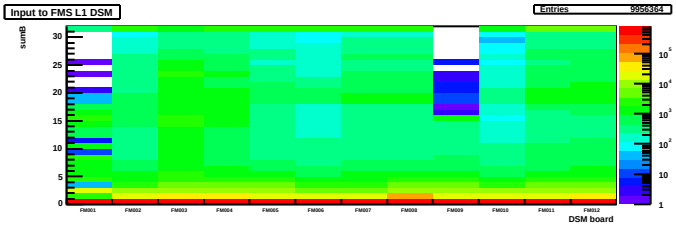

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

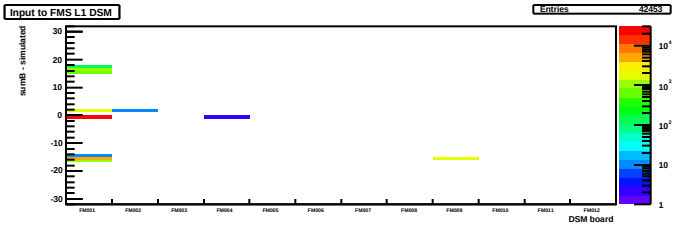

Input to FMS L1 DSM
Entries 9956364
sumC
DSM board

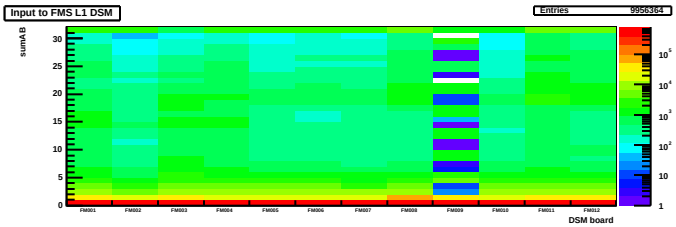

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

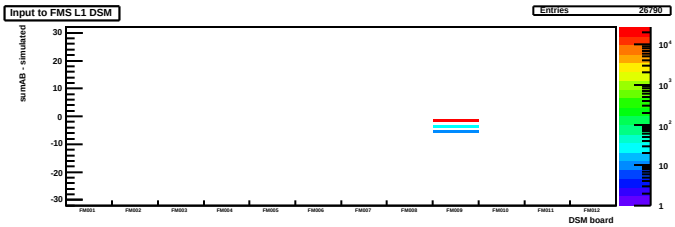

Input to FMS L1 DSM
Entries 9956364
sumBC
DSM board

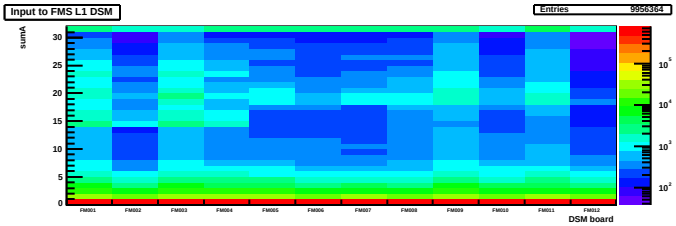

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

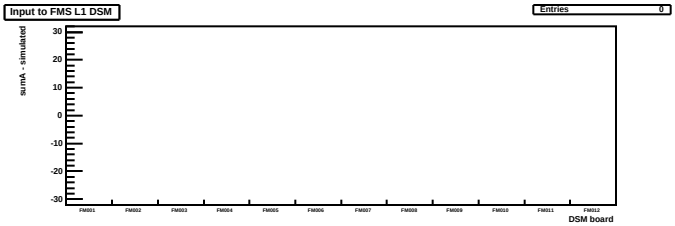

Input to FMS L1 DSM
Entries 9956364
sumB
DSM board

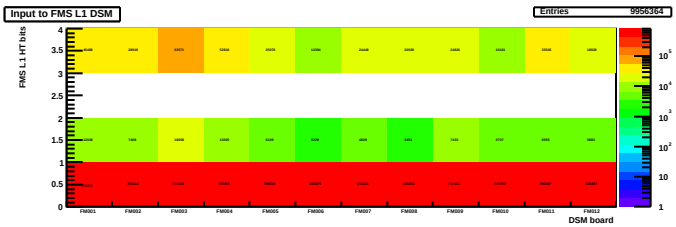

Input to FMS L1 DSM
Entries 42453
sumB - simulated
DSM board

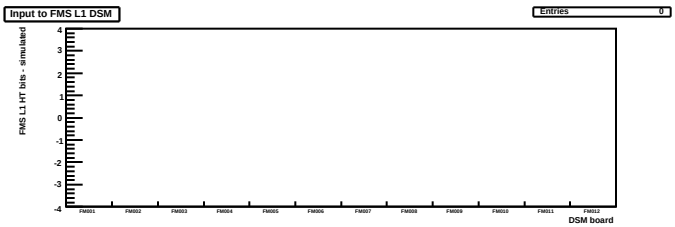

Input to FMS L1 DSM
Entries 9956364
sumAB
DSM board

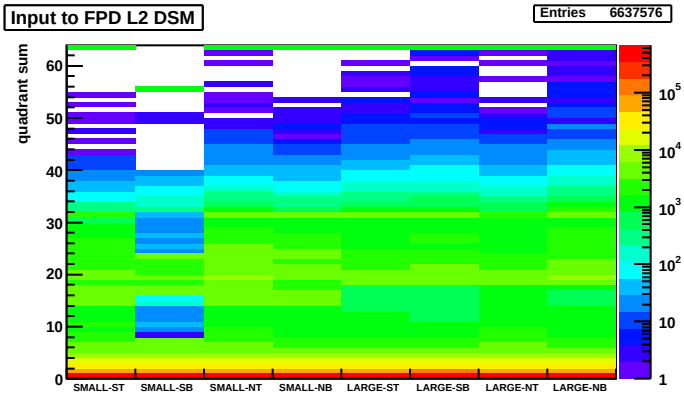

Input to FMS L1 DSM
Entries 26790
sumAB - simulated
DSM board

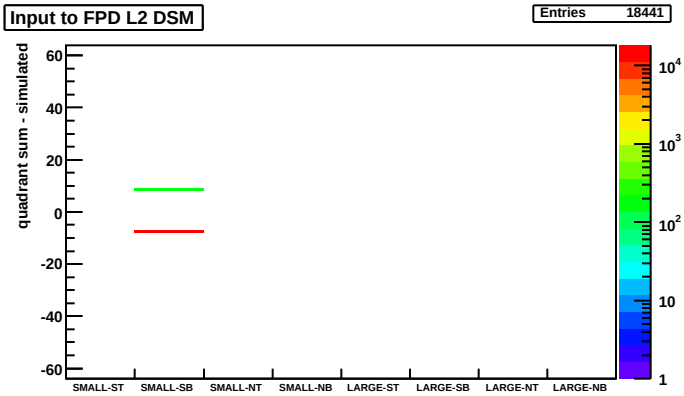

Input to FMS L1 DSM
Entries 9956364
sumA
DSM board

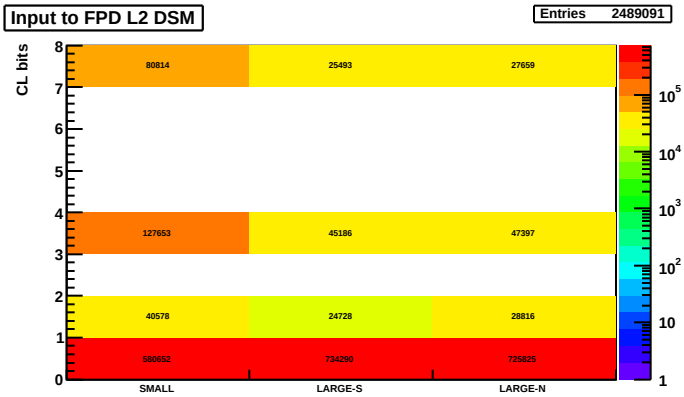

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

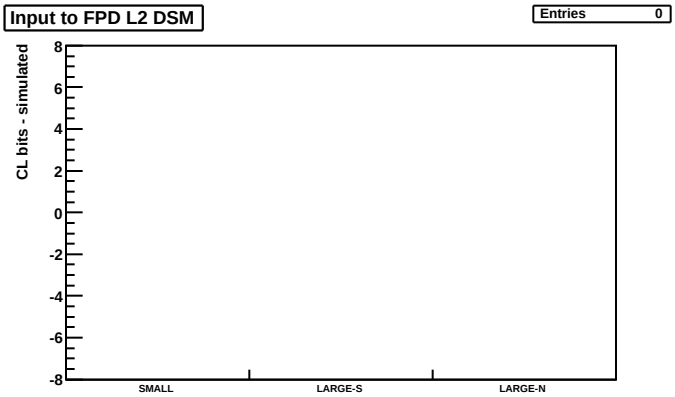

Input to FMS L1 DSM
Entries 9956364
FMS L1 HT bits
DSM board

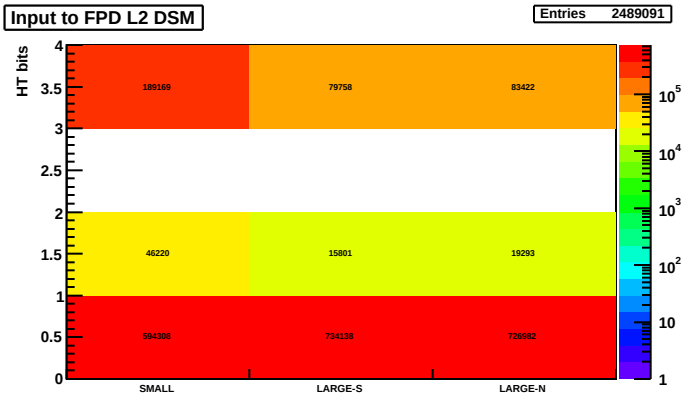

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

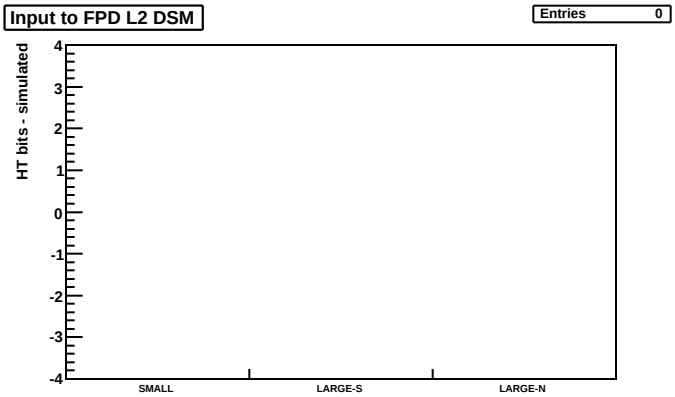

Input to FPD L2 DSM
Entries 6637576
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

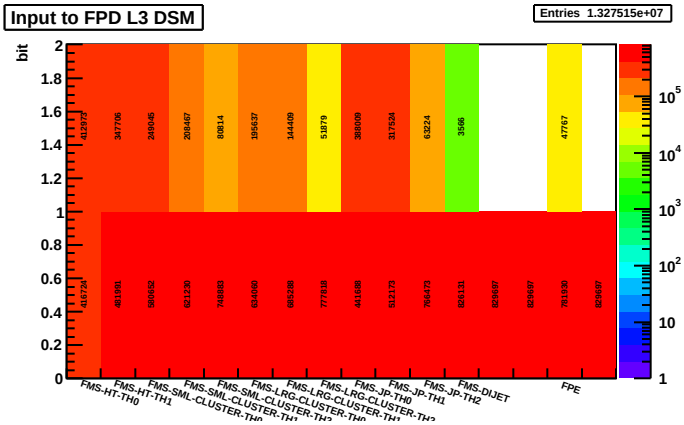

Input to FPD L2 DSM
Entries 18441
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

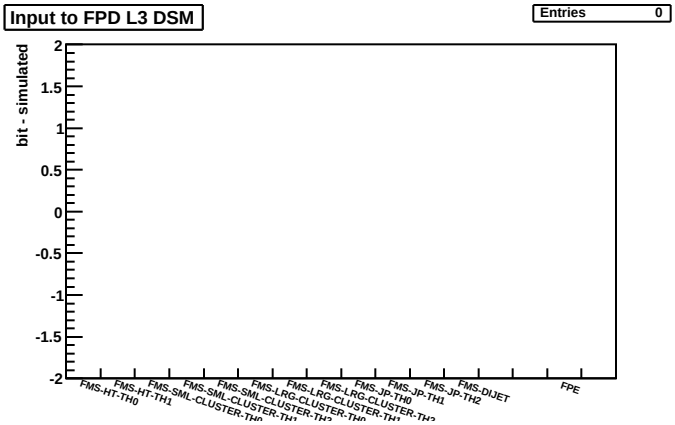

Input to FPD L2 DSM
Entries 2489091
CL bits
80814
25493
27659
127653
45186
47397
40578
24728
28816
580652
734290
725825
SMALL
LARGE-S
LARGE-N

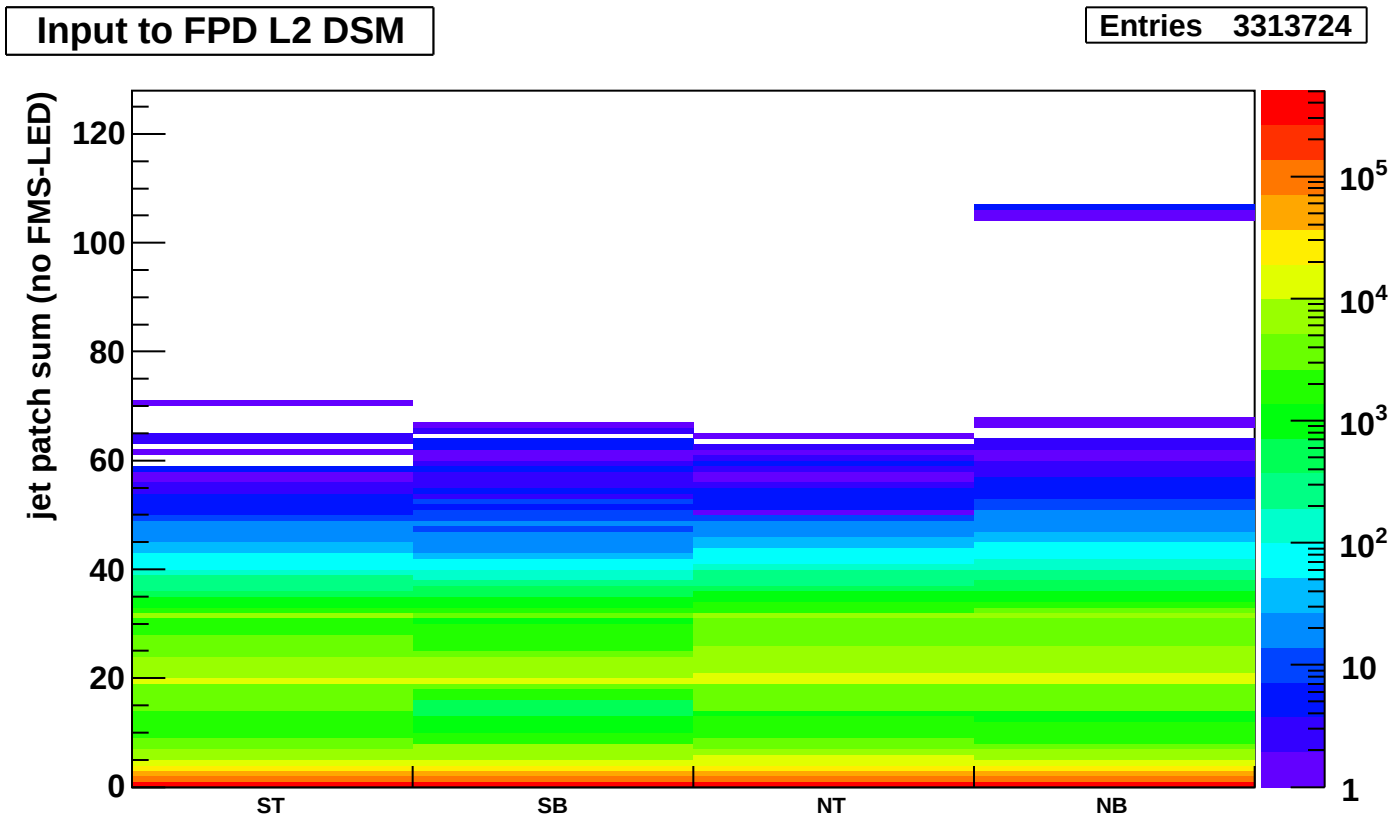

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

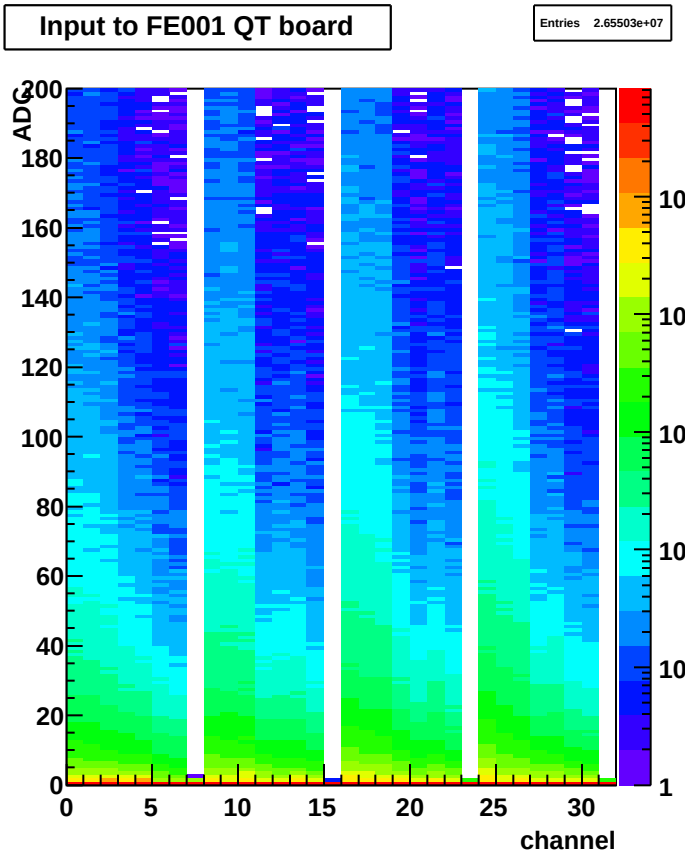

Input to FPD L2 DSM
Entries 2489091
HT bits
189169
79758
83422
46220
15801
19293
594308
734138
726982
SMALL
LARGE-S
LARGE-N

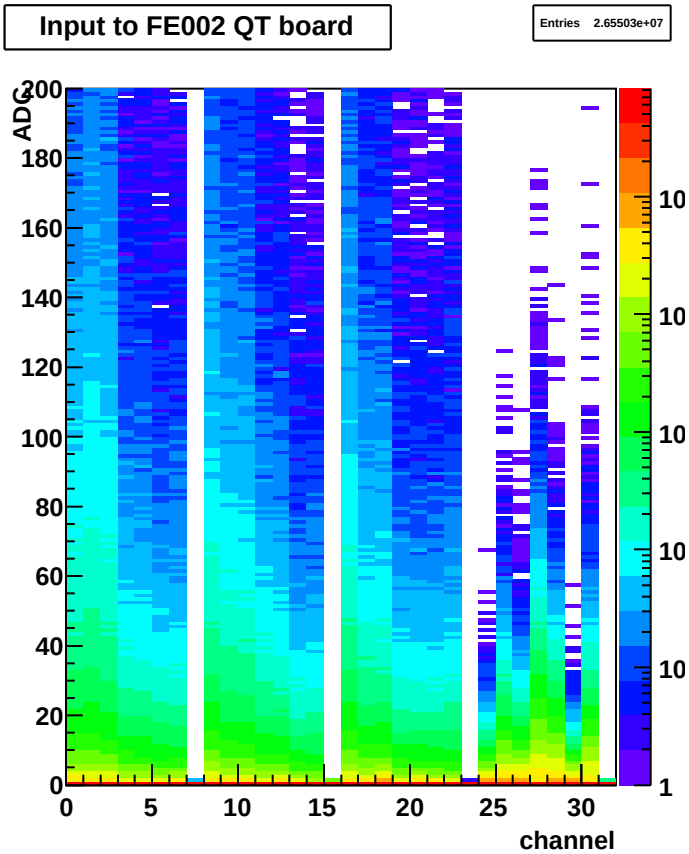

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

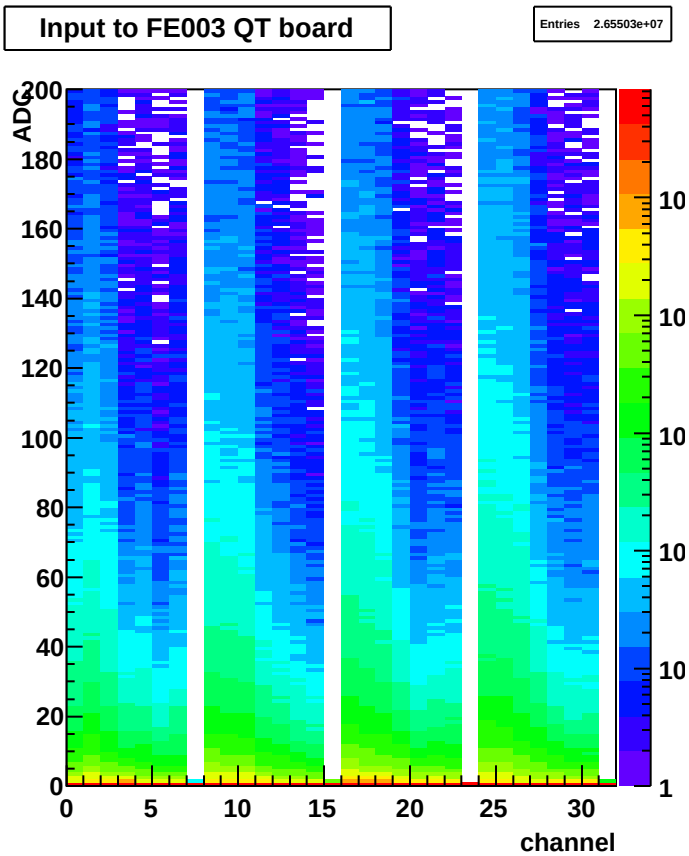

Input to FPD L3 DSM
Entries 1.327515e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

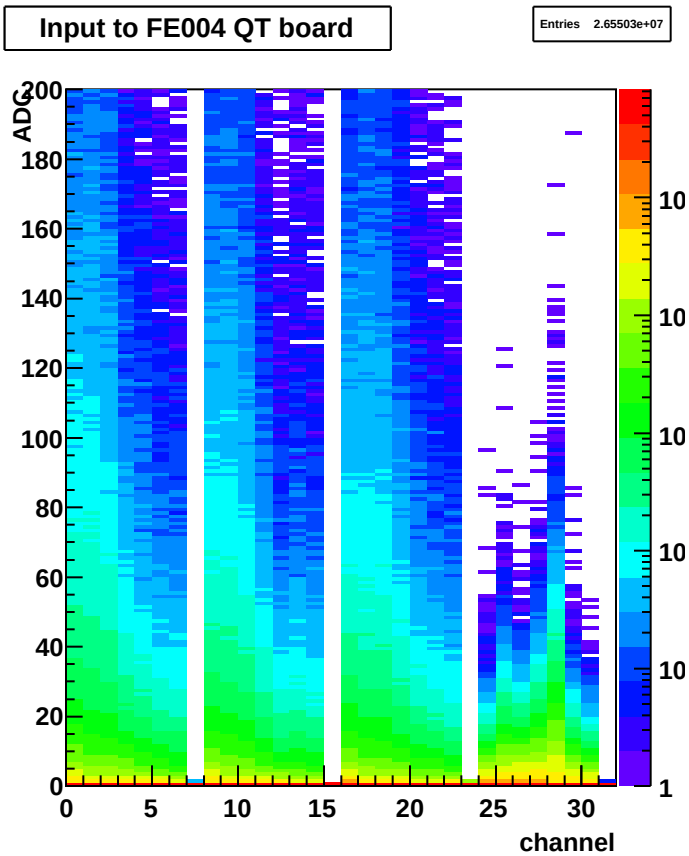

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

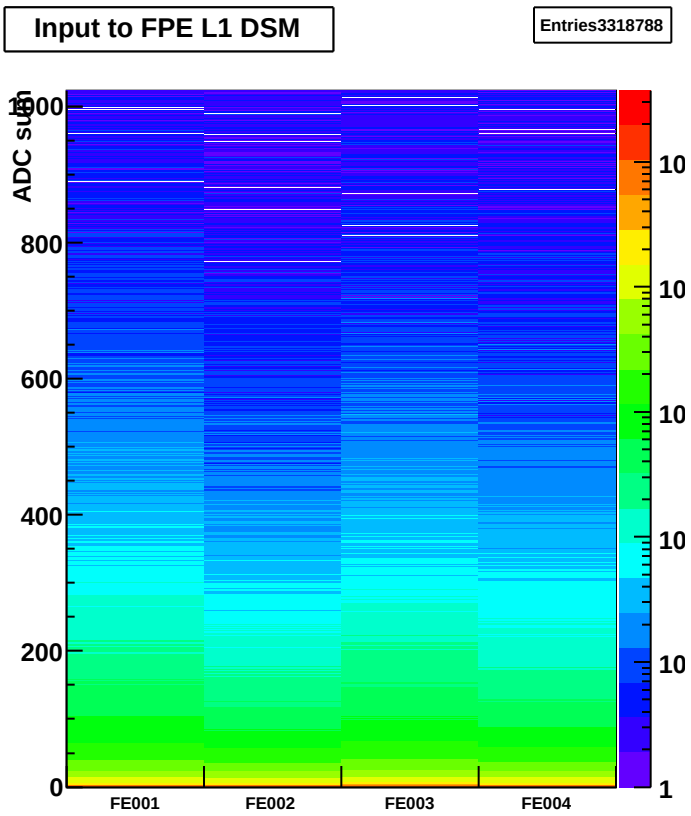

Input to FPD L2 DSM
Entries 3313724
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

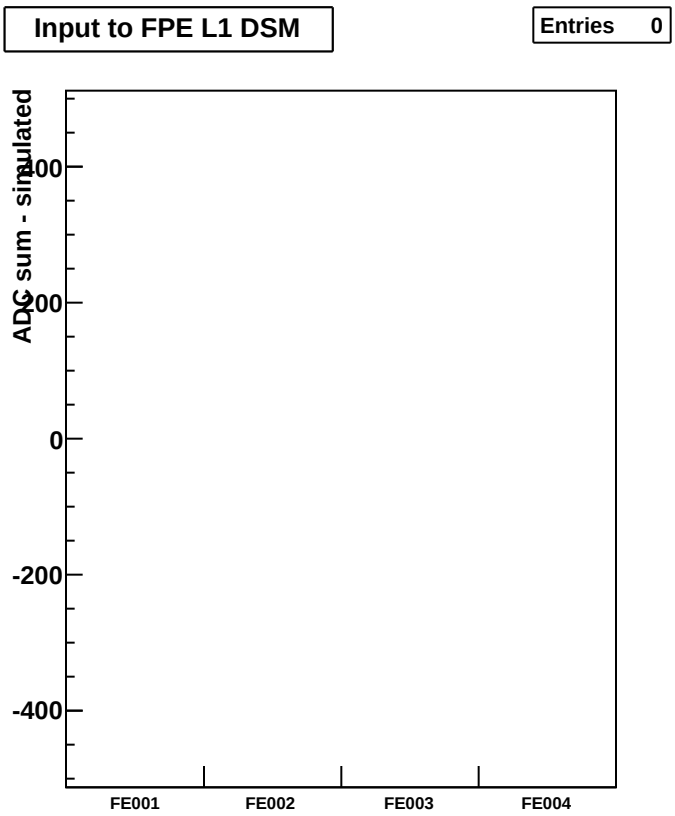

Input to FE001 QT board
Entries 2.65503e+07
ADC
channel

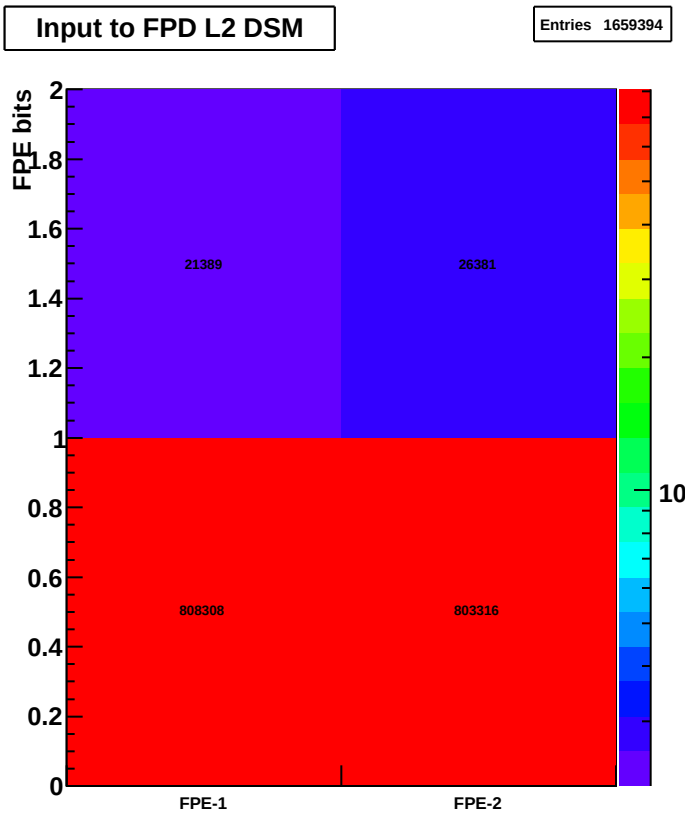

Input to FE002 QT board
Entries 2.65503e+07
ADC
channel

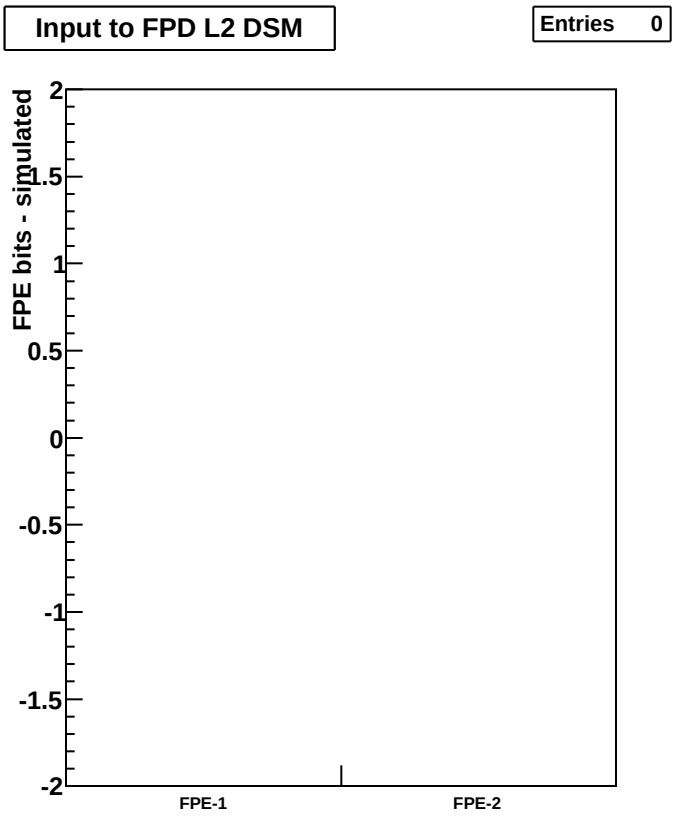

Input to FE003 QT board
Entries 2.65503e+07
ADC
channel


Input to FE004 QT board
Entries 2.65503e+07
ADC
channel


Input to FPE L1 DSM
Entries3318788
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 1659394
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
21389
26381
808308
803316
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2