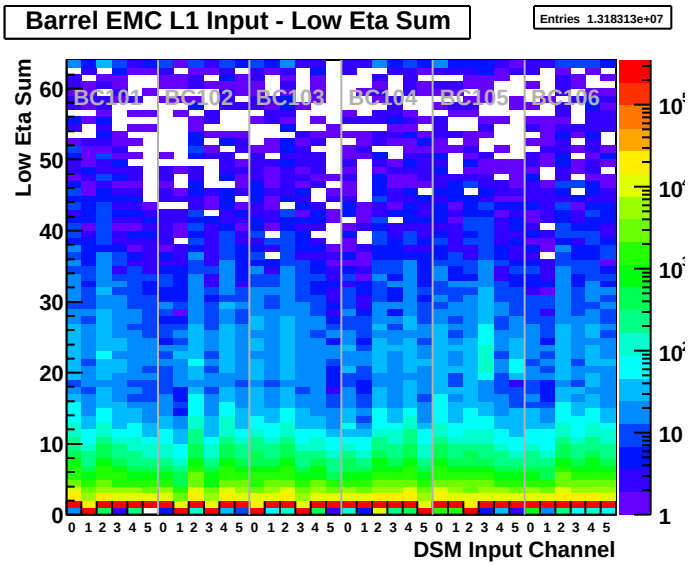
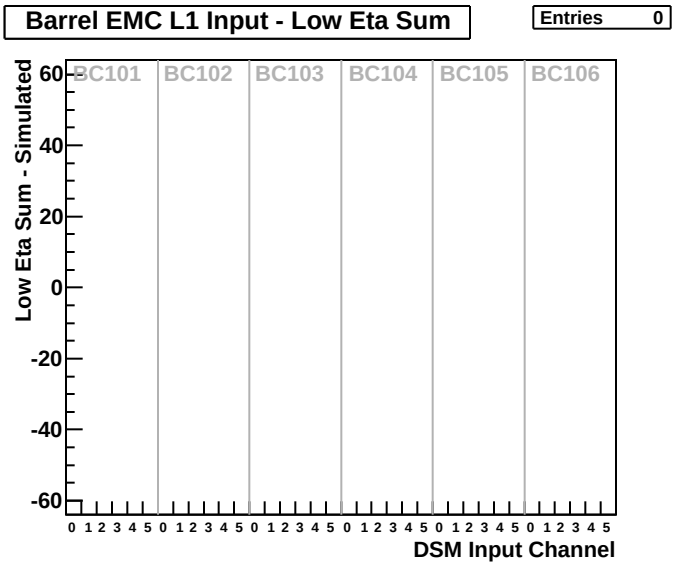
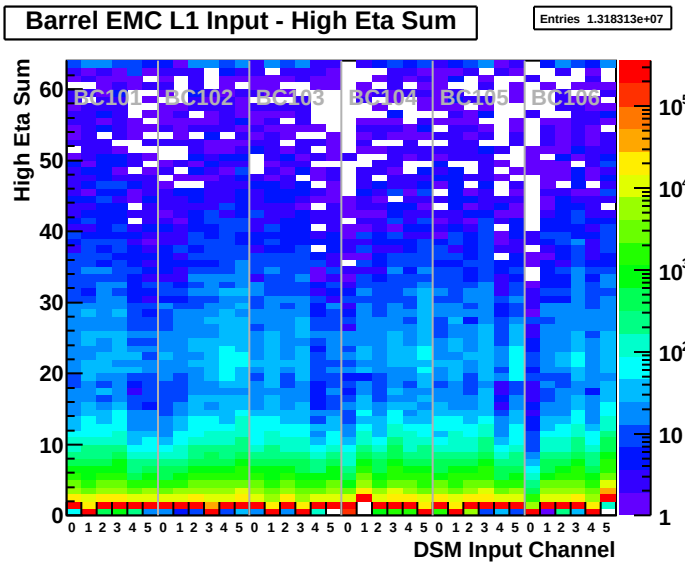
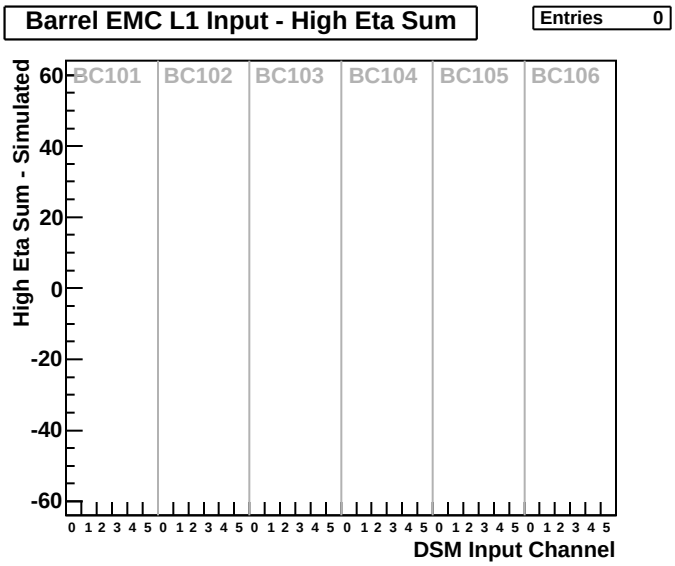
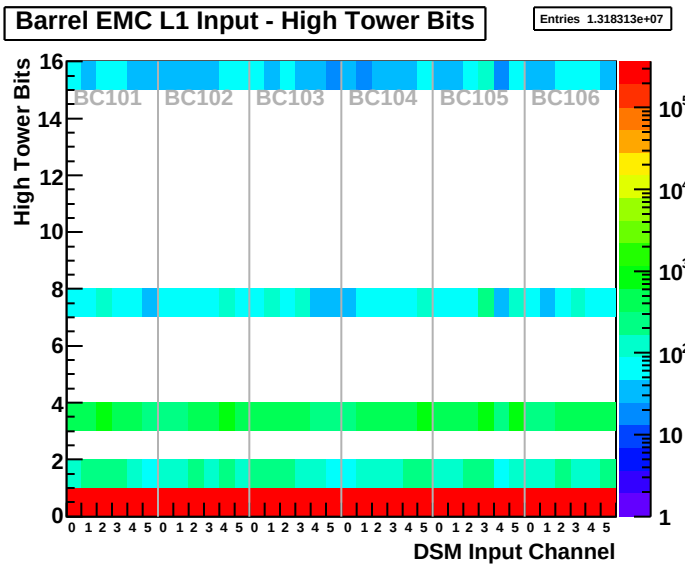
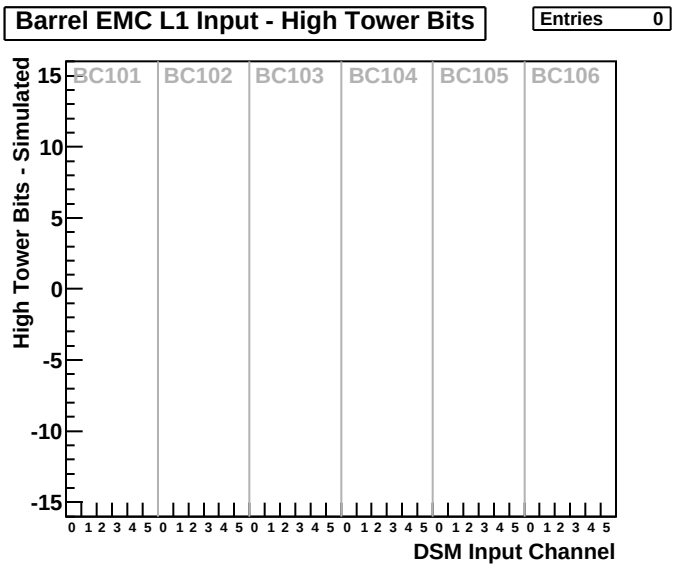

Barrel EMC L1 Input - Low Eta Sum
Entries 1.318313e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

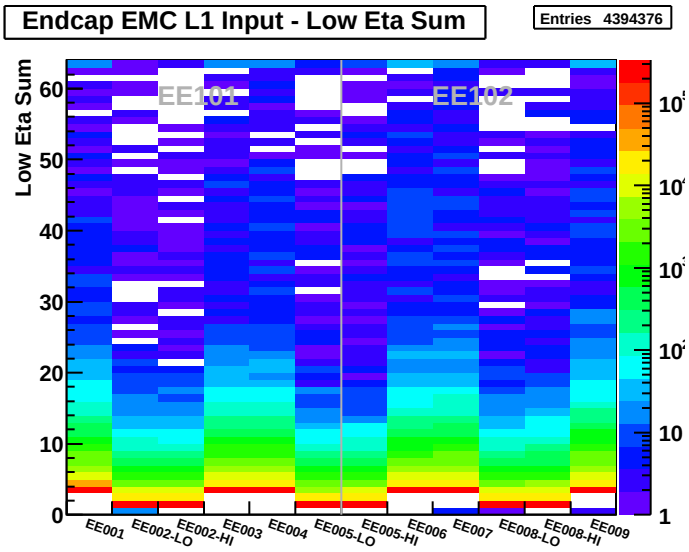

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

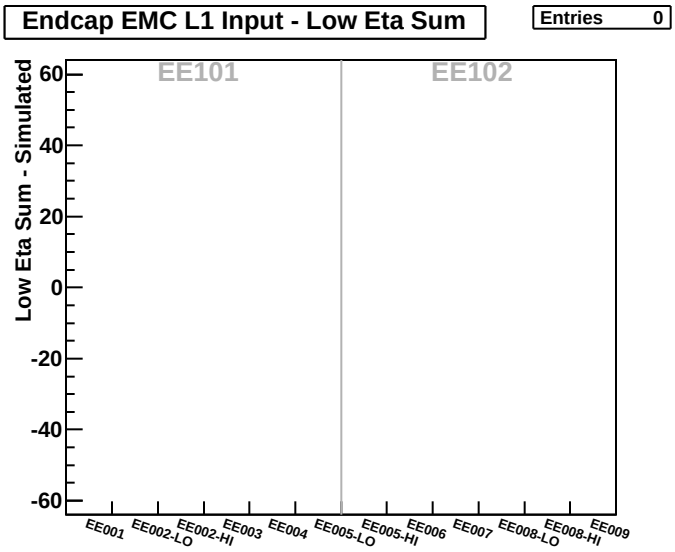

Barrel EMC L1 Input - High Eta Sum
Entries 1.318313e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

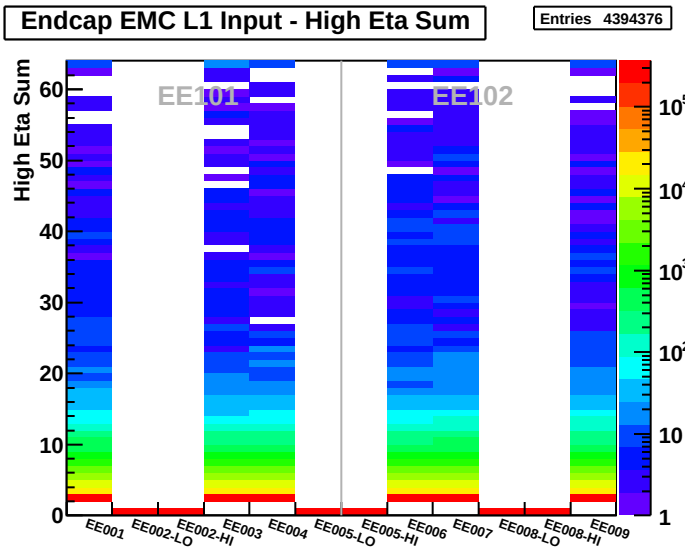

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

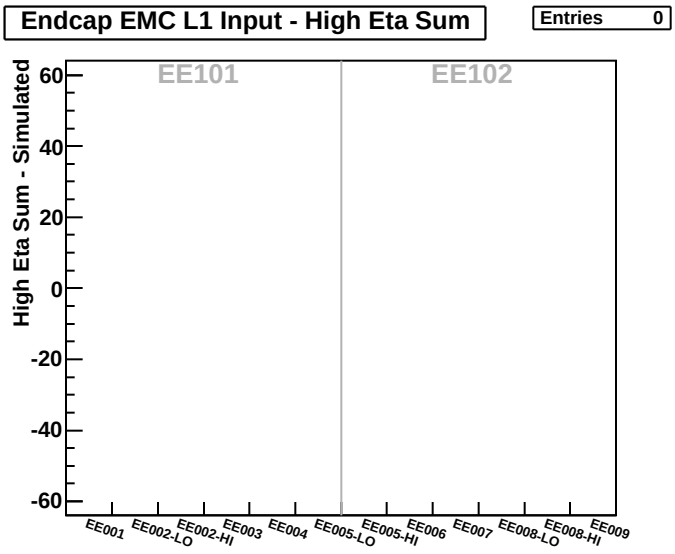

Barrel EMC L1 Input - High Tower Bits
Entries 1.318313e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

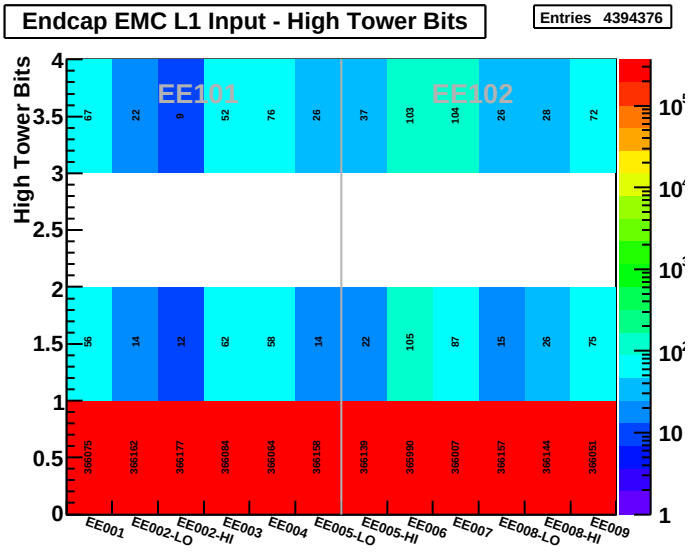

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

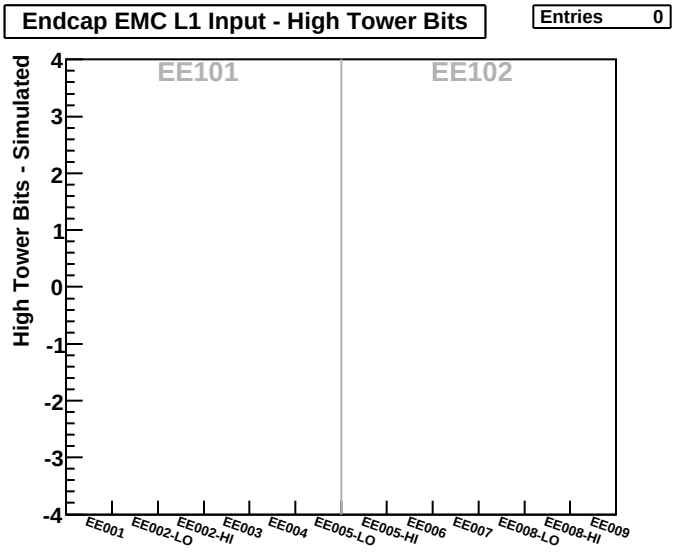

Endcap EMC L1 Input - Low Eta Sum
Entries 4394376
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

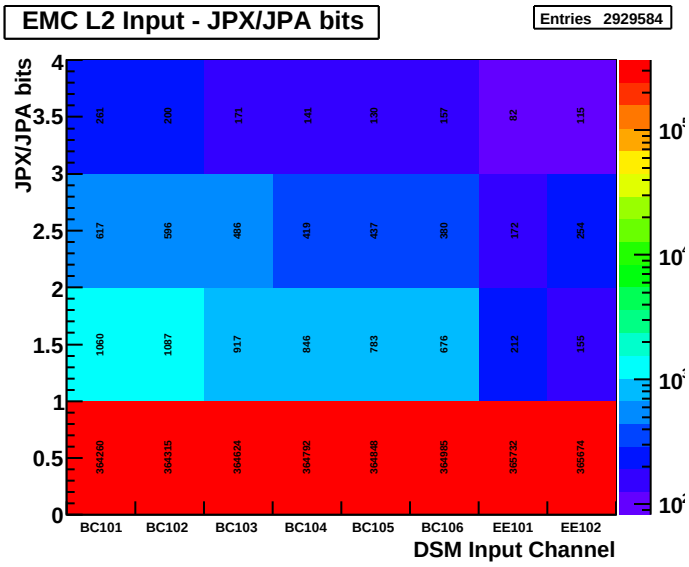

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

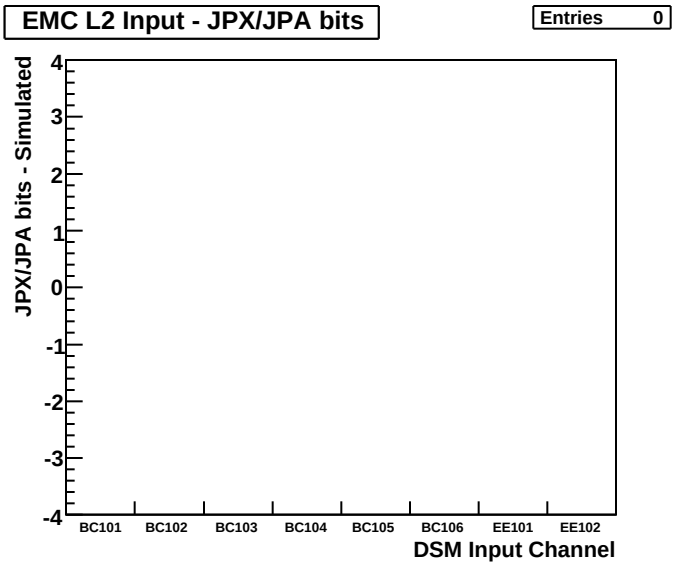

Endcap EMC L1 Input - High Eta Sum
Entries 4394376
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

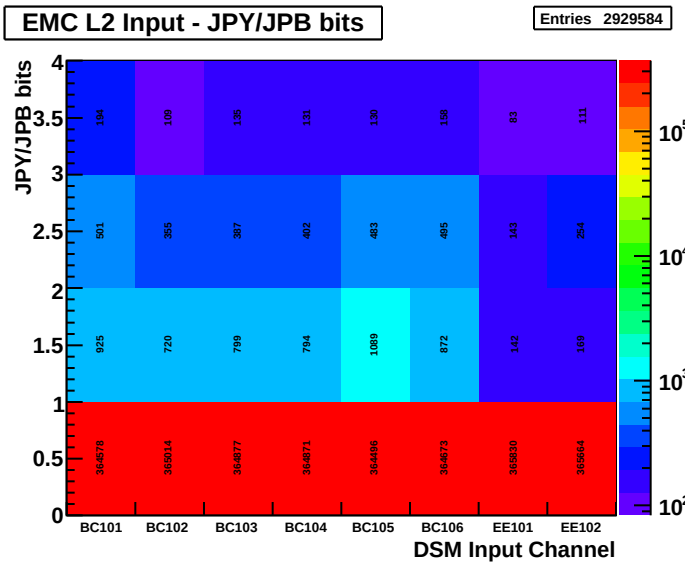

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

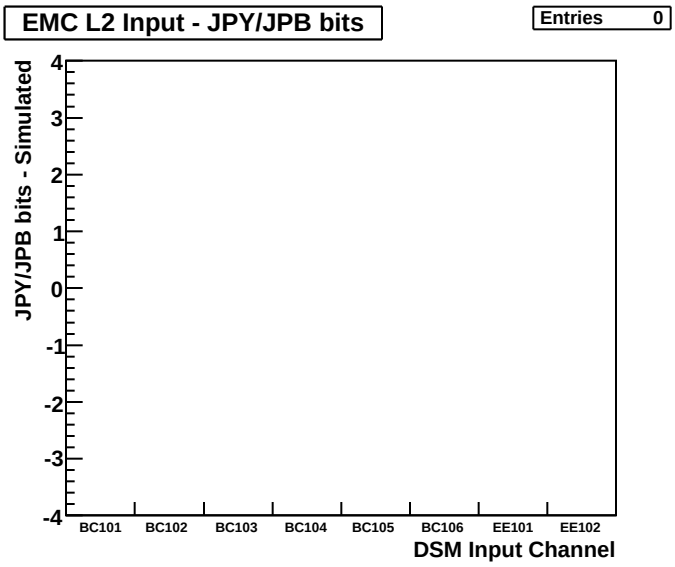

Endcap EMC L1 Input - High Tower Bits
Entries 4394376
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

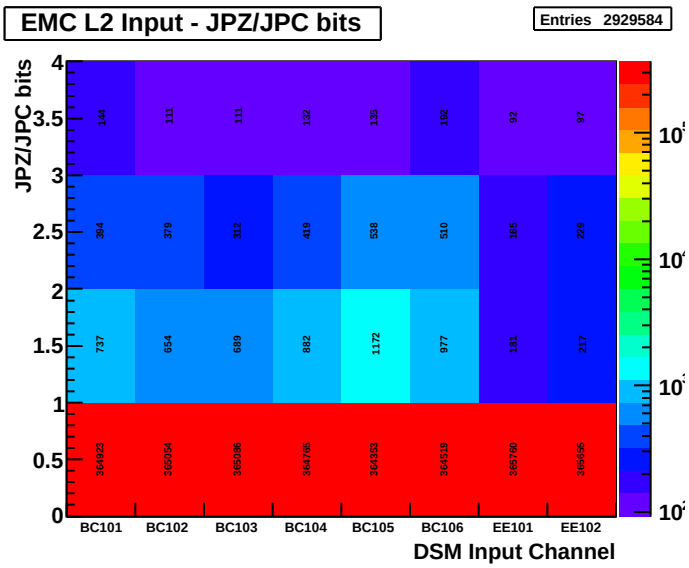

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

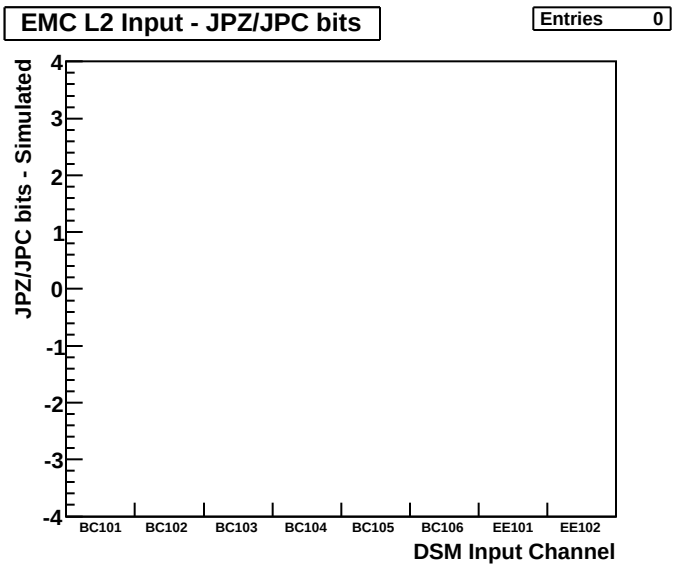

EMC L2 Input - JPX/JPA bits
Entries 2929584
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
261 200 171 141 130 157 82 115
617 596 486 419 437 380 172 254
1060 1087 917 846 783 676 212 155
364260 364315 364424 364792 364948 364985 365732 365674

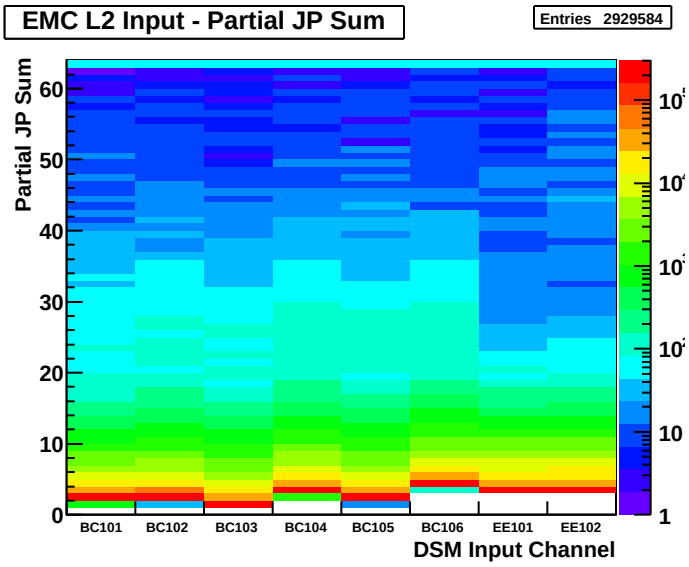

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

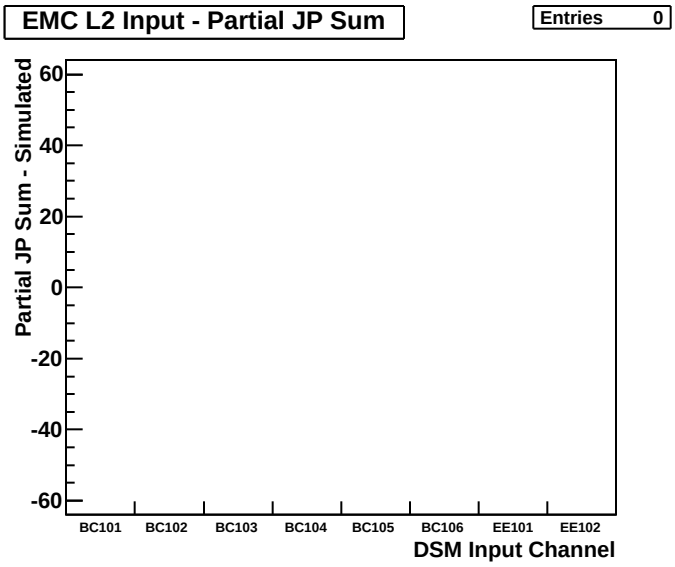

EMC L2 Input - JPY/JPB bits
Entries 2929584
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
194 109 135 131 130 158 83 111
501 355 387 402 483 495 143 254
925 720 799 794 1089 872 142 169
364578 364014 364877 364871 364496 364673 365830 365564

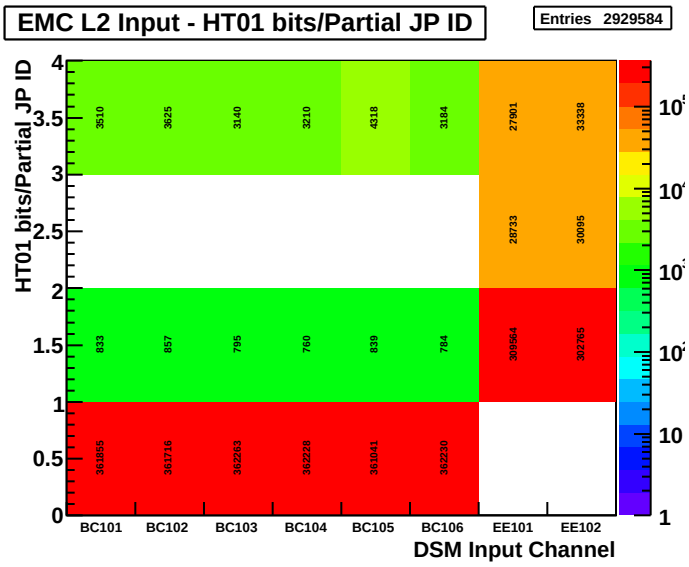

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

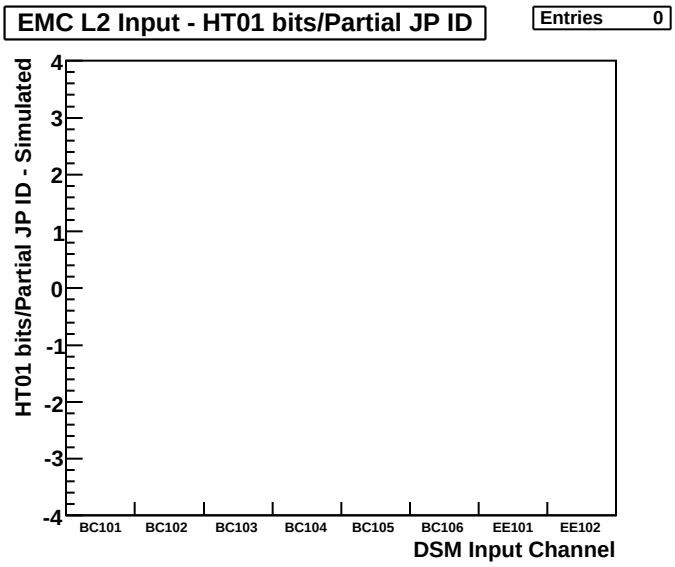

EMC L2 Input - JPZ/JPC bits
Entries 2929584
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
146 111 111 132 135 192 92 97
384 379 312 419 538 510 165 229
737 654 689 882 1172 977 181 217
364923 365054 365086 364765 364353 364519 365760 365655

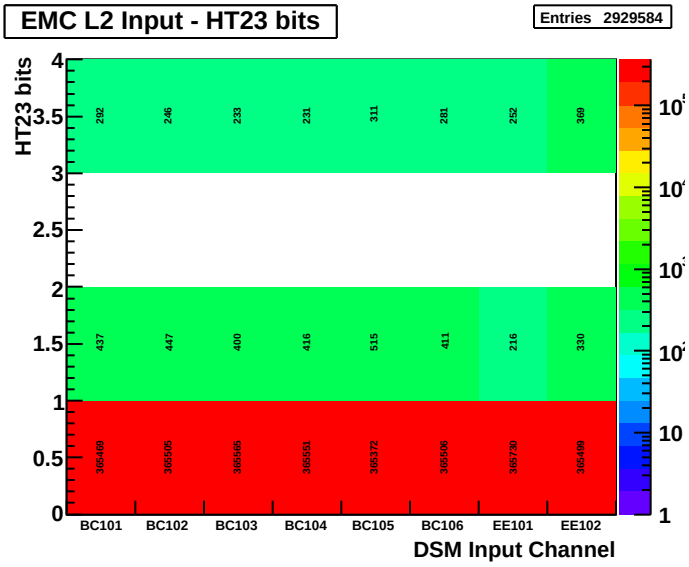

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

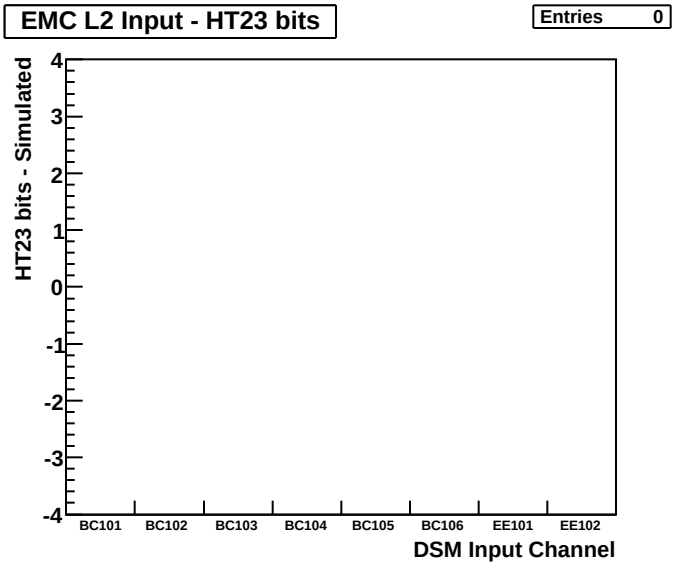

EMC L2 Input - Partial JP Sum
Entries 2929584
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

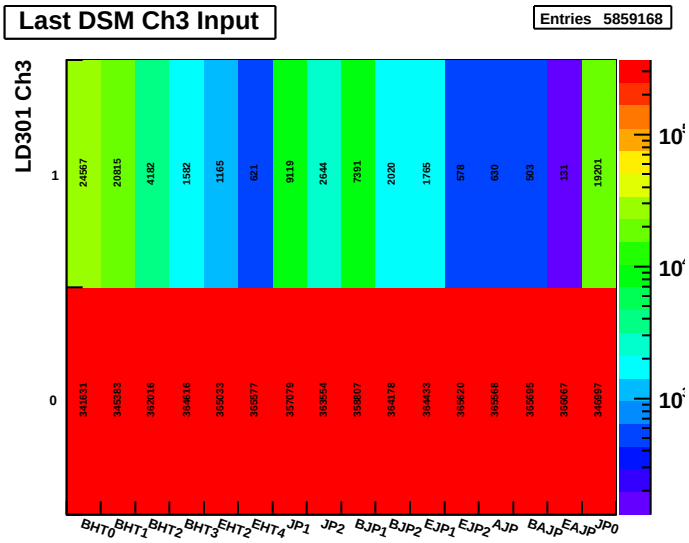

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

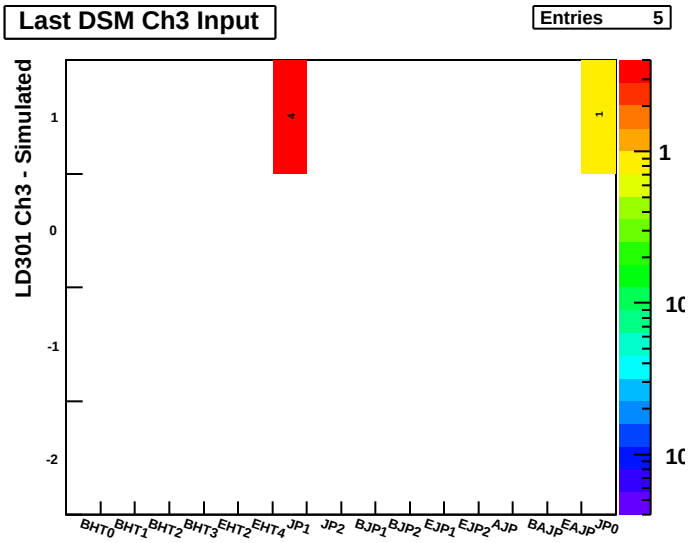

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2929584
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

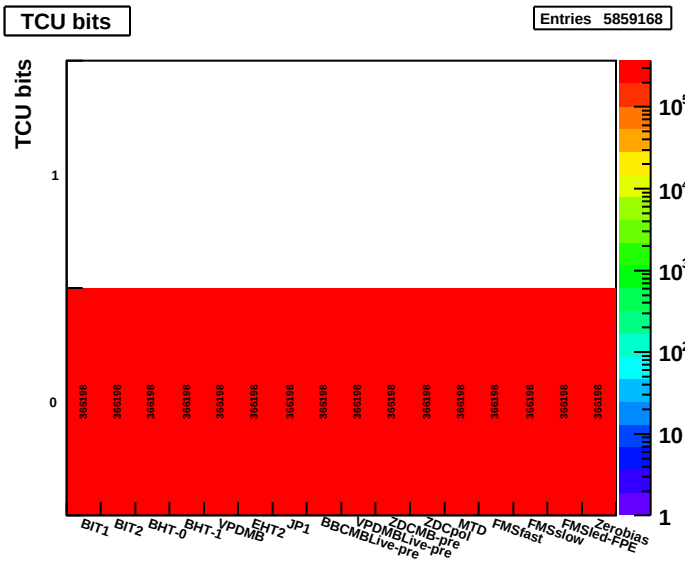

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

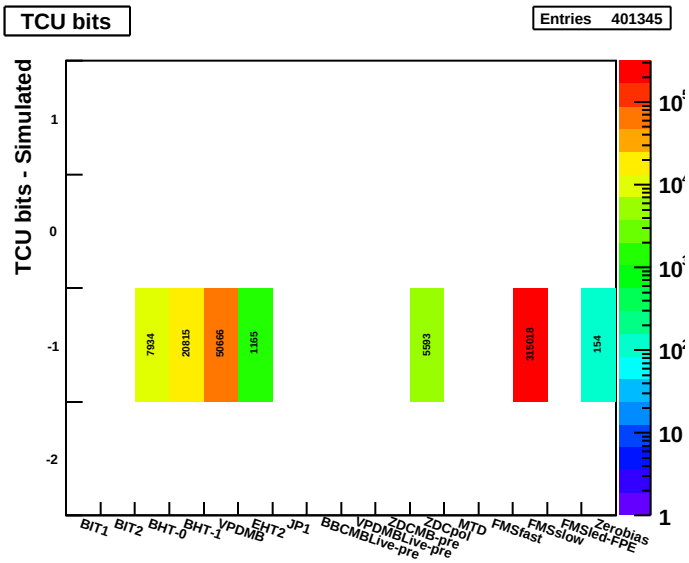

EMC L2 Input - HT23 bits
Entries 2929584
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

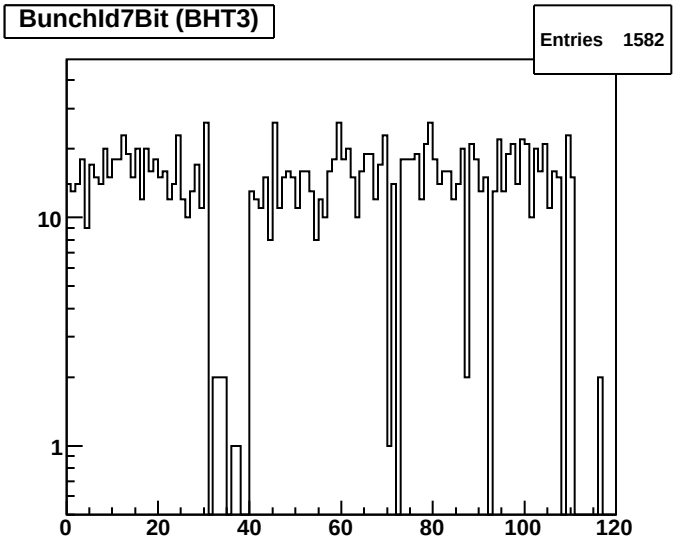

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

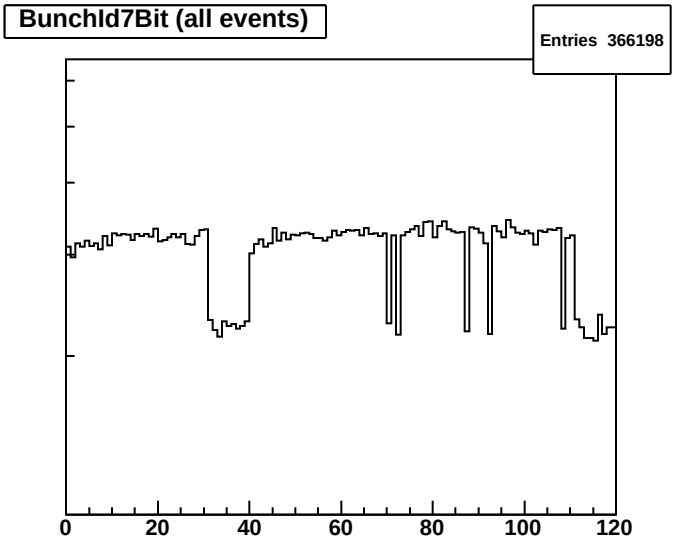

Last DSM Ch3 Input
Entries 5859168
LD301 Ch3
24567 20815 4182 1582 1165 621 9119 2644 7391 2020 1765 578 630 503 131 19201
341631 345383 362016 364616 365033 365577 357079 363554 358807 364178 364433 365520 365568 365595 366067 346997
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

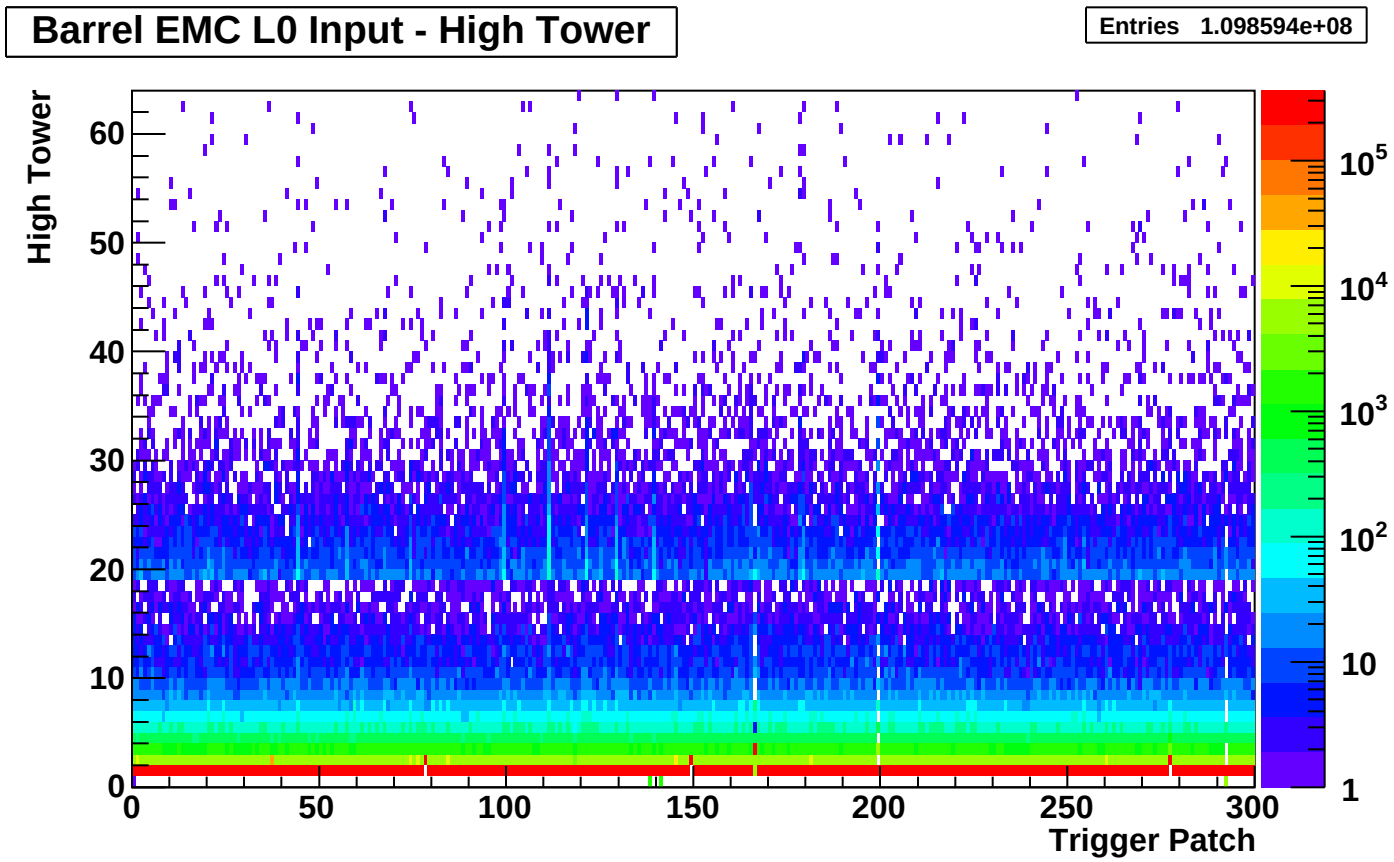

Last DSM Ch3 Input
Entries 5
LD301 Ch3 - Simulated
4
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

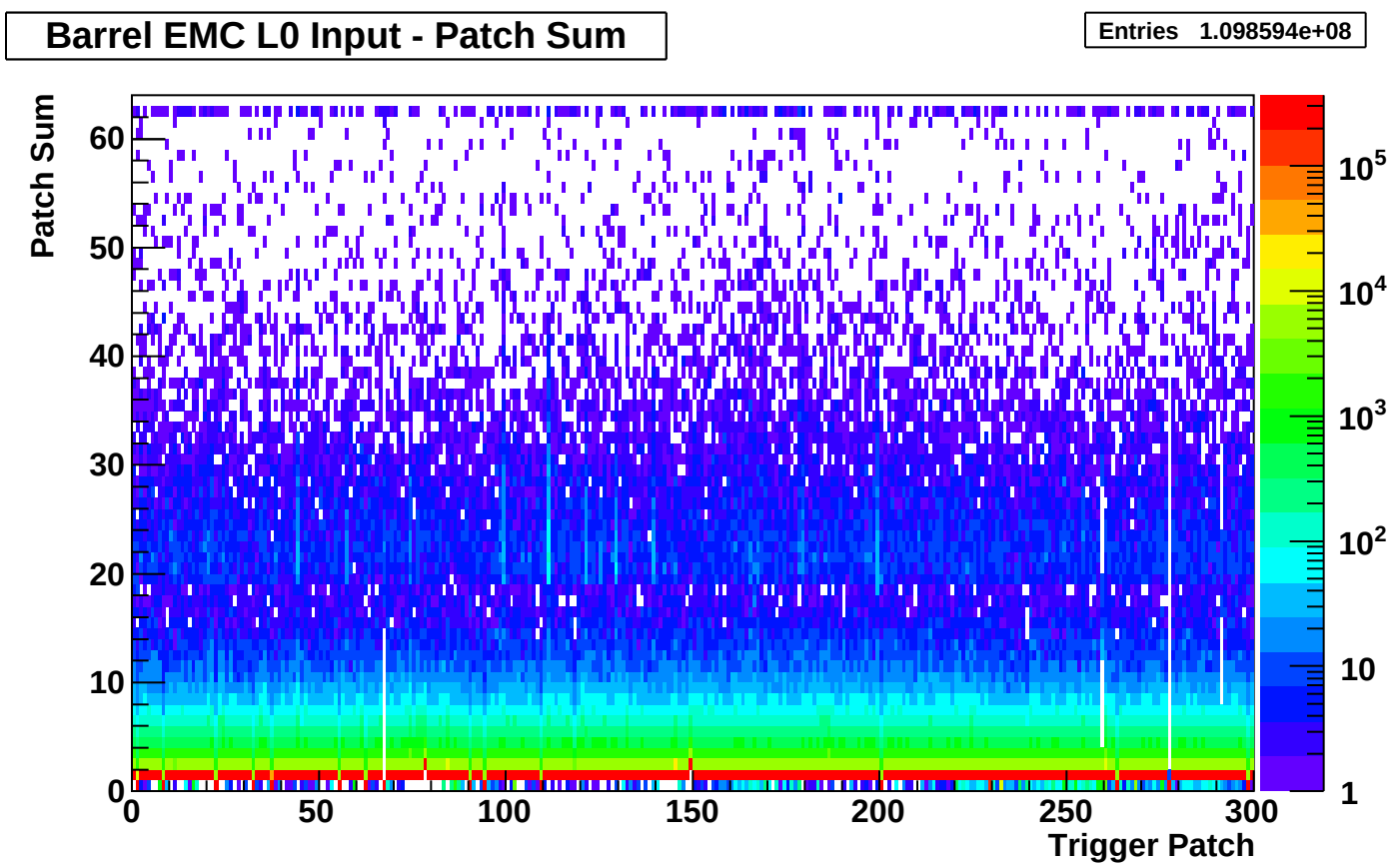

TCU bits
Entries 5859168
TCU bits
366198
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

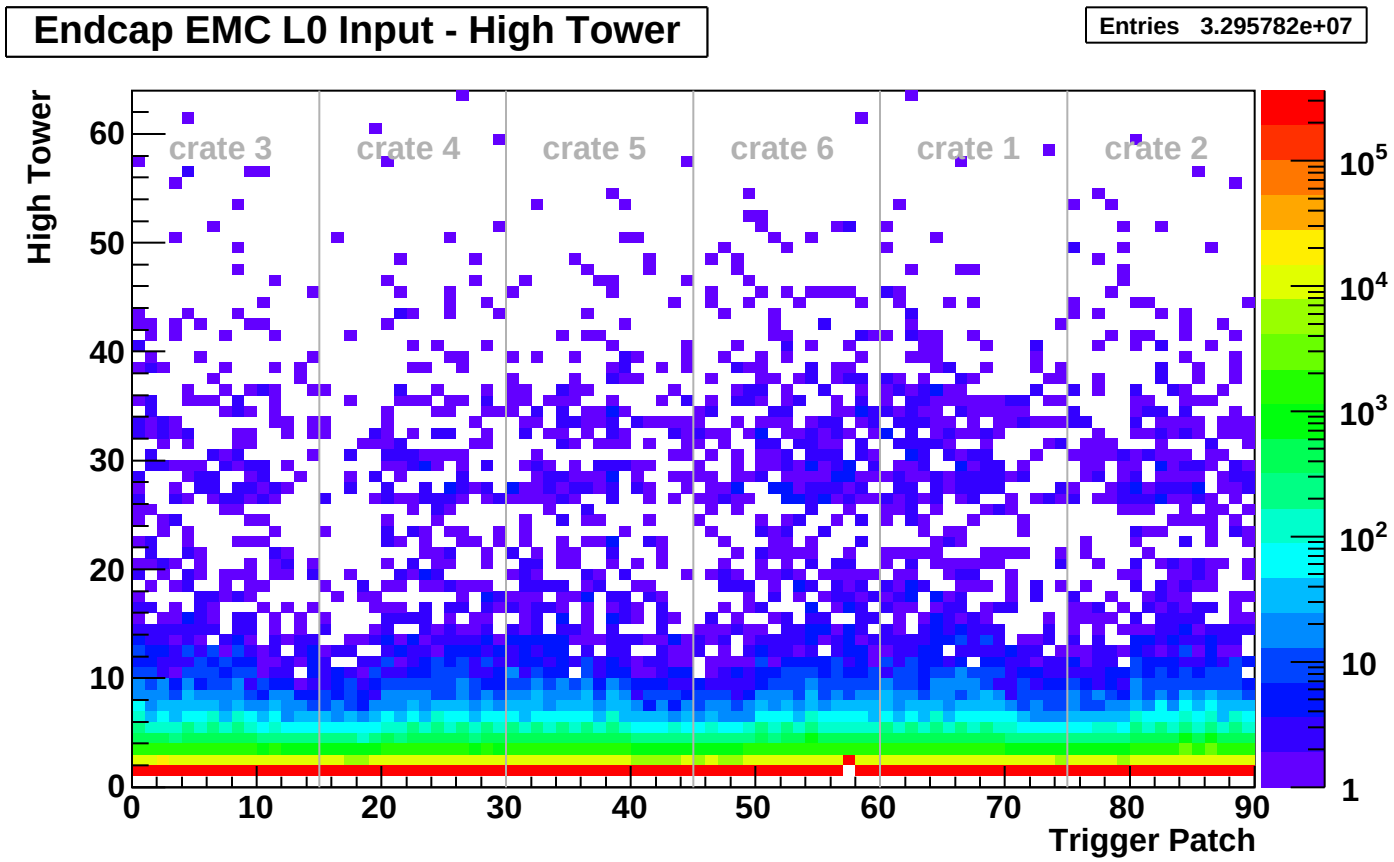

TCU bits
Entries 401345
TCU bits - Simulated
7934 20815 50666 1165 5993 315018 154
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

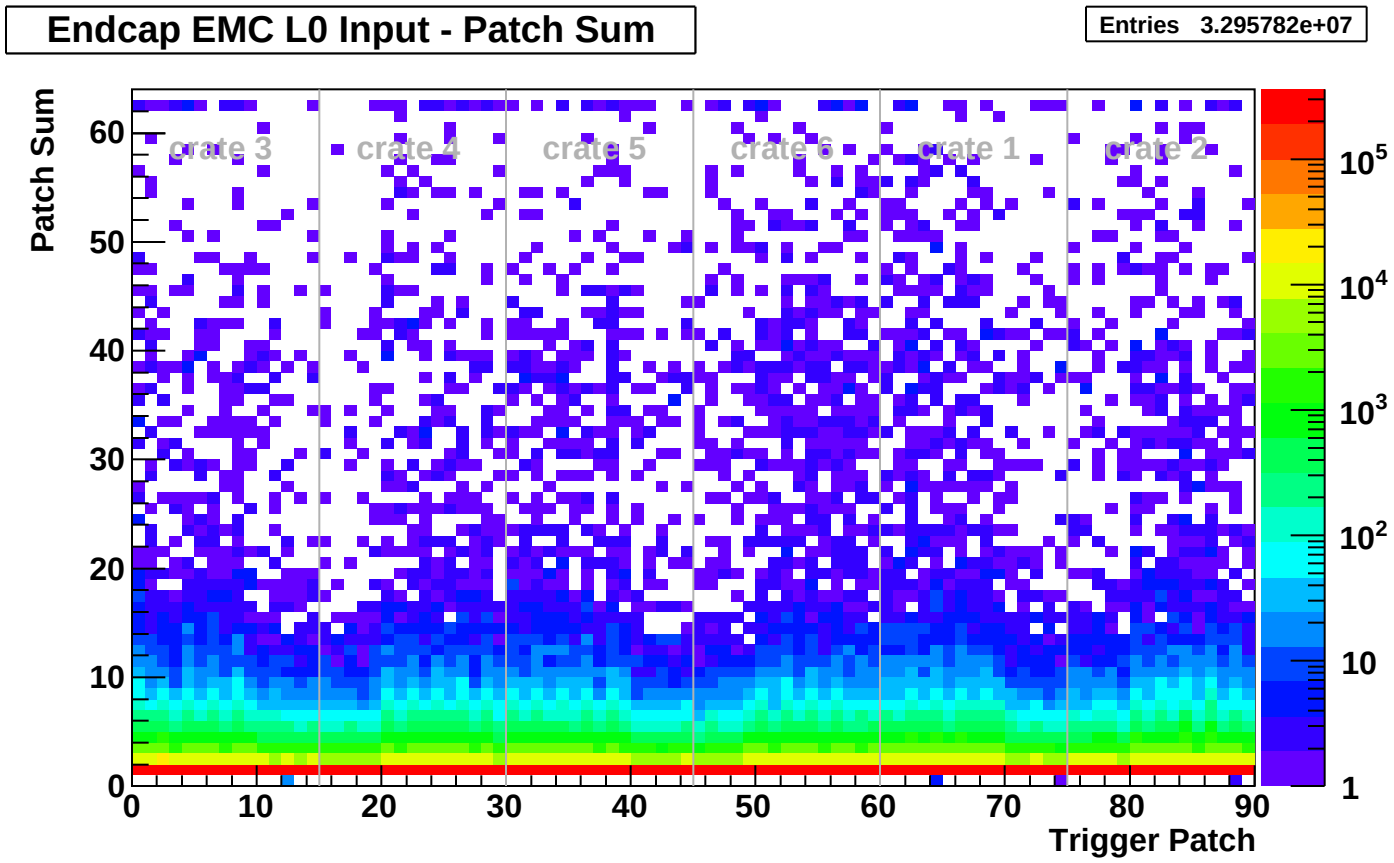

BunchId7Bit (BHT3)
Entries 1582

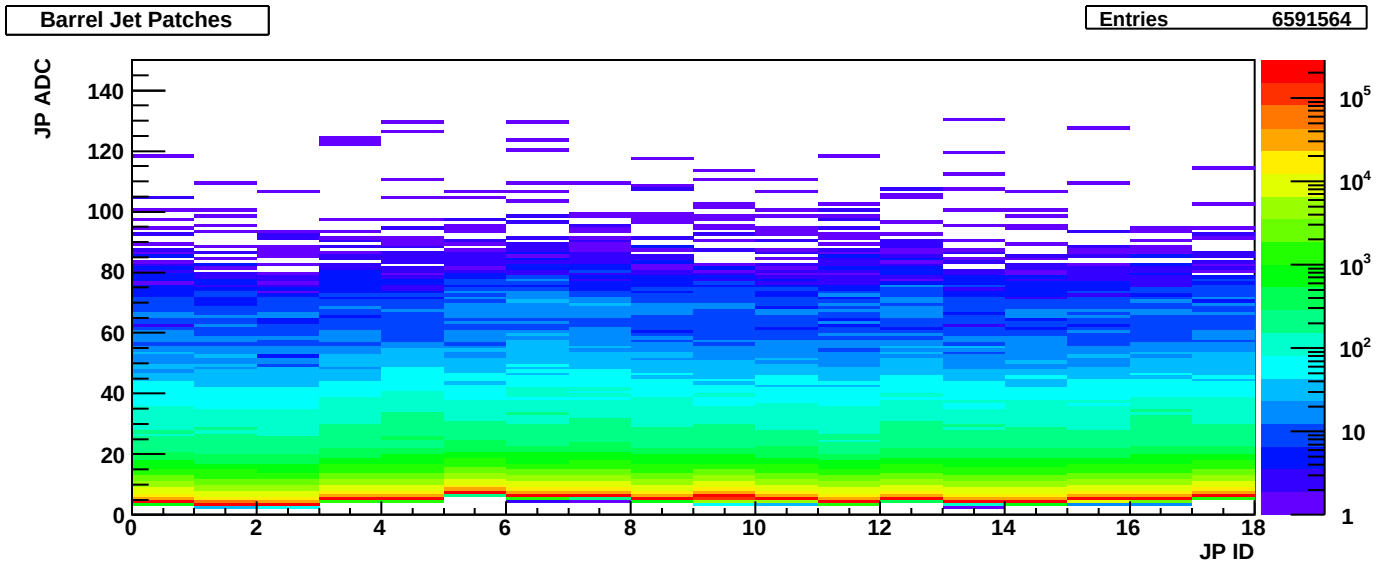

BunchId7Bit (all events)
Entries 366198

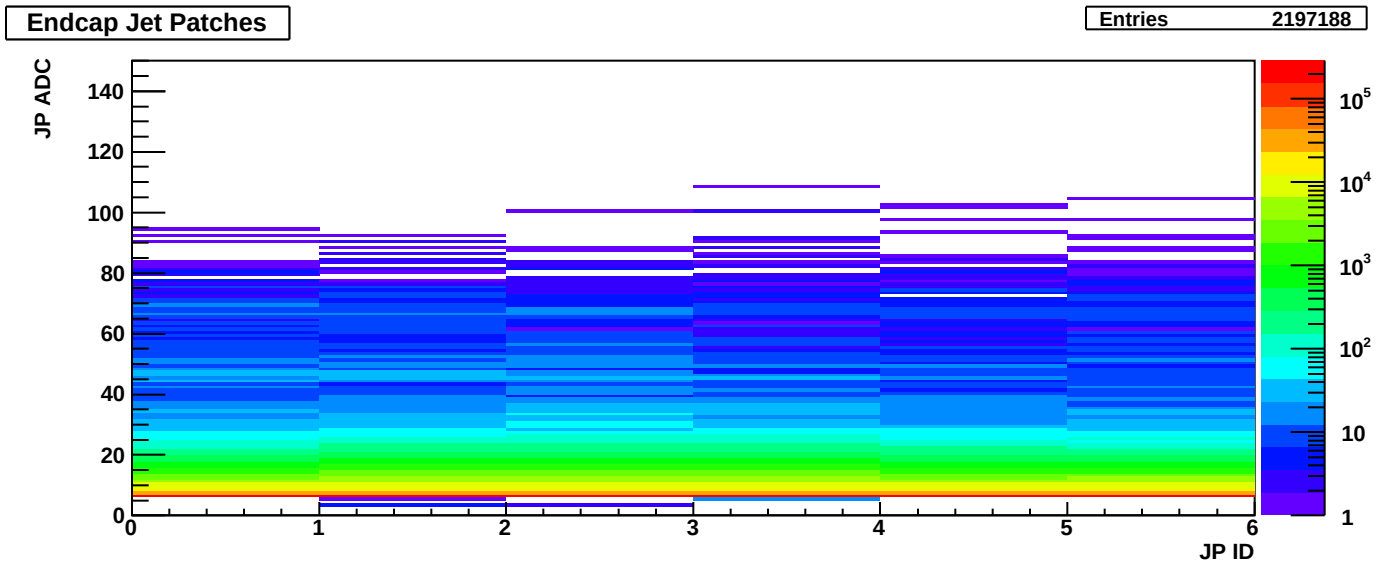

Barrel EMC L0 Input - High Tower
Entries 1.098594e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

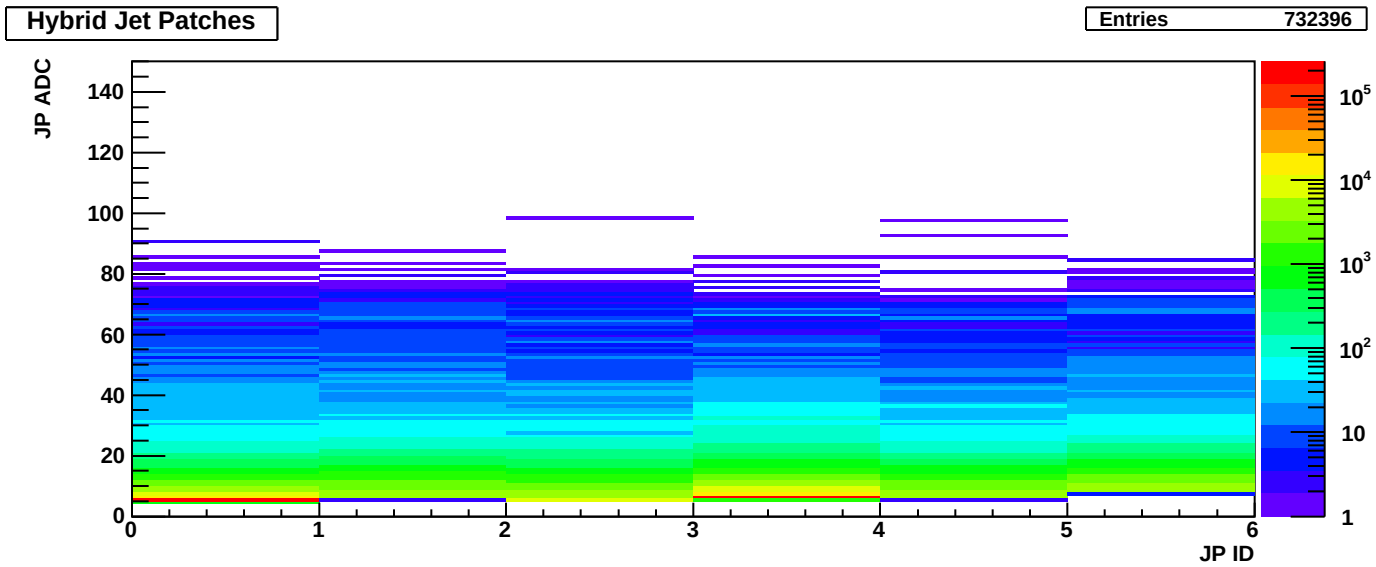

Barrel EMC L0 Input - Patch Sum
Entries 1.098594e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

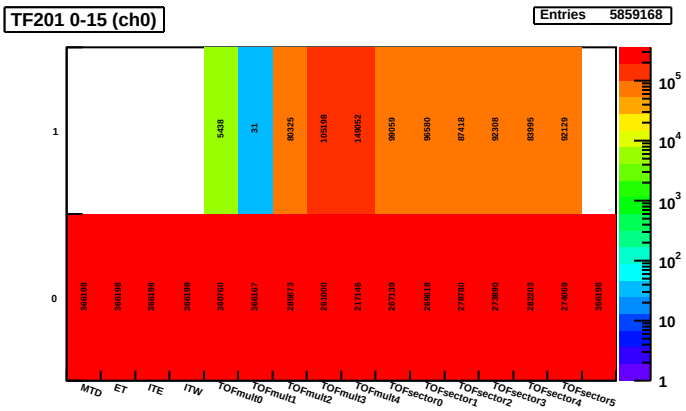

Endcap EMC L0 Input - High Tower
Entries 3.295782e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

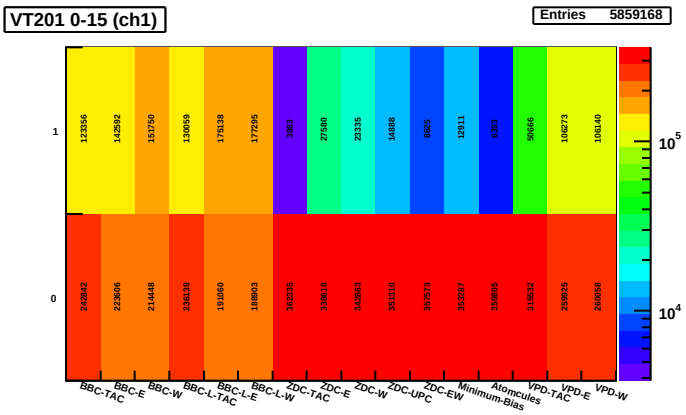

Endcap EMC L0 Input - Patch Sum
Entries 3.295782e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

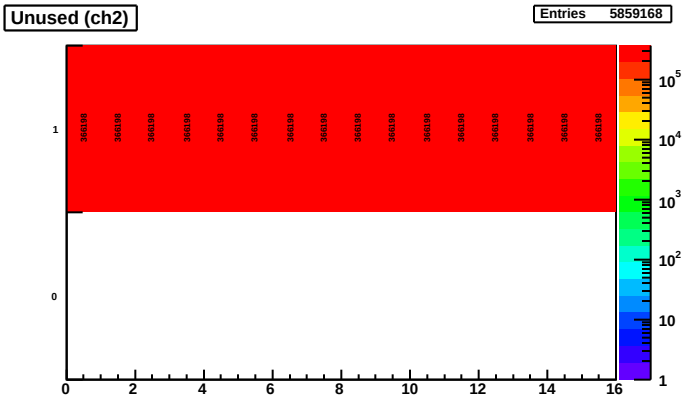

Barrel Jet Patches
Entries 6591564
JP ADC
JP ID

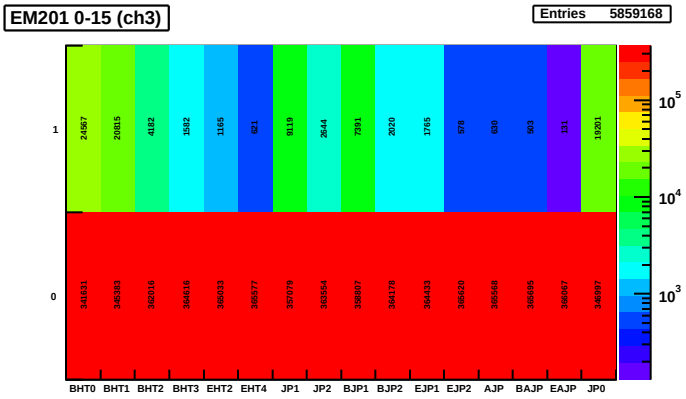

Endcap Jet Patches
Entries 2197188
JP ADC
JP ID

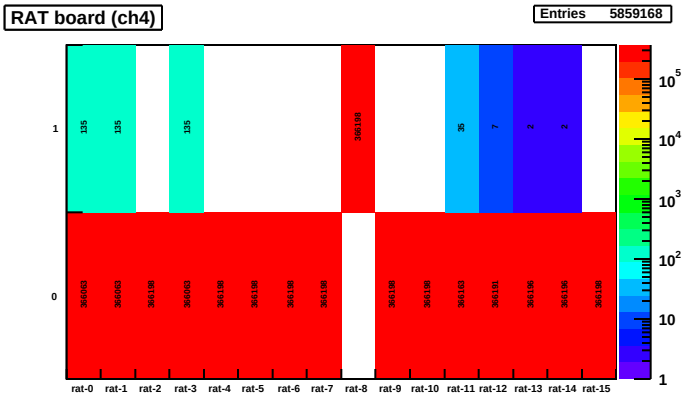

Hybrid Jet Patches
Entries 732396
JP ADC
JP ID

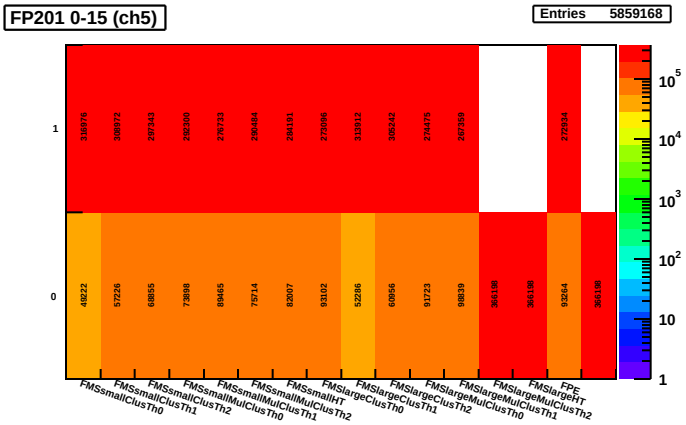

TF201 0-15 (ch0)
Entries 5859168
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

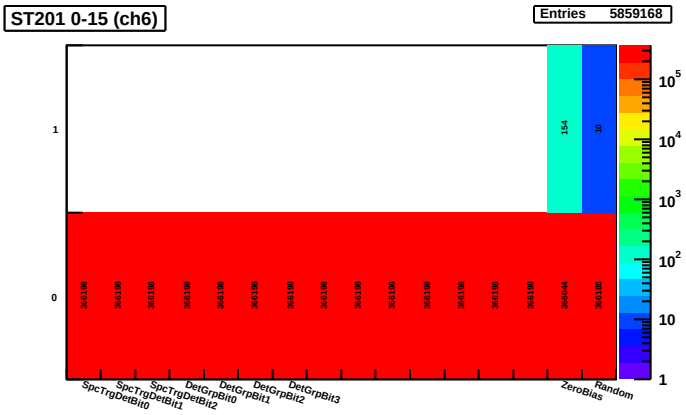

VT201 0-15 (ch1)
Entries 5859168
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

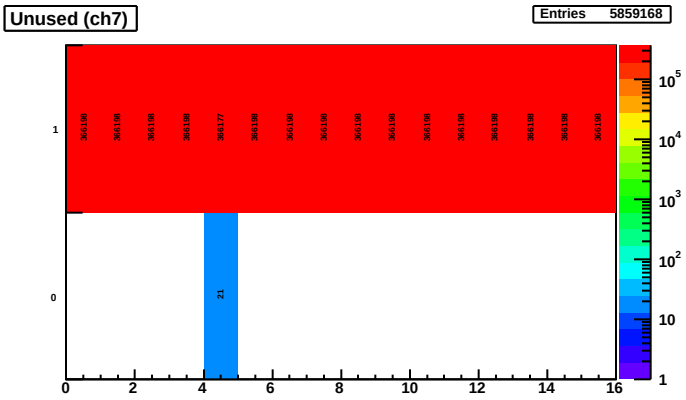

Unused (ch2)
Entries 5859168

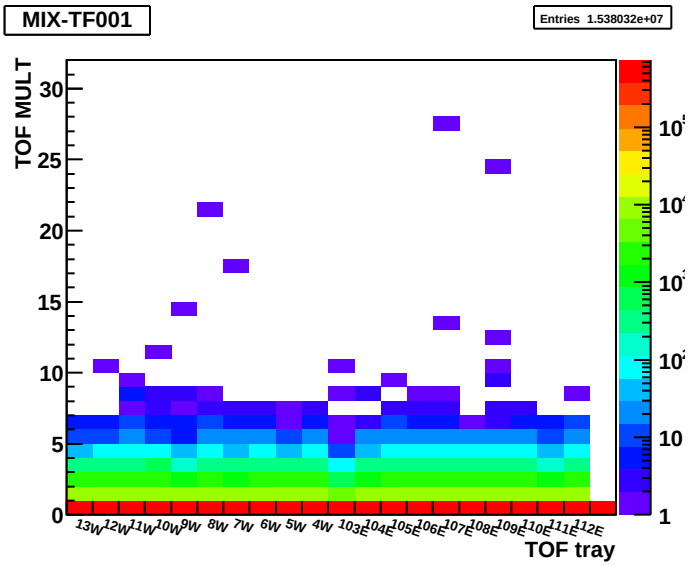

EM201 0-15 (ch3)
Entries 5859168
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

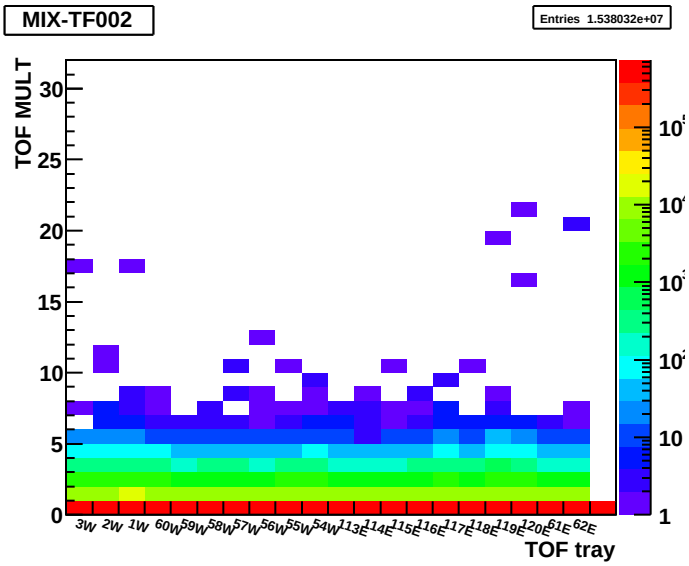

RAT board (ch4)
Entries 5859168
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

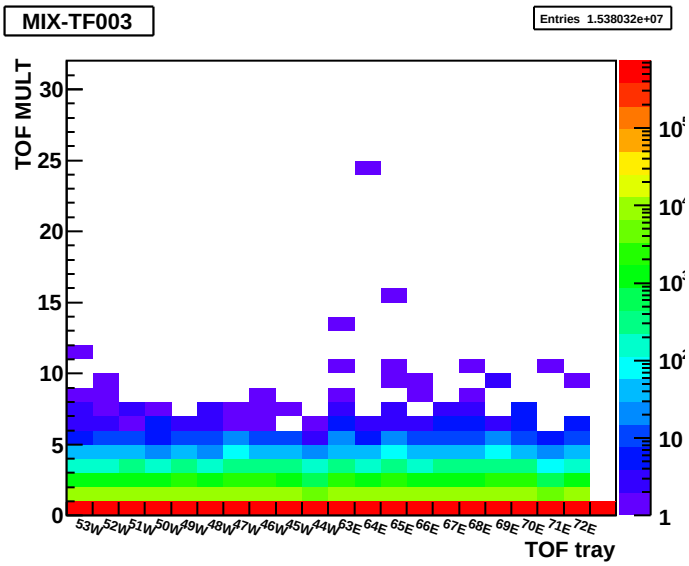

FP201 0-15 (ch5)
Entries 5859168
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

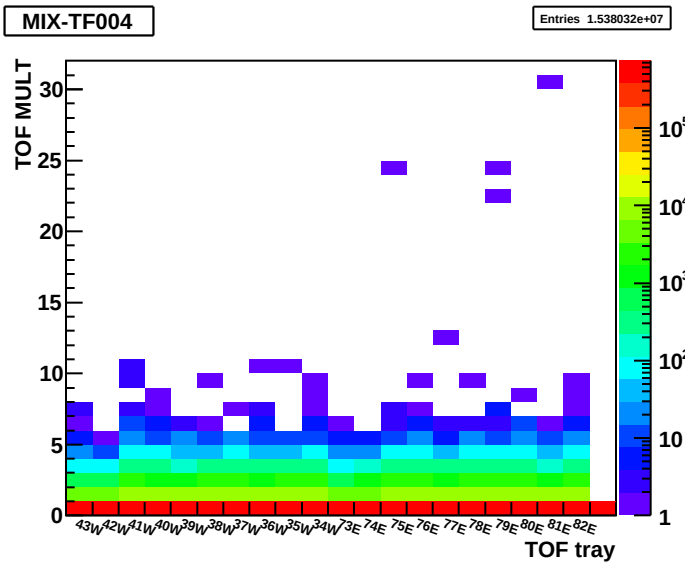

ST201 0-15 (ch6)
Entries 5859168
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

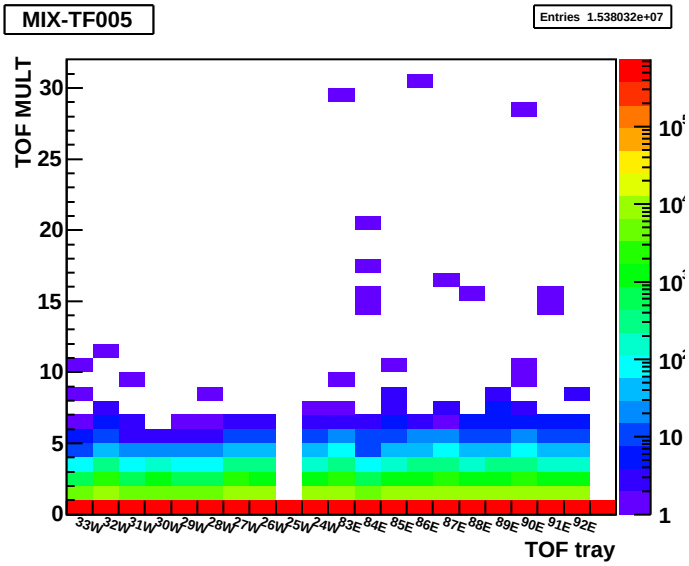

Unused (ch7)
Entries 5859168

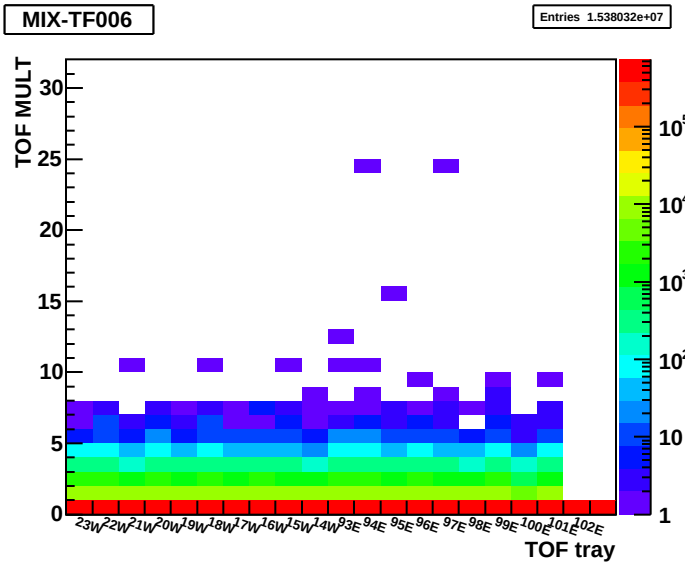

MIX-TF001
Entries 1.538032e+07
TOF MULT
TOF tray

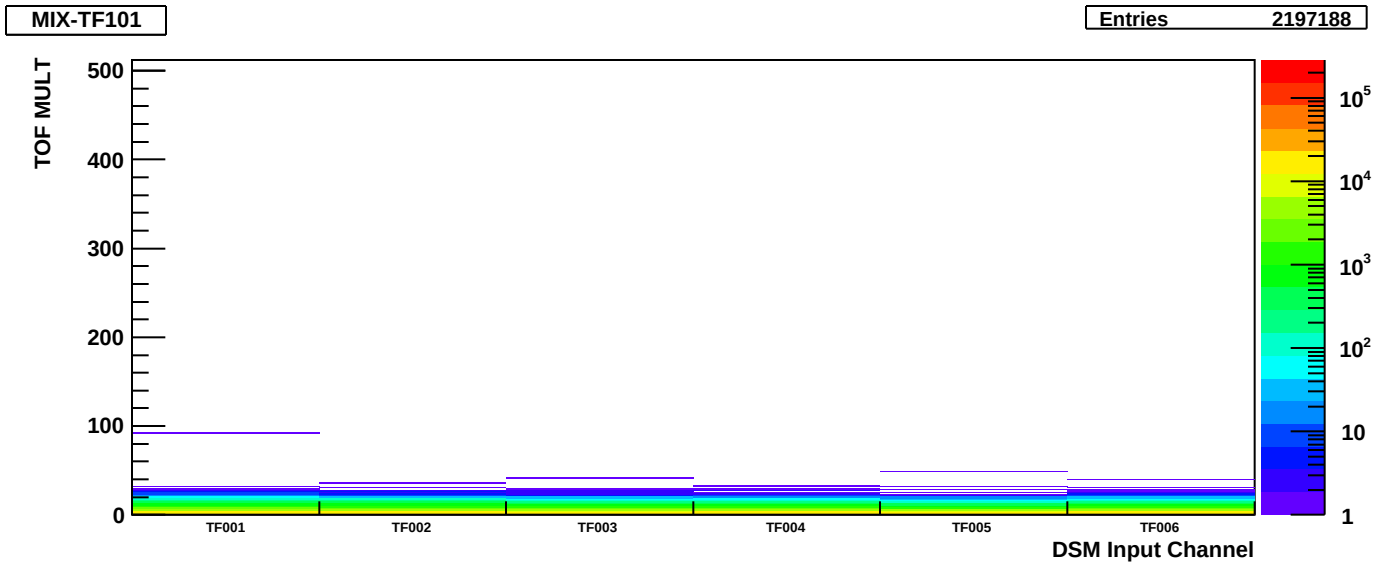

MIX-TF002
Entries 1.538032e+07
TOF MULT
TOF tray

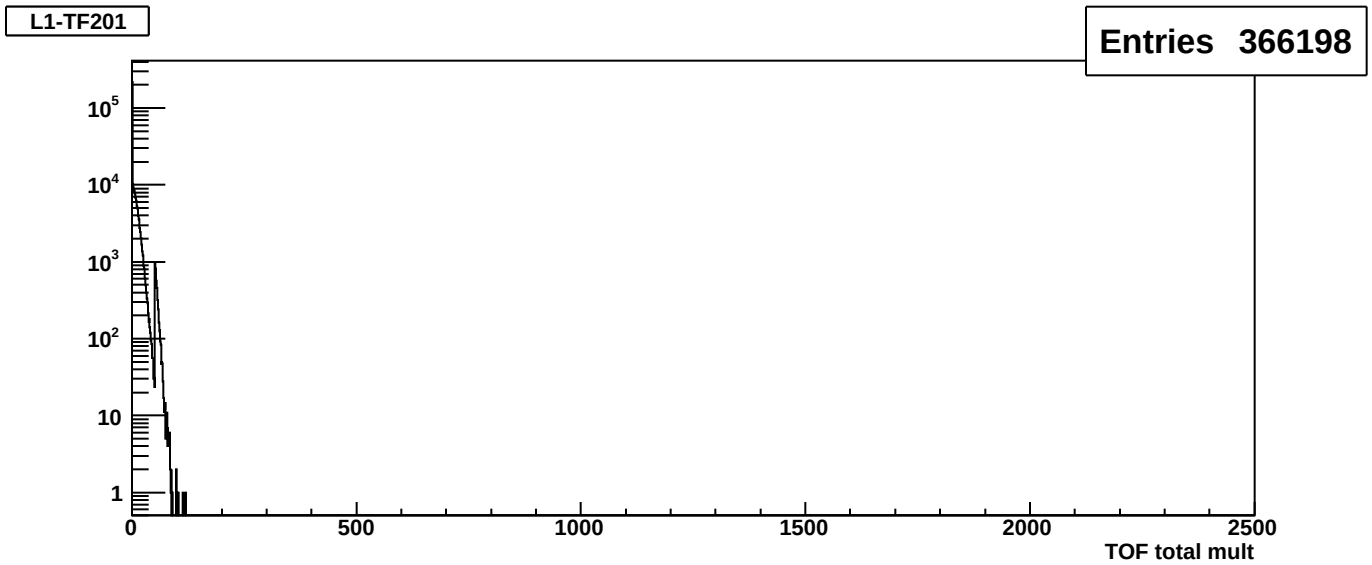

MIX-TF003
Entries 1.538032e+07
TOF MULT
TOF tray

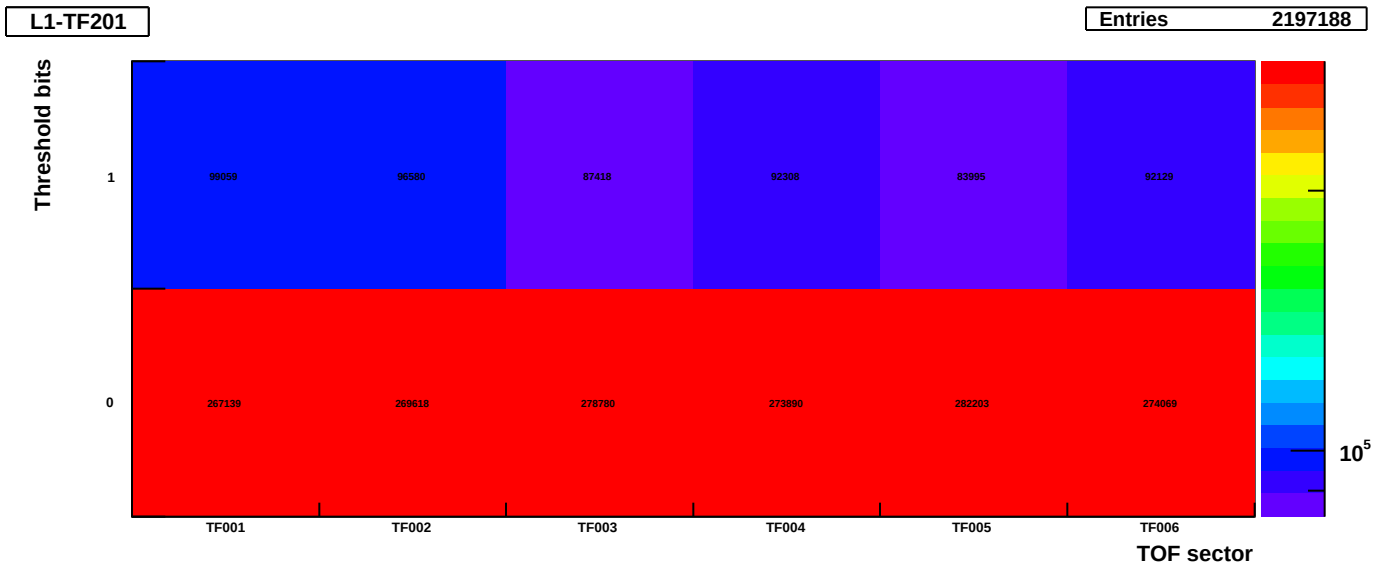

MIX-TF004
Entries 1.538032e+07
TOF MULT
TOF tray

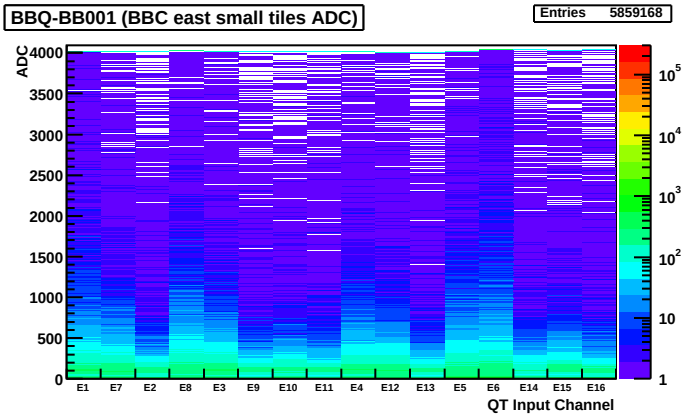

MIX-TF005
Entries 1.538032e+07
TOF MULT
TOF tray

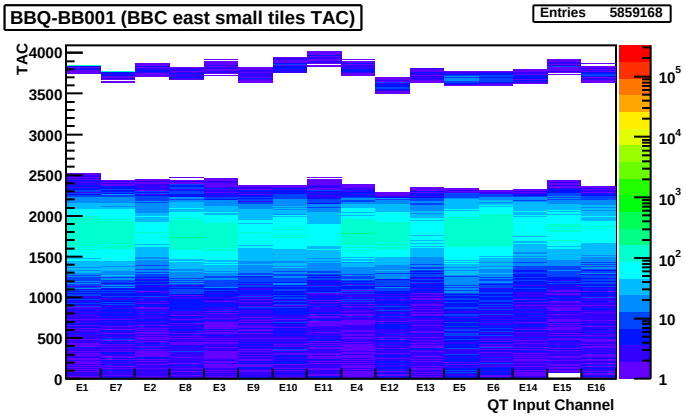

MIX-TF006
Entries 1.538032e+07
TOF MULT
TOF tray

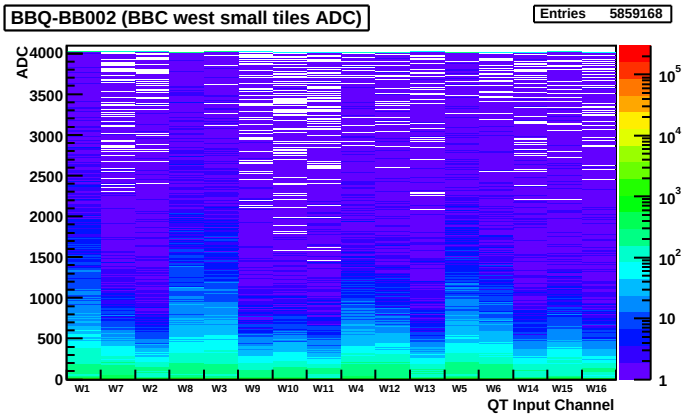

MIX-TF101
Entries 2197188
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

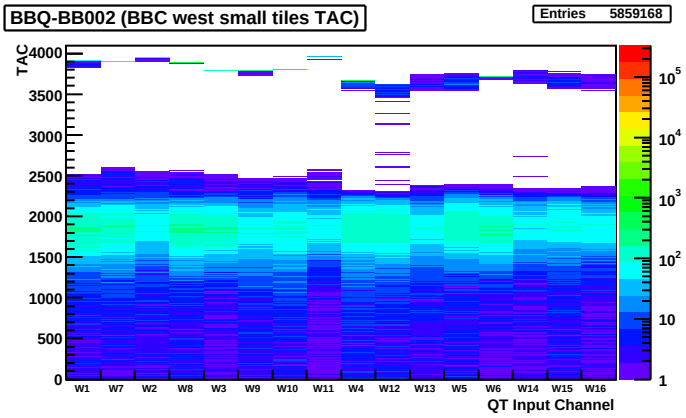

L1-TF201
Entries 366198
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

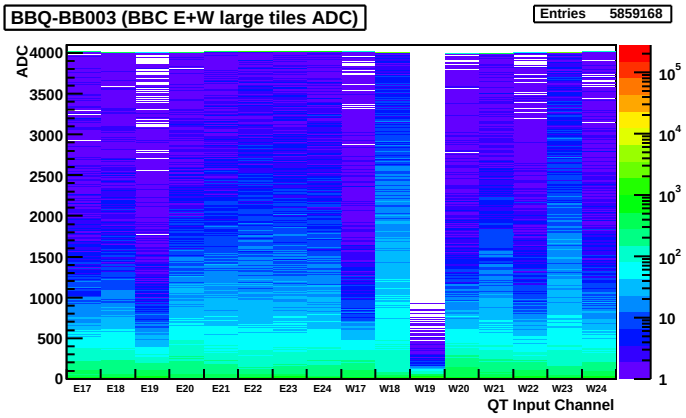

L1-TF201
Entries 2197188
Threshold bits
1
0
99059
96580
87418
92308
83995
92129
267139
269618
278780
273890
282203
274069
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

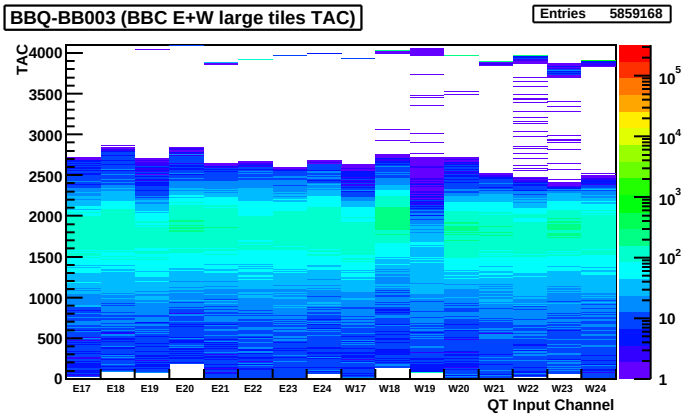

BBQ-BB001 (BBC east small tiles ADC)
Entries 5859168
ADC
QT Input Channel

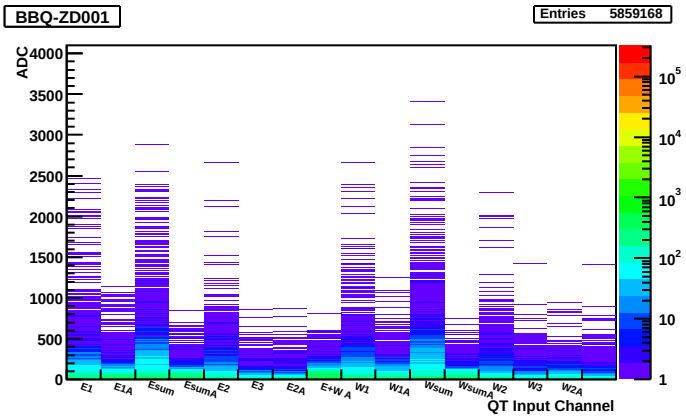

BBQ-BB001 (BBC east small tiles TAC)
Entries 5859168
TAC
QT Input Channel

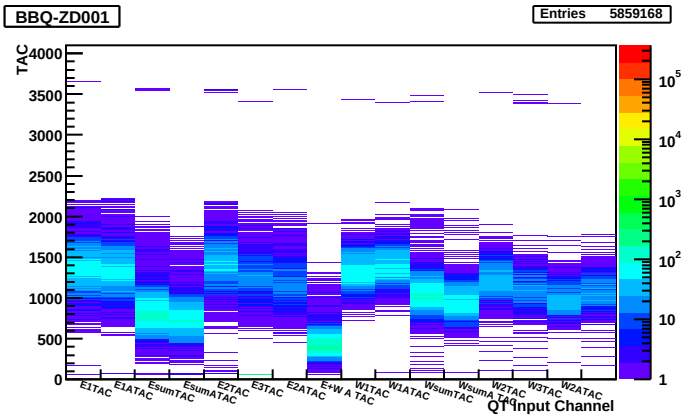

BBQ-BB002 (BBC west small tiles ADC)
Entries 5859168
ADC
QT Input Channel

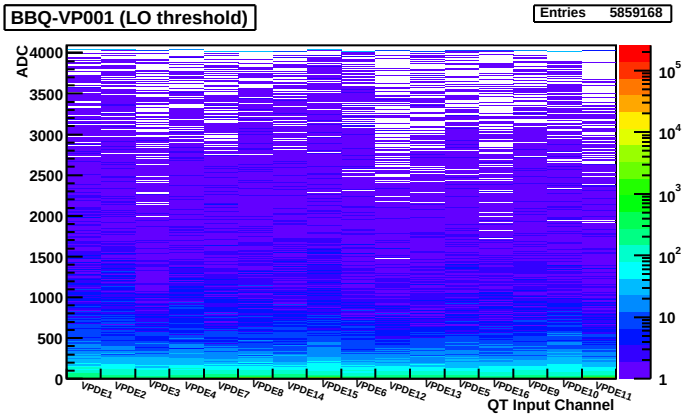

BBQ-BB002 (BBC west small tiles TAC)
Entries 5859168
TAC
QT Input Channel

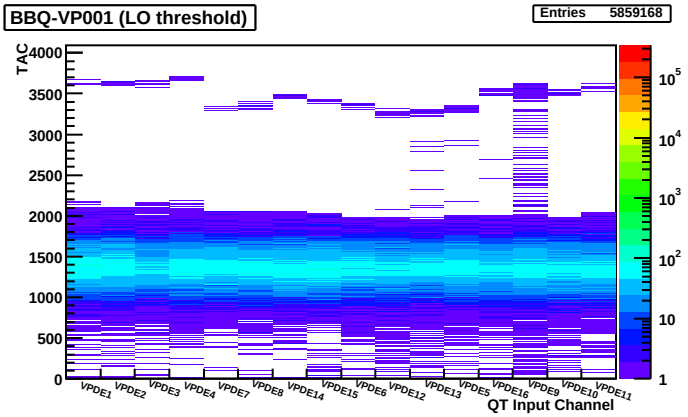

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5859168
ADC
QT Input Channel

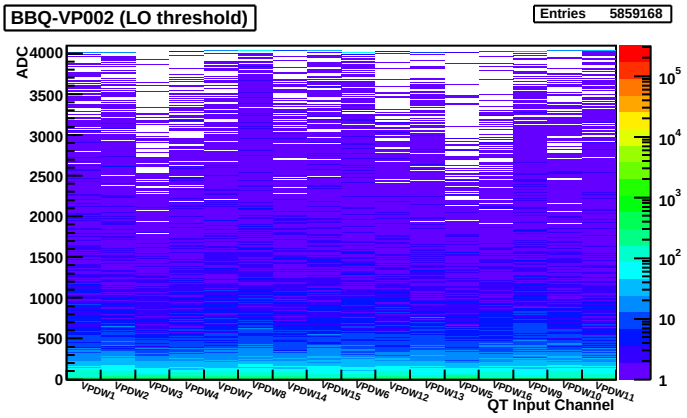

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5859168
TAC
QT Input Channel

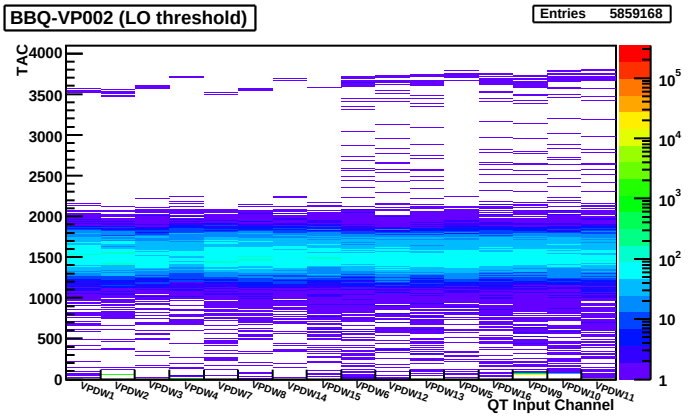

BBQ-ZD001
Entries 5859168
ADC
QT Input Channel

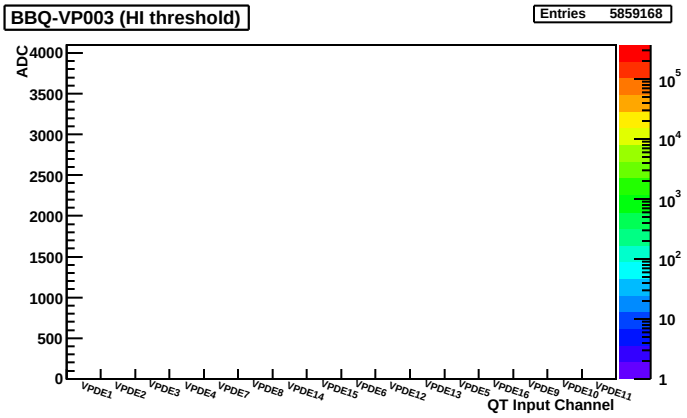

BBQ-ZD001
Entries 5859168
TAC
QT Input Channel

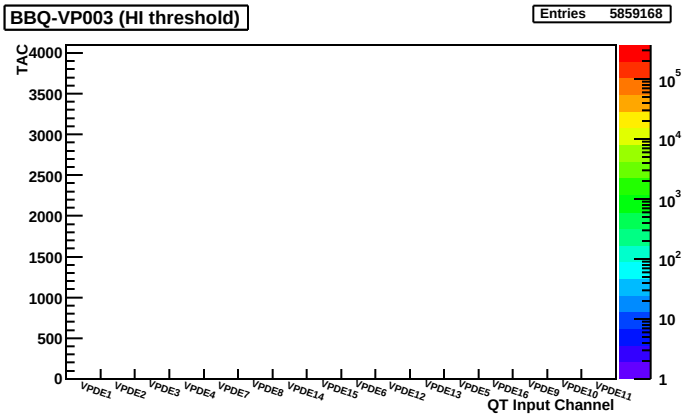

BBQ-VP001 (LO threshold)
Entries 5859168
ADC
QT Input Channel

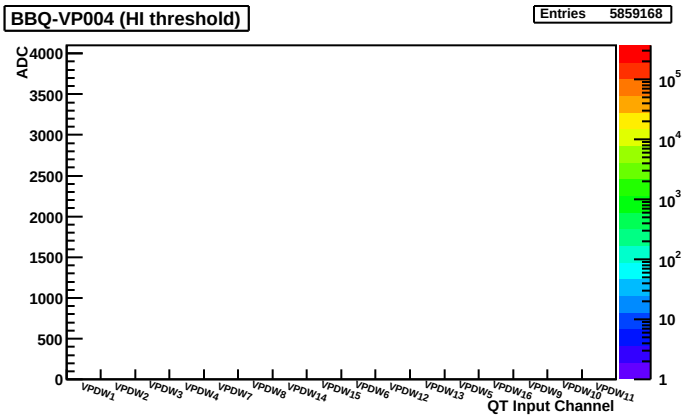

BBQ-VP001 (LO threshold)
Entries 5859168
TAC
QT Input Channel

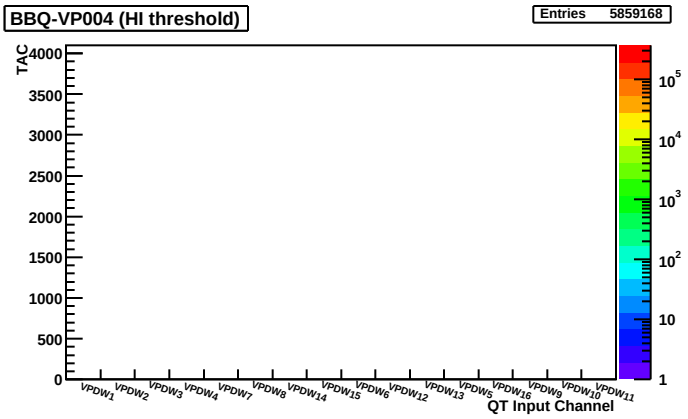

BBQ-VP002 (LO threshold)
Entries 5859168
ADC
QT Input Channel

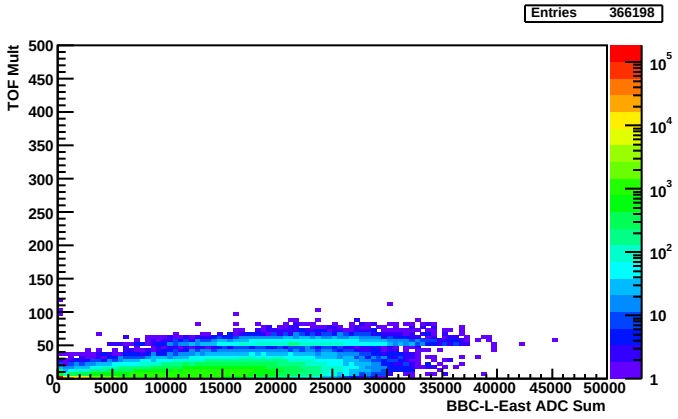

BBQ-VP002 (LO threshold)
Entries 5859168
TAC
QT Input Channel

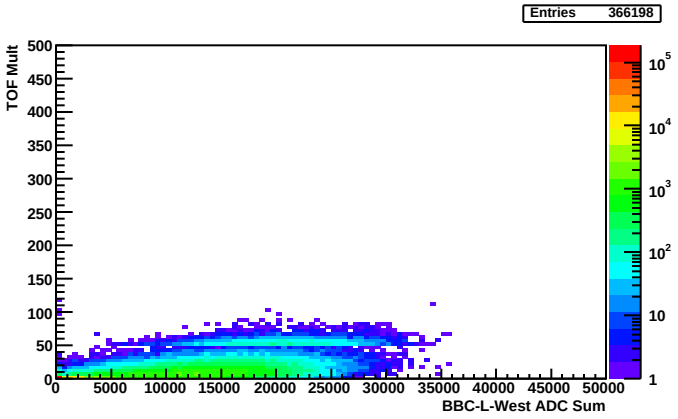

BBQ-VP003 (HI threshold)
Entries 5859168
ADC
QT Input Channel

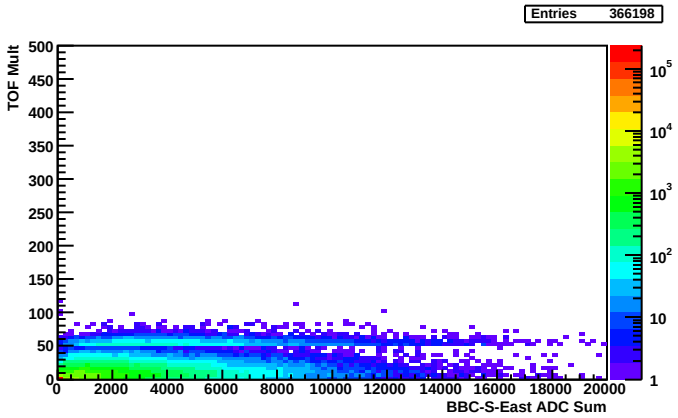

BBQ-VP003 (HI threshold)
Entries 5859168
TAC
QT Input Channel

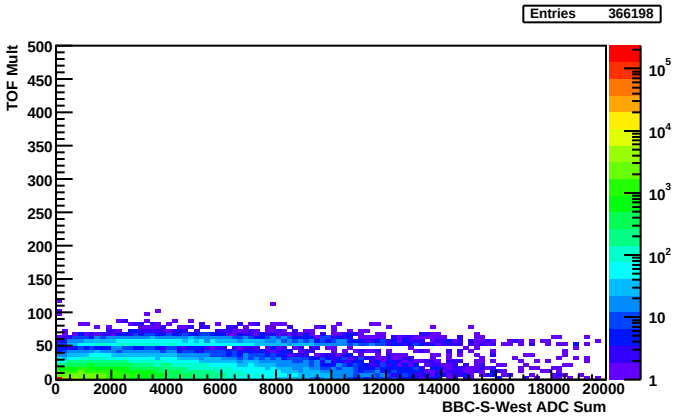

BBQ-VP004 (HI threshold)
Entries 5859168
ADC
QT Input Channel

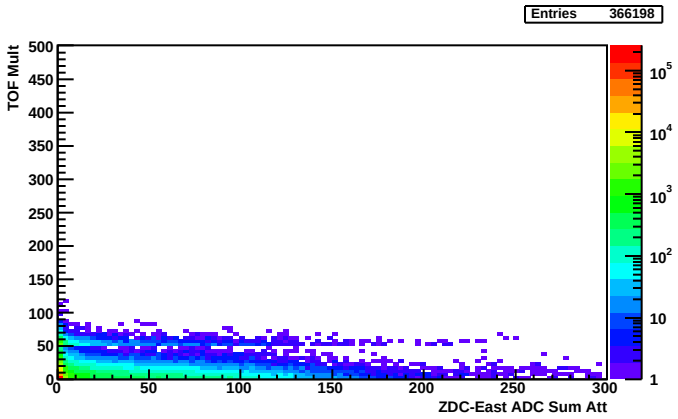

BBQ-VP004 (HI threshold)
Entries 5859168
TAC
QT Input Channel

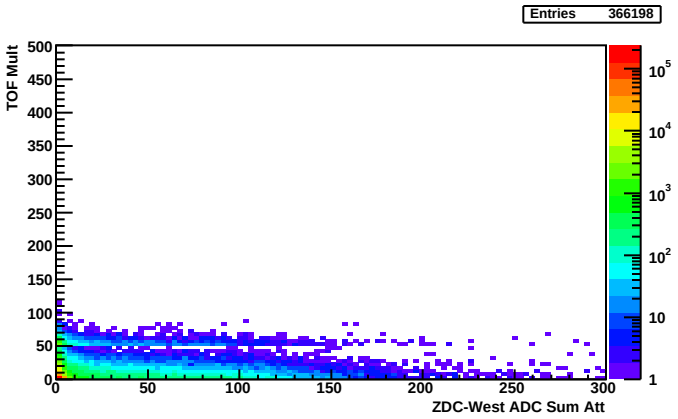

Entries 366198
TOF Mult
BBC-L-East ADC Sum

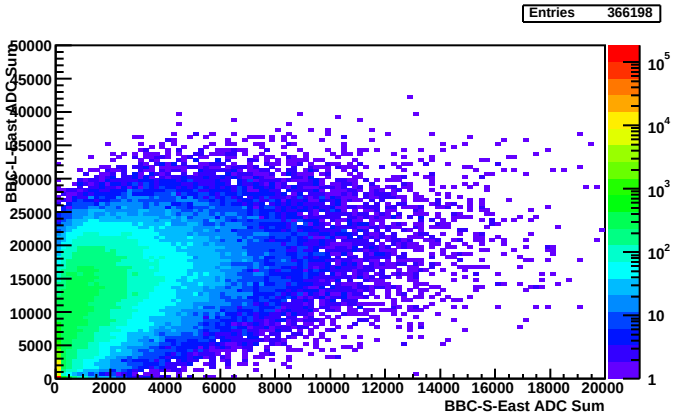

Entries 366198
TOF Mult
BBC-L-West ADC Sum

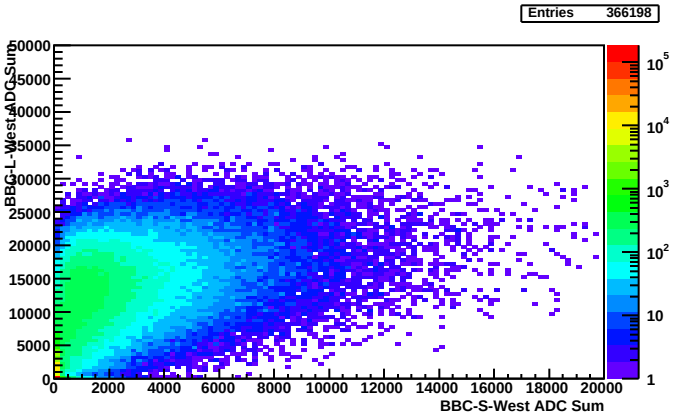

Entries 366198
TOF Mult
BBC-S-East ADC Sum

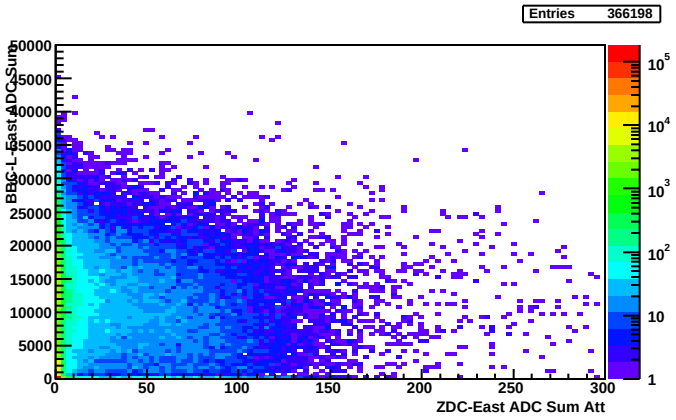

Entries 366198
TOF Mult
BBC-S-West ADC Sum

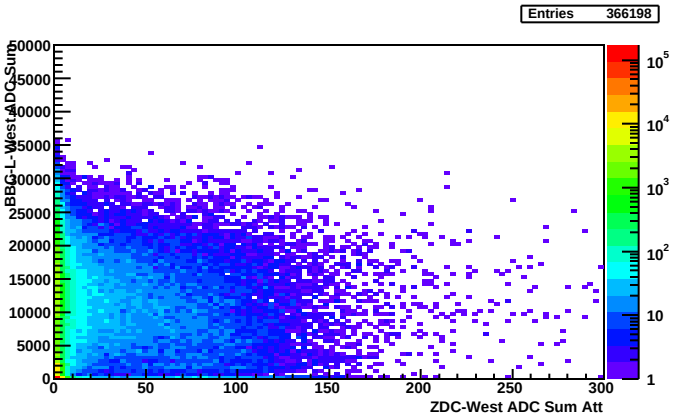

Entries 366198
TOF Mult
ZDC-East ADC Sum Att

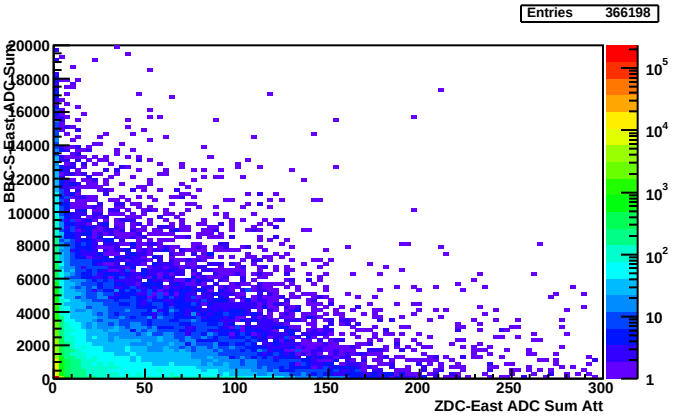

Entries 366198
TOF Mult
ZDC-West ADC Sum Att

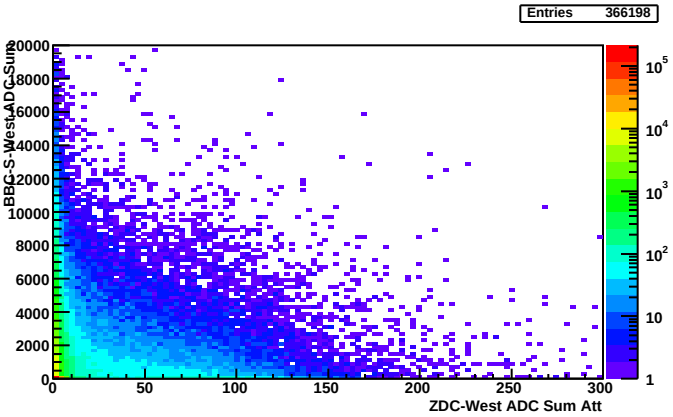

Entries 366198
BBC-L-East ADC Sum
BBC-S-East ADC Sum

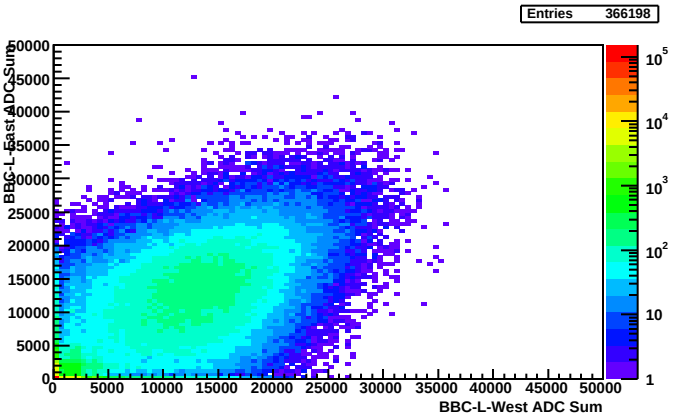

Entries 366198
BBC-L-West ADC Sum
BBC-S-West ADC Sum

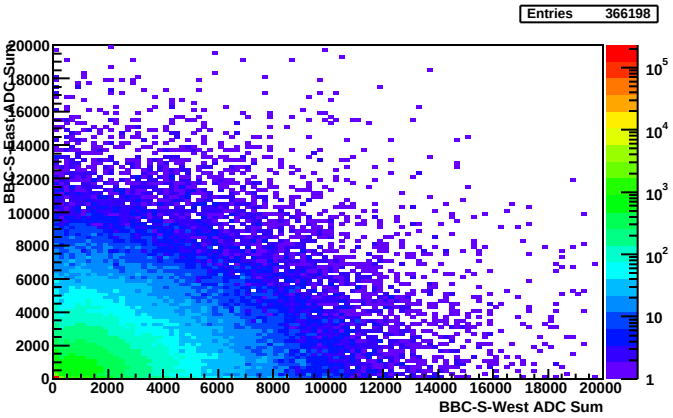

Entries 366198
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

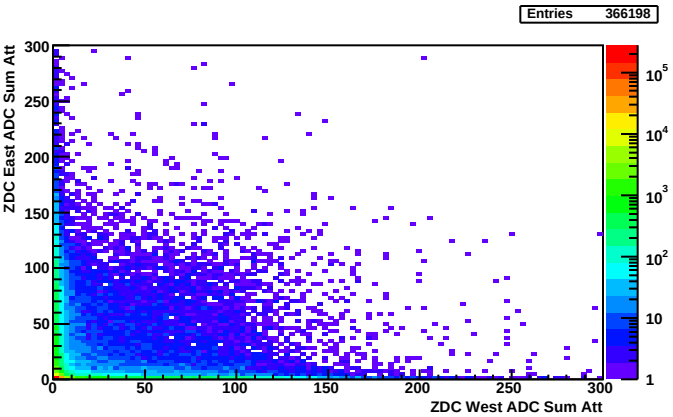

Entries 366198
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

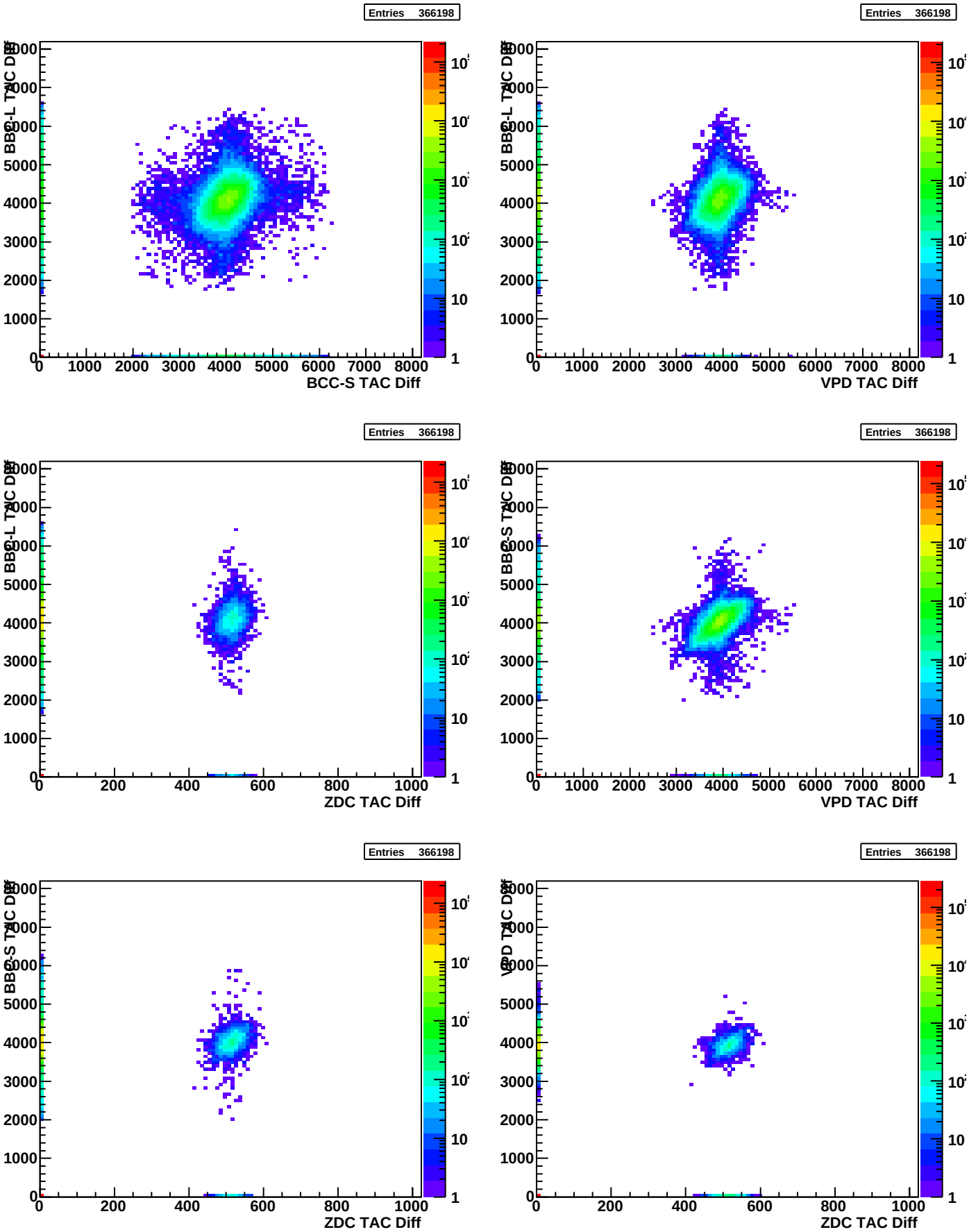

Entries 366198
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

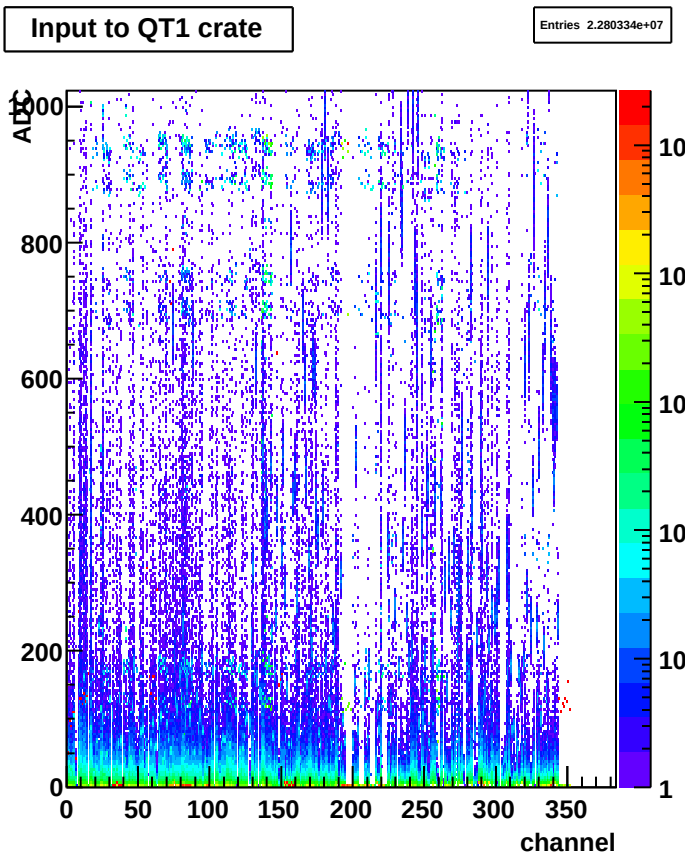

Entries 366198
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

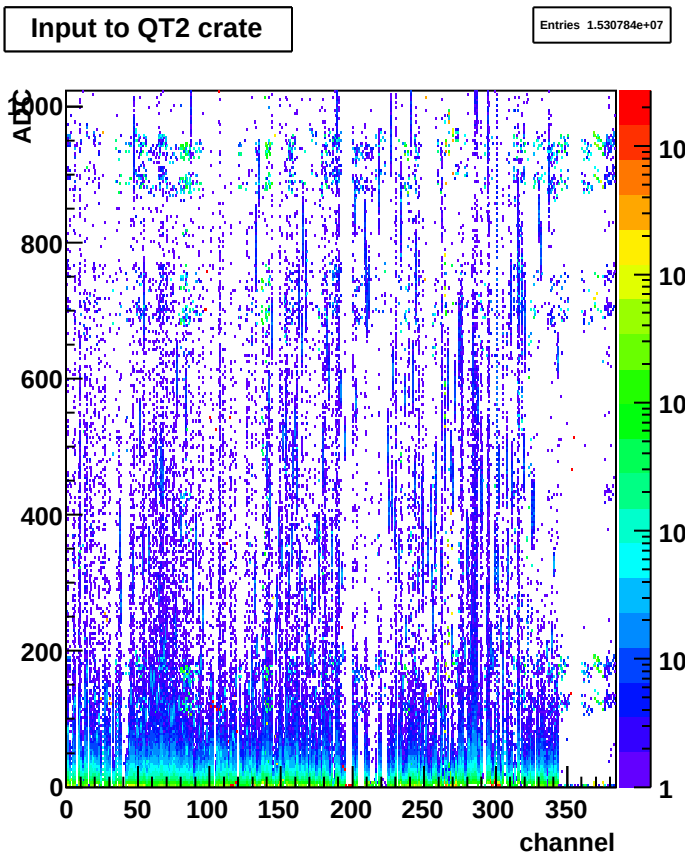

Entries 366198
BBC-L-East ADC Sum
BBC-L-West ADC Sum

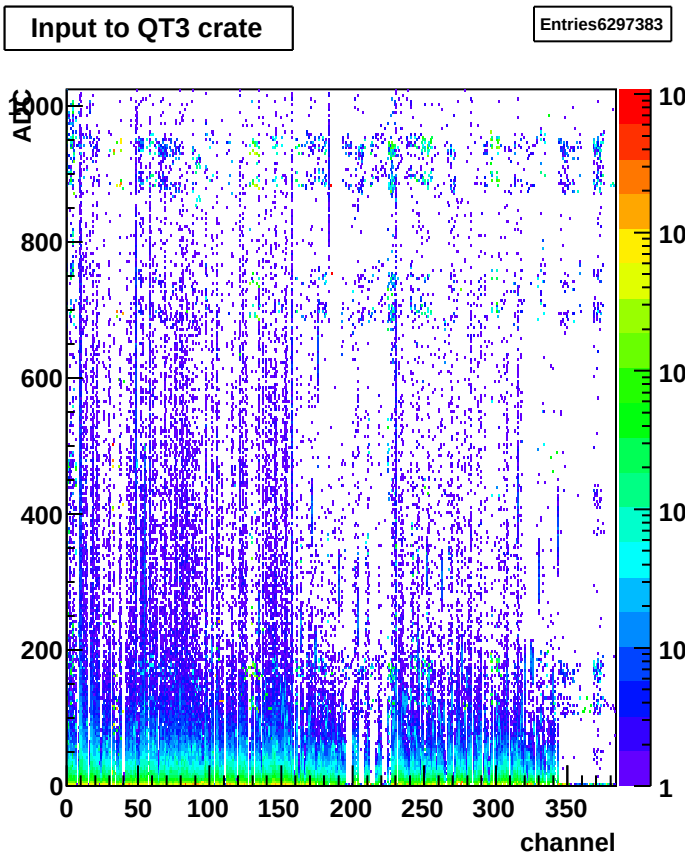

Entries 366198
BBC-S-East ADC Sum
BBC-S-West ADC Sum

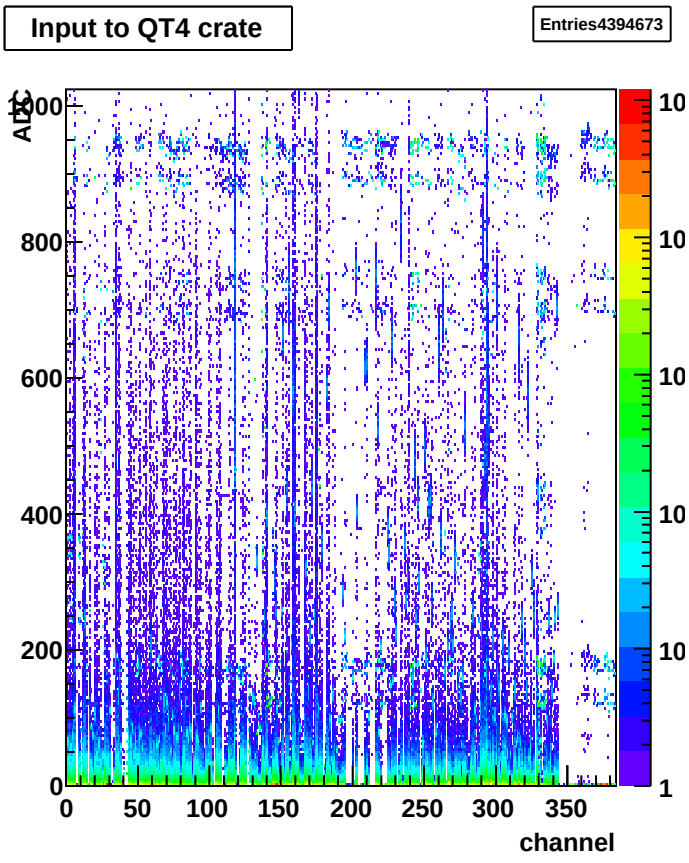

Entries 366198
ZDC East ADC Sum Att
ZDC West ADC Sum Att

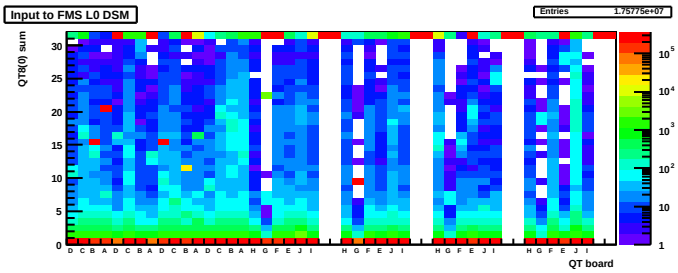

Entries 366198
BBC-L TAC Diff
BCC-S TAC Diff
Entries 366198
BBC-L TAC Diff
VPD TAC Diff
Entries 366198
BBC-L TAC Diff
ZDC TAC Diff
Entries 366198
BBC-S TAC Diff
VPD TAC Diff
Entries 366198
BBC-S TAC Diff
ZDC TAC Diff
Entries 366198
VPD TAC Diff
ZDC TAC Diff

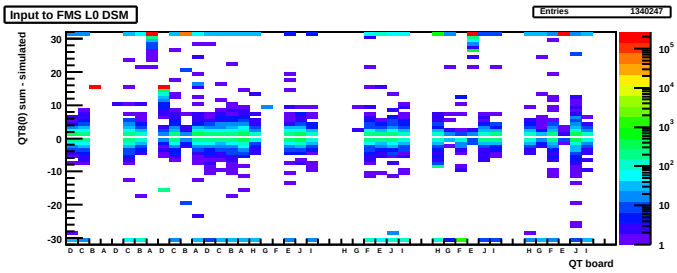

Input to QT1 crate
Entries 2.280334e+07
ADC
channel

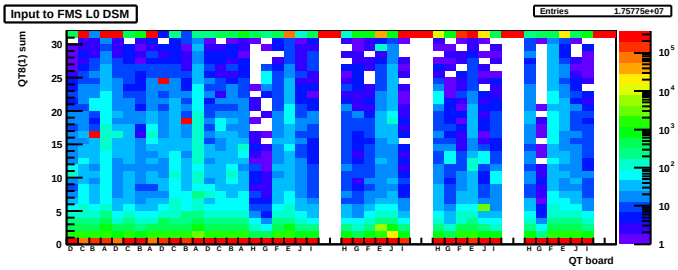

Input to QT2 crate
Entries 1.530784e+07
ADC
channel

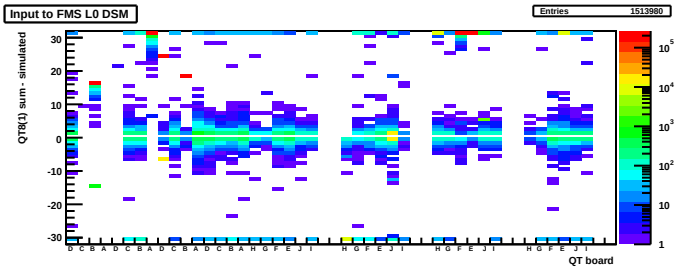

Input to QT3 crate
Entries6297383
ADC
channel

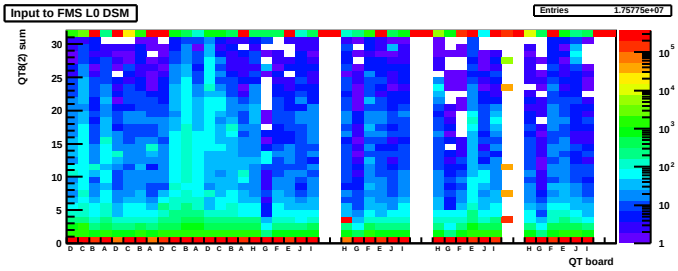

Input to QT4 crate
Entries4394673
ADC
channel

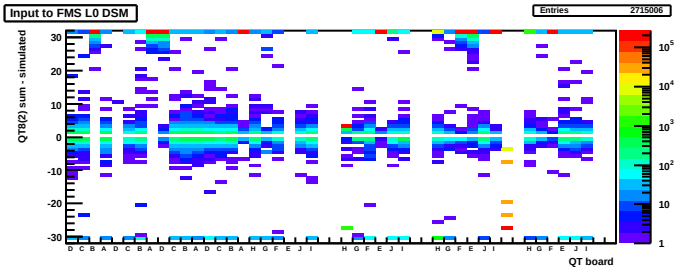

Input to FMS L0 DSM
Entries 1.75775e+07
QT8(0) sum
QT board

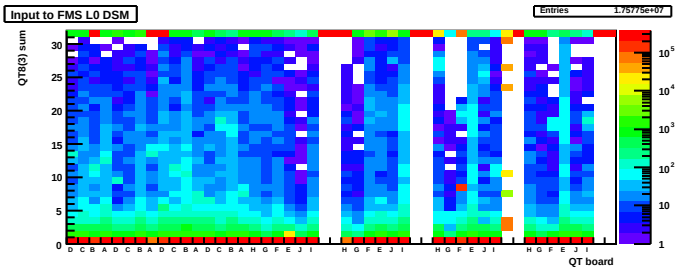

Input to FMS L0 DSM
Entries 1340247
QT8(0) sum - simulated
QT board

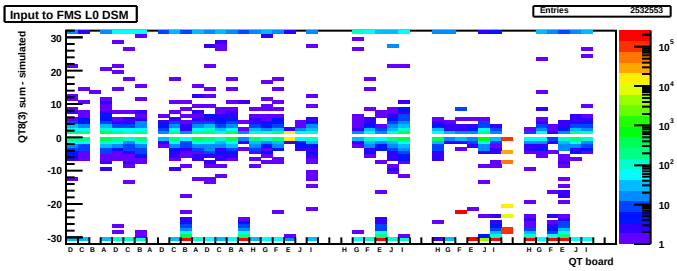

Input to FMS L0 DSM
Entries 1.75775e+07
QT8(1) sum
QT board

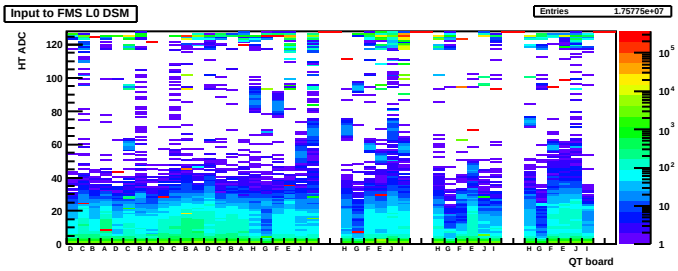

Input to FMS L0 DSM
Entries 1513980
QT8(1) sum - simulated
QT board

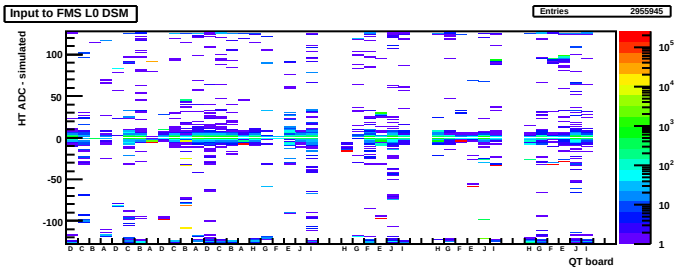

Input to FMS L0 DSM
Entries 1.75775e+07
QT8(2) sum
QT board

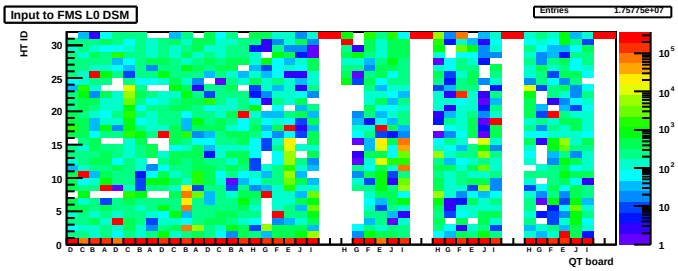

Input to FMS L0 DSM
Entries 2715006
QT8(2) sum - simulated
QT board

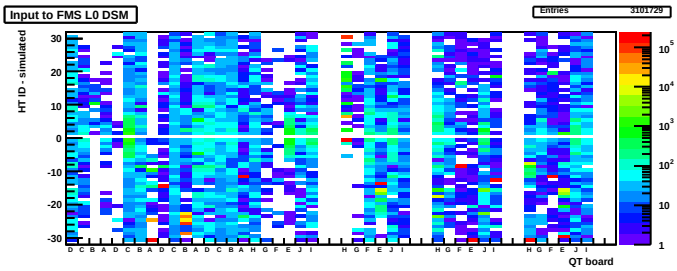

Input to FMS L0 DSM
Entries 1.75775e+07
QT8(3) sum
QT board

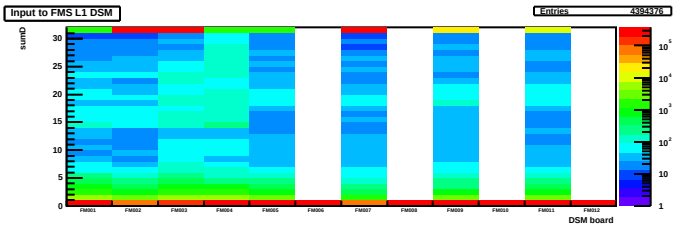

Input to FMS L0 DSM
Entries 2532553
QT8(3) sum - simulated
QT board

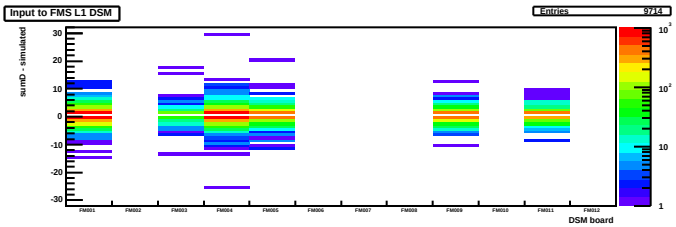

Input to FMS L0 DSM
Entries 1.75775e+07
HT ADC
QT board

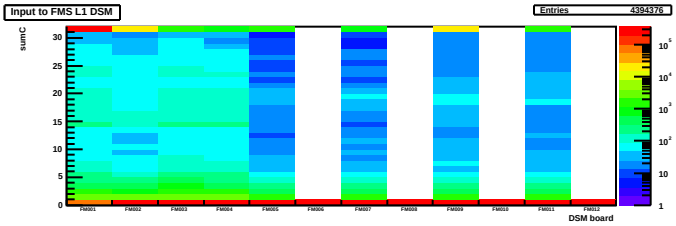

Input to FMS L0 DSM
Entries 2955945
HT ADC - simulated
QT board

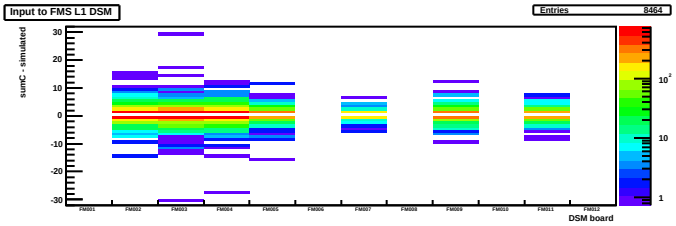

Input to FMS L0 DSM
Entries 1.75775e+07
HT ID
QT board

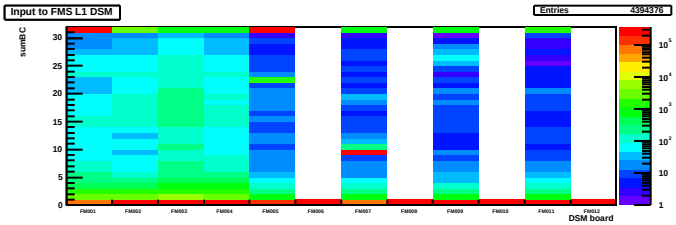

Input to FMS L0 DSM
Entries 3101729
HT ID - simulated
QT board

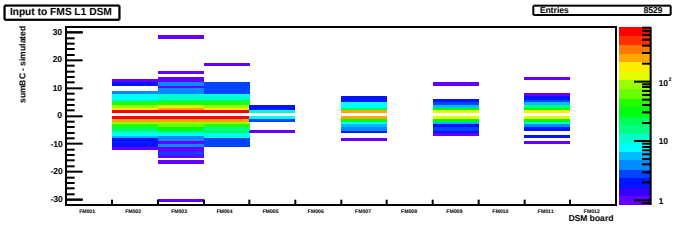

Input to FMS L1 DSM
Entries 4394376
sumD
DSM board

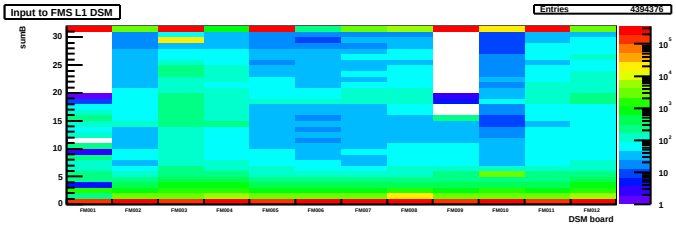

Input to FMS L1 DSM
Entries 9714
sumD - simulated
DSM board

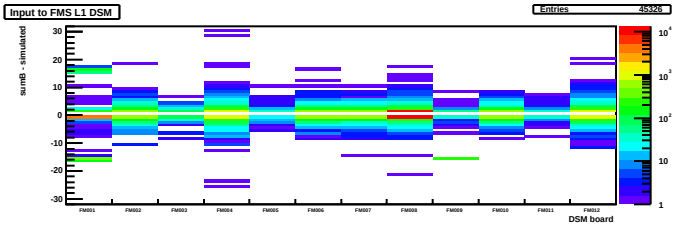

Input to FMS L1 DSM
Entries 4394376
sumC
DSM board

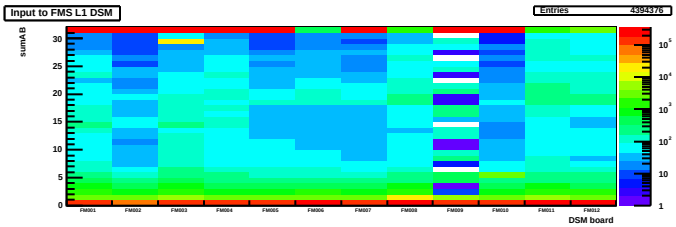

Input to FMS L1 DSM
Entries 8464
sumC - simulated
DSM board

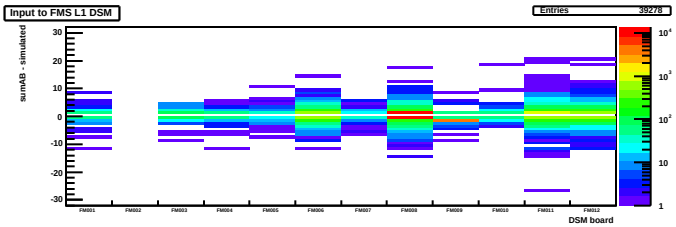

Input to FMS L1 DSM
Entries 4394376
sumBC
DSM board

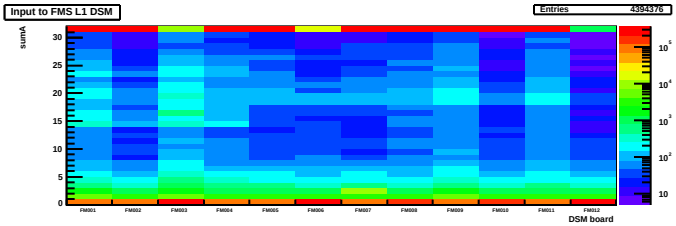

Input to FMS L1 DSM
Entries 8529
sumBC - simulated
DSM board

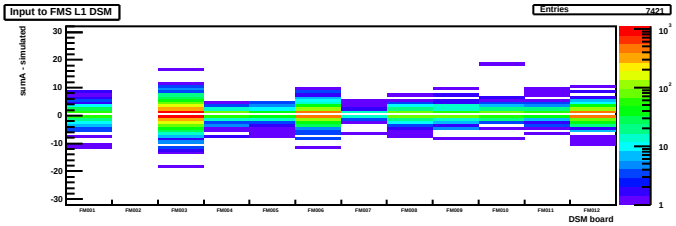

Input to FMS L1 DSM
Entries 4394376
sumB
DSM board

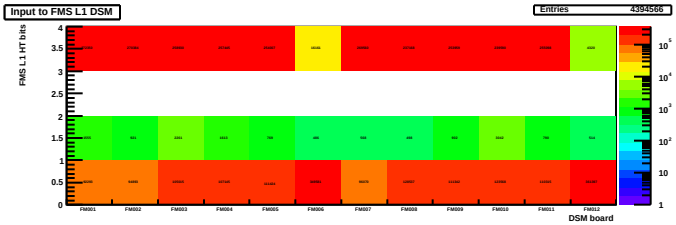

Input to FMS L1 DSM
Entries 45326
sumB - simulated
DSM board

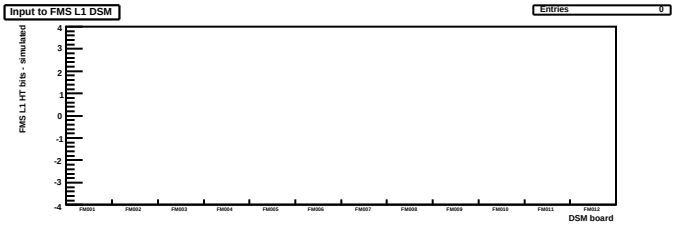

Input to FMS L1 DSM
Entries 4394376
sumAB
DSM board

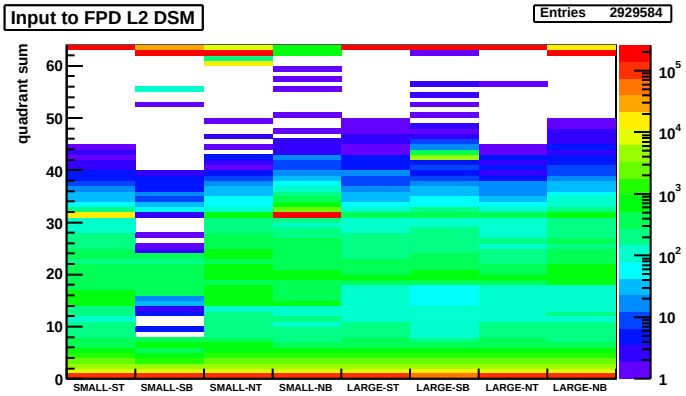

Input to FMS L1 DSM
Entries 39278
sumAB - simulated
DSM board

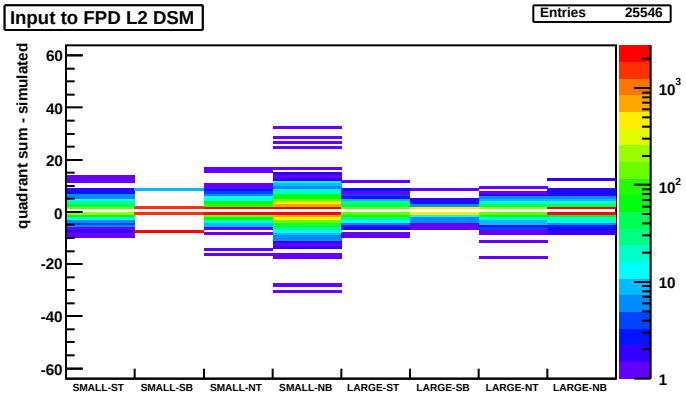

Input to FMS L1 DSM
Entries 4394376
sumA
DSM board

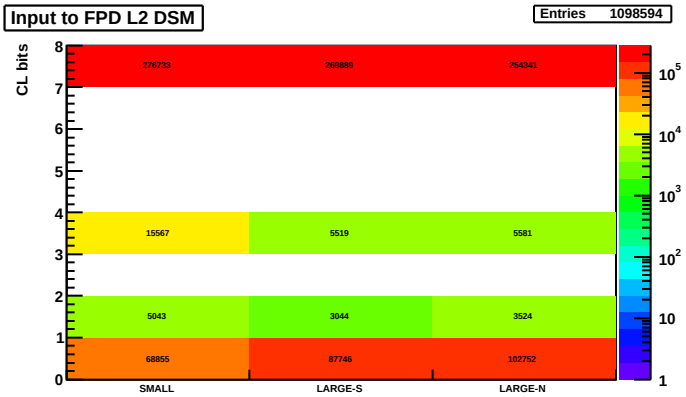

Input to FMS L1 DSM
Entries 7421
sumA - simulated
DSM board

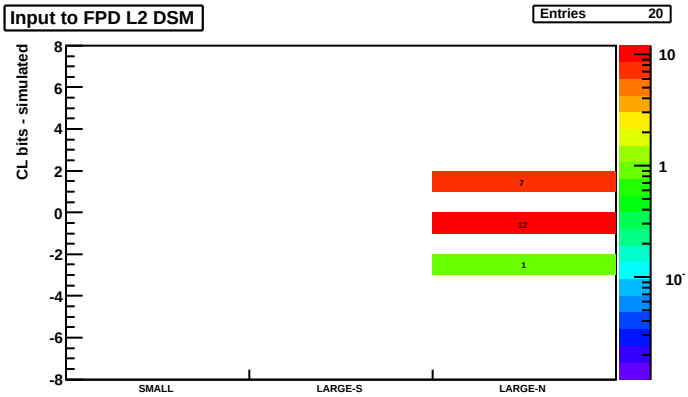

Input to FMS L1 DSM
Entries 4394566
FMS L1 HT bits
DSM board

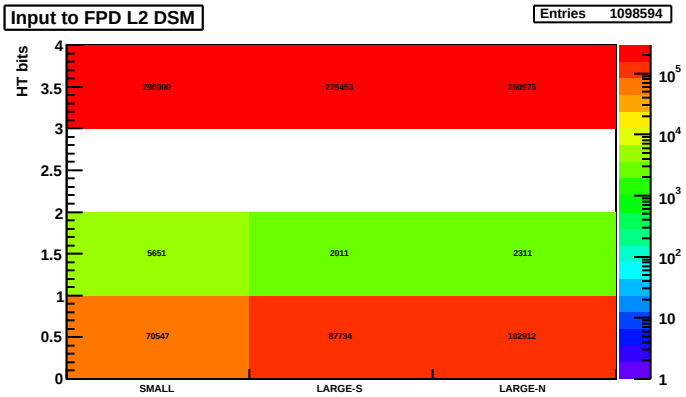

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

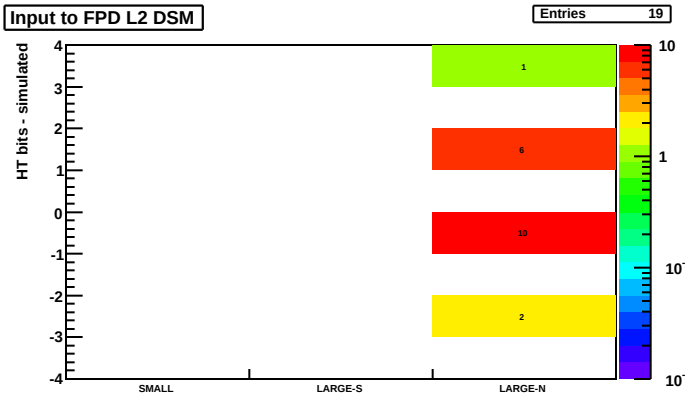

Input to FPD L2 DSM
Entries 2929584
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

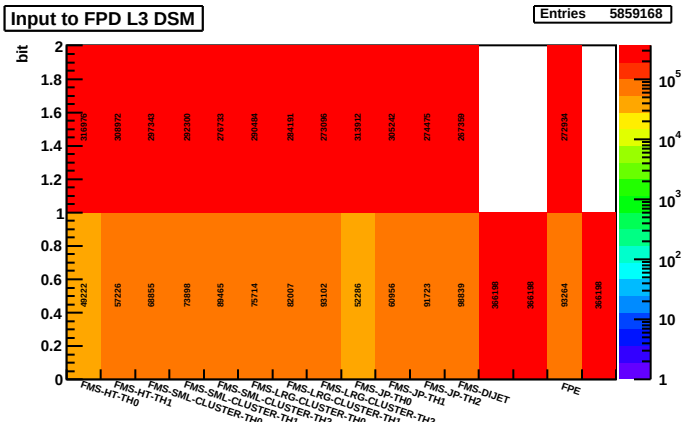

Input to FPD L2 DSM
Entries 25546
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

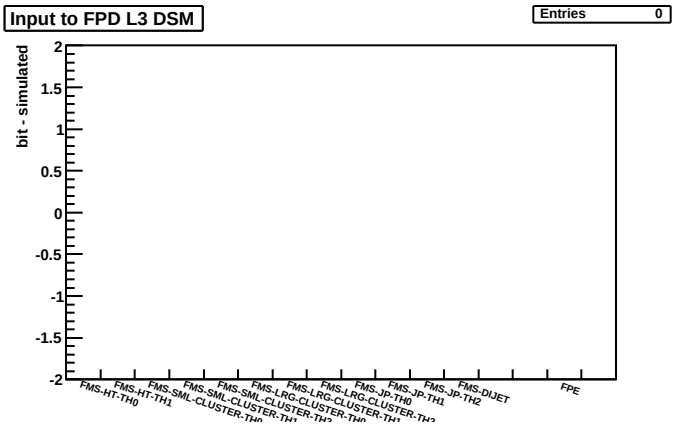

Input to FPD L2 DSM
Entries 1098594
CL bits
276733
269889
254341
15567
5519
5581
5043
3044
3524
68855
87746
102752
SMALL
LARGE-S
LARGE-N

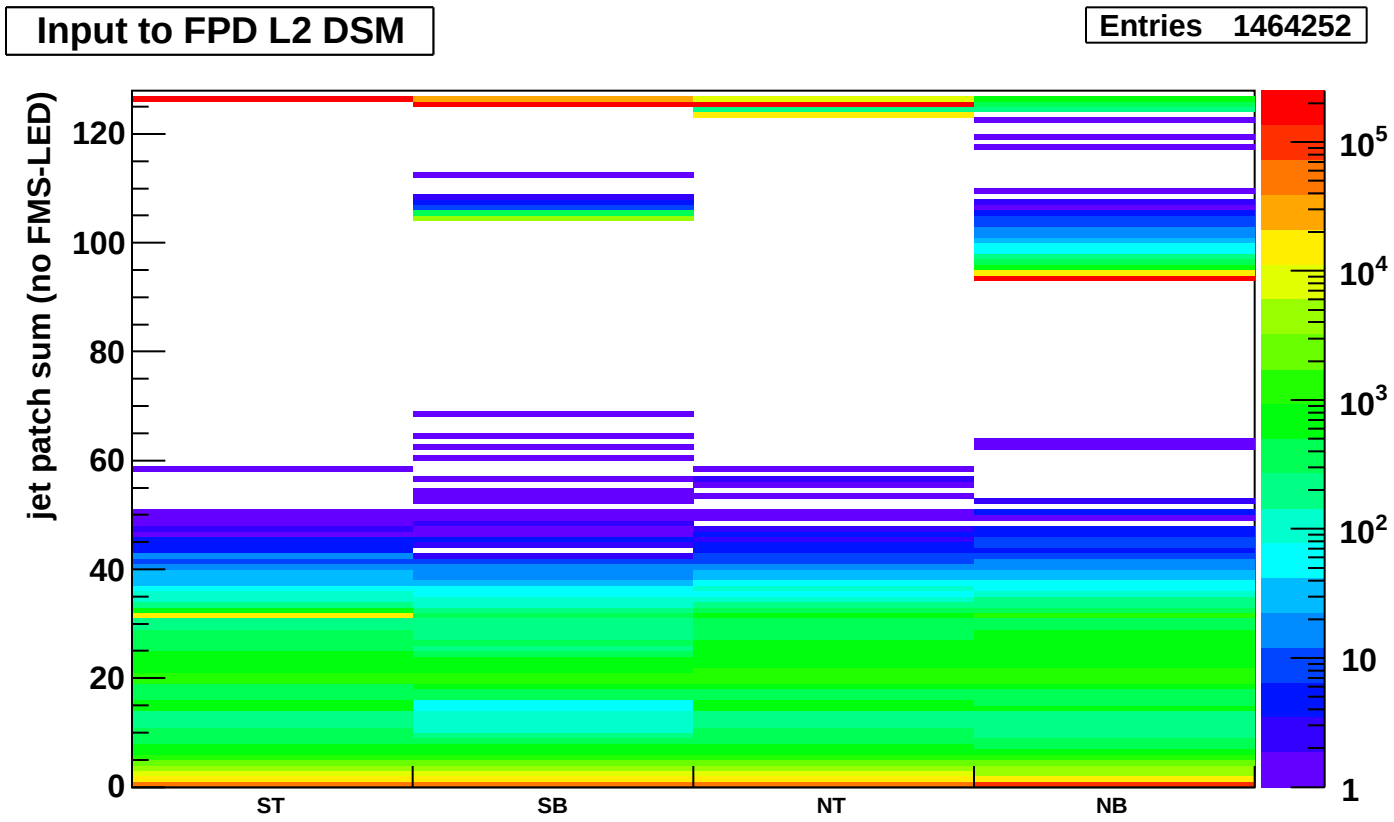

Input to FPD L2 DSM
Entries 20
CL bits - simulated
7
12
1
SMALL
LARGE-S
LARGE-N

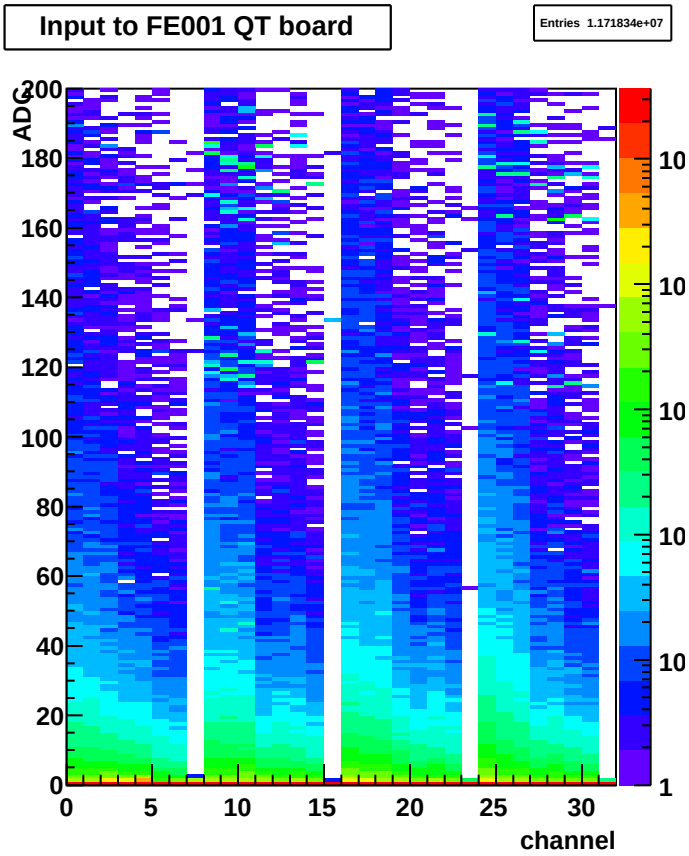

Input to FPD L2 DSM
Entries 1098594
HT bits
290000
276453
260975
5651
2011
2311
70547
87734
102912
SMALL
LARGE-S
LARGE-N

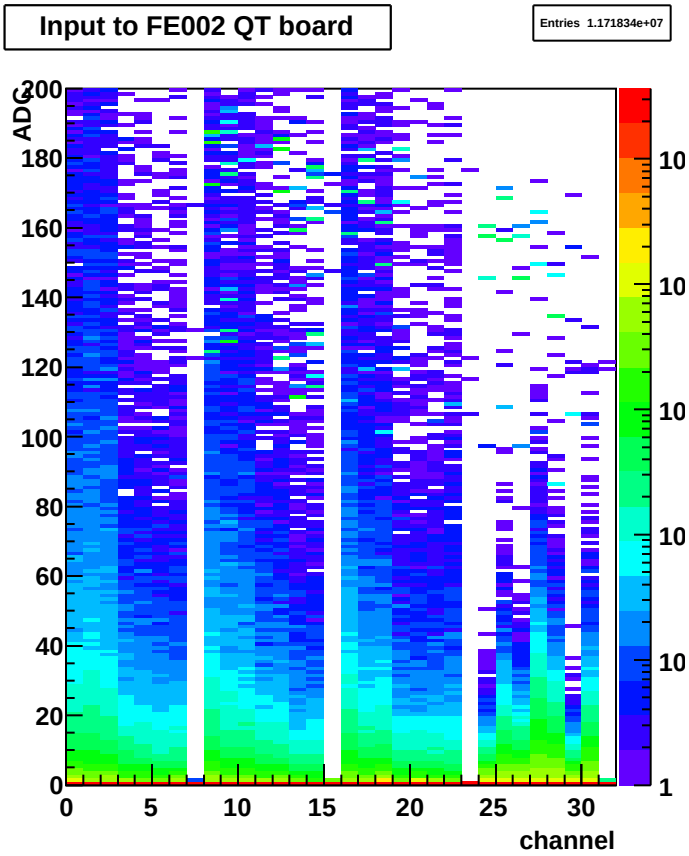

Input to FPD L2 DSM
Entries 19
HT bits - simulated
1
6
10
2
SMALL
LARGE-S
LARGE-N

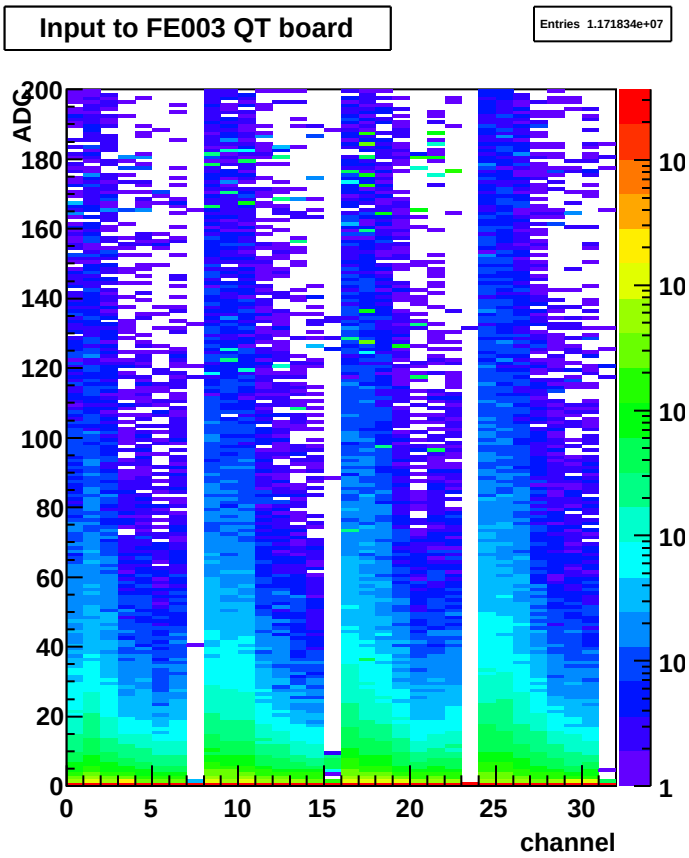

Input to FPD L3 DSM
Entries 5859168
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

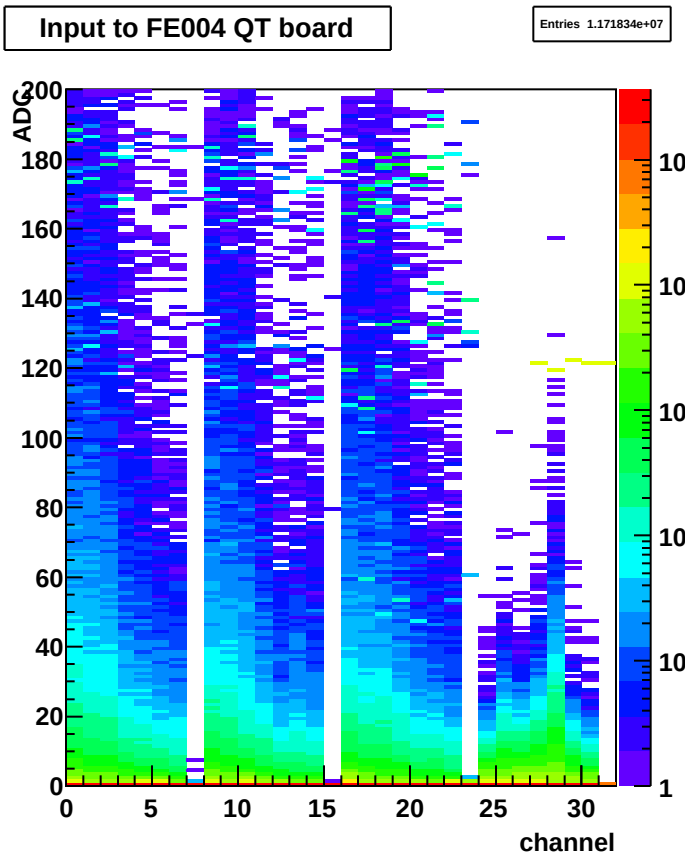

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

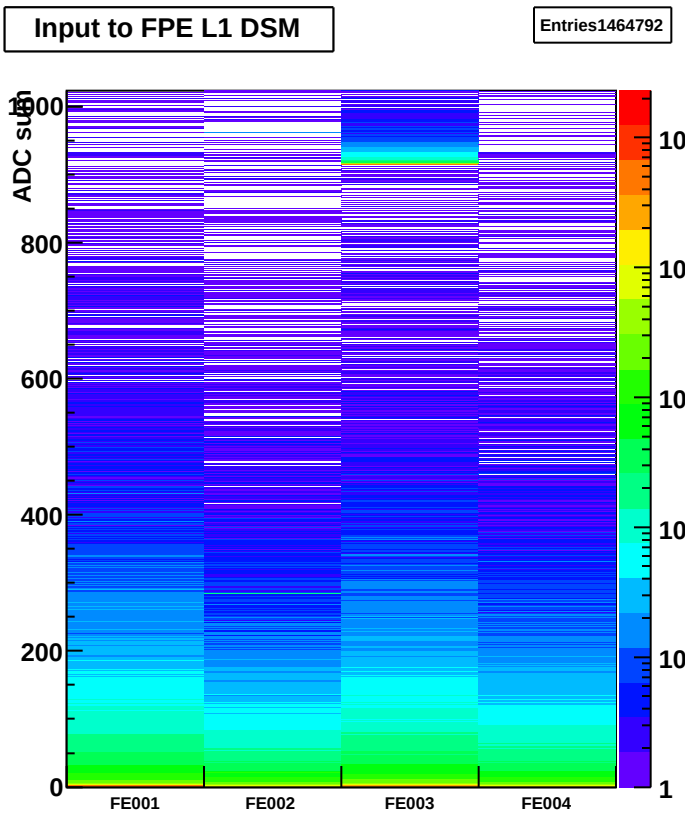

Input to FPD L2 DSM
Entries 1464252
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

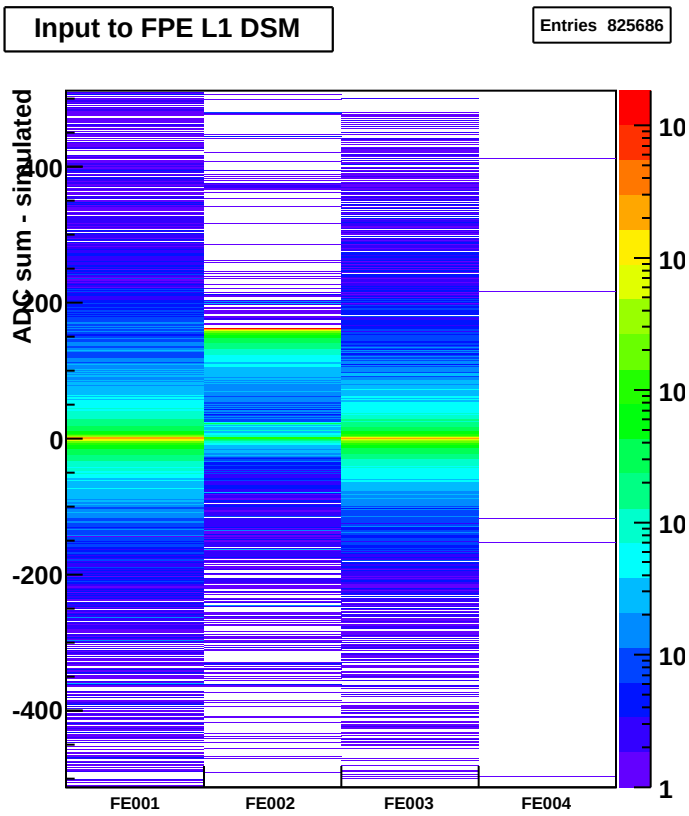

Input to FE001 QT board
Entries 1.171834e+07
ADC
channel

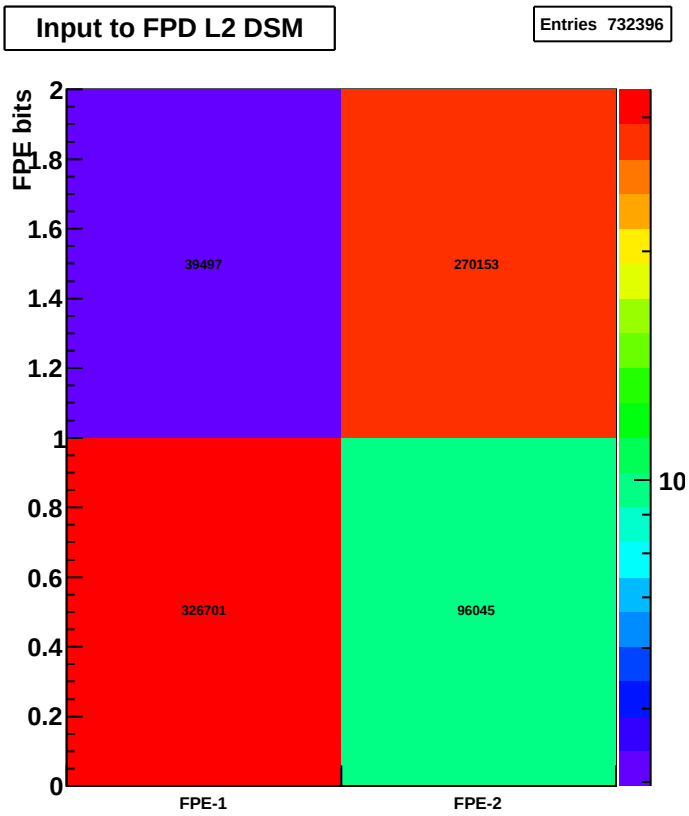

Input to FE002 QT board
Entries 1.171834e+07
ADC
channel

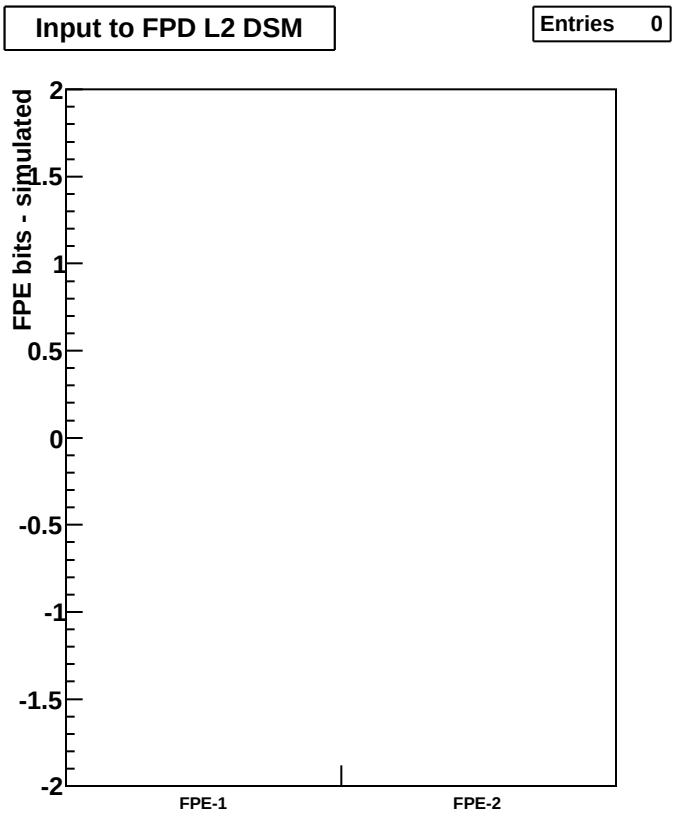

Input to FE003 QT board
Entries 1.171834e+07
ADC
channel


Input to FE004 QT board
Entries 1.171834e+07
ADC
channel


Input to FPE L1 DSM
Entries1464792
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 825686
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 732396
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
39497
270153
326701
96045
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2