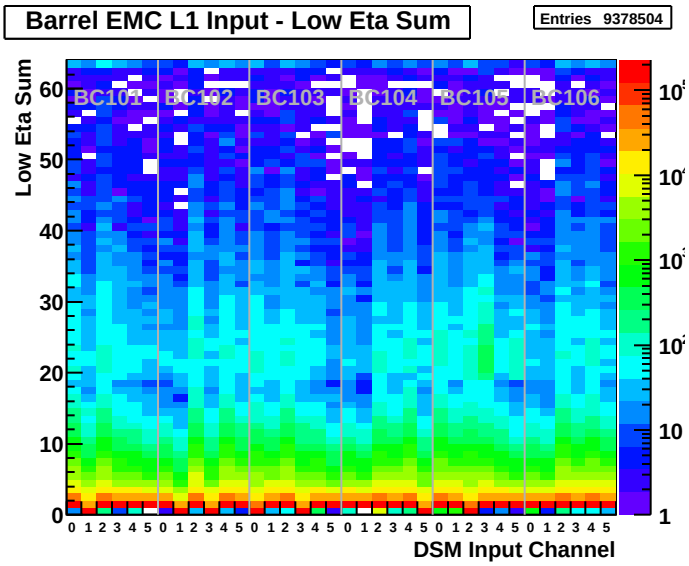
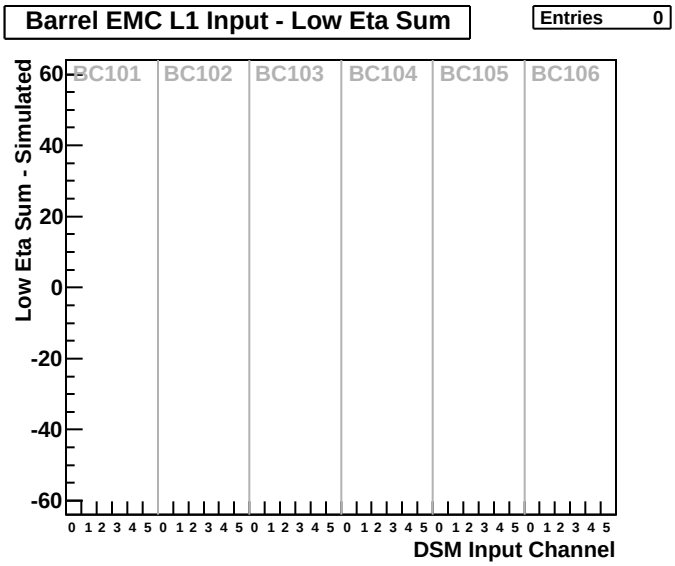
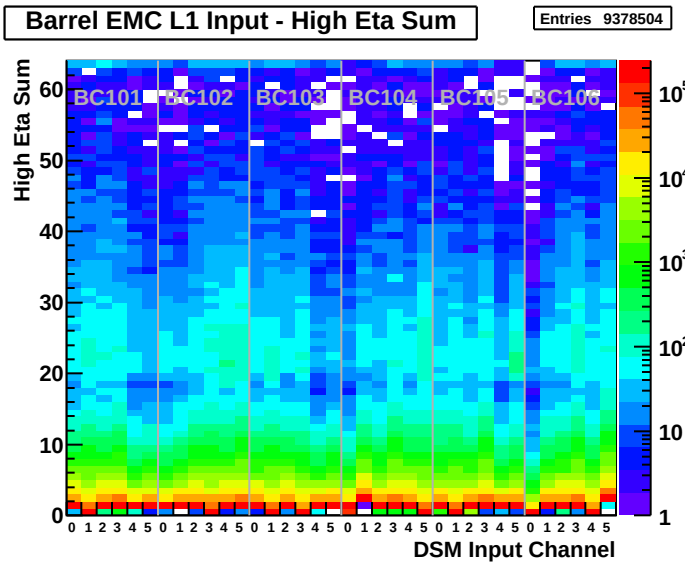
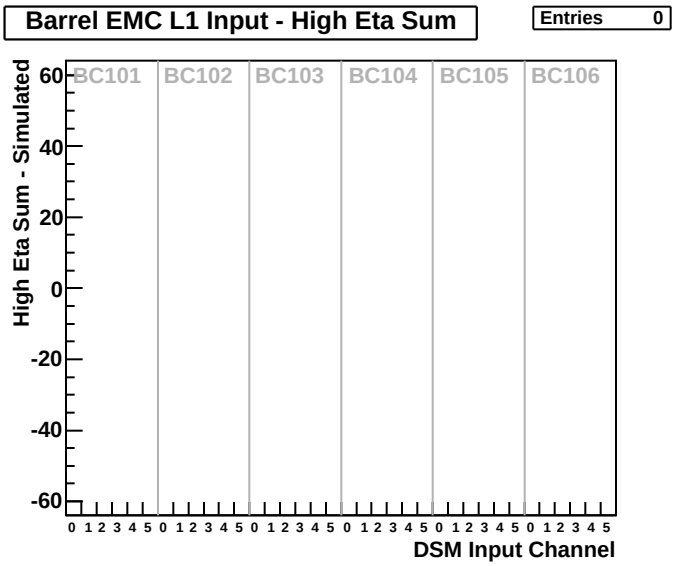
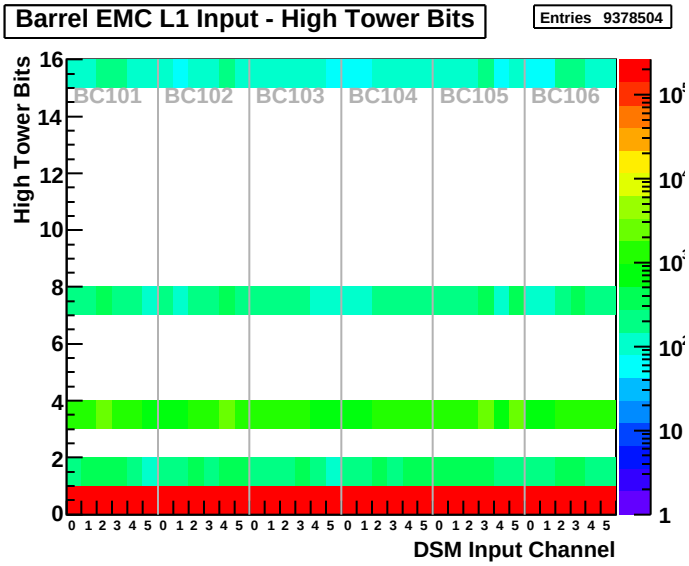
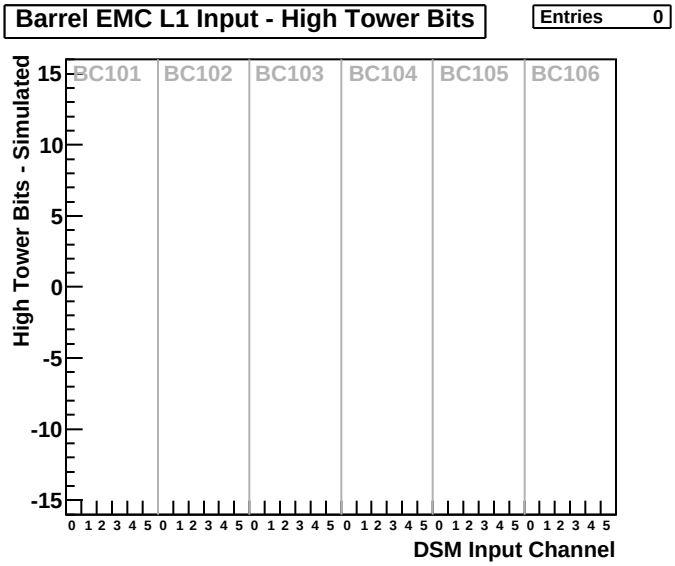

Barrel EMC L1 Input - Low Eta Sum
Entries 9378504
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

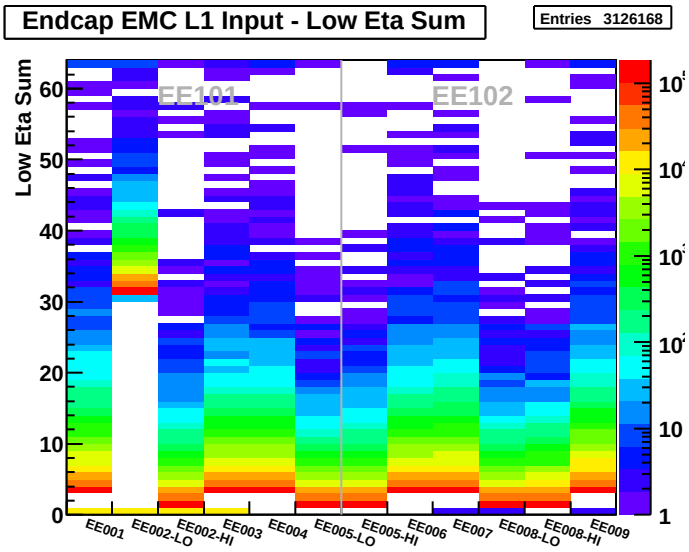

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

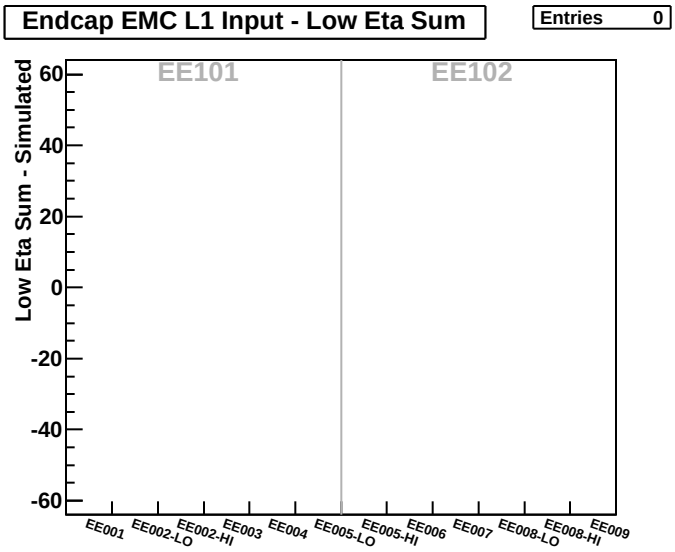

Barrel EMC L1 Input - High Eta Sum
Entries 9378504
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

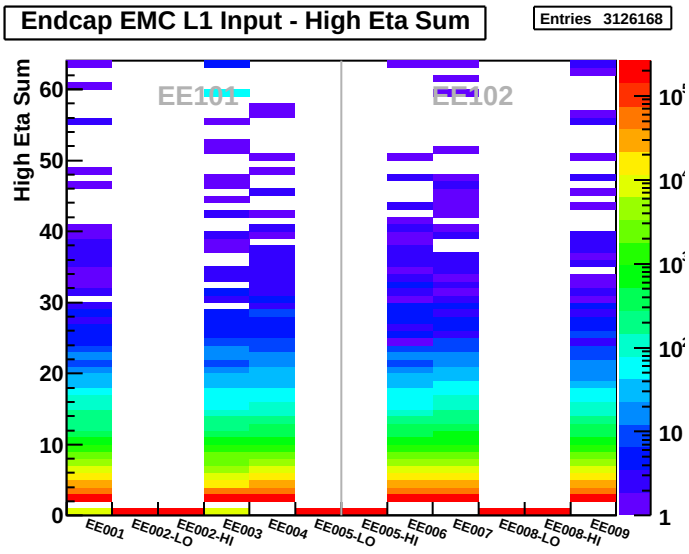

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

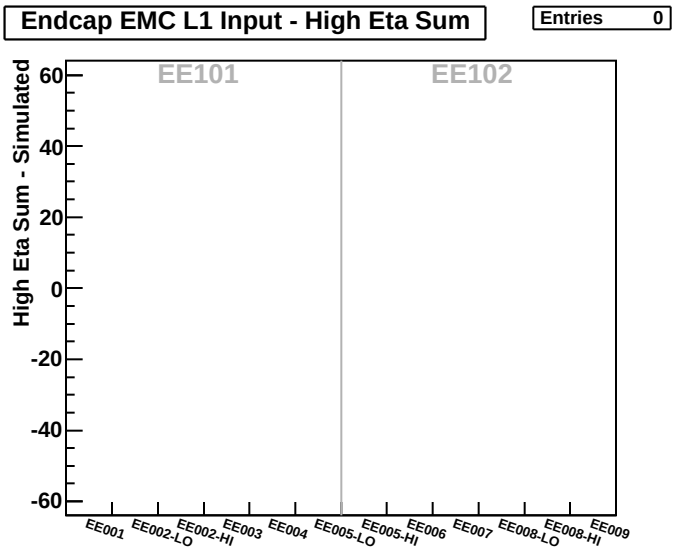

Barrel EMC L1 Input - High Tower Bits
Entries 9378504
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

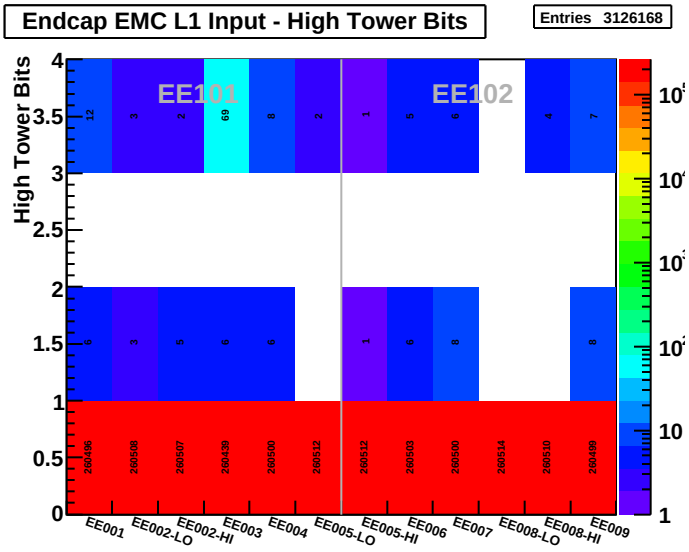

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

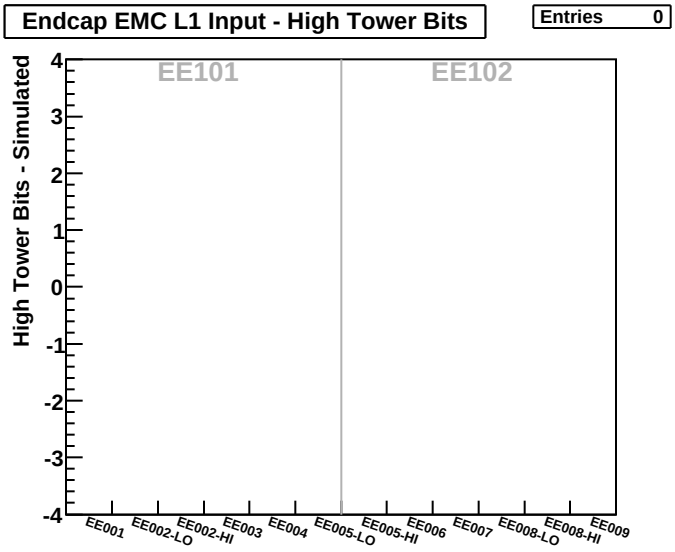

Endcap EMC L1 Input - Low Eta Sum
Entries 3126168
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

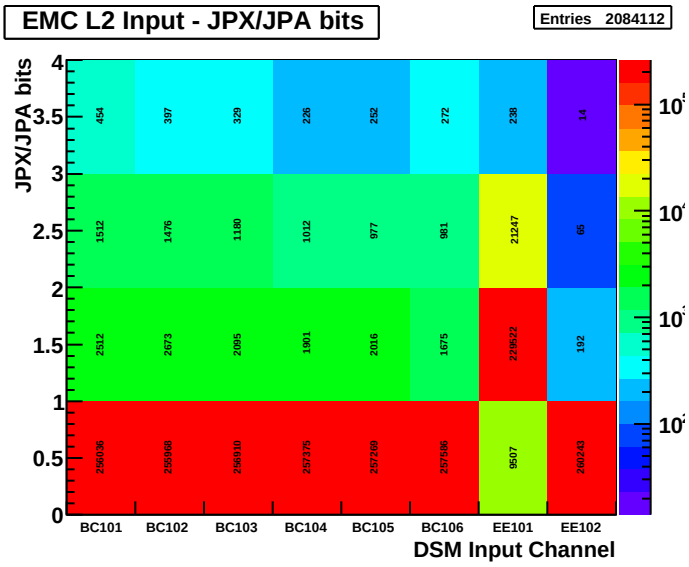

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

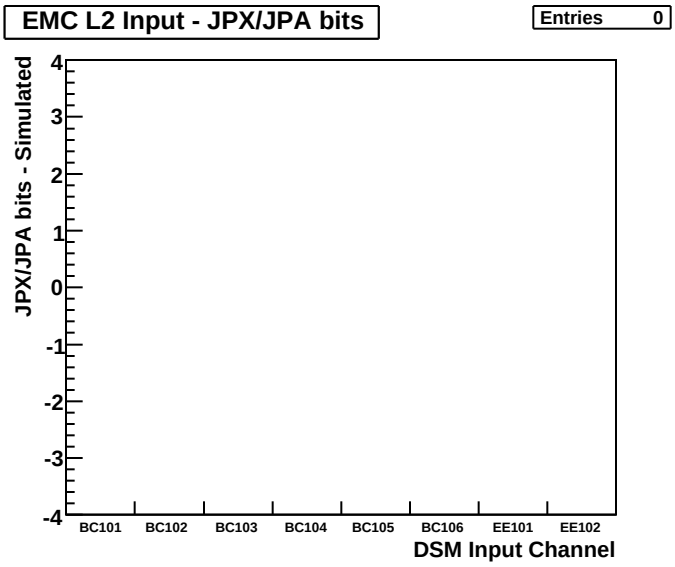

Endcap EMC L1 Input - High Eta Sum
Entries 3126168
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

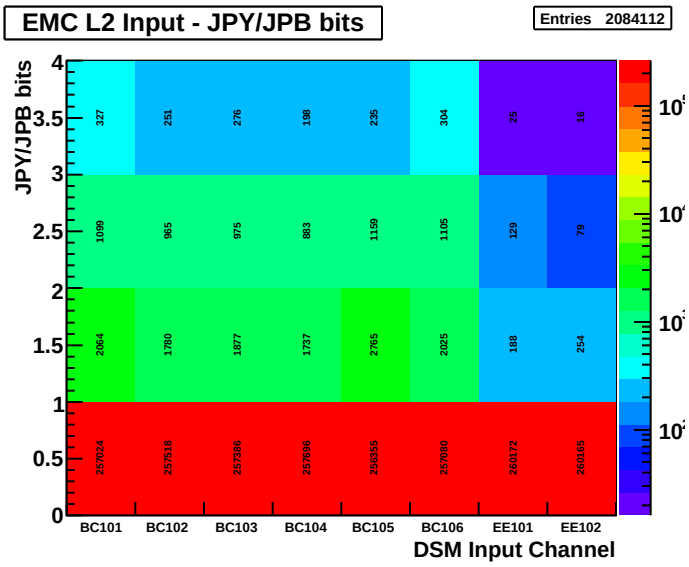

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

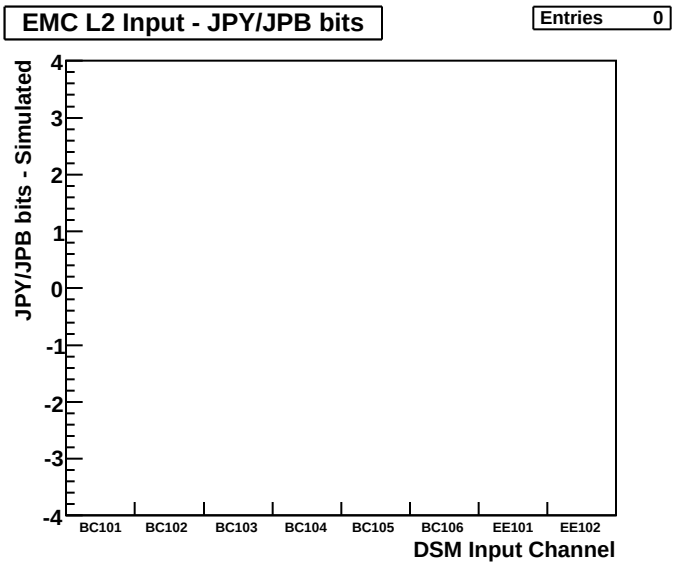

Endcap EMC L1 Input - High Tower Bits
Entries 3126168
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

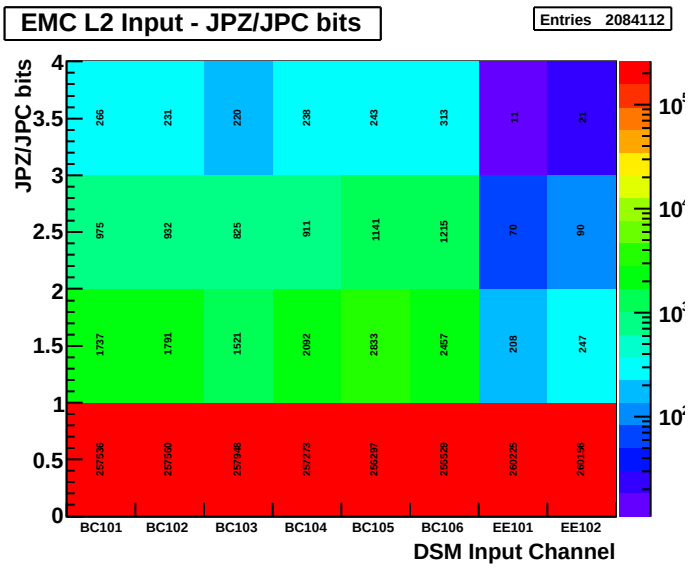

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

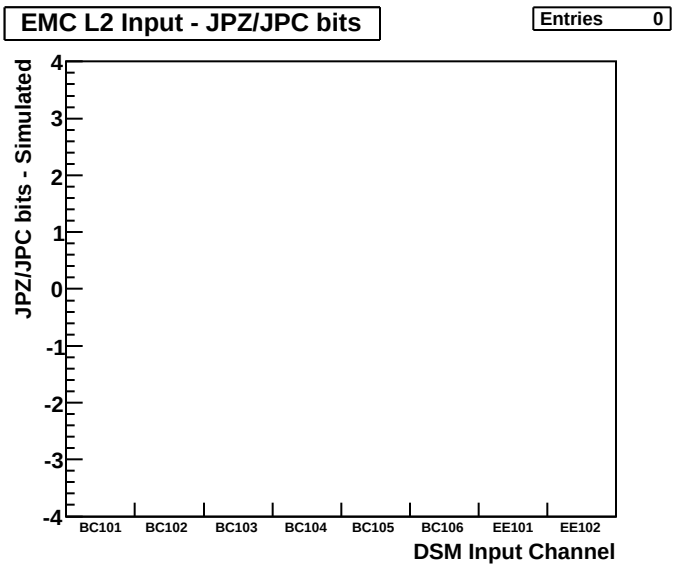

EMC L2 Input - JPX/JPA bits
Entries 2084112
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
454 397 329 226 252 272 238 14
1512 1476 1180 1012 977 981 21247 65
2512 2673 2095 1901 2016 1675 229522 192
256036 255968 256910 257375 257269 257586 9507 260243

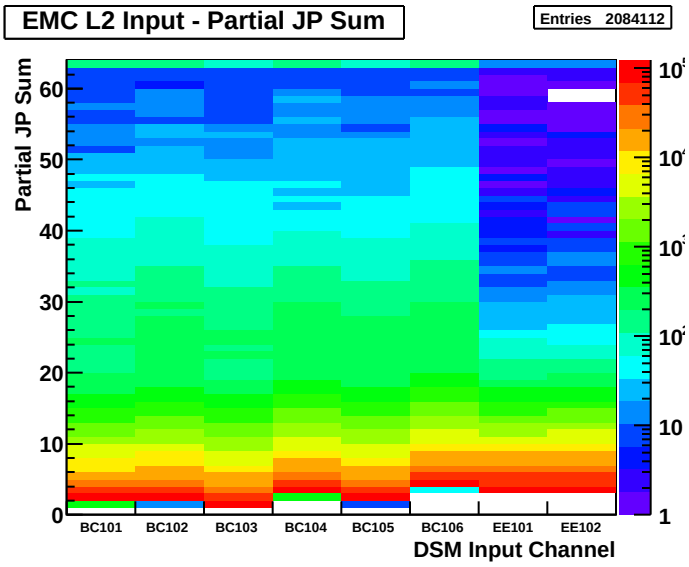

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

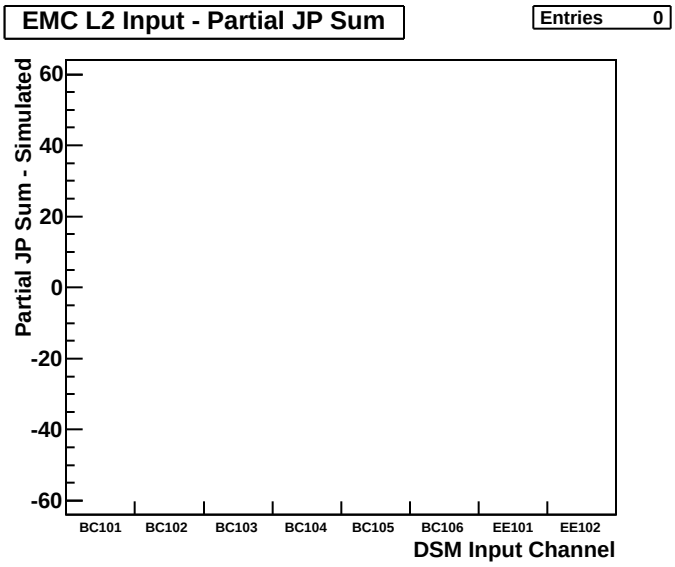

EMC L2 Input - JPY/JPB bits
Entries 2084112
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
327 251 276 198 235 304 25 16
1099 965 975 883 1159 1105 129 79
2064 1760 1877 1737 2765 2025 188 254
257024 257518 257386 257696 256355 257080 260172 260165

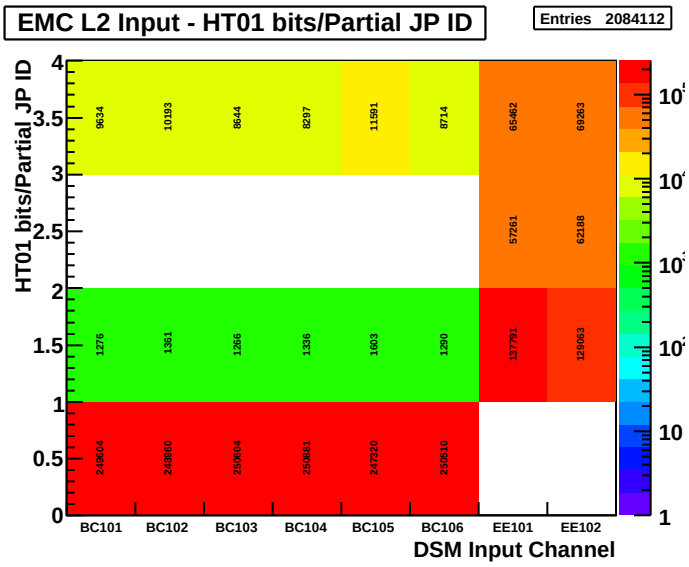

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

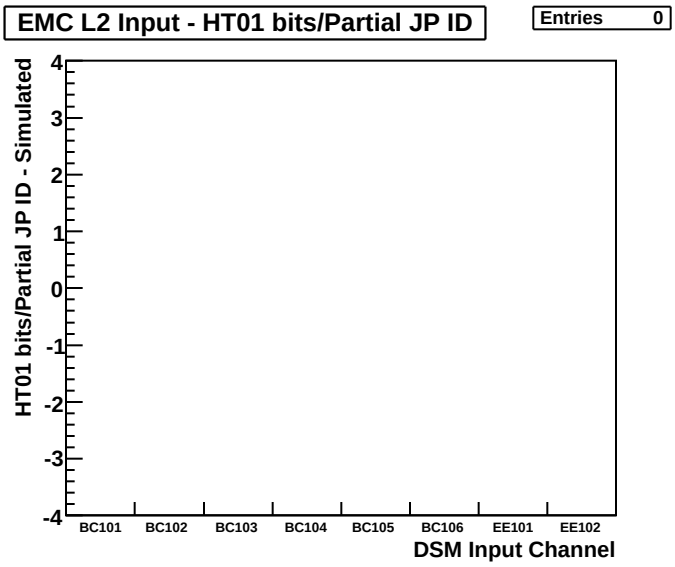

EMC L2 Input - JPZ/JPC bits
Entries 2084112
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
266 231 220 238 243 313 11 21
975 932 825 911 1141 1215 70 90
1737 1791 1521 2092 2833 2457 208 247
257536 257560 257948 257273 256297 256529 260225 260156

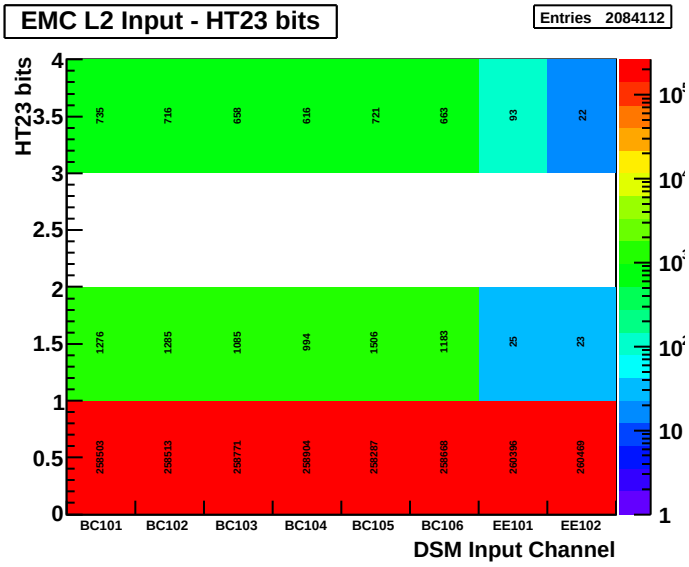

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

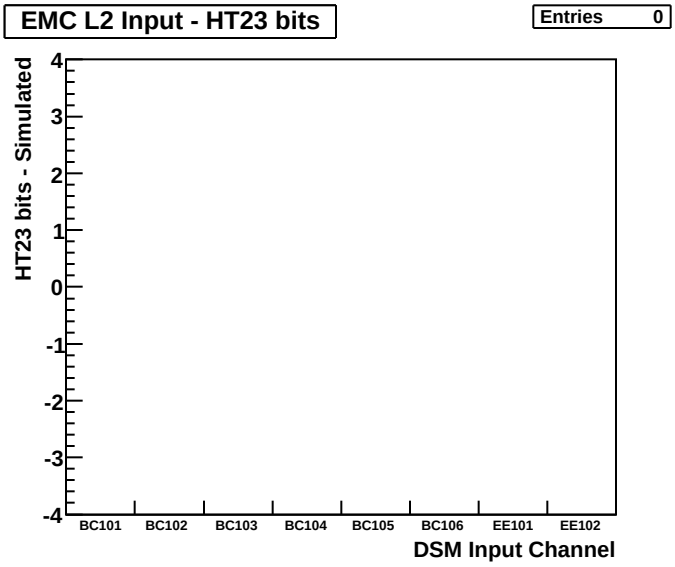

EMC L2 Input - Partial JP Sum
Entries 2084112
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

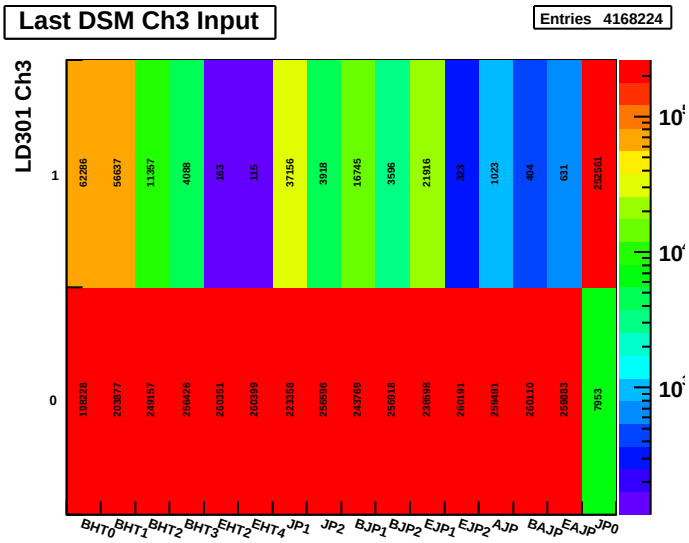

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

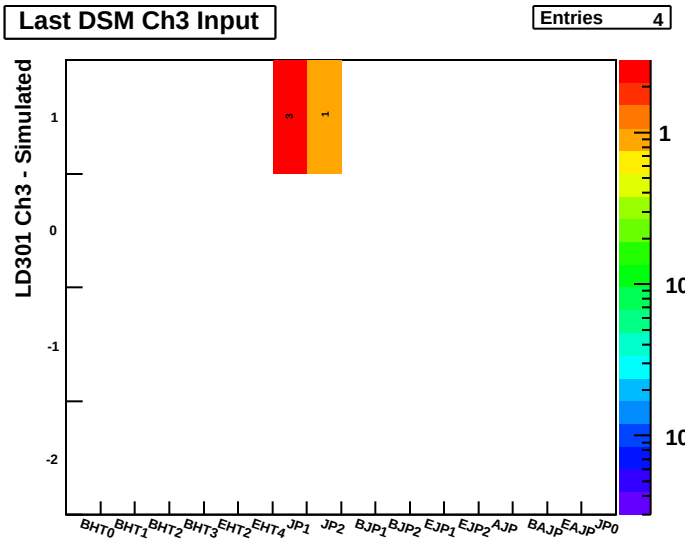

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2084112
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

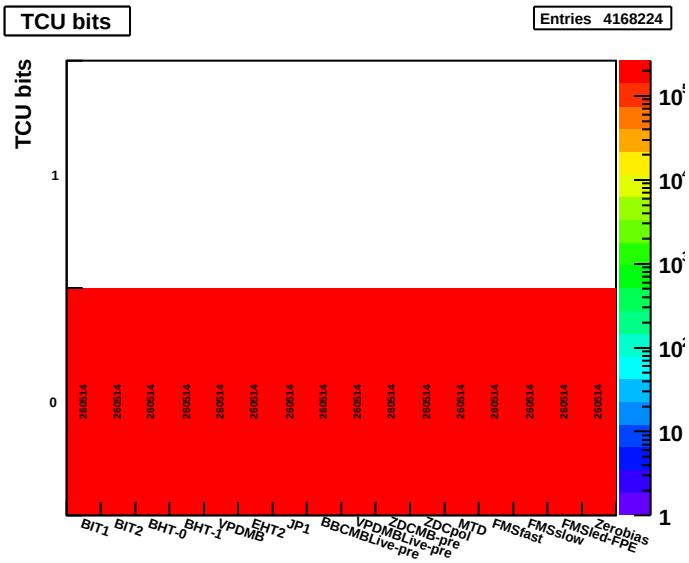

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

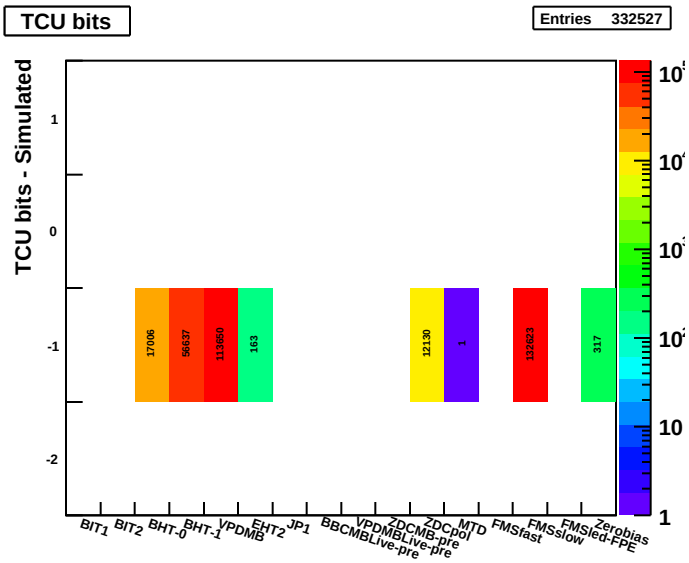

EMC L2 Input - HT23 bits
Entries 2084112
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

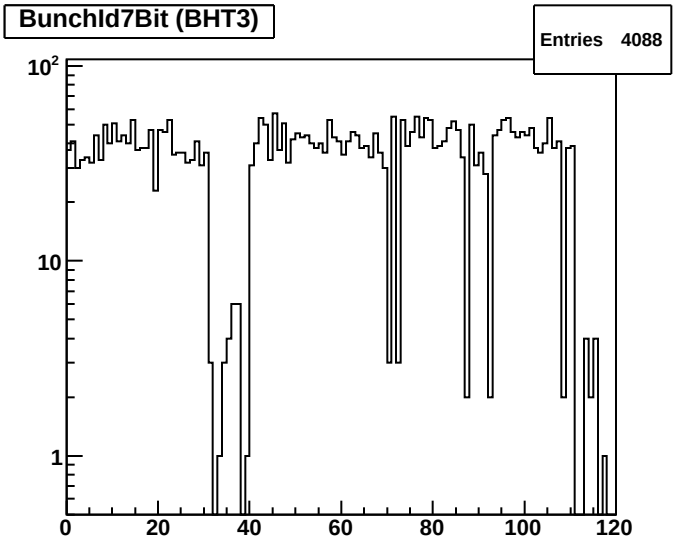

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

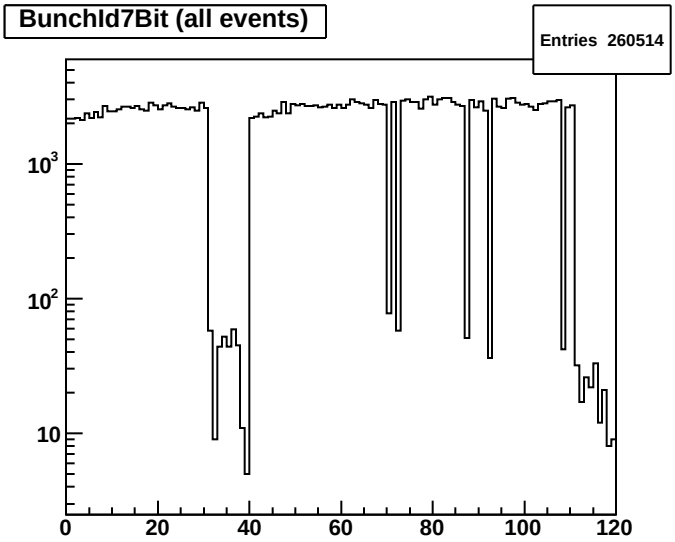

Last DSM Ch3 Input
Entries 4168224
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

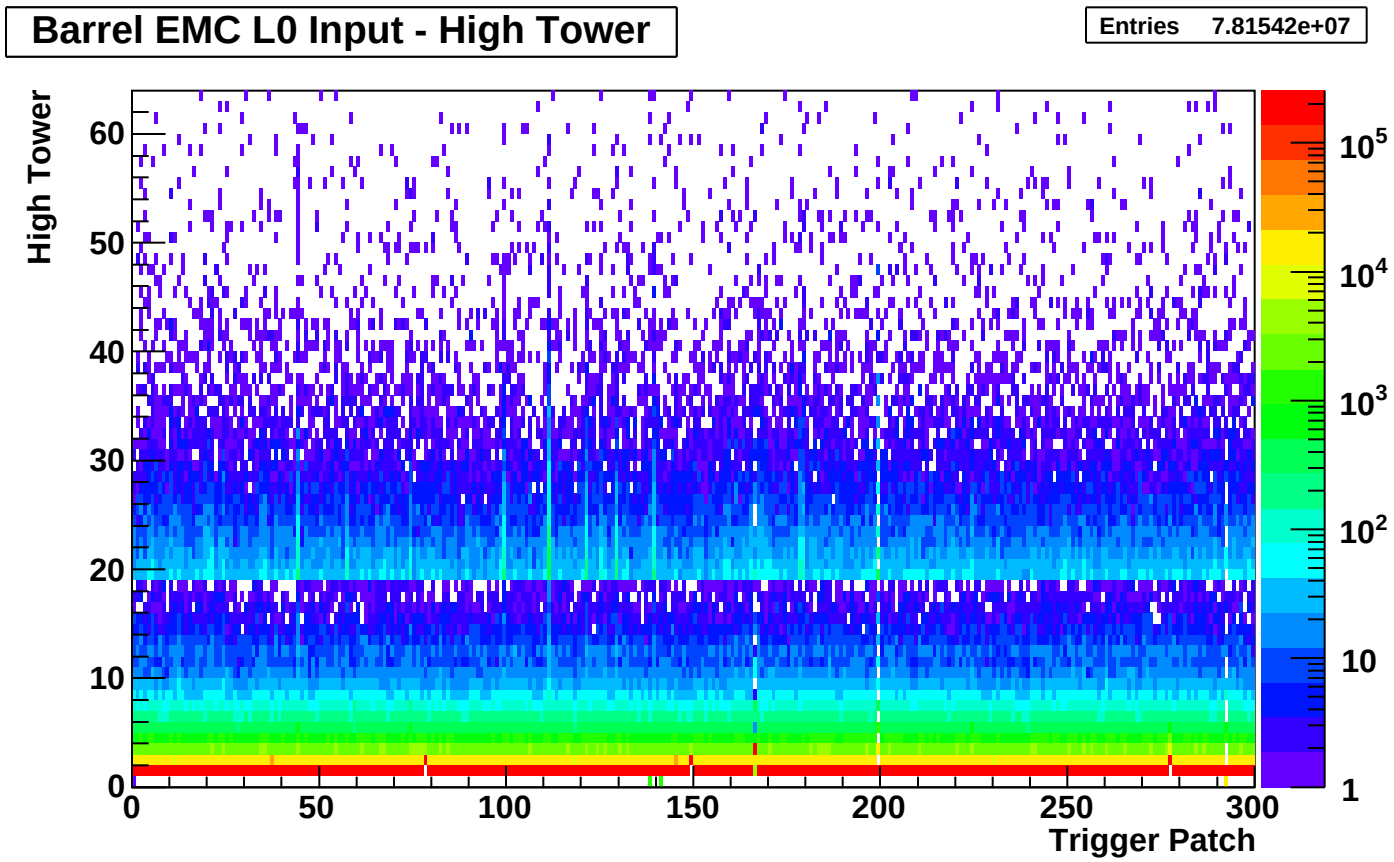

Last DSM Ch3 Input
Entries 4
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

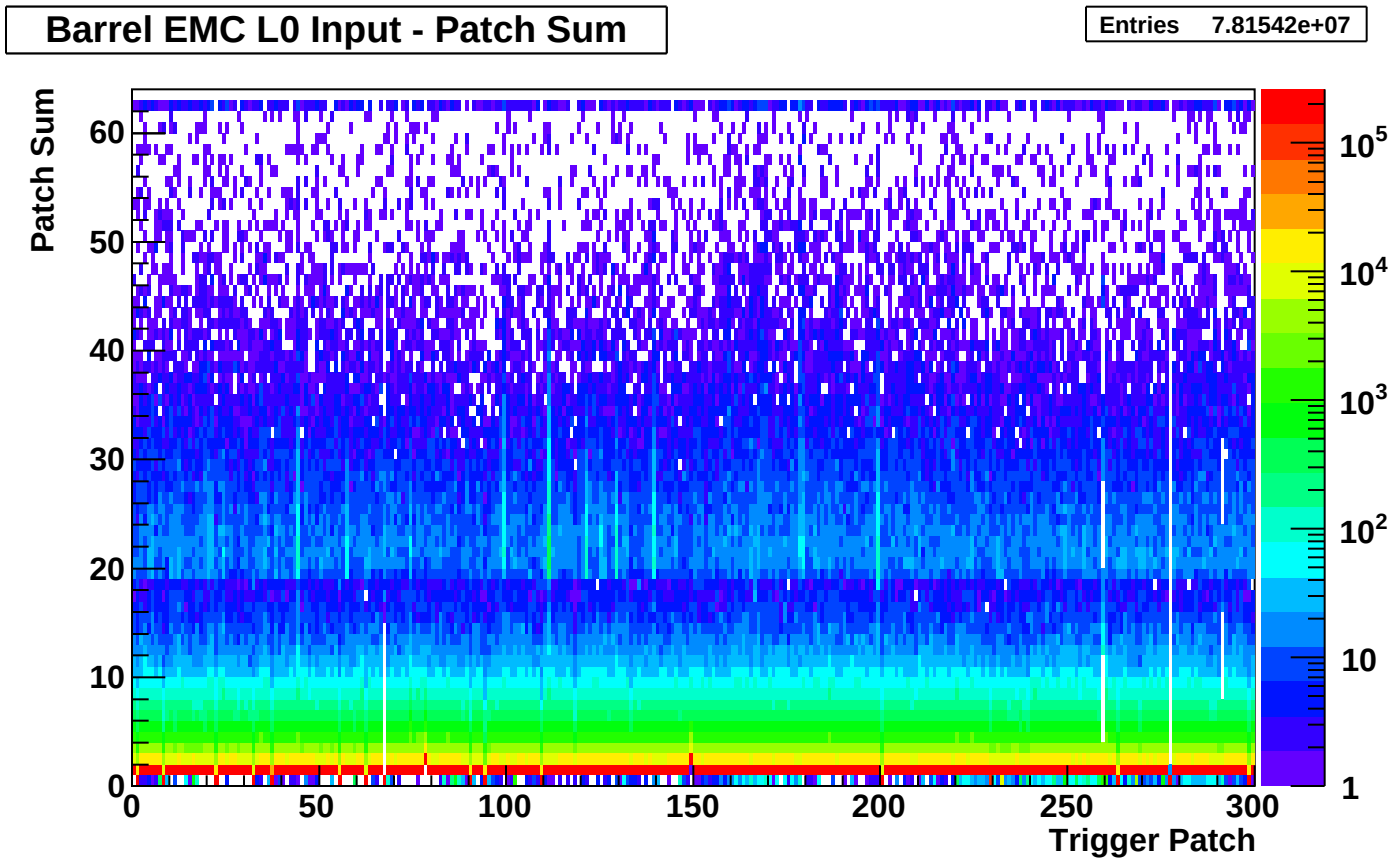

TCU bits
Entries 4168224
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

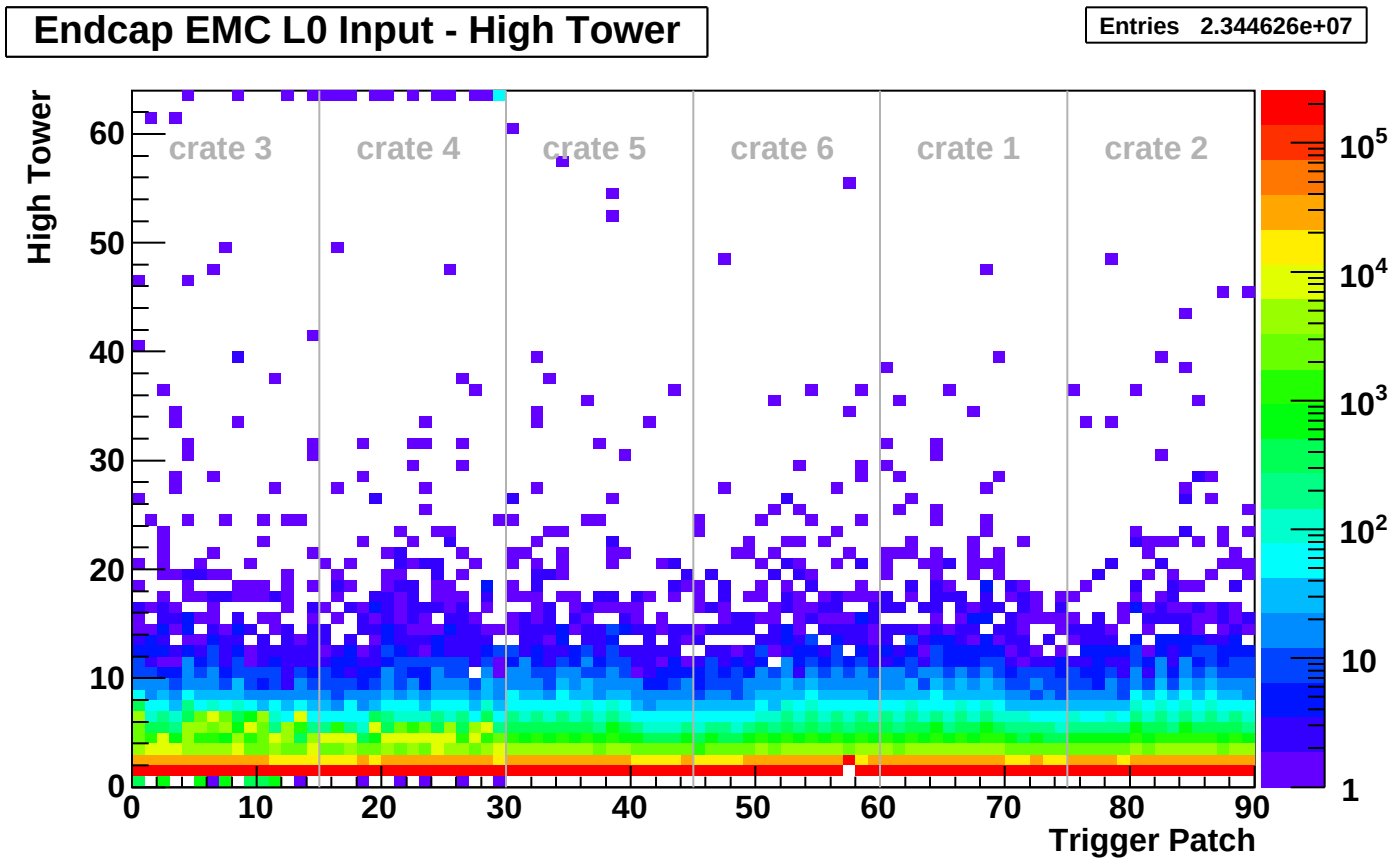

TCU bits
Entries 332527
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

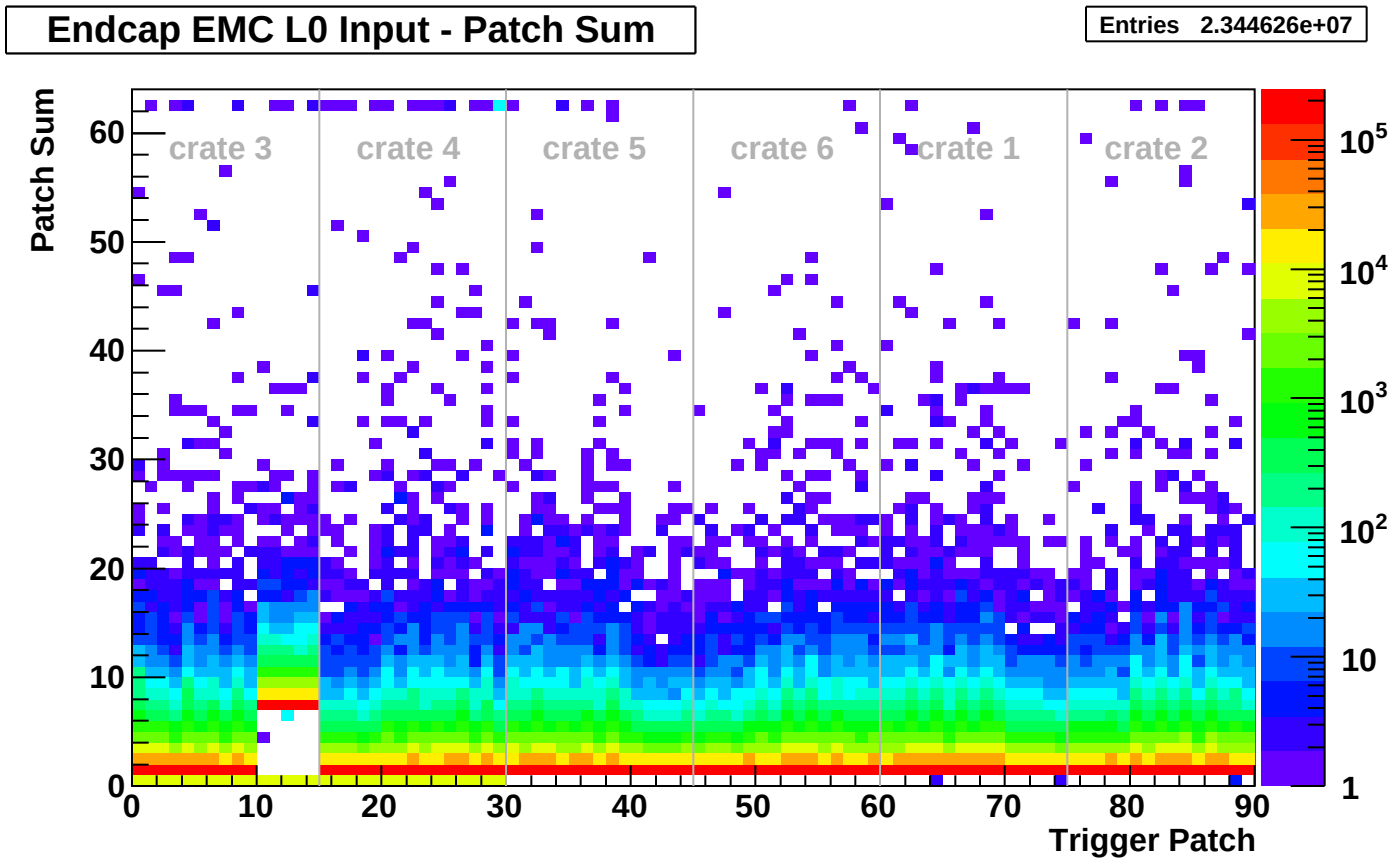

BunchId7Bit (BHT3)
Entries 4088

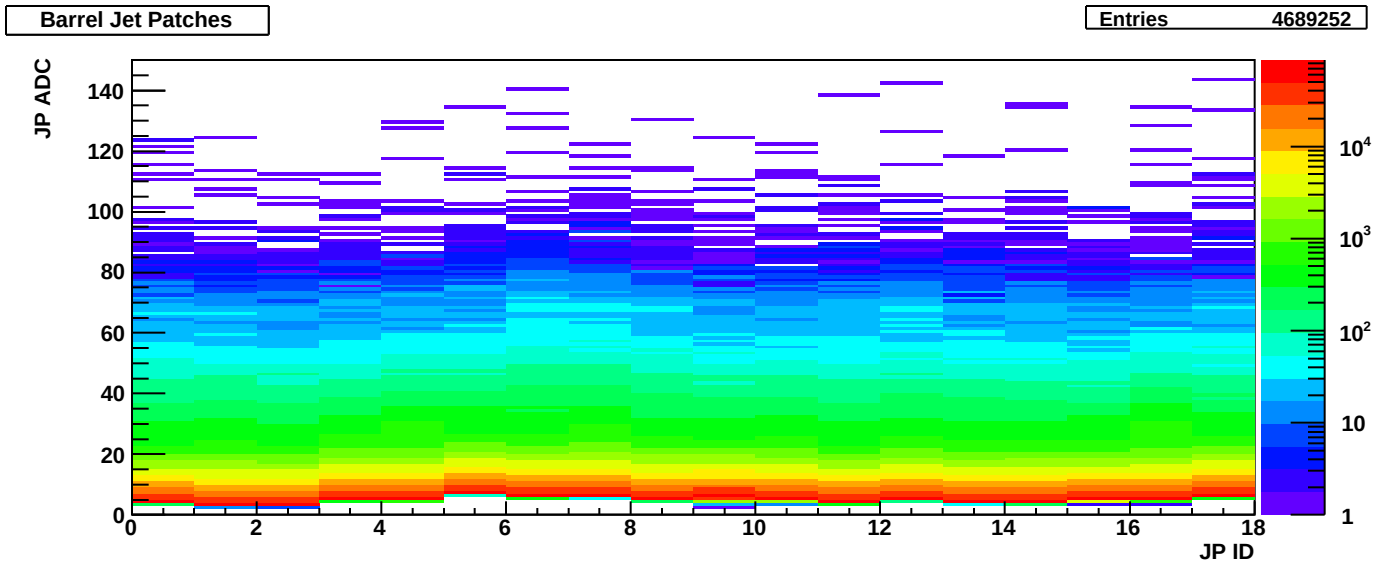

BunchId7Bit (all events)
Entries 260514

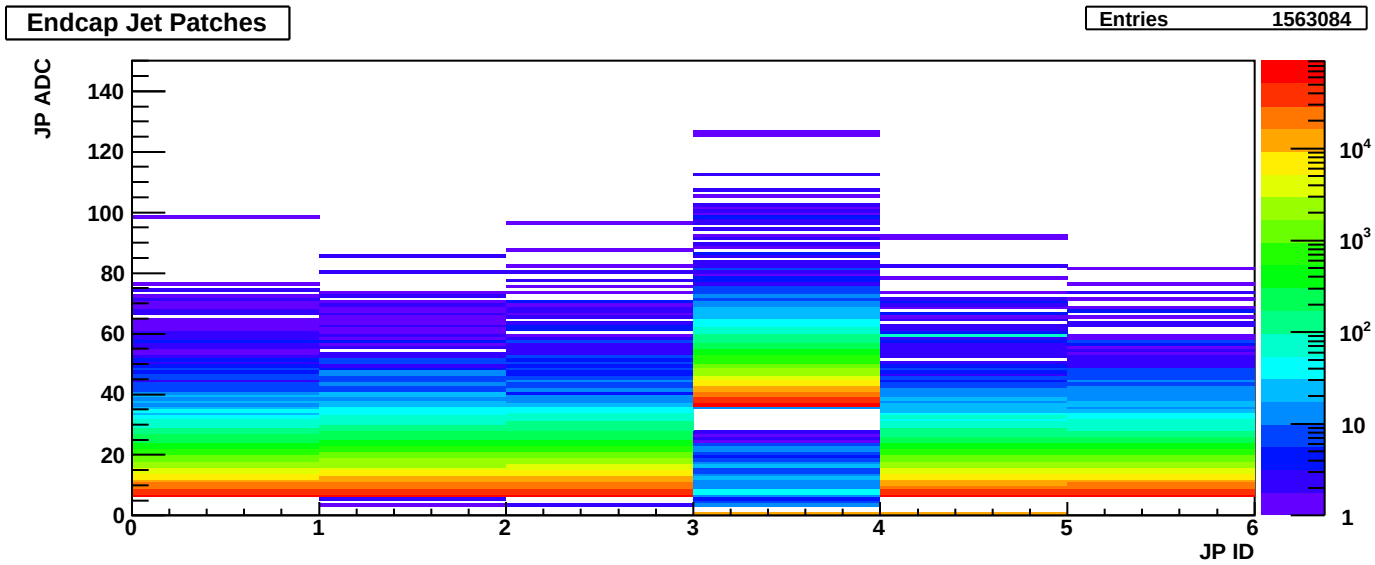

Barrel EMC L0 Input - High Tower
Entries 7.81542e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

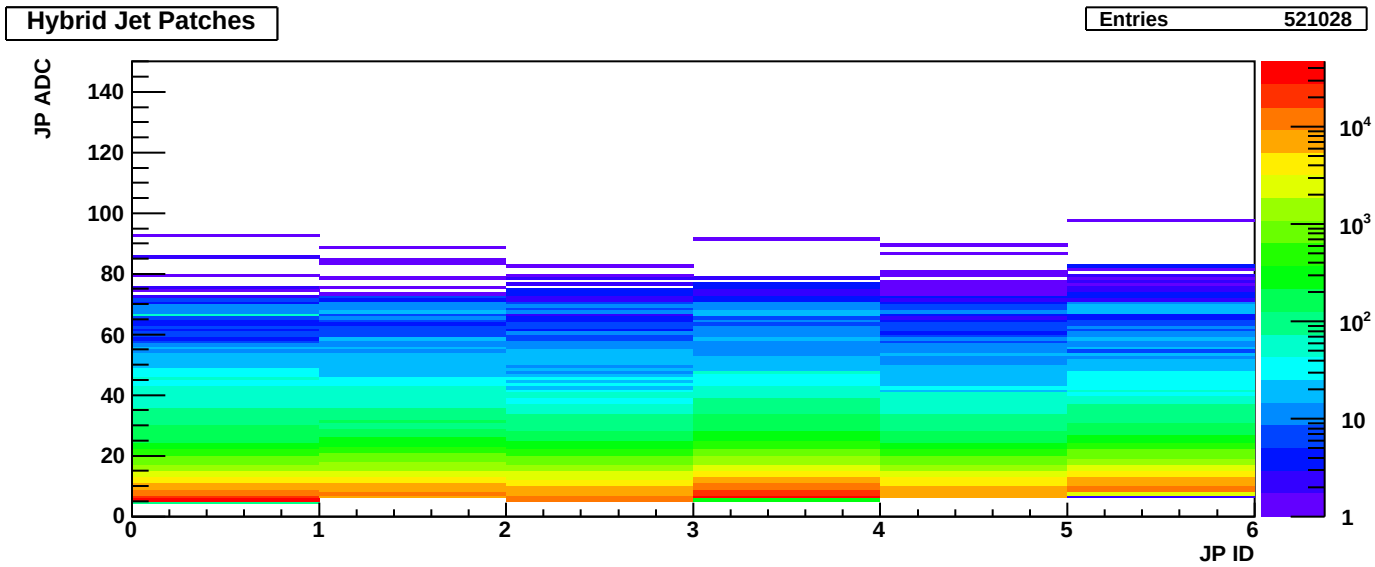

Barrel EMC L0 Input - Patch Sum
Entries 7.81542e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

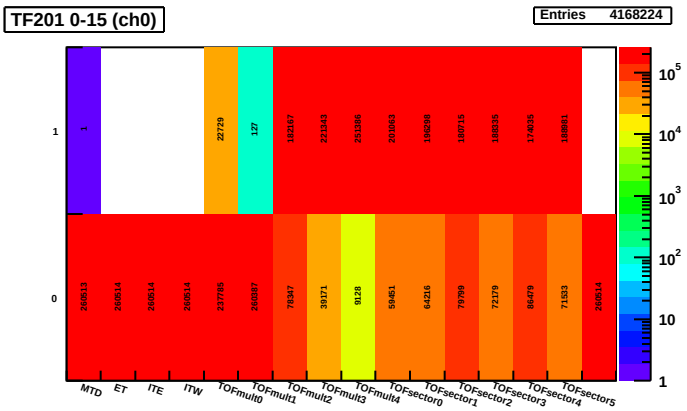

Endcap EMC L0 Input - High Tower
Entries 2.344626e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

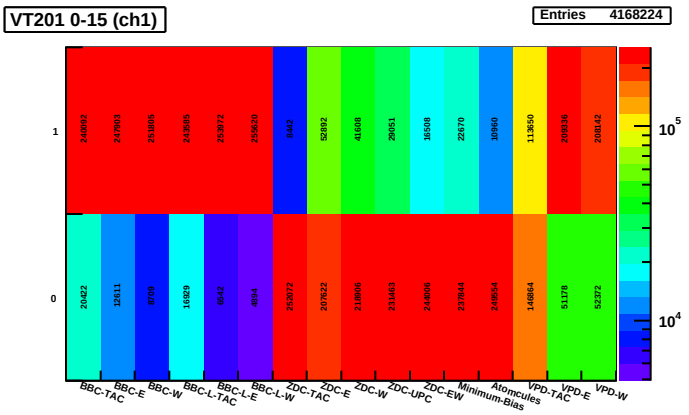

Endcap EMC L0 Input - Patch Sum
Entries 2.344626e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

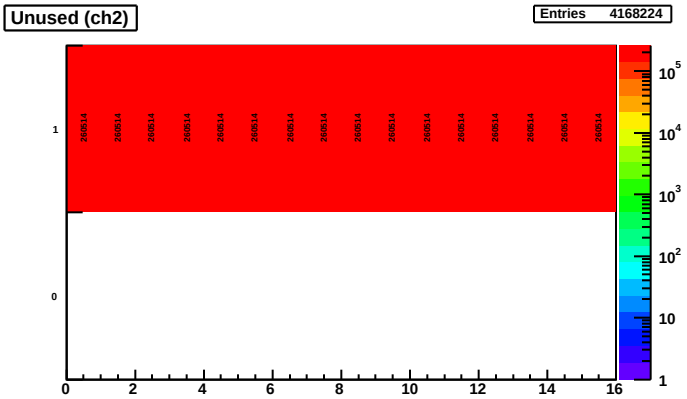

Barrel Jet Patches
Entries 4689252
JP ADC
JP ID

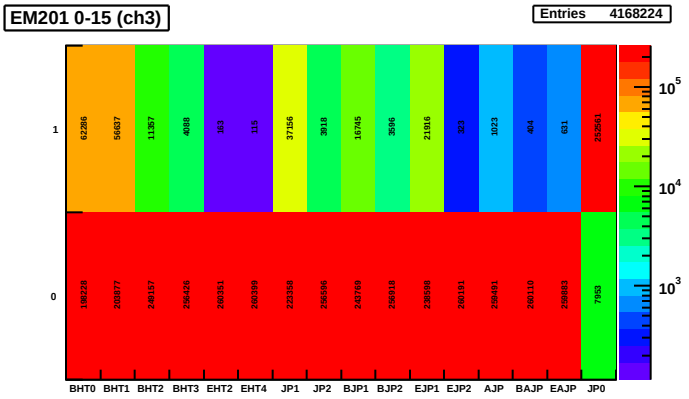

Endcap Jet Patches
Entries 1563084
JP ADC
JP ID

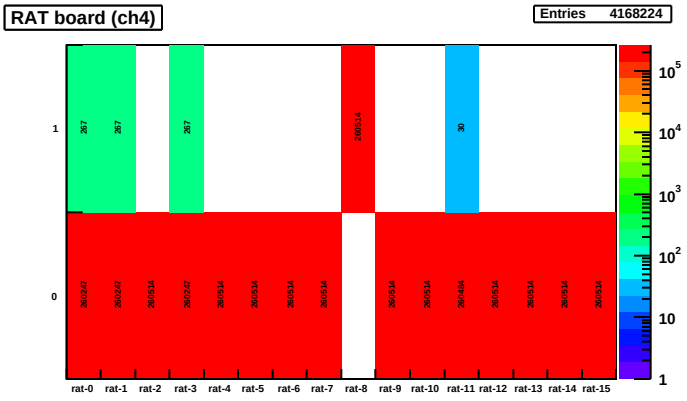

Hybrid Jet Patches
Entries 521028
JP ADC
JP ID

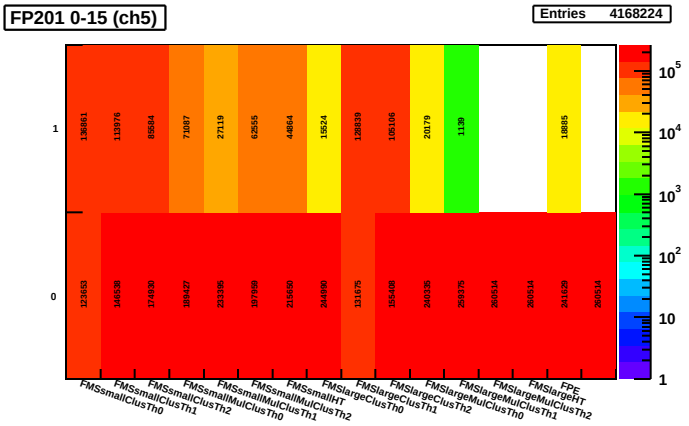

TF201 0-15 (ch0)
Entries 4168224
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

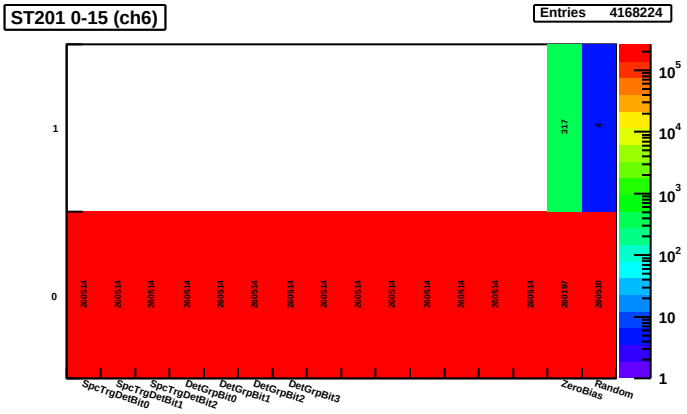

VT201 0-15 (ch1)
Entries 4168224
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

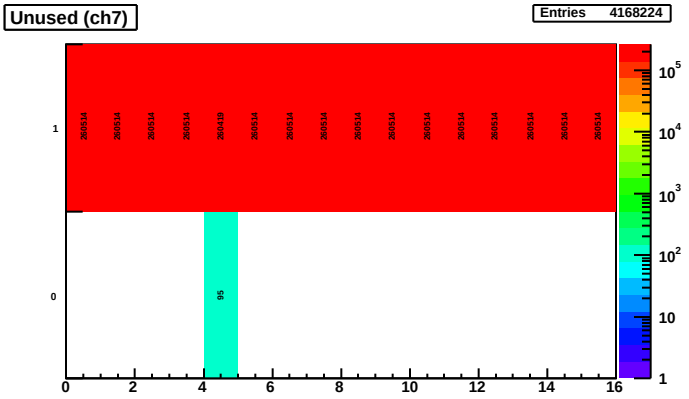

Unused (ch2)
Entries 4168224

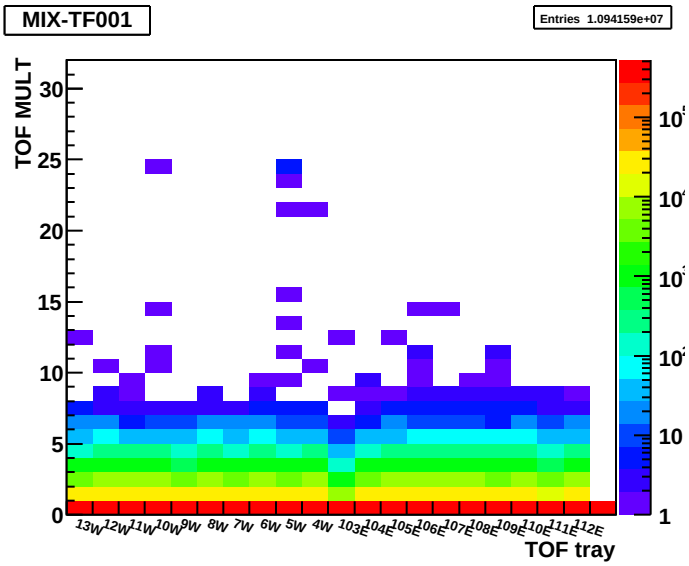

EM201 0-15 (ch3)
Entries 4168224
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

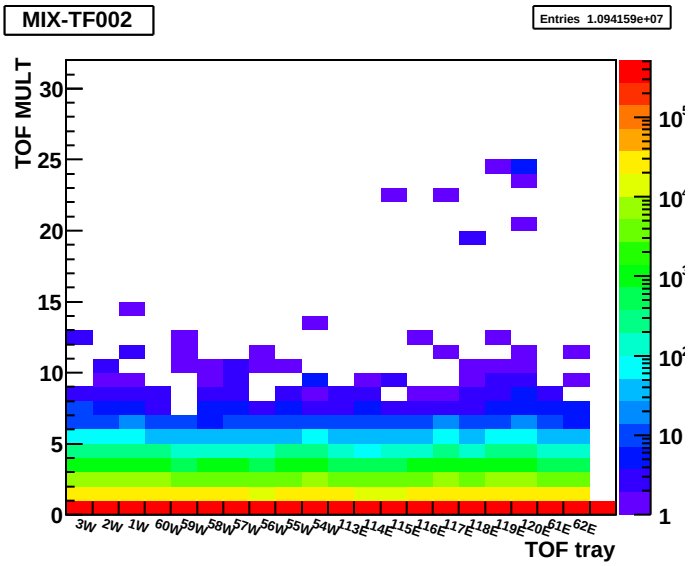

RAT board (ch4)
Entries 4168224
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

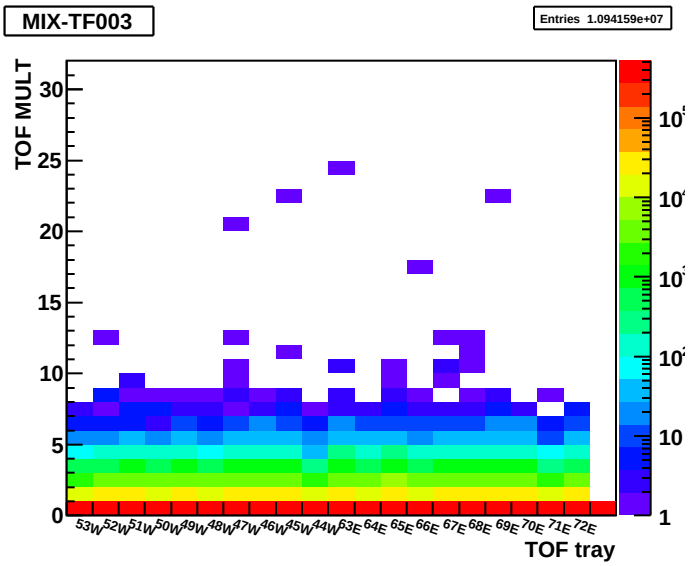

FP201 0-15 (ch5)
Entries 4168224
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

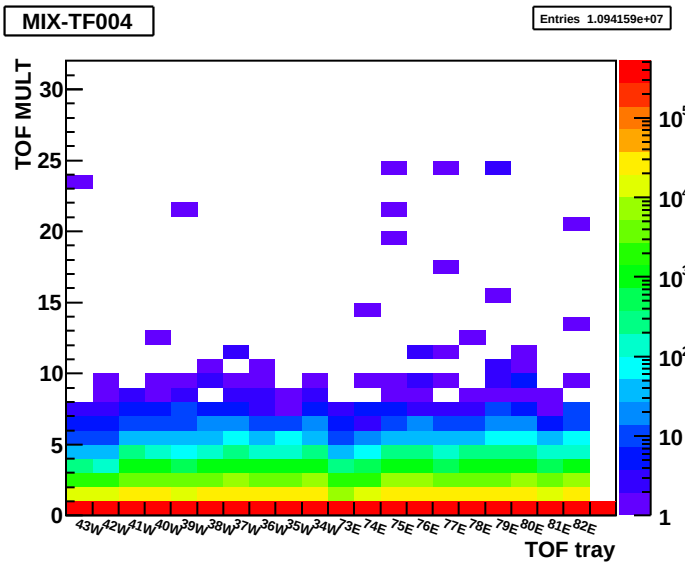

ST201 0-15 (ch6)
Entries 4168224
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

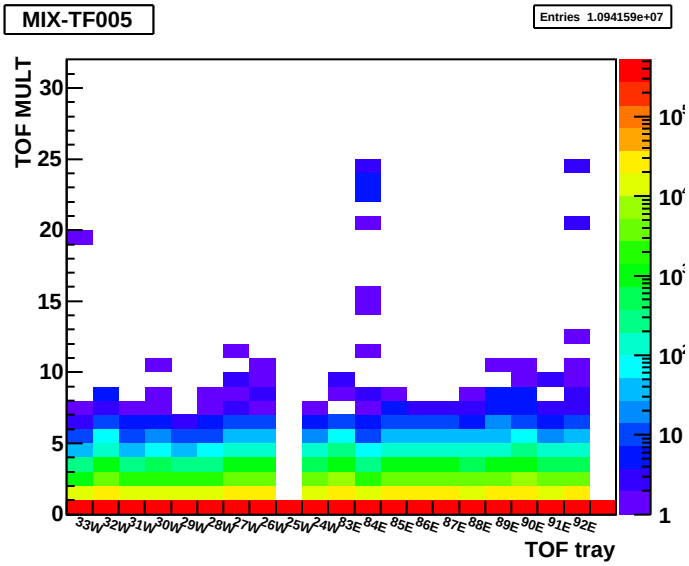

Unused (ch7)
Entries 4168224

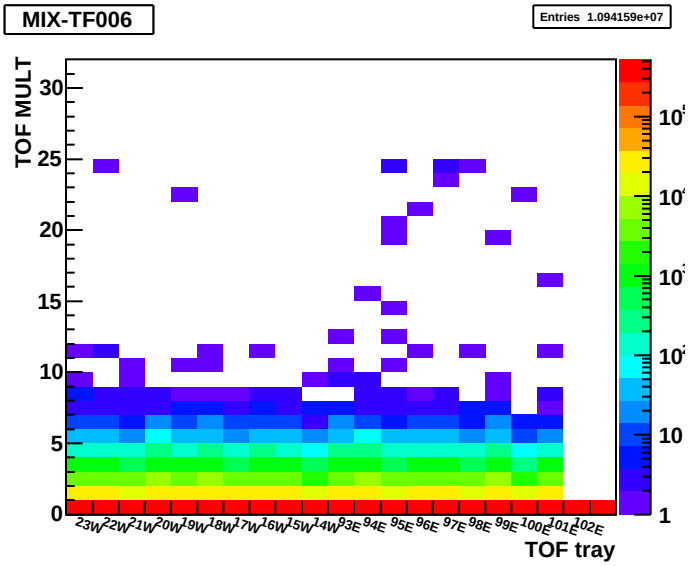

MIX-TF001
Entries 1.094159e+07
TOF MULT
TOF tray

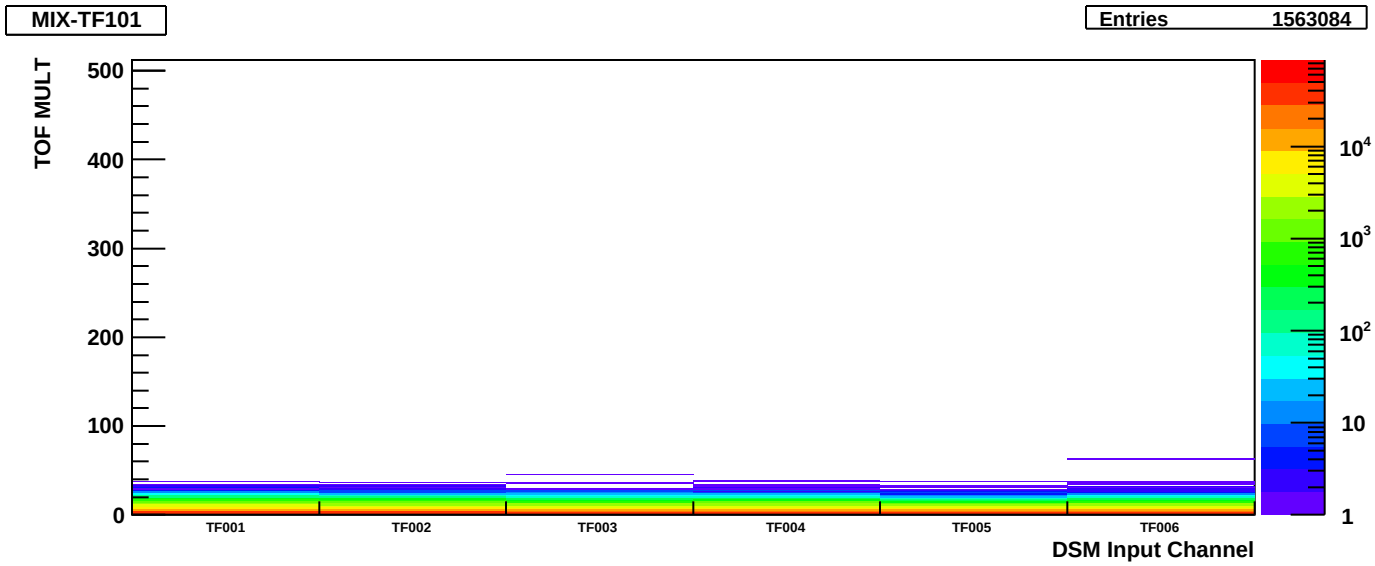

MIX-TF002
Entries 1.094159e+07
TOF MULT
TOF tray

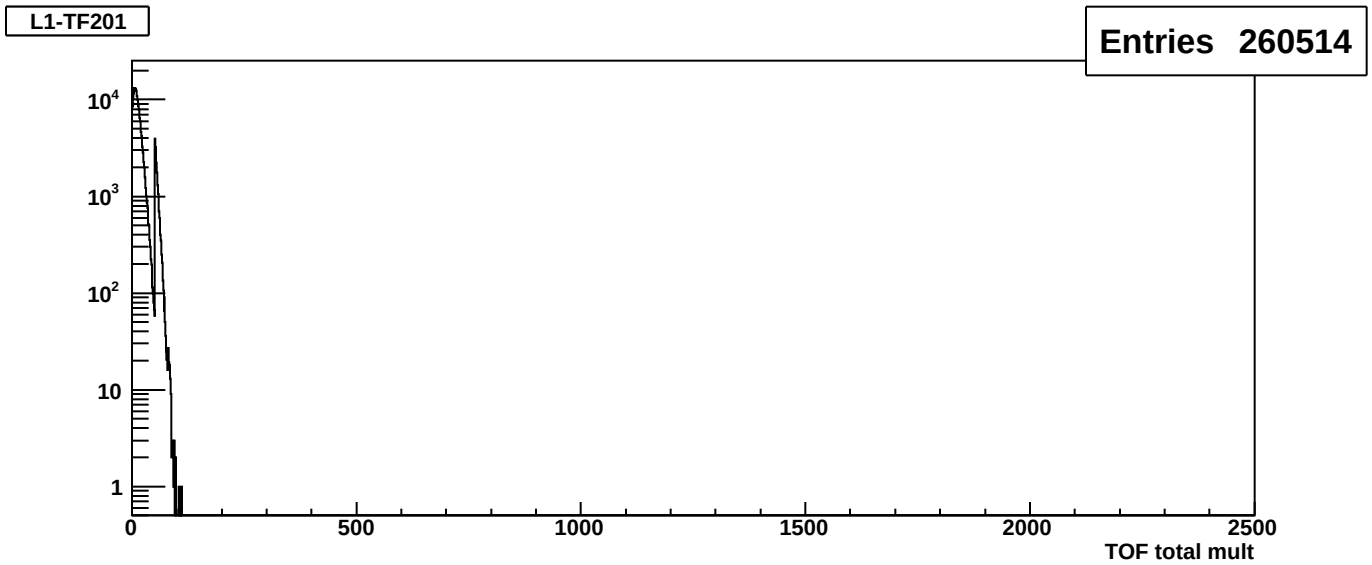

MIX-TF003
Entries 1.094159e+07
TOF MULT
TOF tray

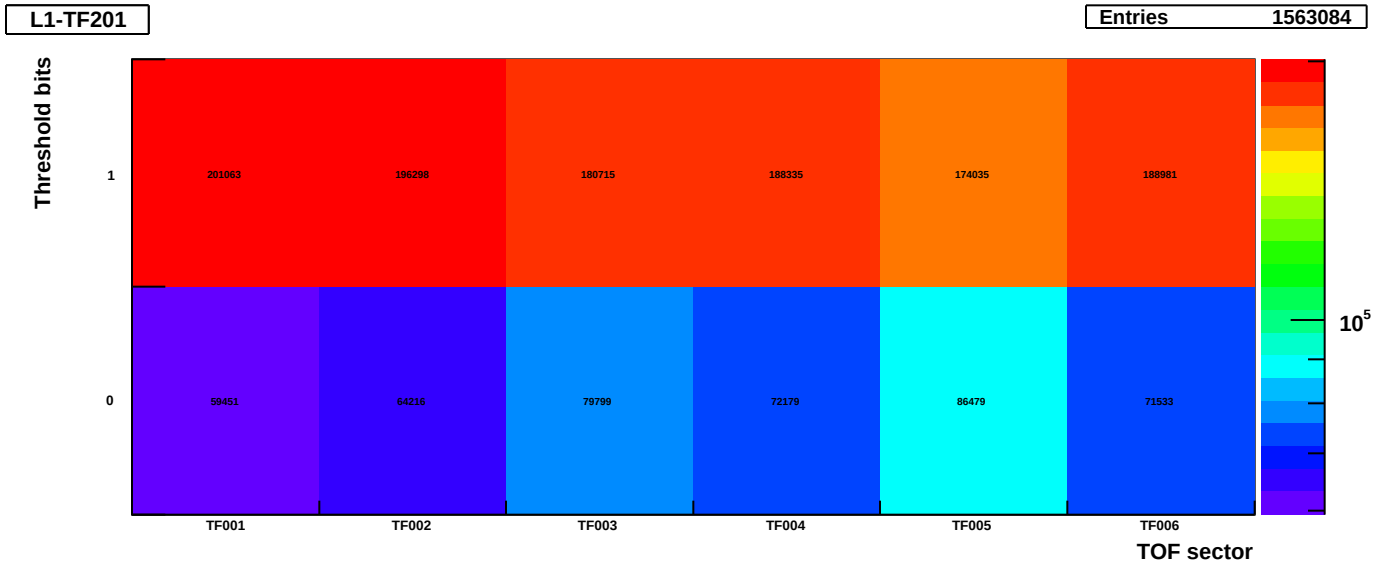

MIX-TF004
Entries 1.094159e+07
TOF MULT
TOF tray

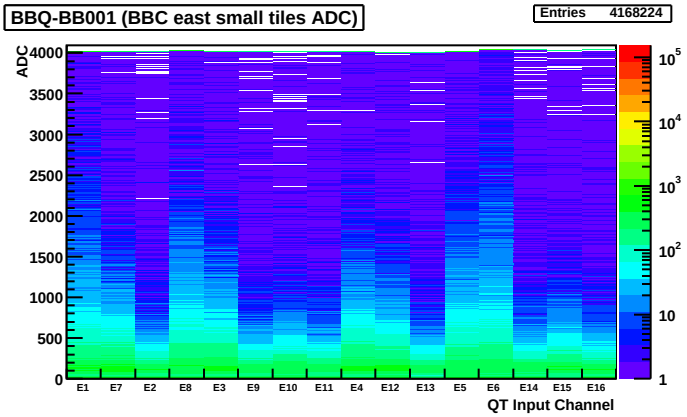

MIX-TF005
Entries 1.094159e+07
TOF MULT
TOF tray

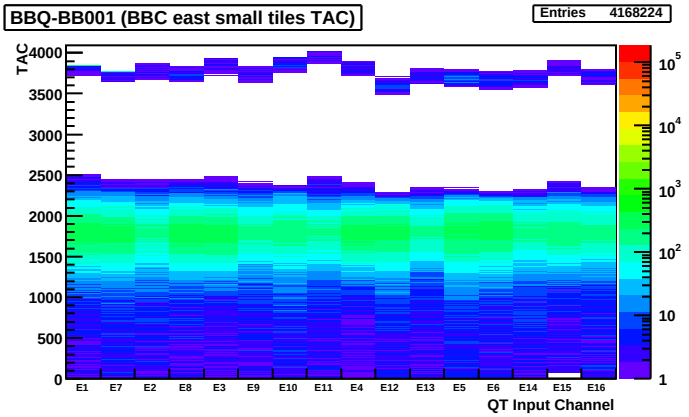

MIX-TF006
Entries 1.094159e+07
TOF MULT
TOF tray

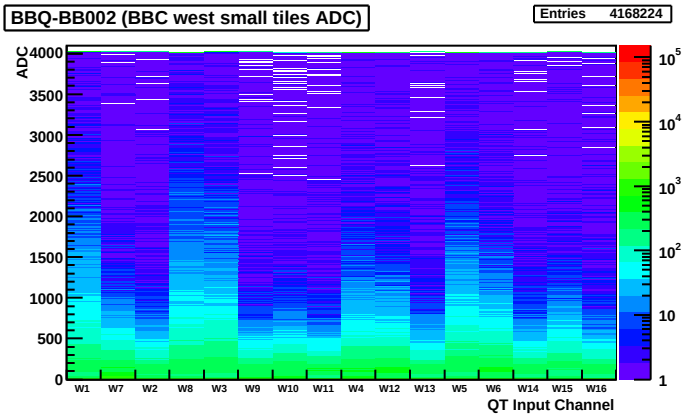

MIX-TF101
Entries 1563084
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

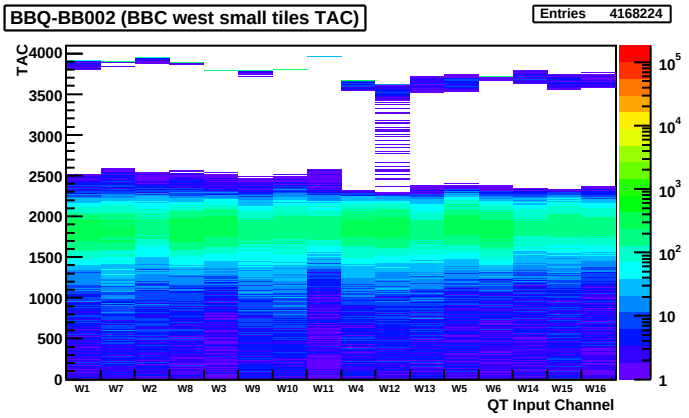

L1-TF201
Entries 260514
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

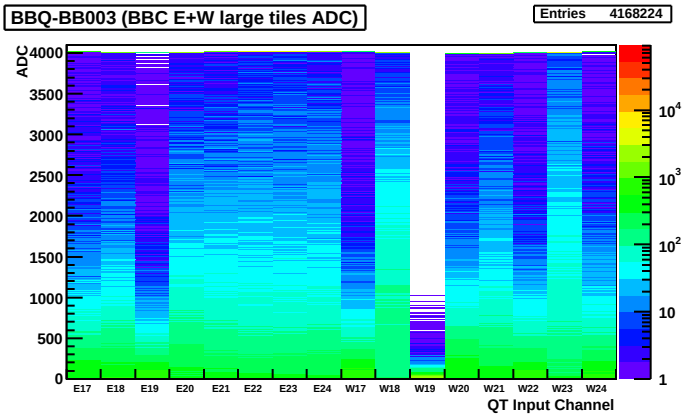

L1-TF201
Entries 1563084
Threshold bits
1
0
201063
196298
180715
188335
174035
188981
59451
64216
79799
72179
86479
71533
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

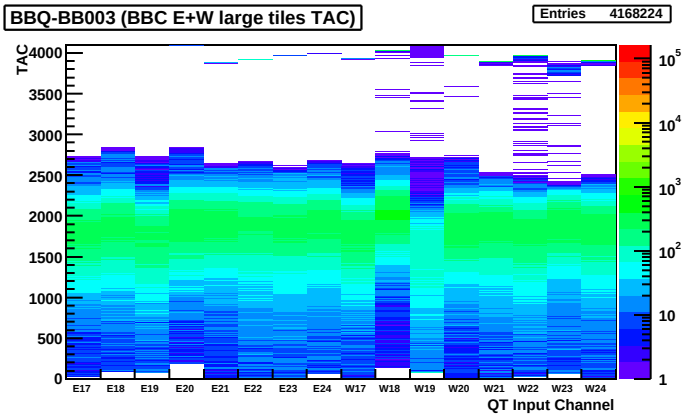

BBQ-BB001 (BBC east small tiles ADC)
Entries 4168224
ADC
QT Input Channel

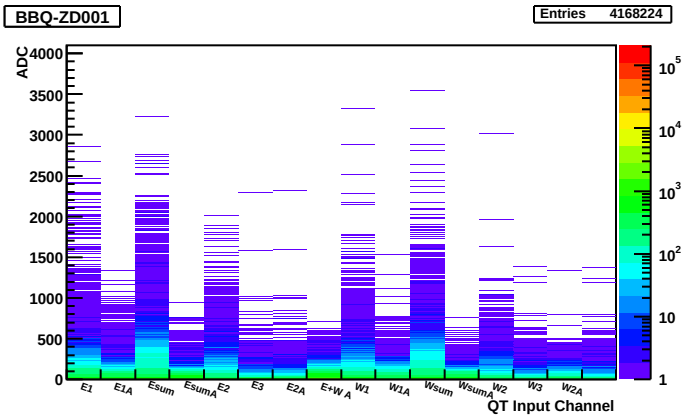

BBQ-BB001 (BBC east small tiles TAC)
Entries 4168224
TAC
QT Input Channel

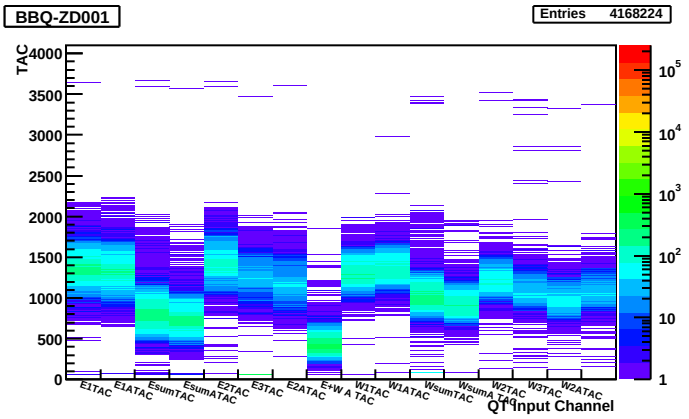

BBQ-BB002 (BBC west small tiles ADC)
Entries 4168224
ADC
QT Input Channel

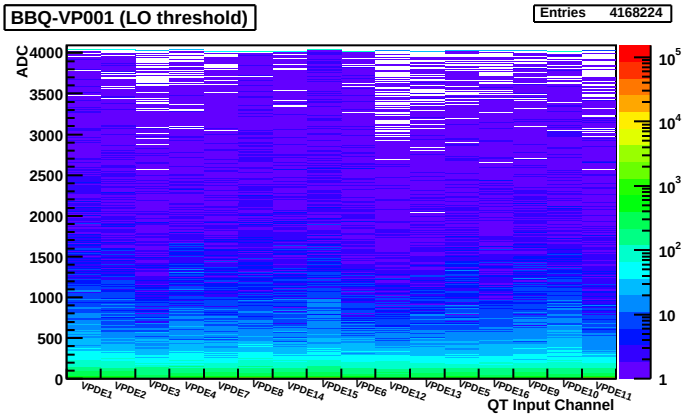

BBQ-BB002 (BBC west small tiles TAC)
Entries 4168224
TAC
QT Input Channel

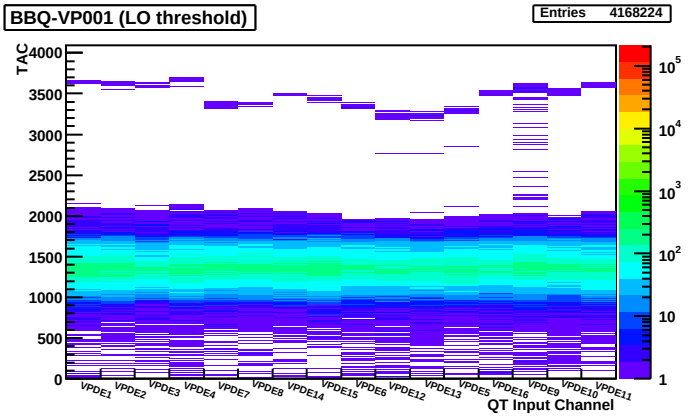

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4168224
ADC
QT Input Channel

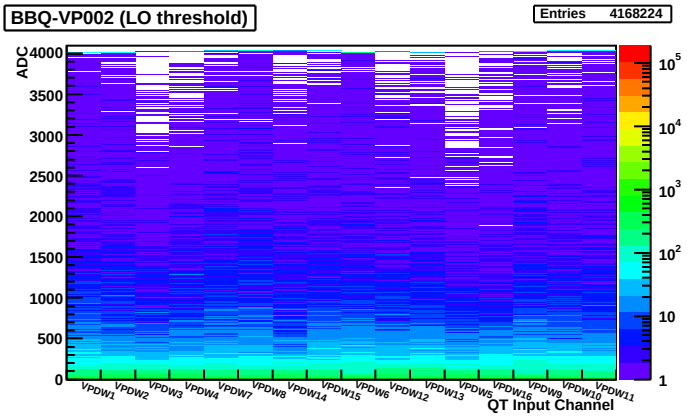

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4168224
TAC
QT Input Channel

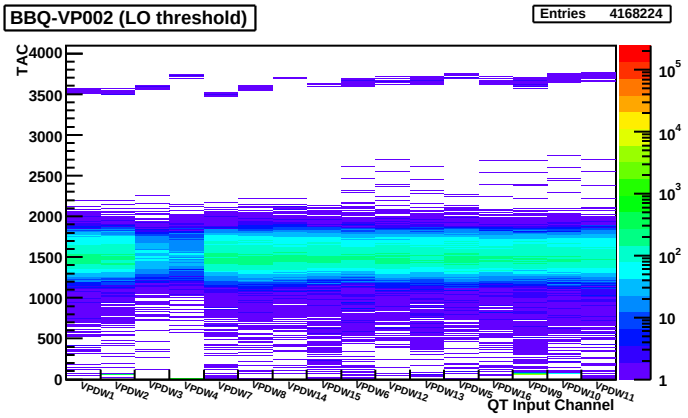

BBQ-ZD001
Entries 4168224
ADC
QT Input Channel

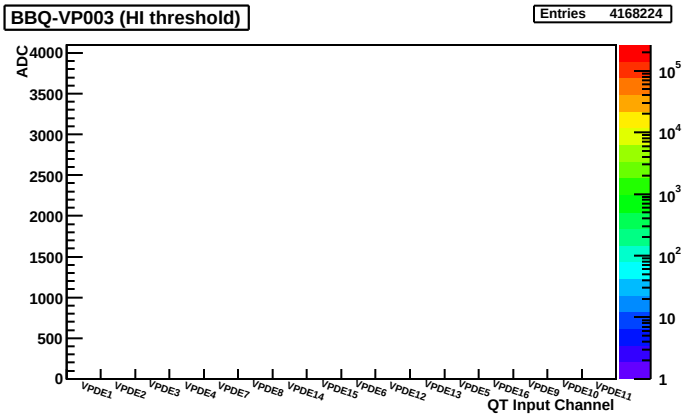

BBQ-ZD001
Entries 4168224
TAC
QT Input Channel

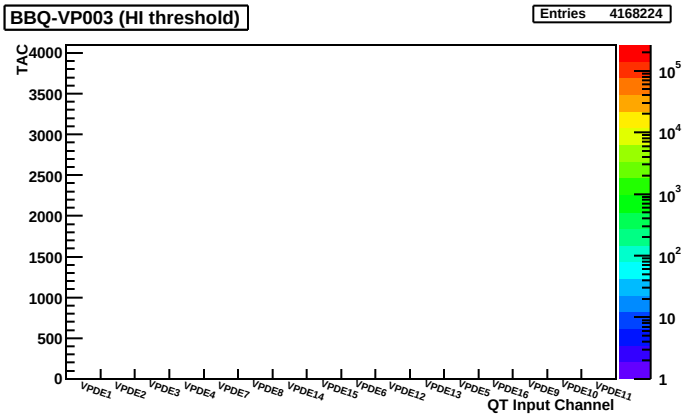

BBQ-VP001 (LO threshold)
Entries 4168224
ADC
QT Input Channel

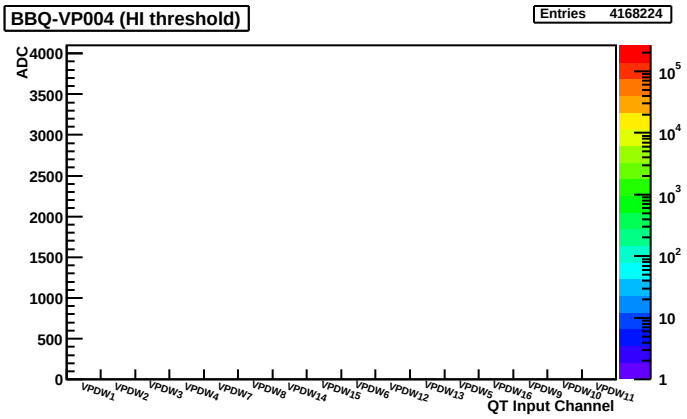

BBQ-VP001 (LO threshold)
Entries 4168224
TAC
QT Input Channel

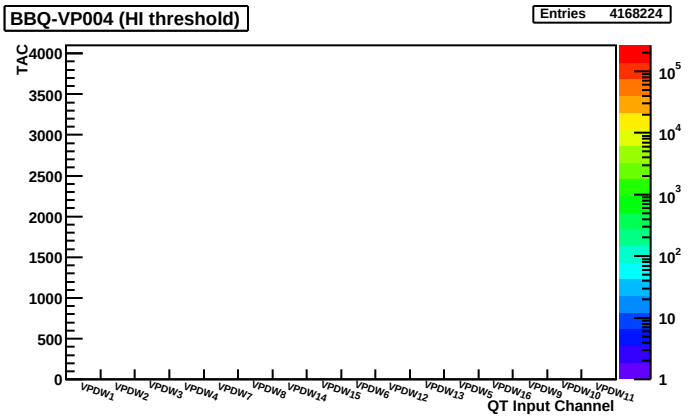

BBQ-VP002 (LO threshold)
Entries 4168224
ADC
QT Input Channel

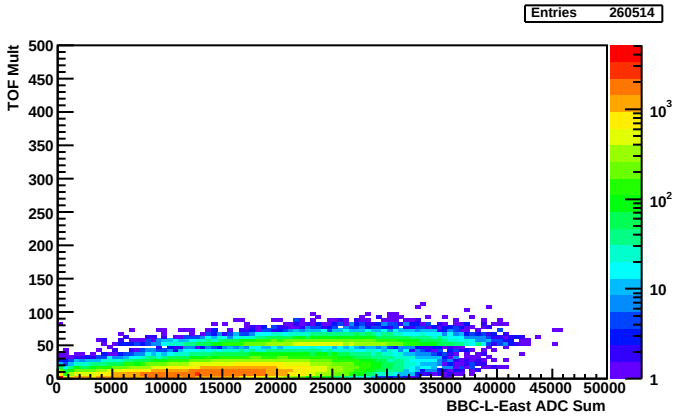

BBQ-VP002 (LO threshold)
Entries 4168224
TAC
QT Input Channel

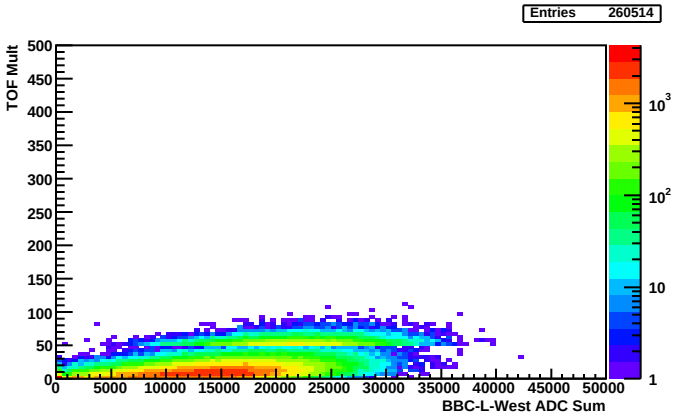

BBQ-VP003 (HI threshold)
Entries 4168224
ADC
QT Input Channel

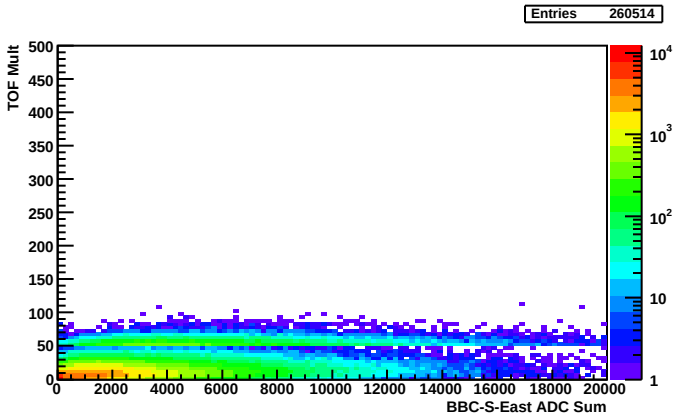

BBQ-VP003 (HI threshold)
Entries 4168224
TAC
QT Input Channel

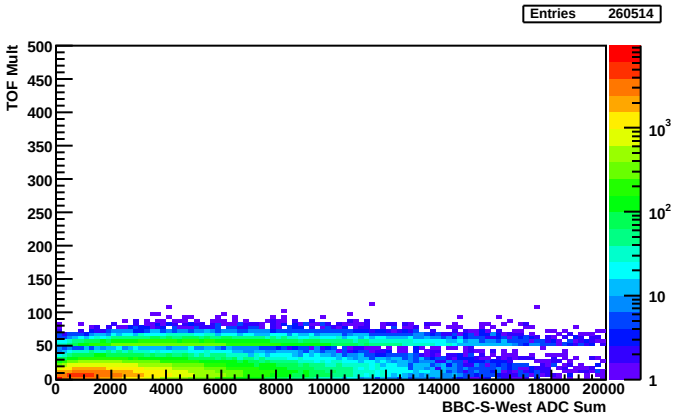

BBQ-VP004 (HI threshold)
Entries 4168224
ADC
QT Input Channel

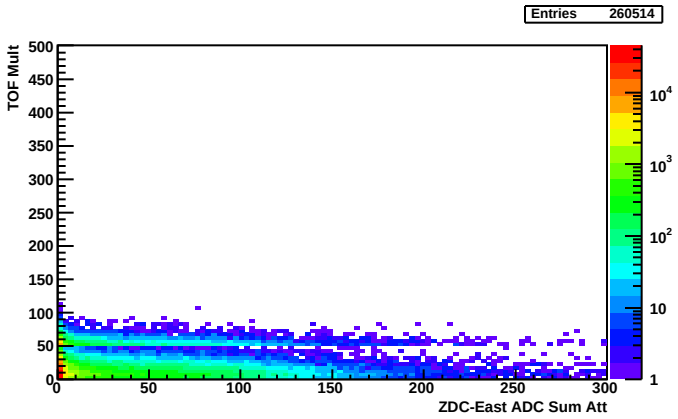

BBQ-VP004 (HI threshold)
Entries 4168224
TAC
QT Input Channel

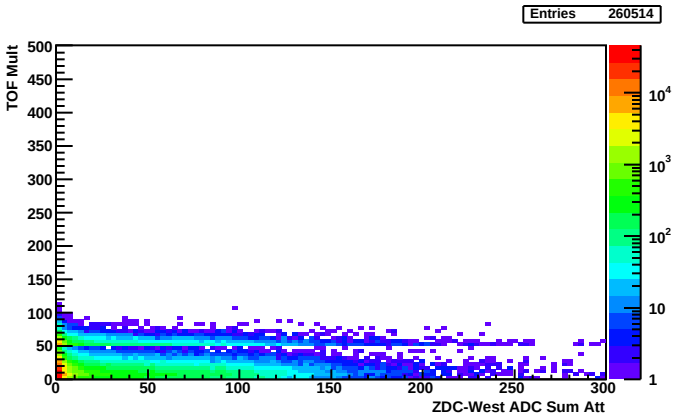

Entries 260514
TOF Mult
BBC-L-East ADC Sum

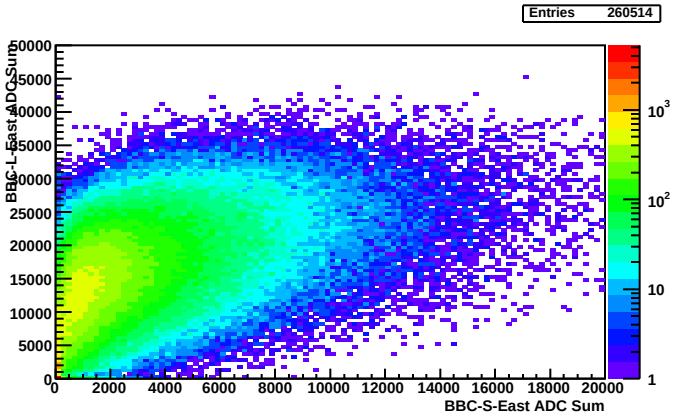

Entries 260514
TOF Mult
BBC-L-West ADC Sum

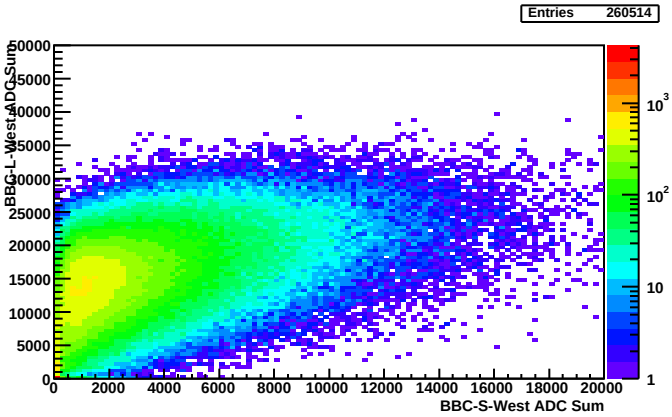

Entries 260514
TOF Mult
BBC-S-East ADC Sum

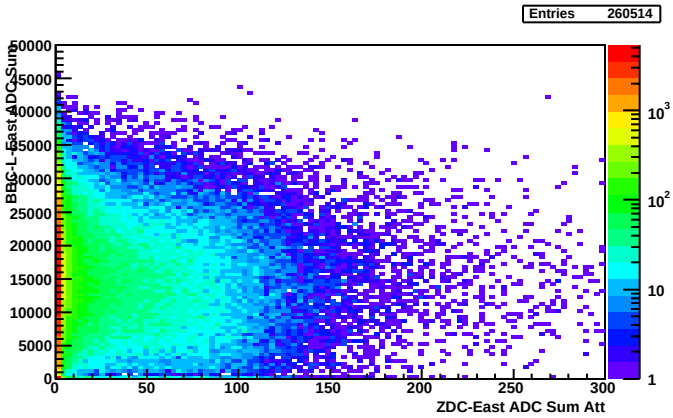

Entries 260514
TOF Mult
BBC-S-West ADC Sum

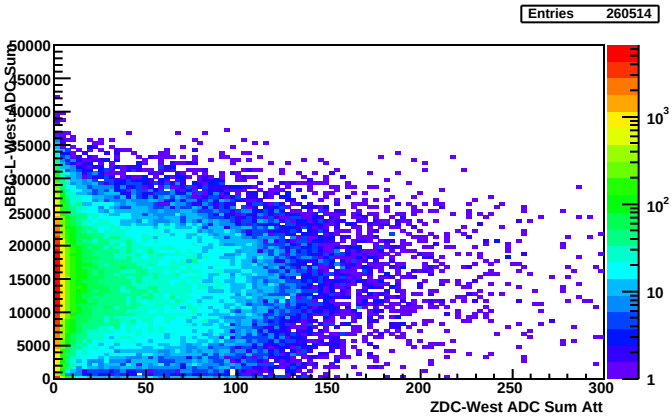

Entries 260514
TOF Mult
ZDC-East ADC Sum Att

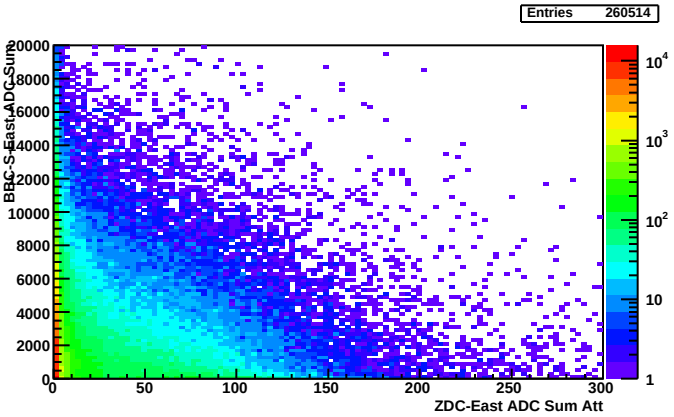

Entries 260514
TOF Mult
ZDC-West ADC Sum Att

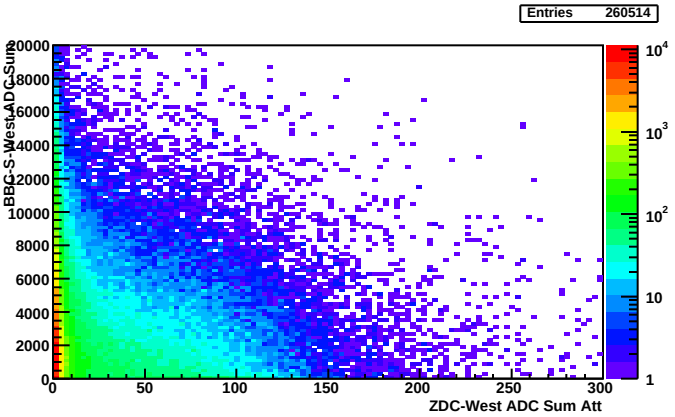

Entries 260514
BBC-L-East ADC Sum
BBC-S-East ADC Sum

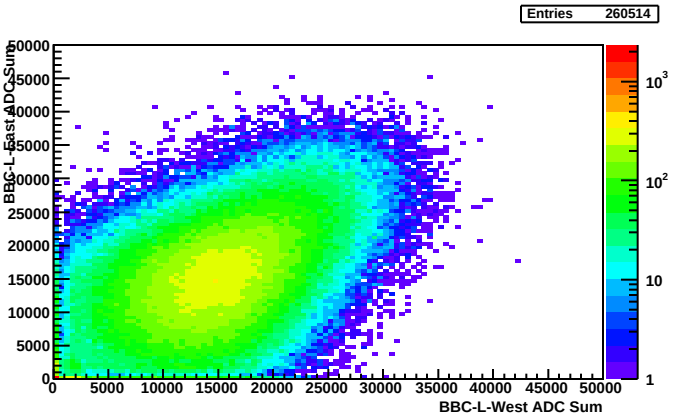

Entries 260514
BBC-L-West ADC Sum
BBC-S-West ADC Sum

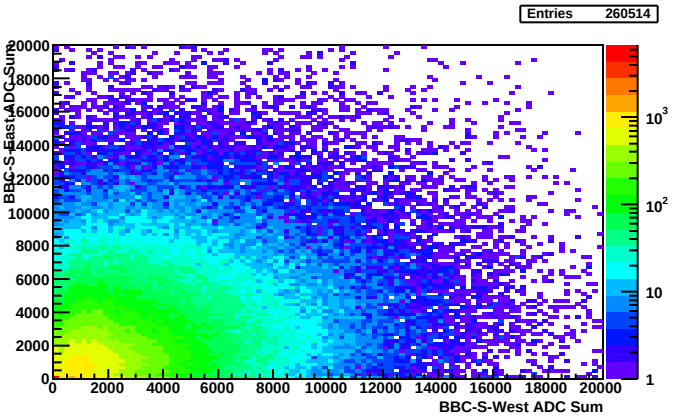

Entries 260514
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

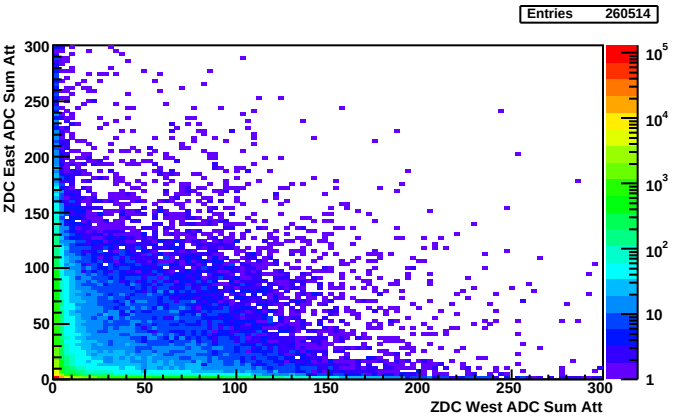

Entries 260514
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

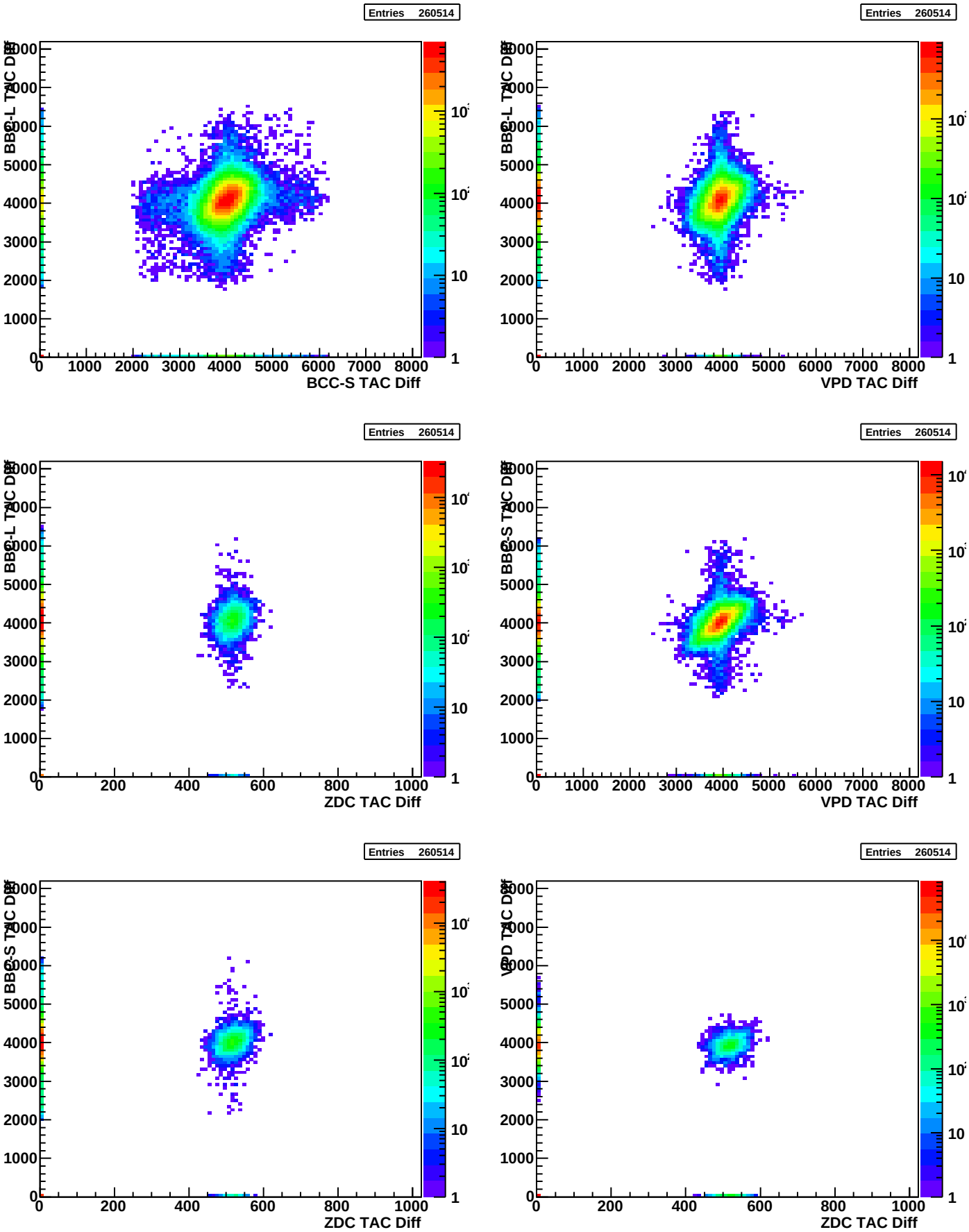

Entries 260514
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

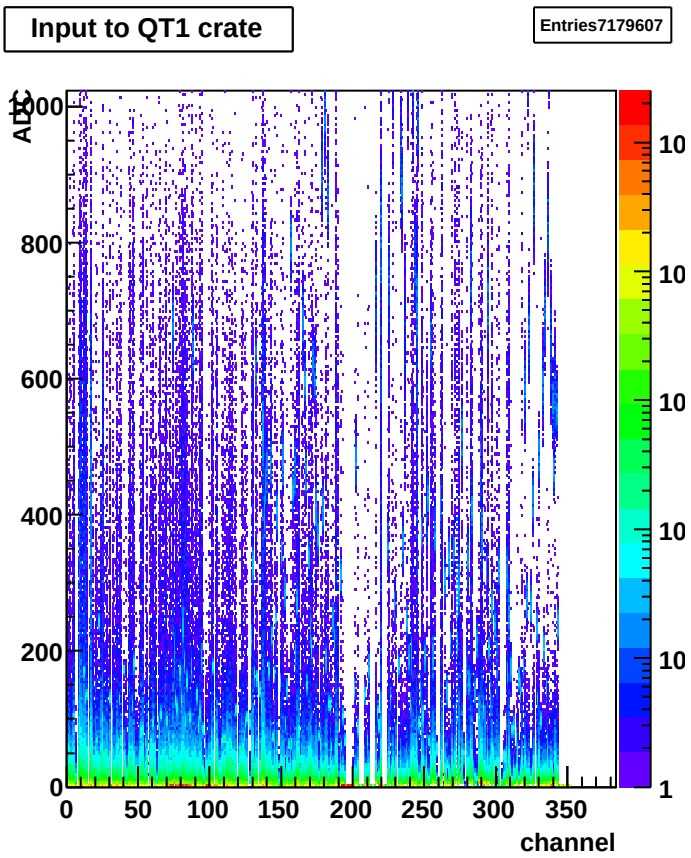

Entries 260514
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

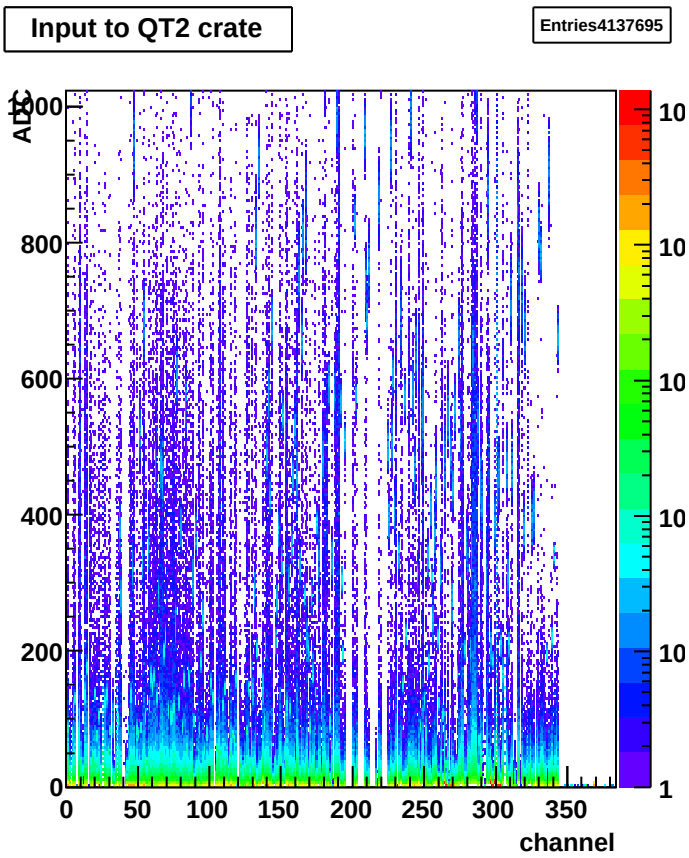

Entries 260514
BBC-L-East ADC Sum
BBC-L-West ADC Sum

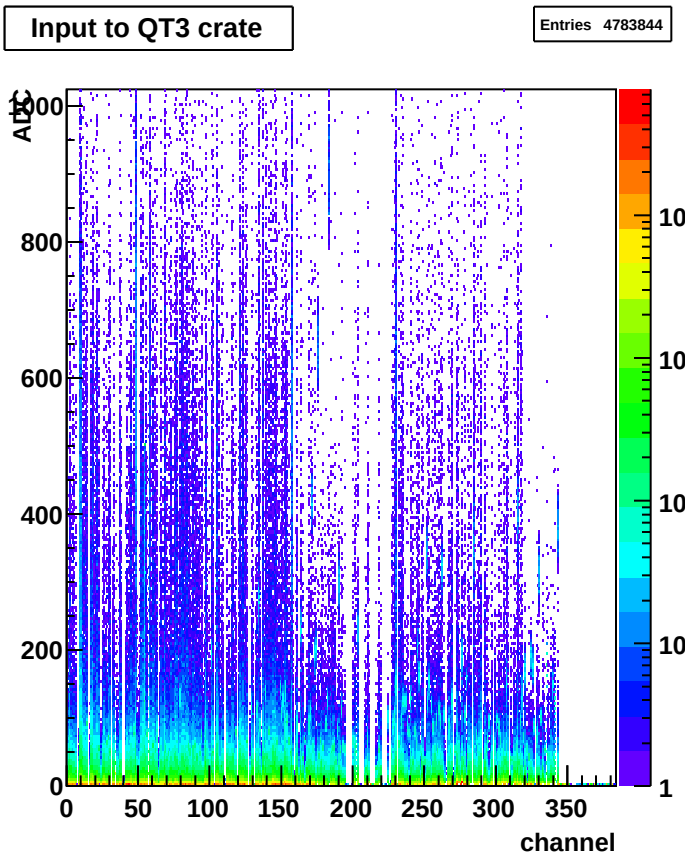

Entries 260514
BBC-S-East ADC Sum
BBC-S-West ADC Sum

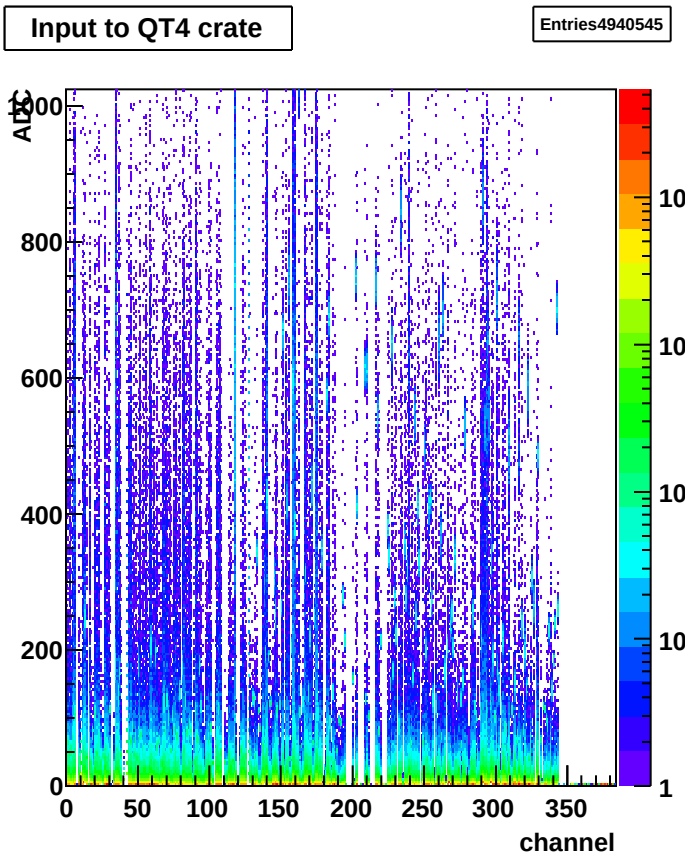

Entries 260514
ZDC East ADC Sum Att
ZDC West ADC Sum Att

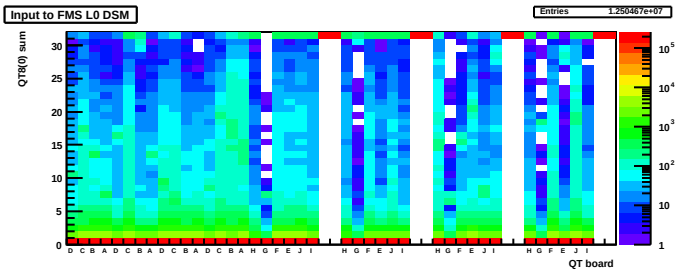

Entries 260514
BBC-L TAC Diff
BCC-S TAC Diff
Entries 260514
BBC-L TAC Diff
VPD TAC Diff
Entries 260514
BBC-L TAC Diff
ZDC TAC Diff
Entries 260514
BBC-S TAC Diff
VPD TAC Diff
Entries 260514
BBC-S TAC Diff
ZDC TAC Diff
Entries 260514
VPD TAC Diff
ZDC TAC Diff

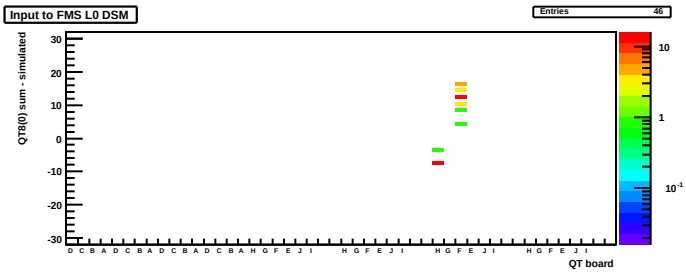

Input to QT1 crate
Entries7179607
ADC
channel

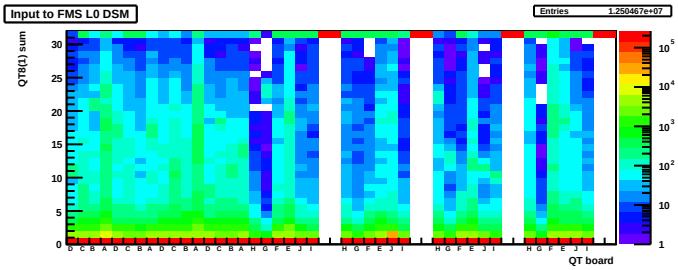

Input to QT2 crate
Entries4137695
ADC
channel

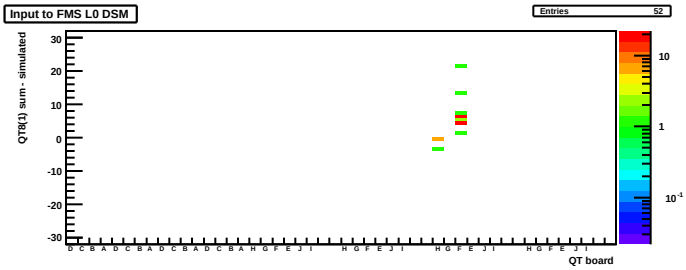

Input to QT3 crate
Entries 4783844
ADC
channel

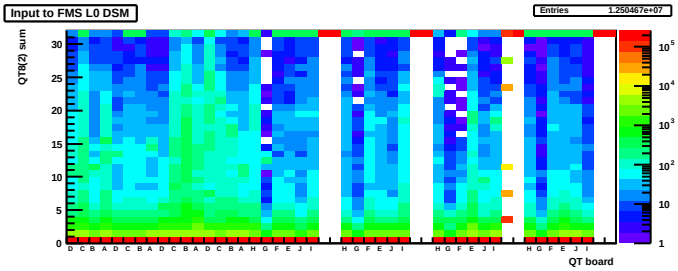

Input to QT4 crate
Entries4940545
ADC
channel

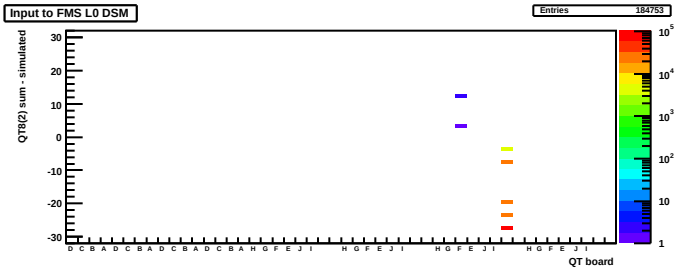

Input to FMS L0 DSM
Entries 1.250467e+07
QT8(0) sum
QT board

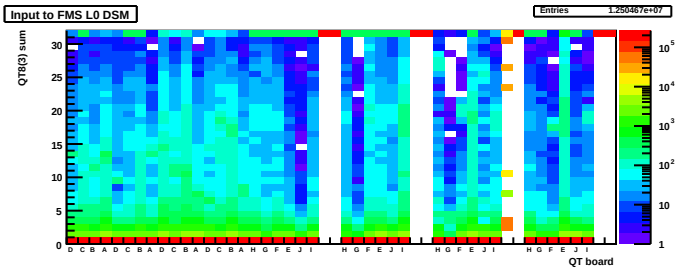

Input to FMS L0 DSM
Entries 46
QT8(0) sum - simulated
QT board

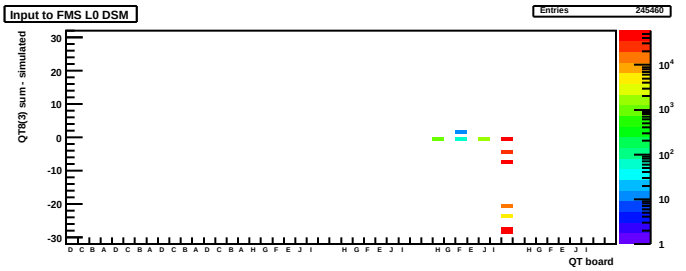

Input to FMS L0 DSM
Entries 1.250467e+07
QT8(1) sum
QT board

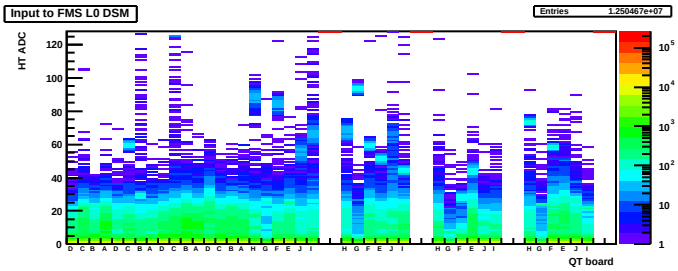

Input to FMS L0 DSM
Entries 52
QT8(1) sum - simulated
QT board

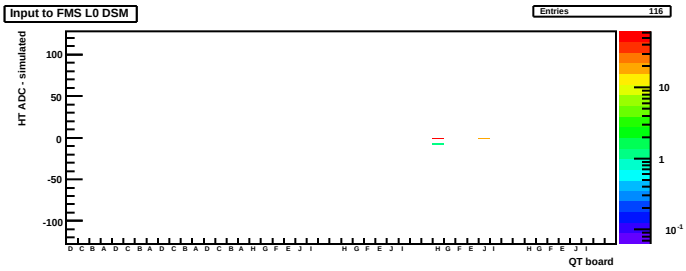

Input to FMS L0 DSM
Entries 1.250467e+07
QT8(2) sum
QT board

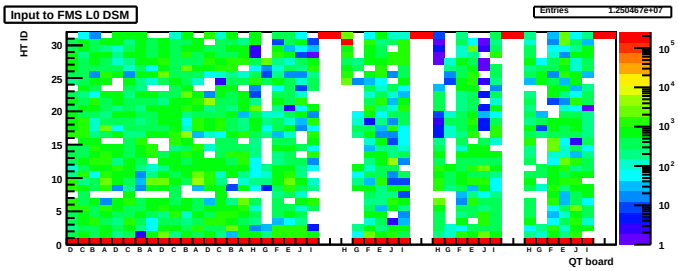

Input to FMS L0 DSM
Entries 184753
QT8(2) sum - simulated
QT board

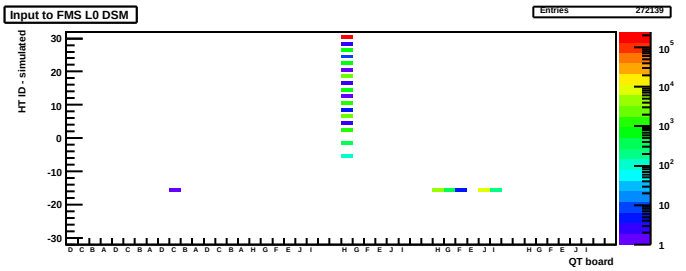

Input to FMS L0 DSM
Entries 1.250467e+07
QT8(3) sum
QT board

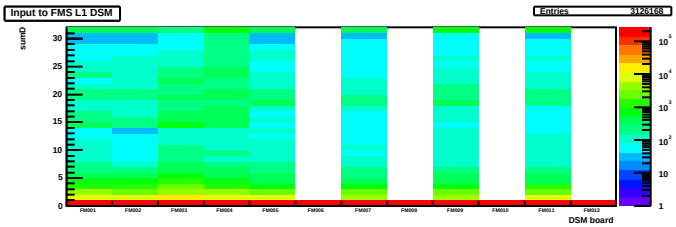

Input to FMS L0 DSM
Entries 245460
QT8(3) sum - simulated
QT board

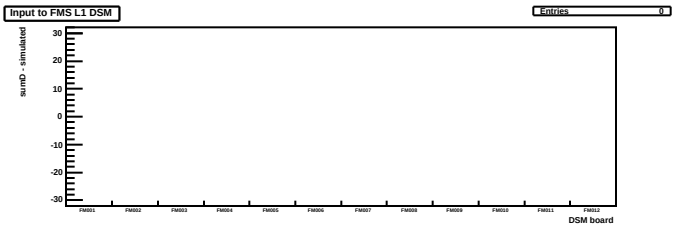

Input to FMS L0 DSM
Entries 1.250467e+07
HT ADC
QT board

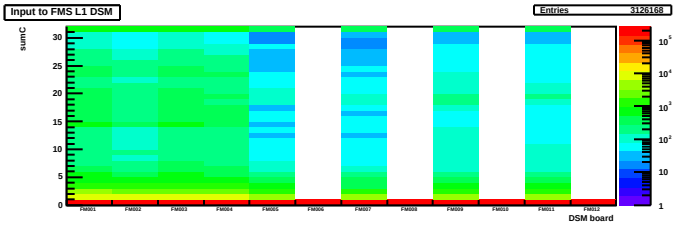

Input to FMS L0 DSM
Entries 116
HT ADC - simulated
QT board

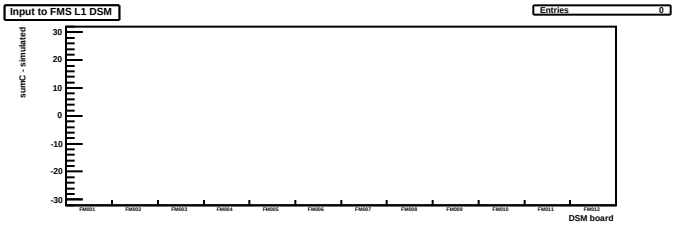

Input to FMS L0 DSM
Entries 1.250467e+07
HT ID
QT board

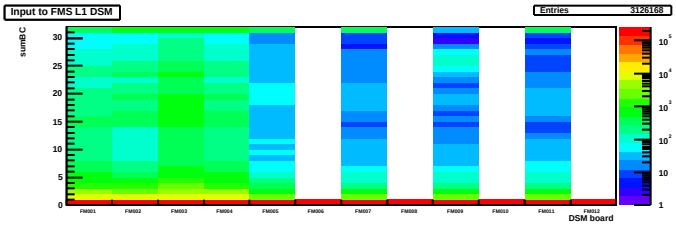

Input to FMS L0 DSM
Entries 272139
HT ID - simulated
QT board

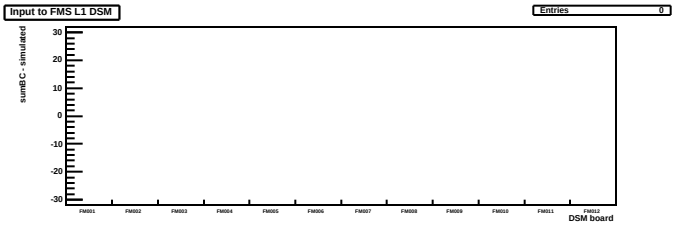

Input to FMS L1 DSM
Entries 3126168
sumD
DSM board

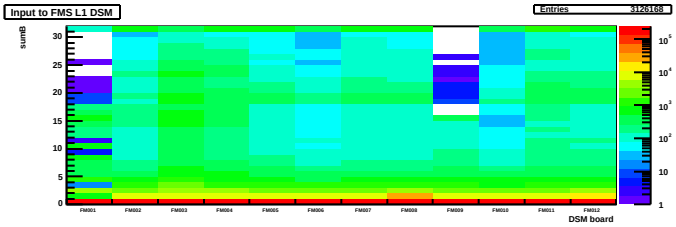

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

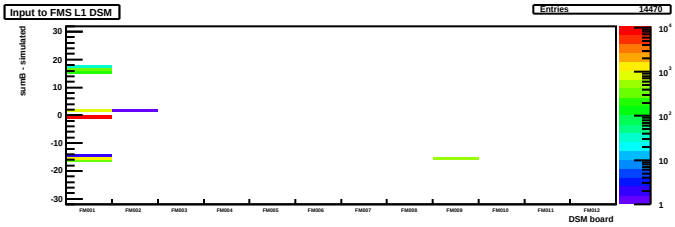

Input to FMS L1 DSM
Entries 3126168
sumC
DSM board

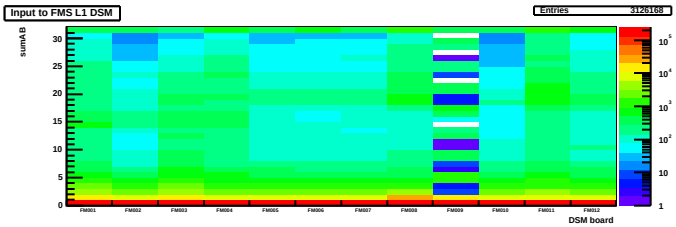

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

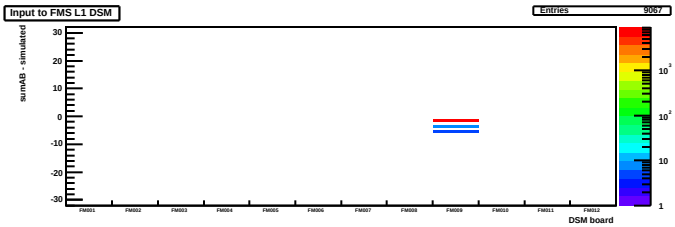

Input to FMS L1 DSM
Entries 3126168
sumBC
DSM board

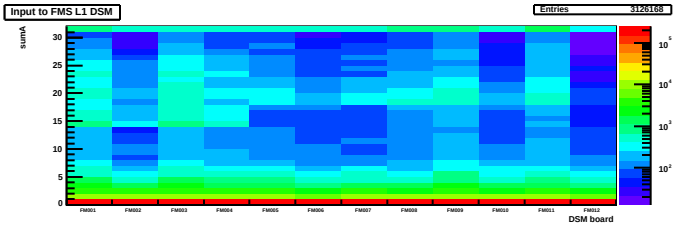

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

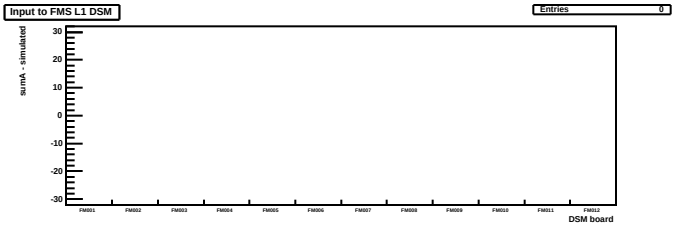

Input to FMS L1 DSM
Entries 3126168
sumB
DSM board

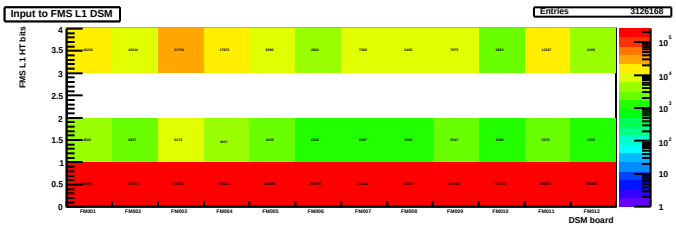

Input to FMS L1 DSM
Entries 14470
sumB - simulated
DSM board

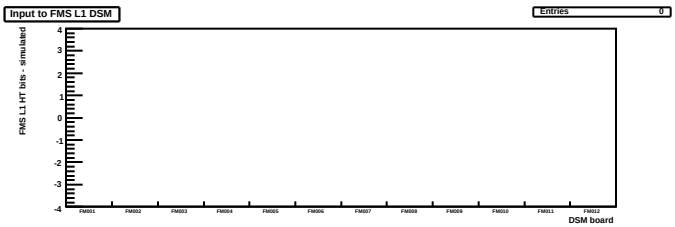

Input to FMS L1 DSM
Entries 3126168
sumAB
DSM board

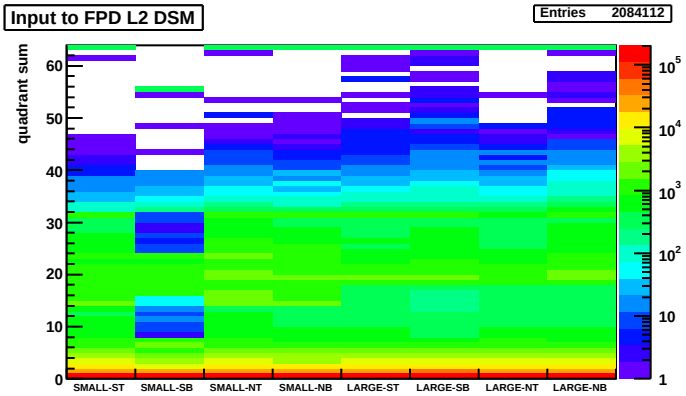

Input to FMS L1 DSM
Entries 9067
sumAB - simulated
DSM board

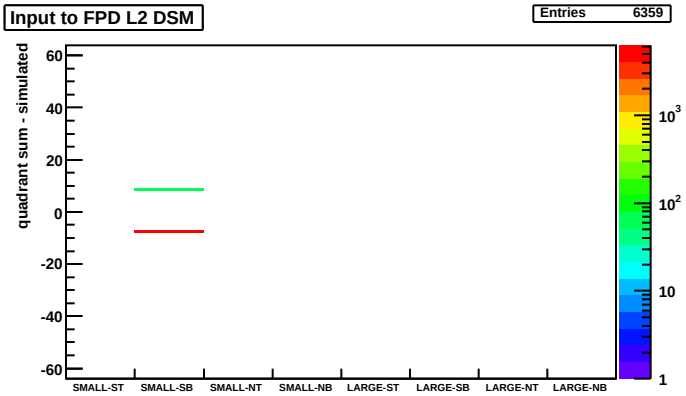

Input to FMS L1 DSM
Entries 3126168
sumA
DSM board

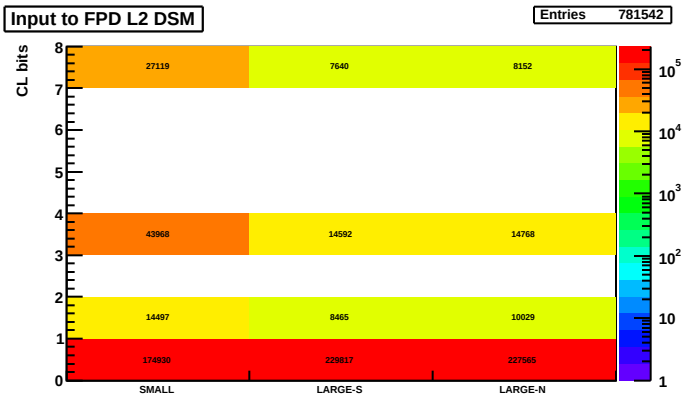

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

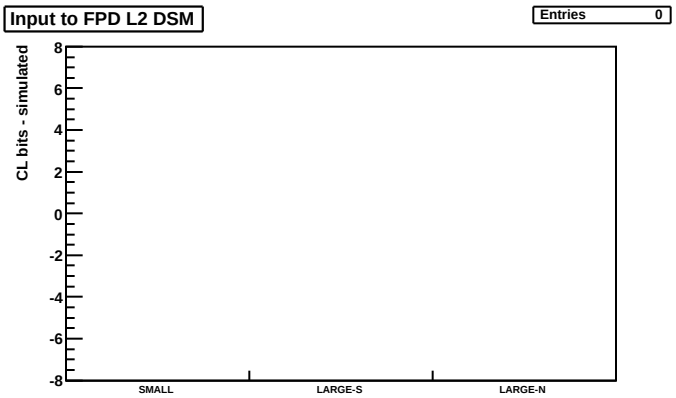

Input to FMS L1 DSM
Entries 3126168
FMS L1 HT bits
DSM board

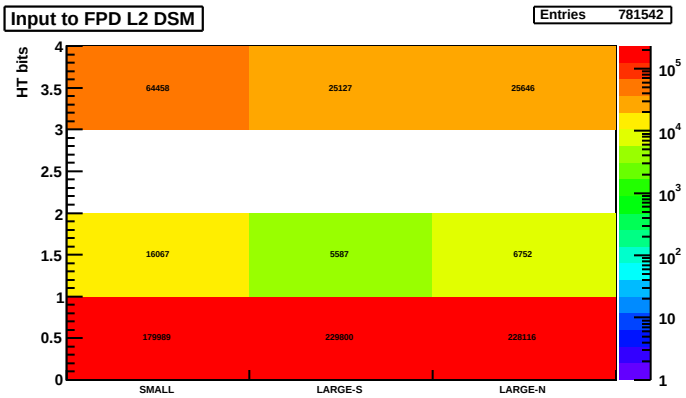

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

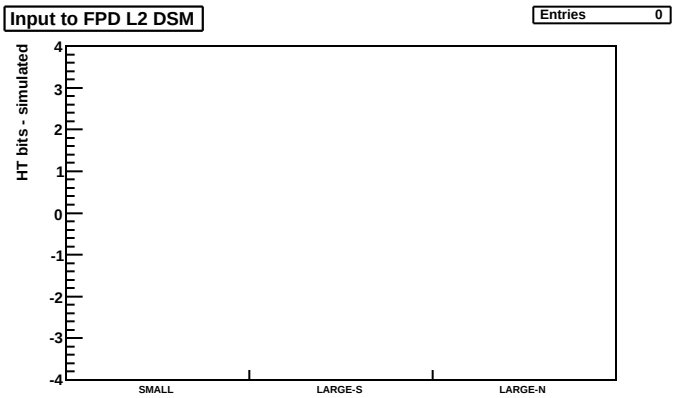

Input to FPD L2 DSM
Entries 2084112
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

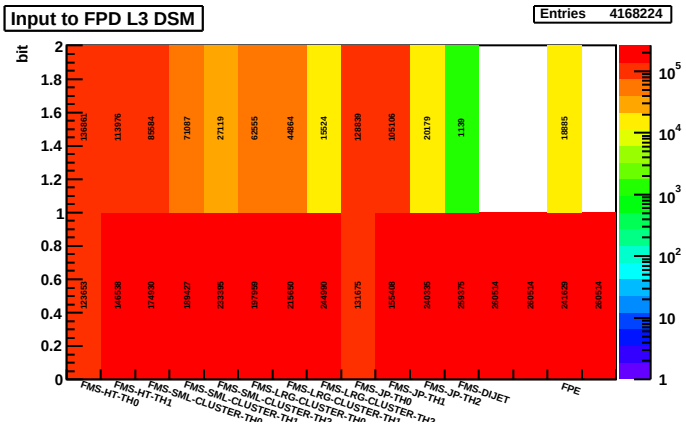

Input to FPD L2 DSM
Entries 6359
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

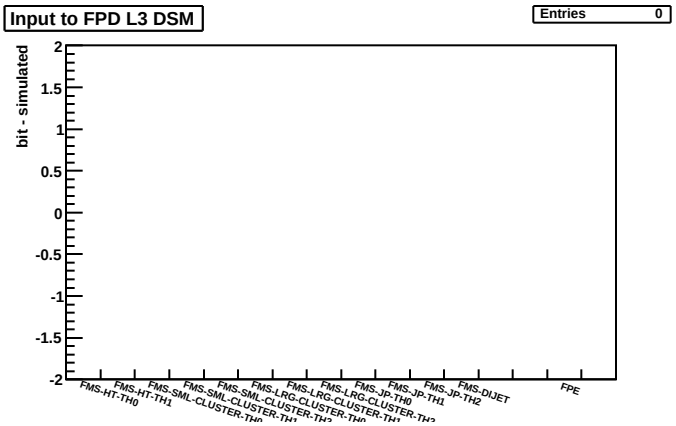

Input to FPD L2 DSM
Entries 781542
CL bits
27119
7640
8152
43968
14592
14768
14497
8465
10029
174930
229817
227565
SMALL
LARGE-S
LARGE-N

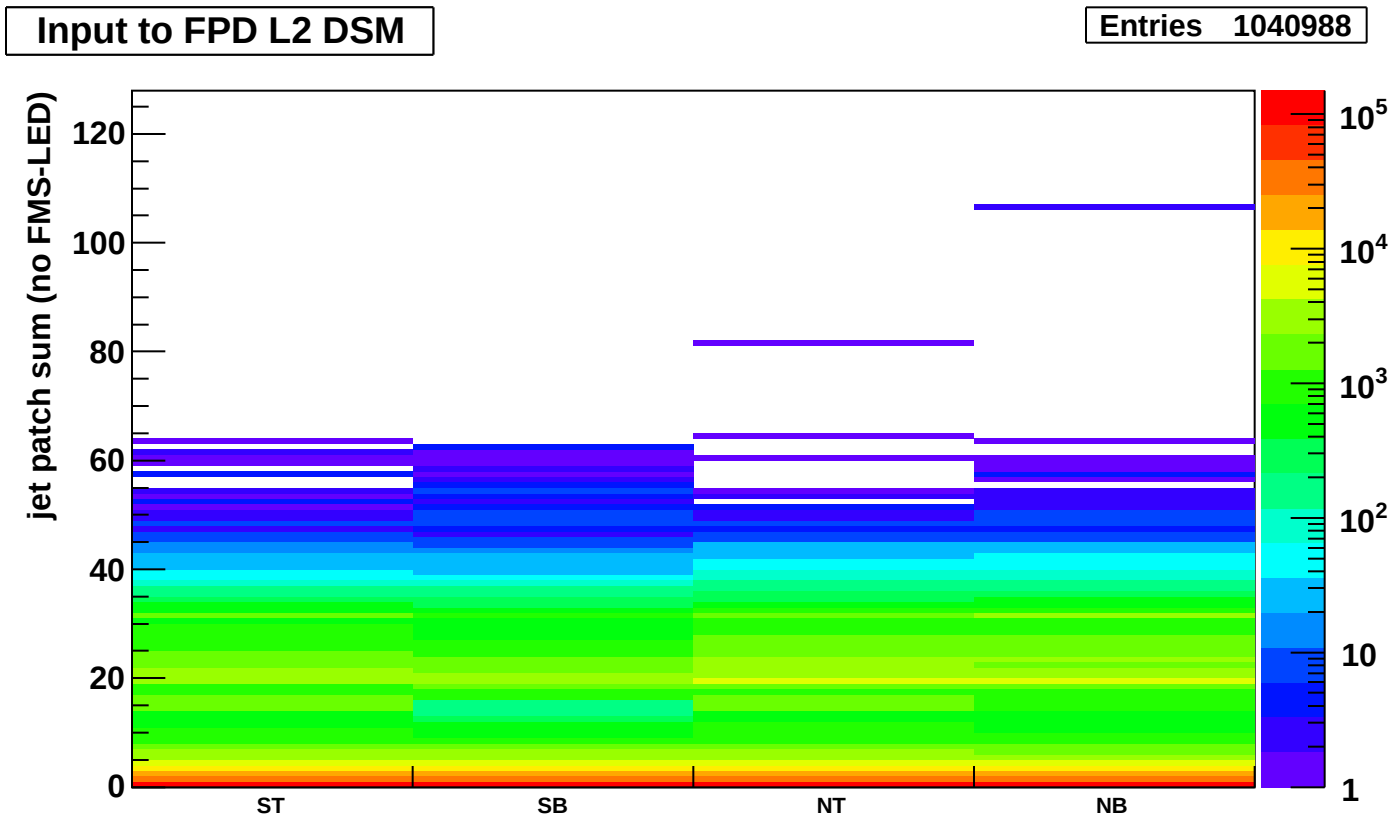

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

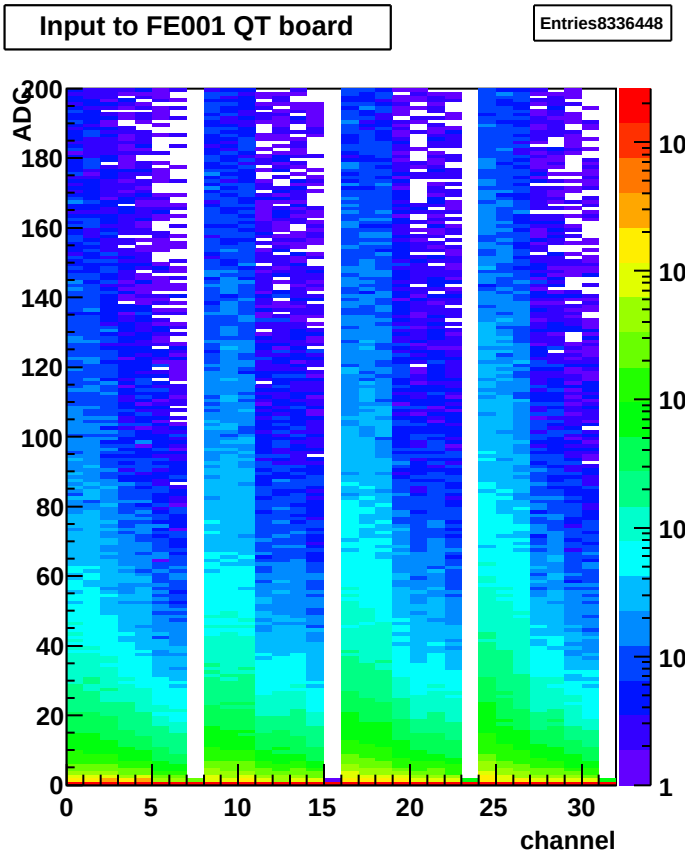

Input to FPD L2 DSM
Entries 781542
HT bits
64458
25127
25646
16067
5587
6752
179989
229800
228116
SMALL
LARGE-S
LARGE-N

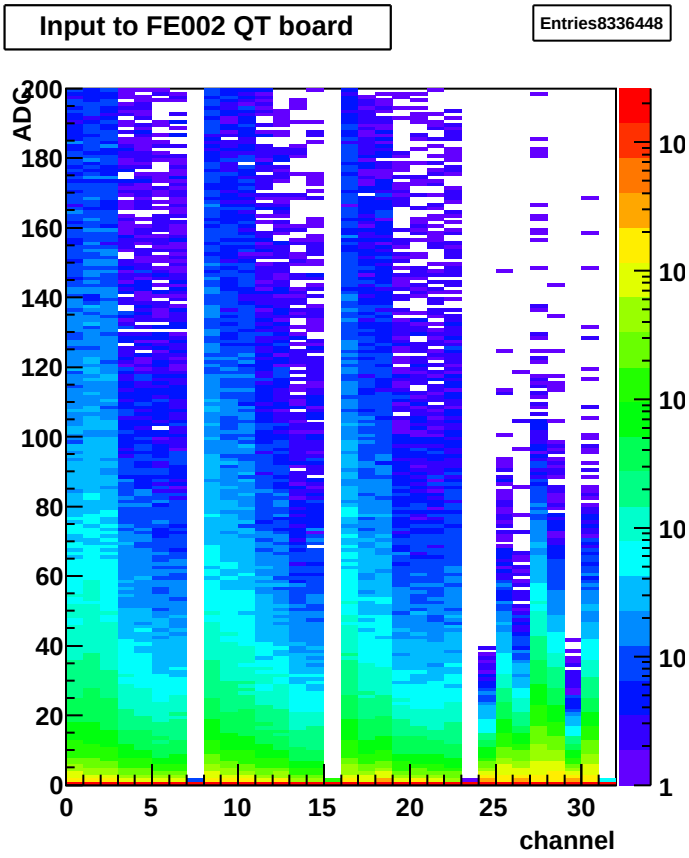

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

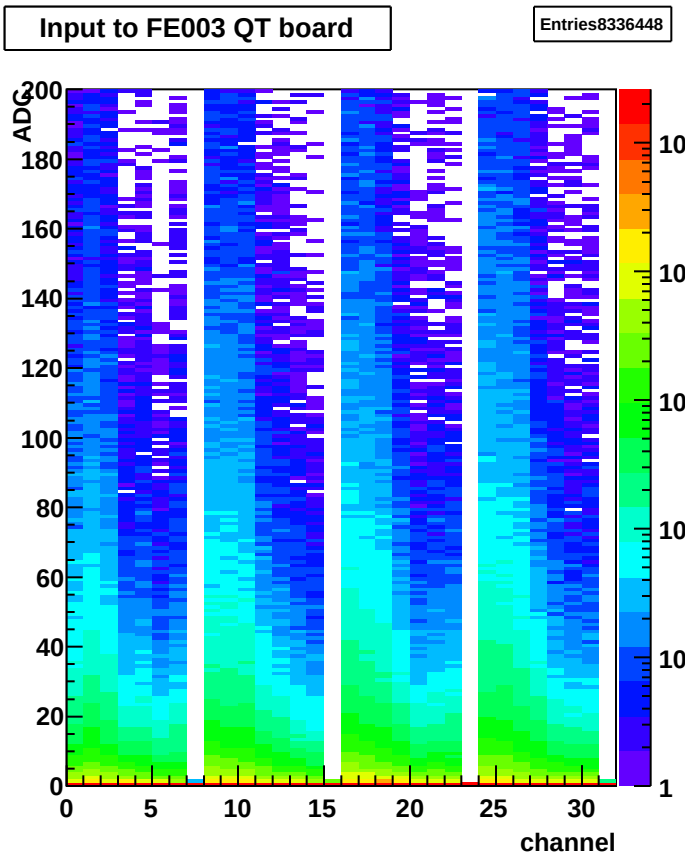

Input to FPD L3 DSM
Entries 4168224
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

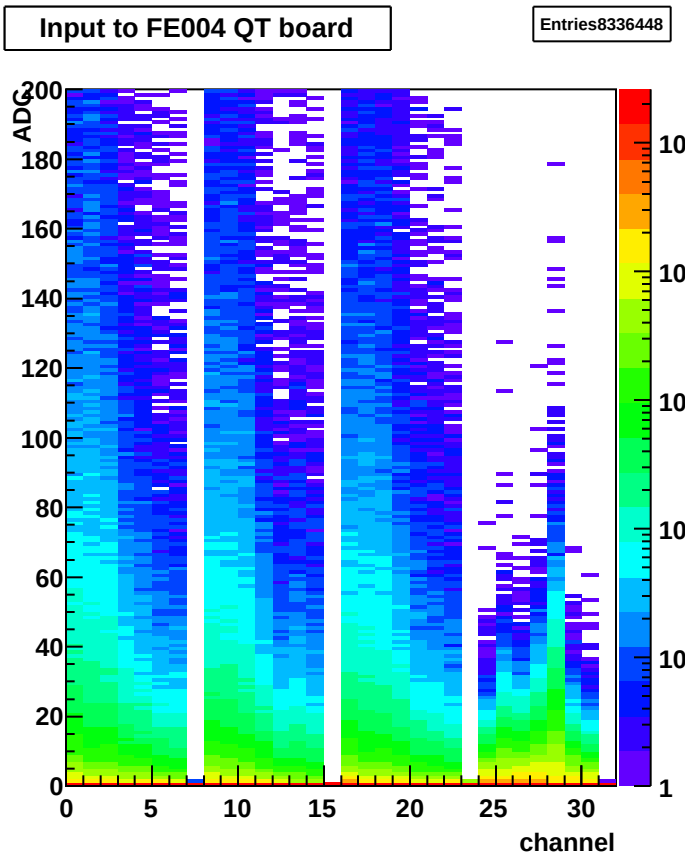

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

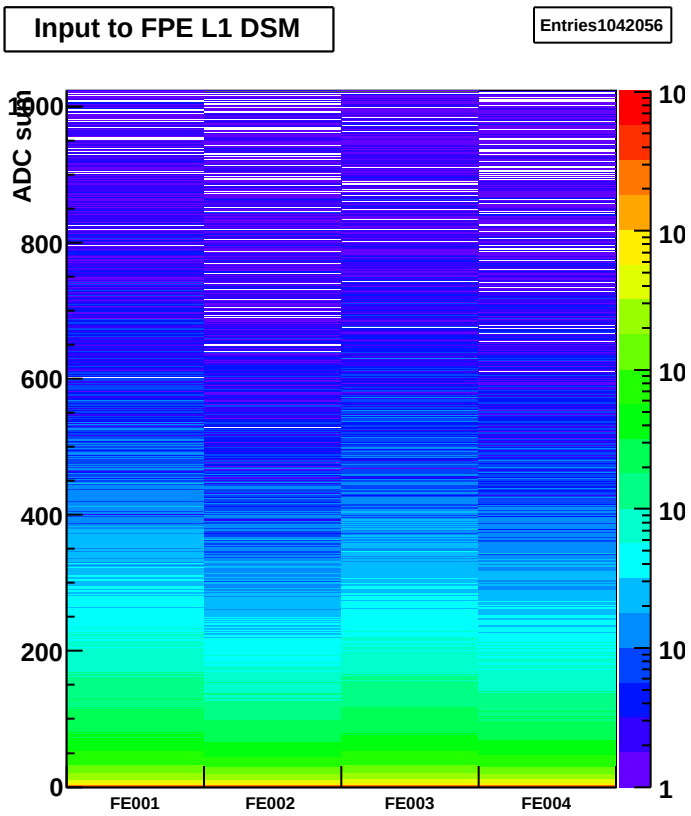

Input to FPD L2 DSM
Entries 1040988
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

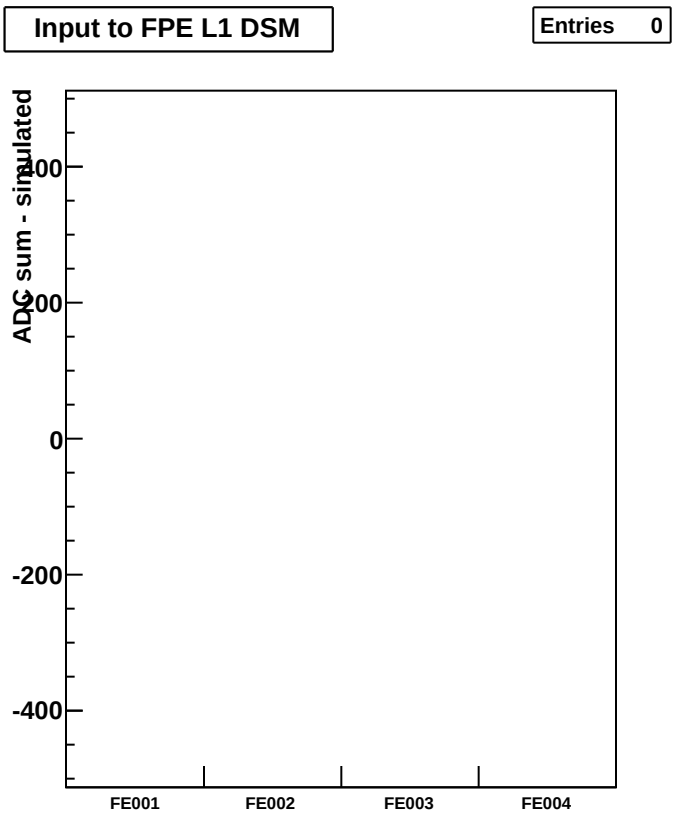

Input to FE001 QT board
Entries8336448
ADC
channel

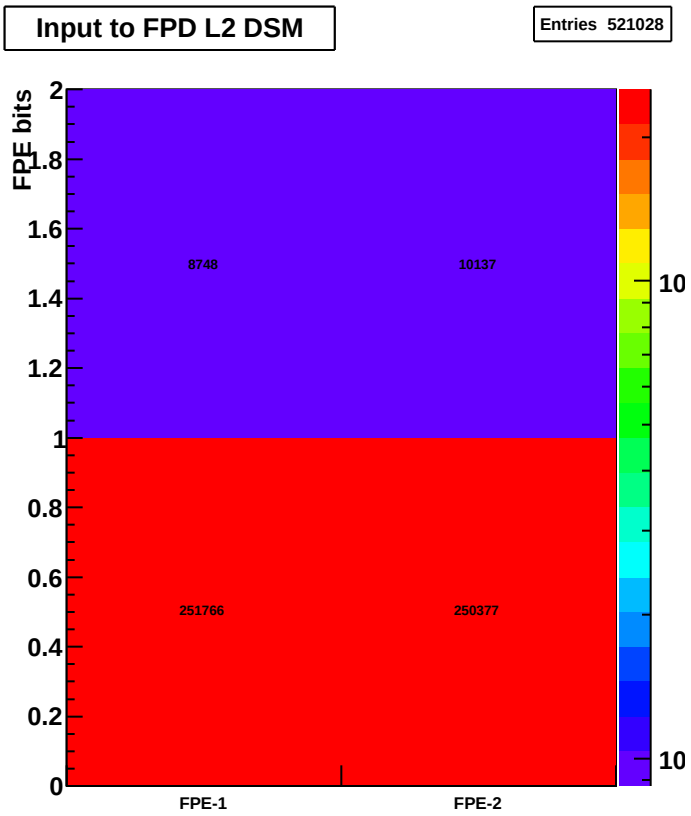

Input to FE002 QT board
Entries8336448
ADC
channel

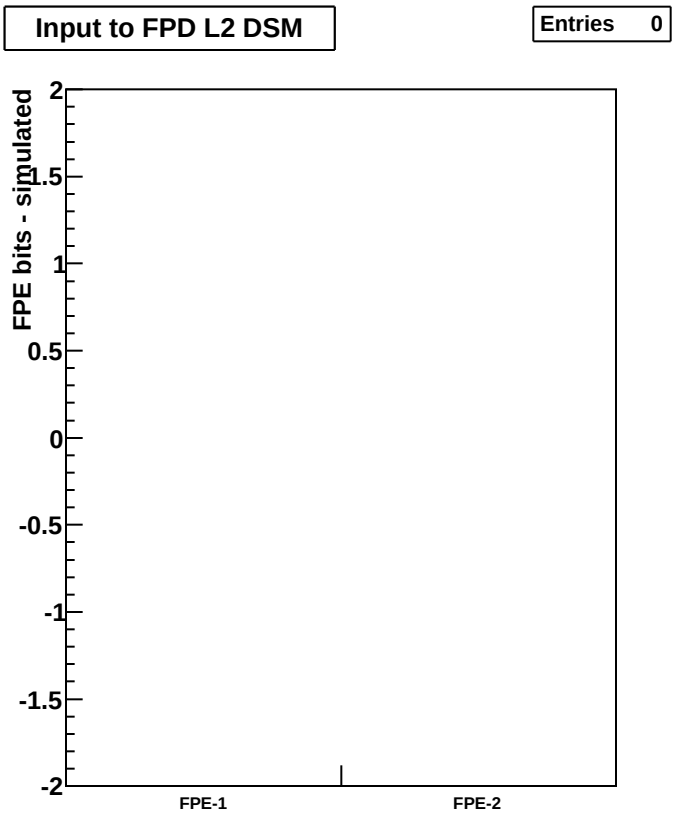

Input to FE003 QT board
Entries8336448
ADC
channel


Input to FE004 QT board
Entries8336448
ADC
channel


Input to FPE L1 DSM
Entries1042056
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 521028
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
8748
10137
251766
250377
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2