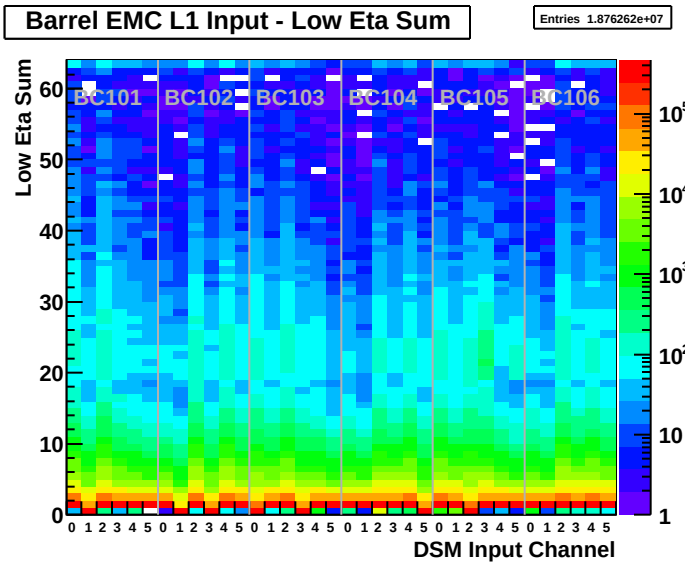
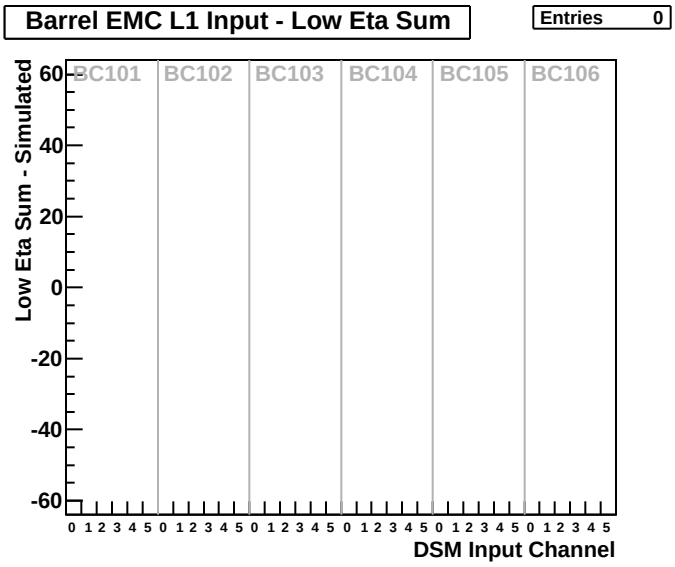
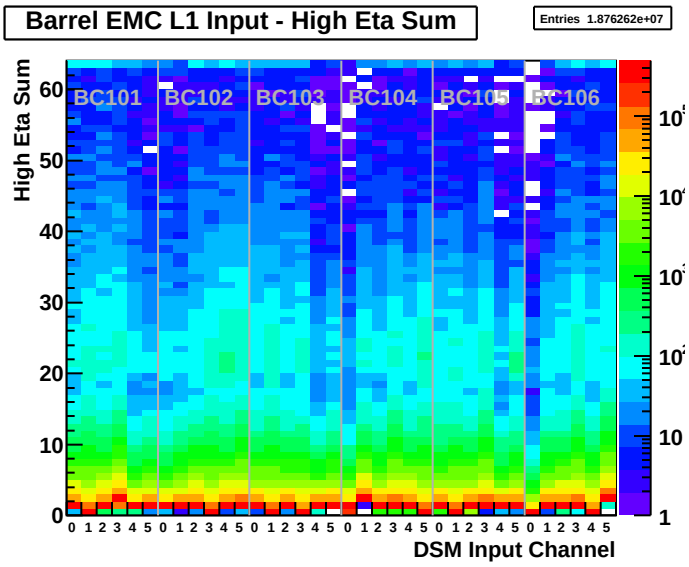
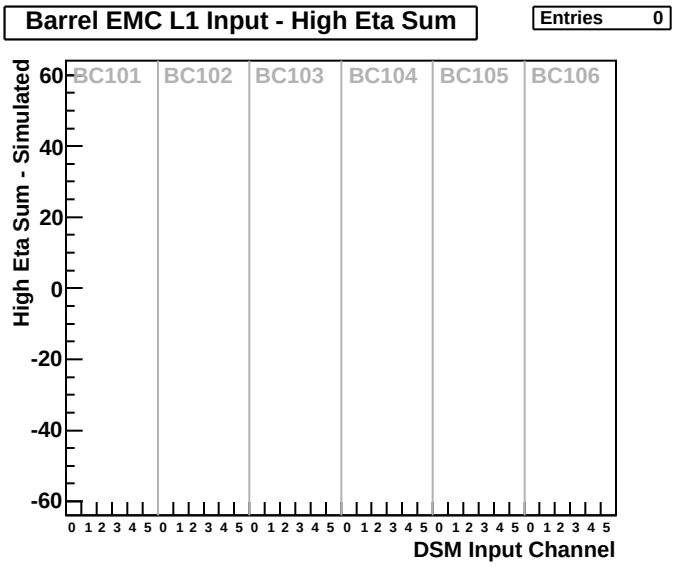
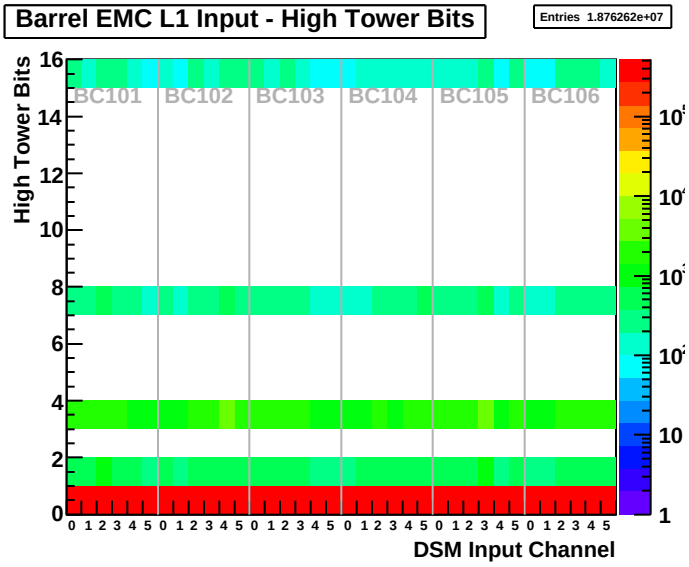
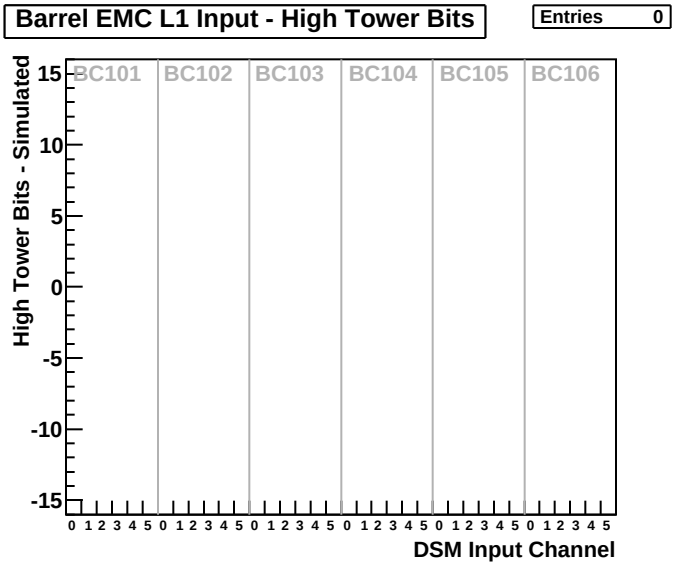

Barrel EMC L1 Input - Low Eta Sum
Entries 1.876262e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

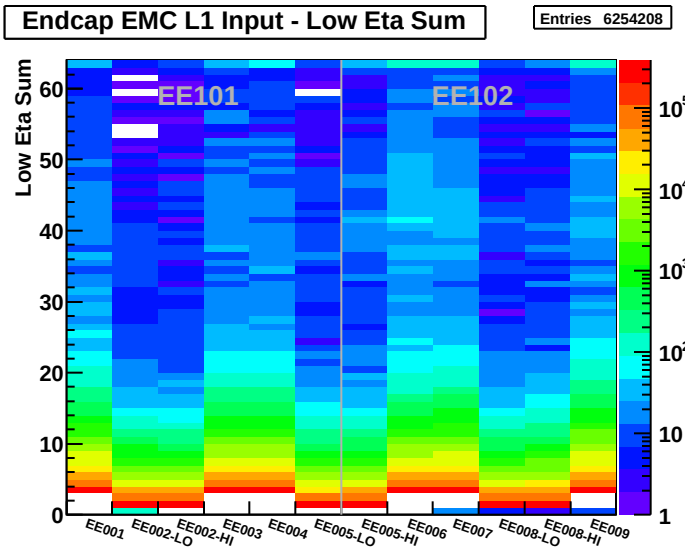

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

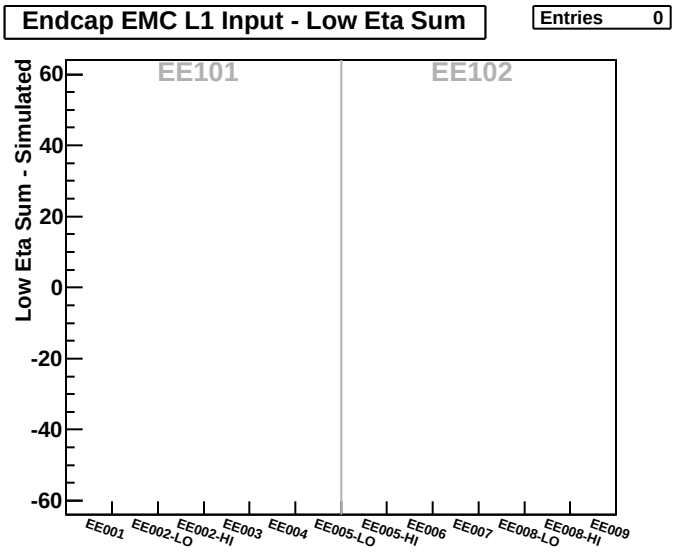

Barrel EMC L1 Input - High Eta Sum
Entries 1.876262e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

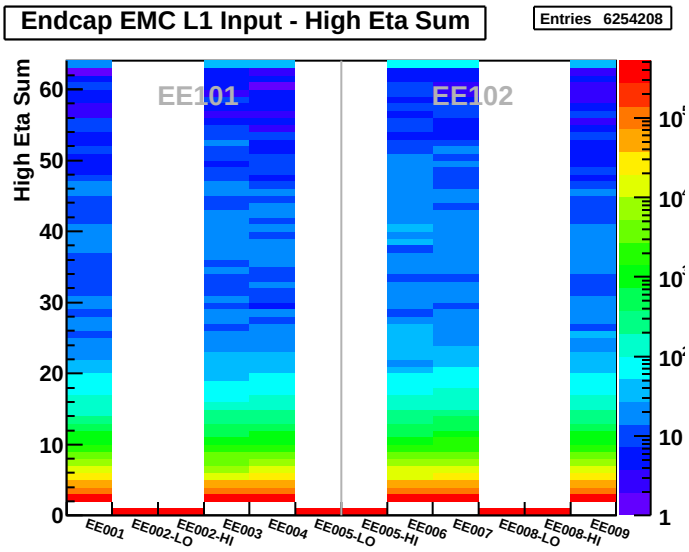

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

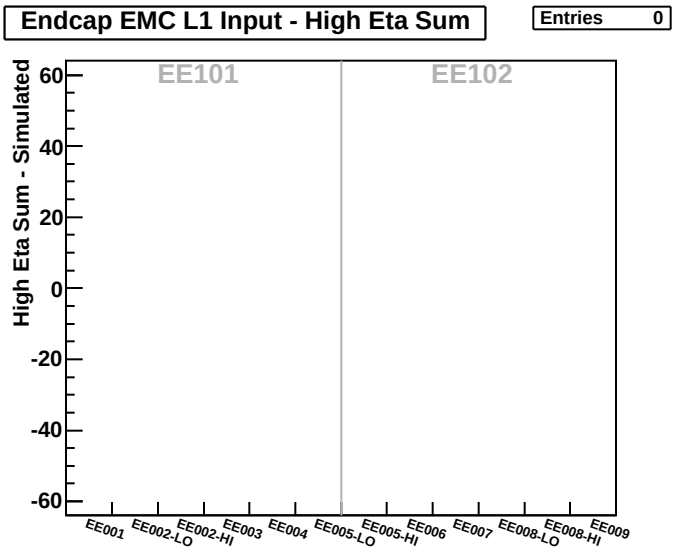

Barrel EMC L1 Input - High Tower Bits
Entries 1.876262e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

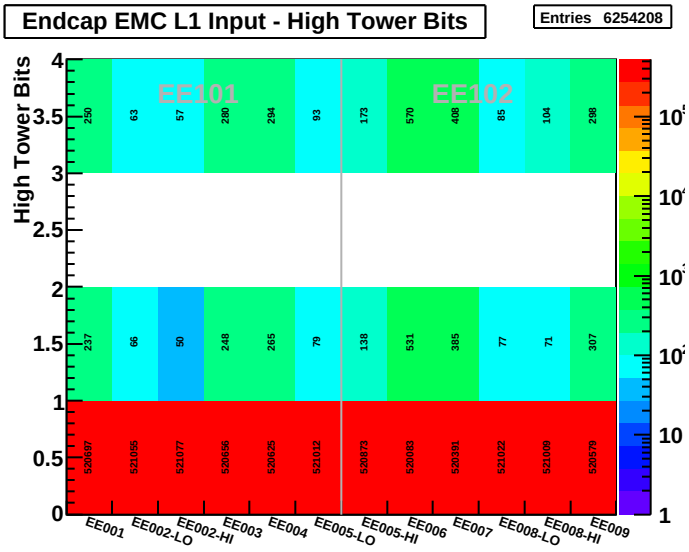

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

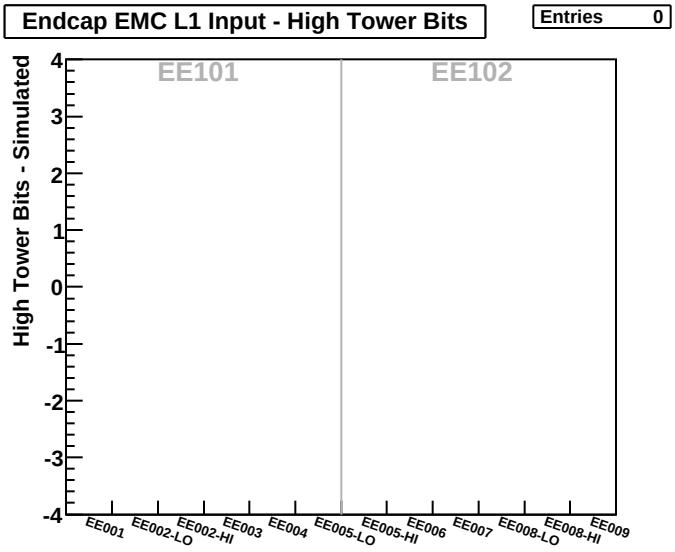

Endcap EMC L1 Input - Low Eta Sum
Entries 6254208
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

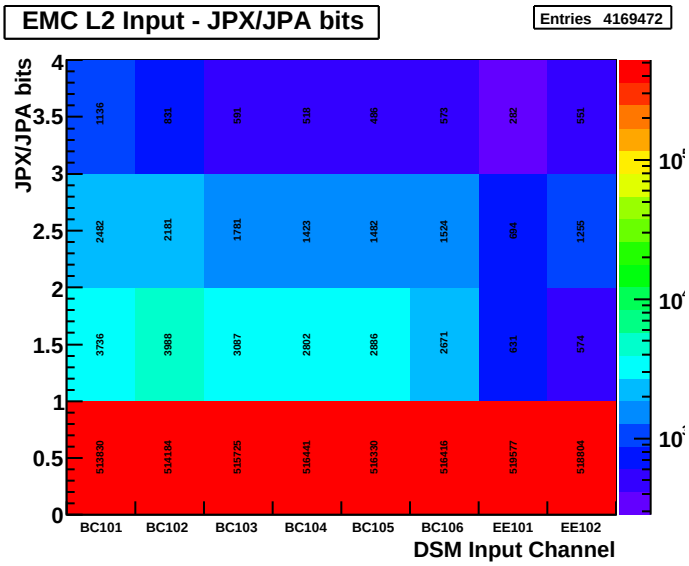

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

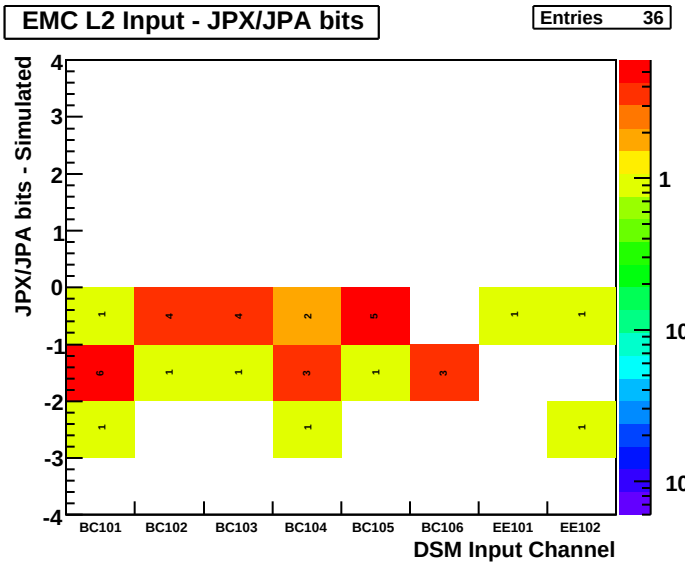

Endcap EMC L1 Input - High Eta Sum
Entries 6254208
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

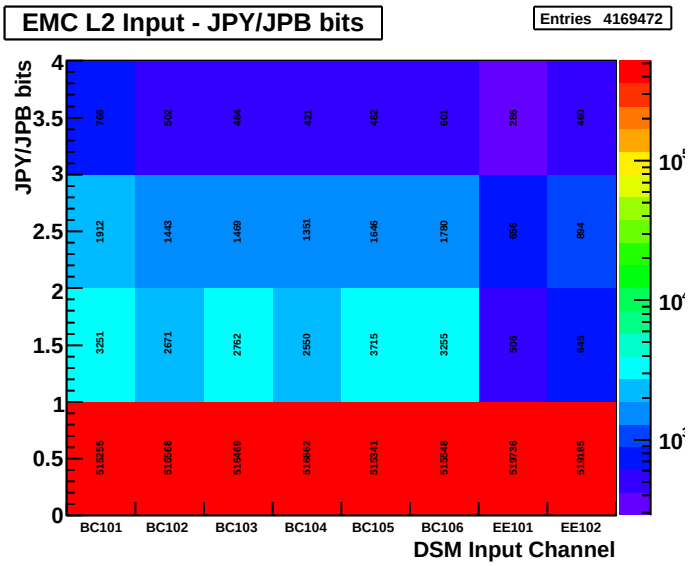

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

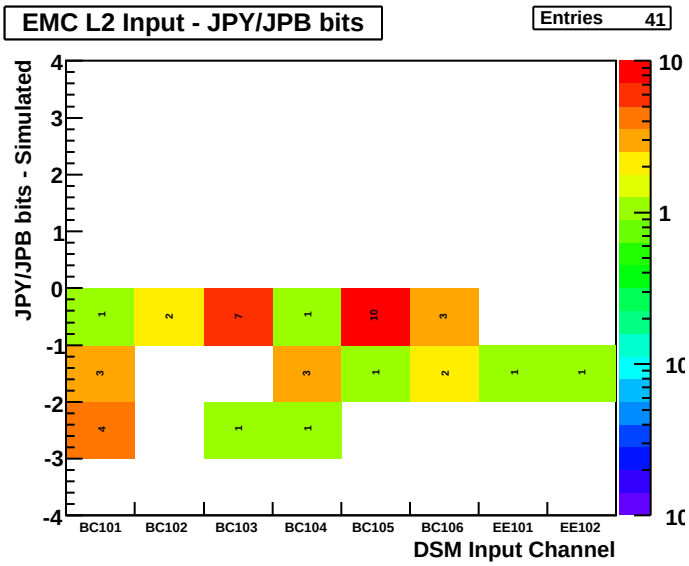

Endcap EMC L1 Input - High Tower Bits
Entries 6254208
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

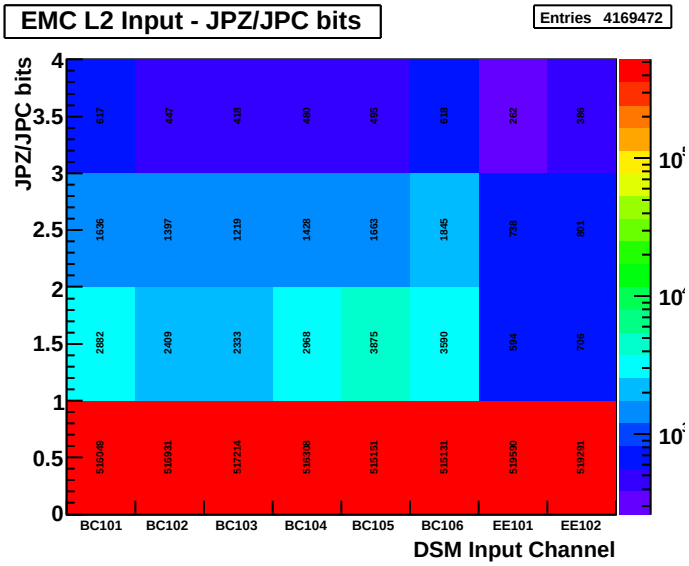

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

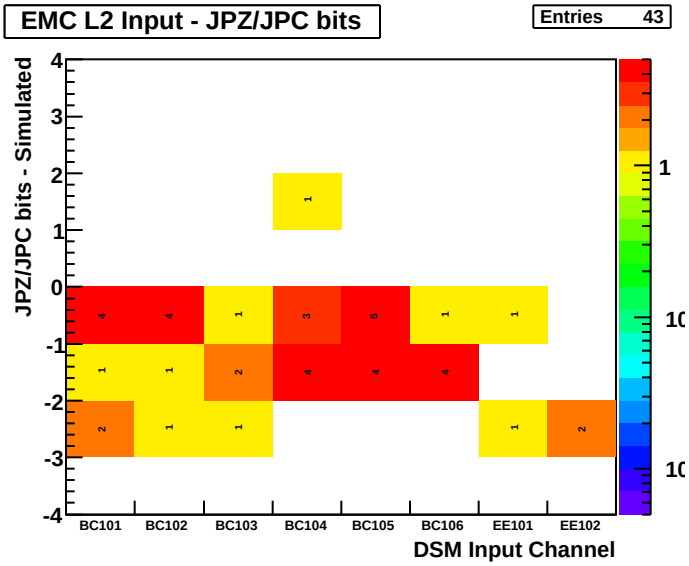

EMC L2 Input - JPX/JPA bits
Entries 4169472
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1136 831 591 518 486 573 282 551
2482 2181 1781 1423 1482 1524 694 1255
3736 3988 3087 2802 2886 2671 631 574
518830 514184 515725 516441 516330 516416 519577 518804

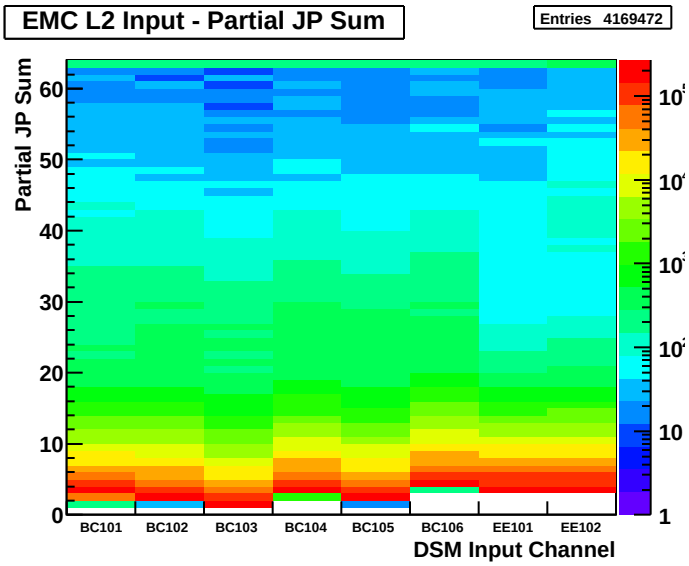

EMC L2 Input - JPX/JPA bits
Entries 36
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

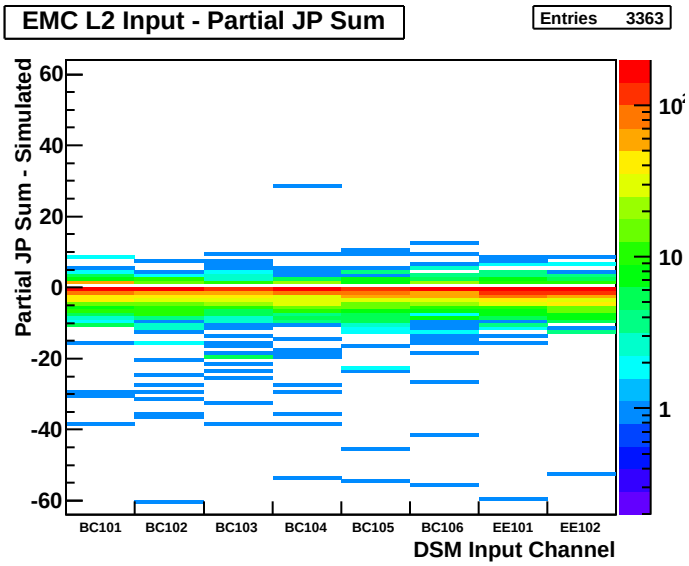

EMC L2 Input - JPY/JPB bits
Entries 4169472
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
766 502 494 421 482 601 286 460
1912 1443 1469 1351 1646 1780 658 894
3251 2671 2762 2550 3715 3255 506 645
518255 516568 516469 516862 515341 515548 519736 519185

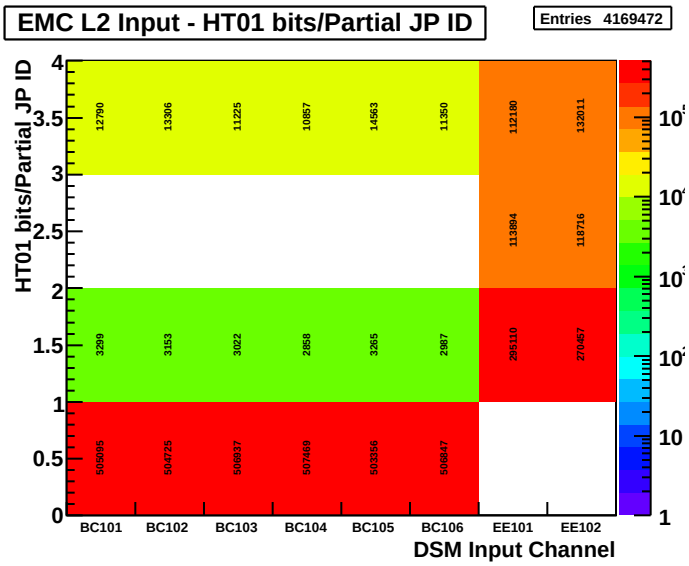

EMC L2 Input - JPY/JPB bits
Entries 41
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

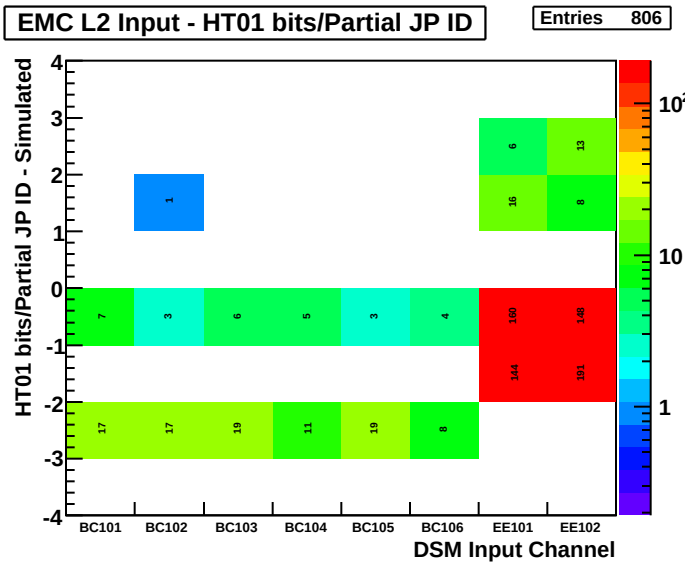

EMC L2 Input - JPZ/JPC bits
Entries 4169472
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
617 447 418 460 495 618 262 386
1636 1397 1219 1428 1663 1845 738 801
2882 2409 2333 2968 3875 3590 594 706
516049 516931 517214 516308 515151 515131 519590 519291

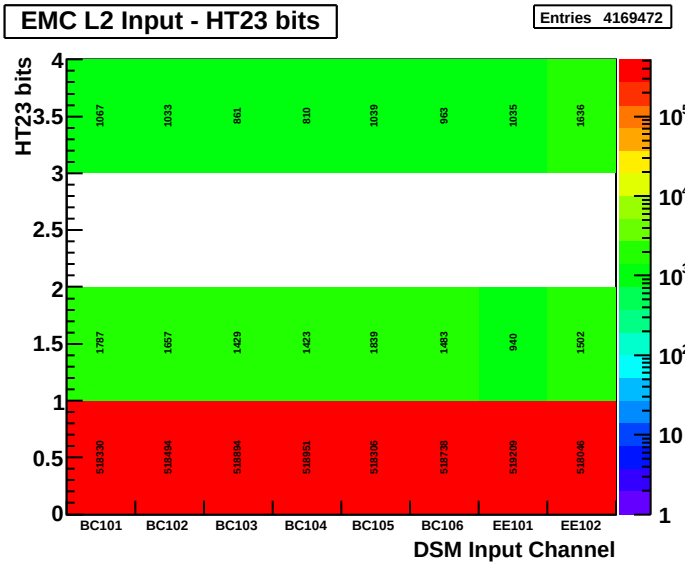

EMC L2 Input - JPZ/JPC bits
Entries 43
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

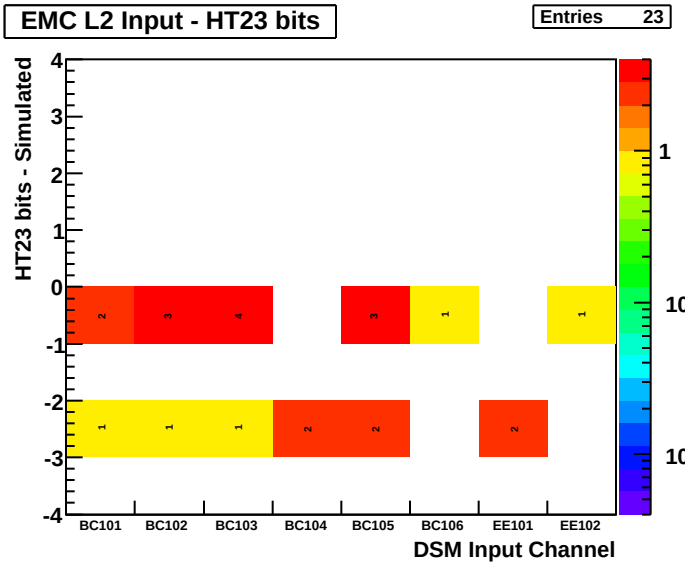

EMC L2 Input - Partial JP Sum
Entries 4169472
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

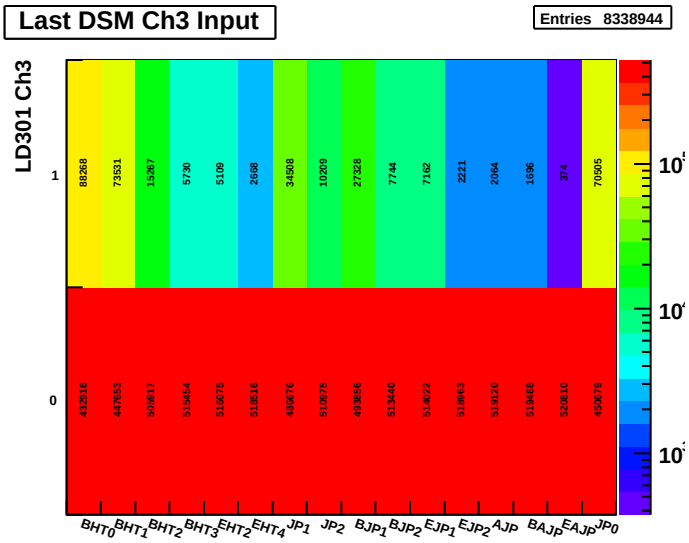

EMC L2 Input - Partial JP Sum
Entries 3363
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

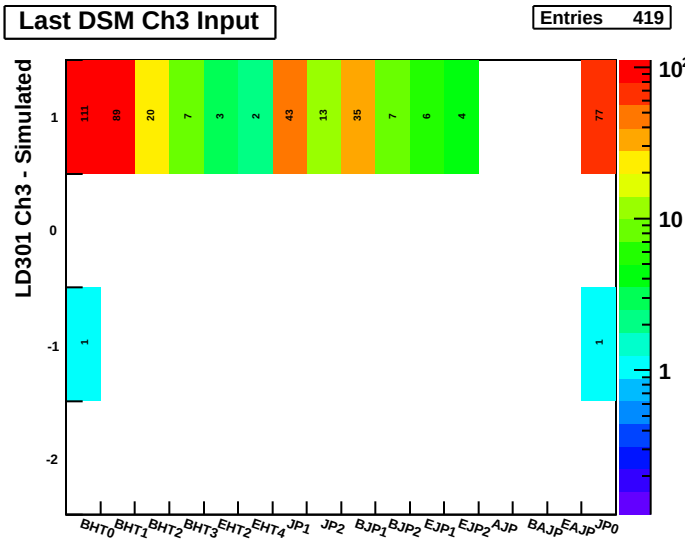

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4169472
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

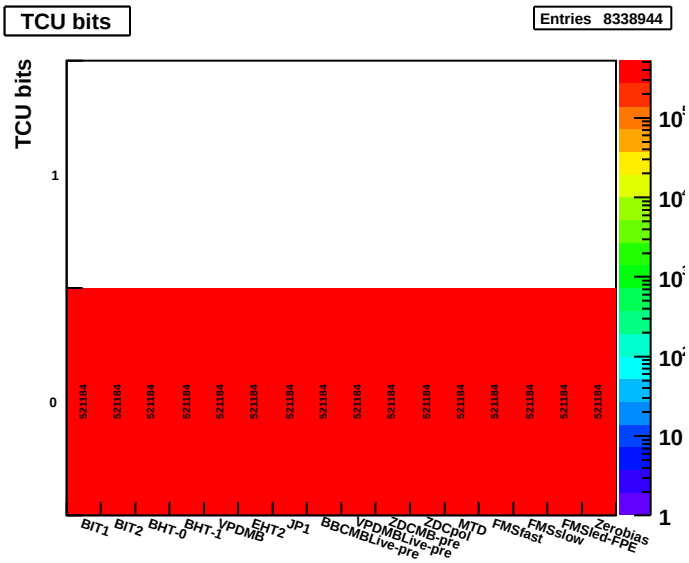

EMC L2 Input - HT01 bits/Partial JP ID
Entries 806
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

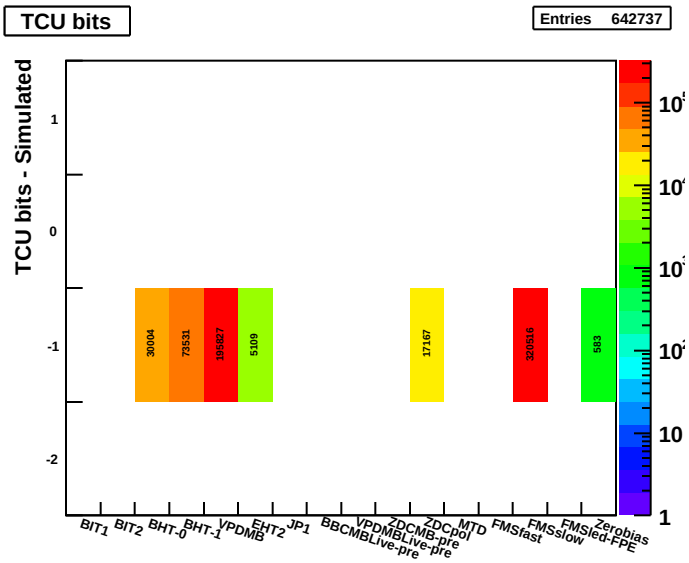

EMC L2 Input - HT23 bits
Entries 4169472
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

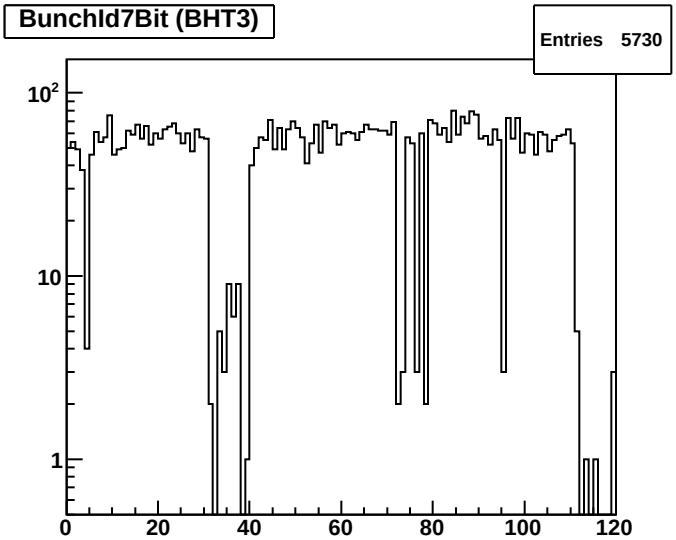

EMC L2 Input - HT23 bits
Entries 23
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

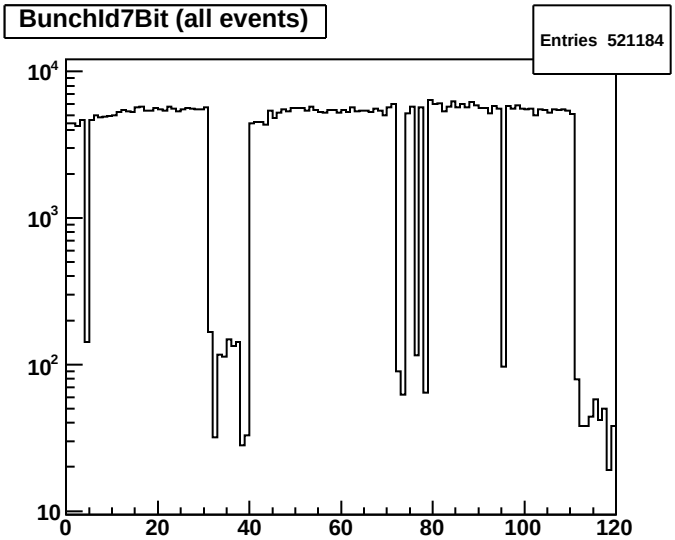

Last DSM Ch3 Input
Entries 8338944
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

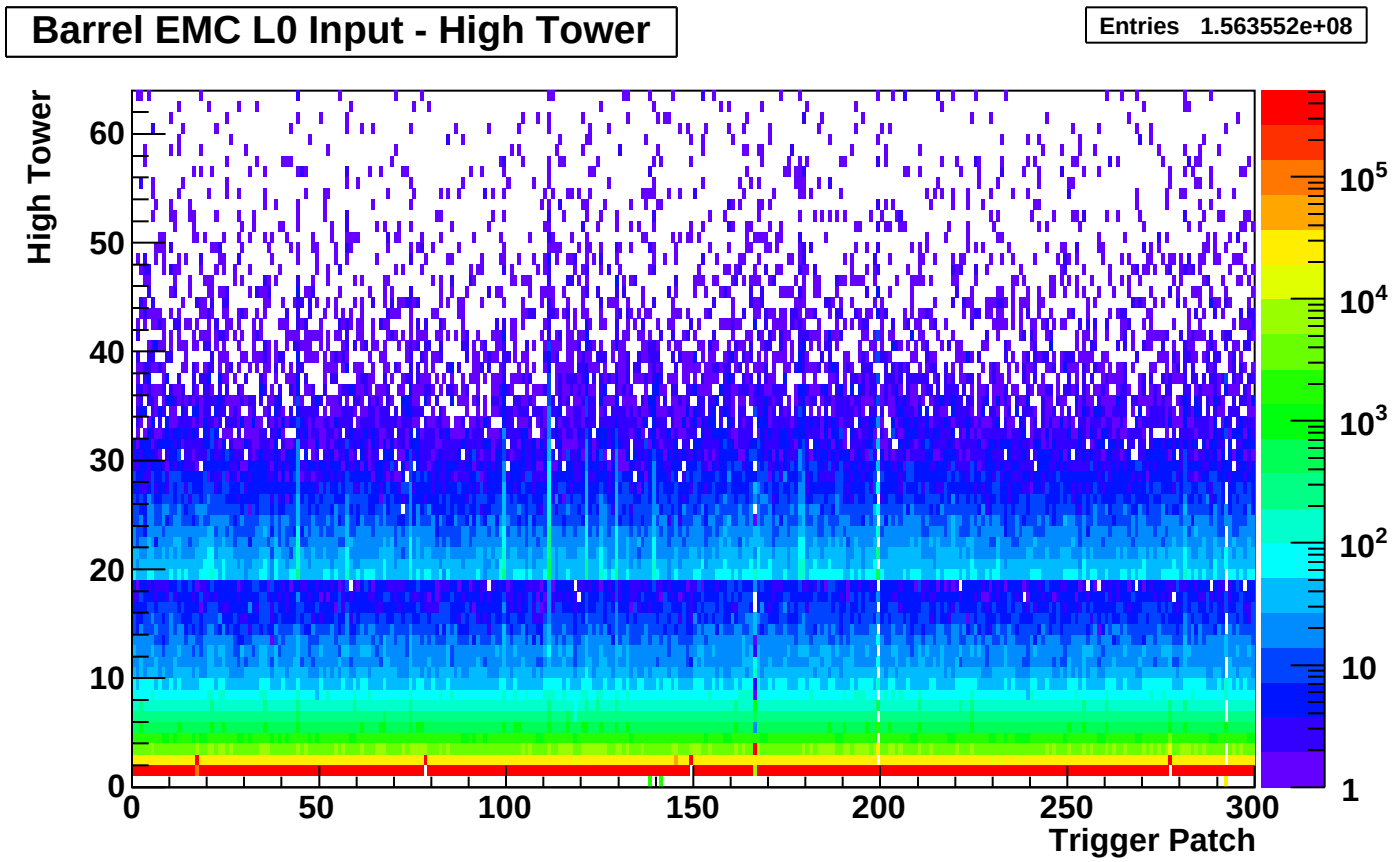

Last DSM Ch3 Input
Entries 419
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

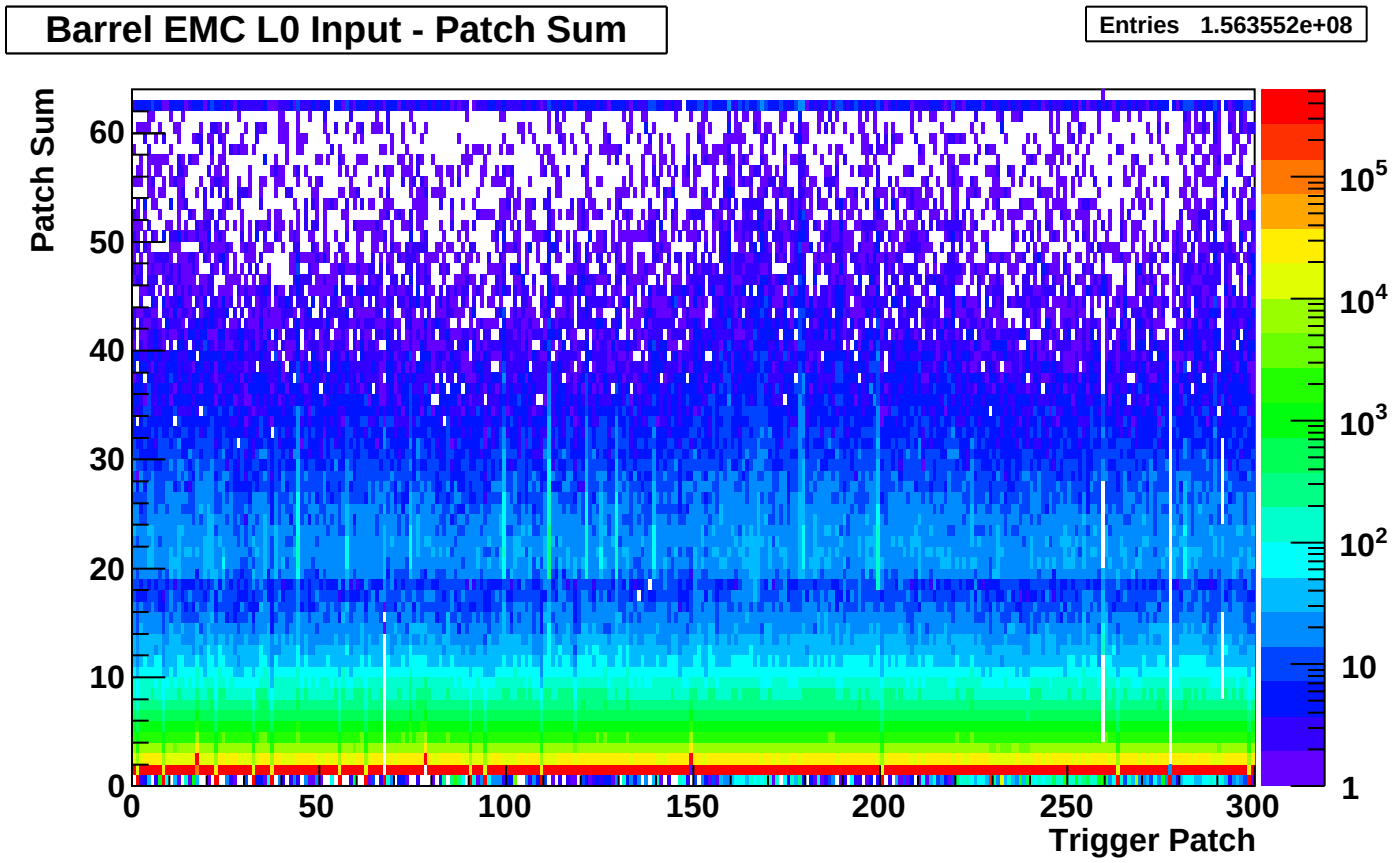

TCU bits
Entries 8338944
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

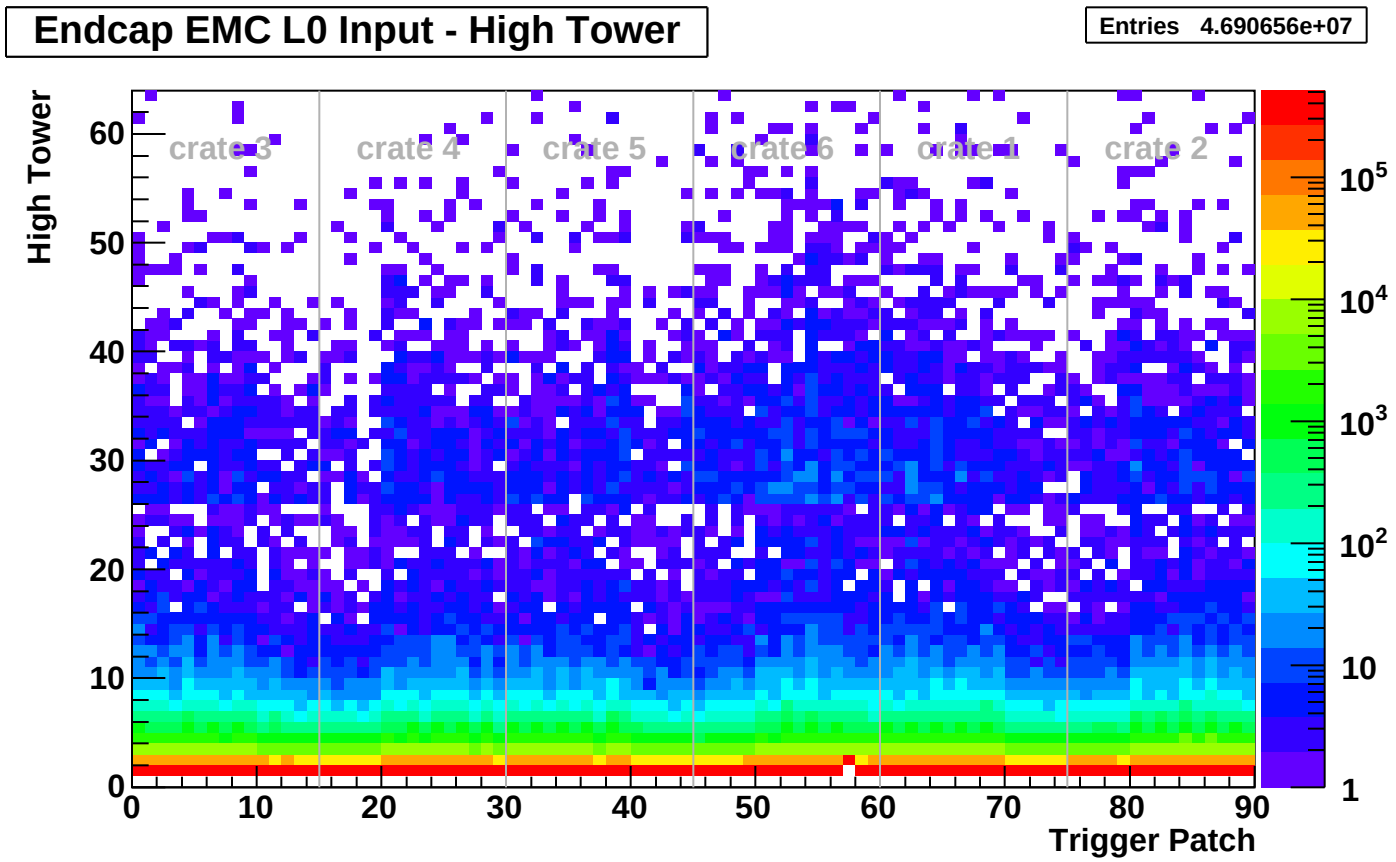

TCU bits
Entries 642737
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

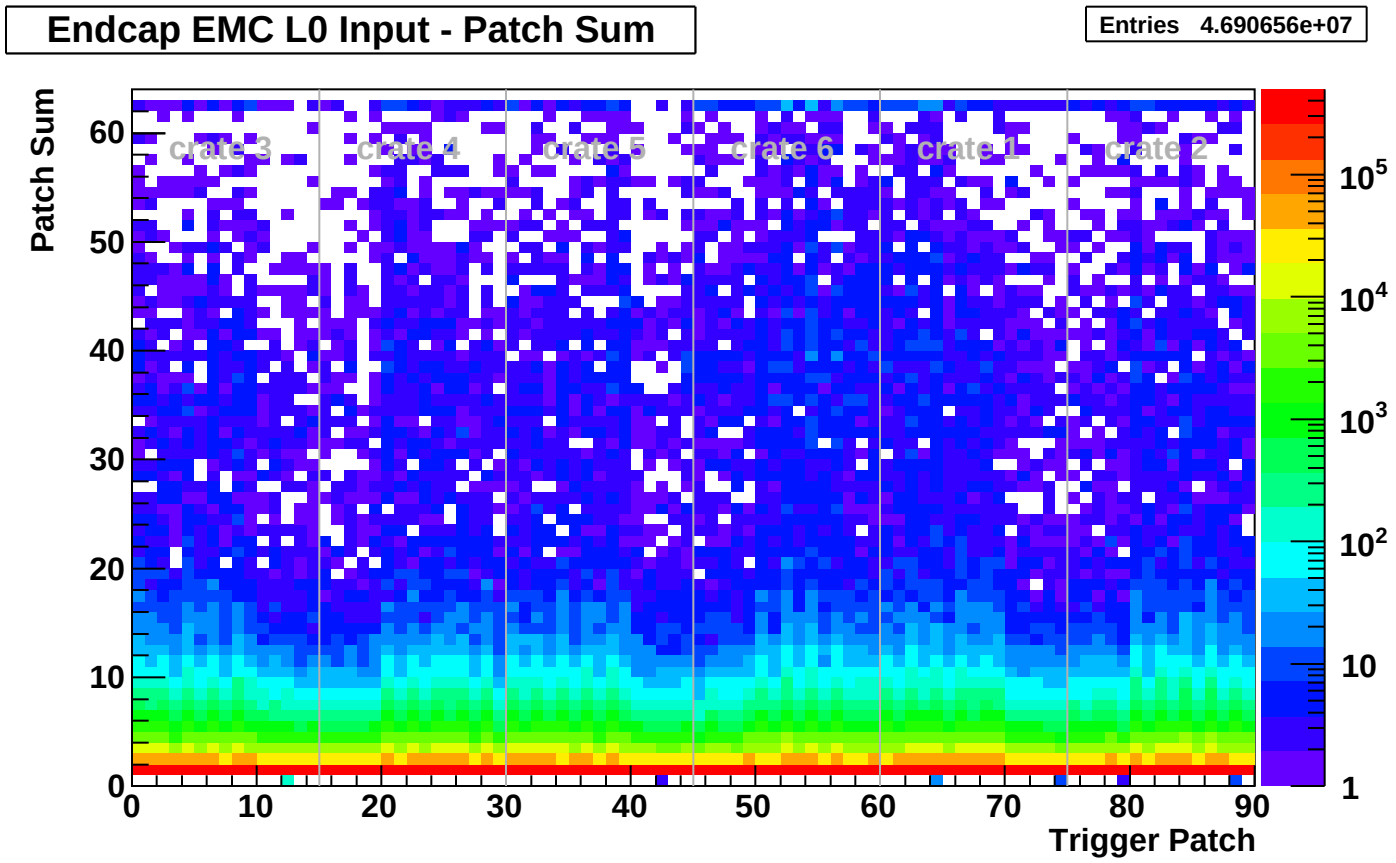

BunchId7Bit (BHT3)
Entries 5730

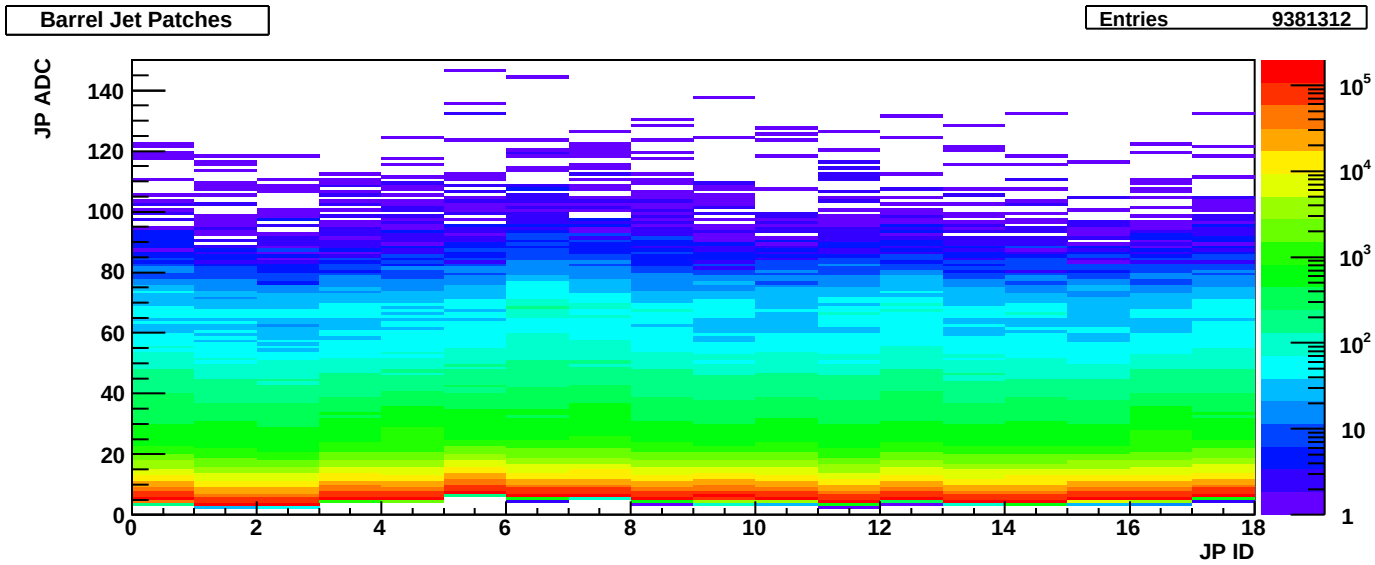

BunchId7Bit (all events)
Entries 521184

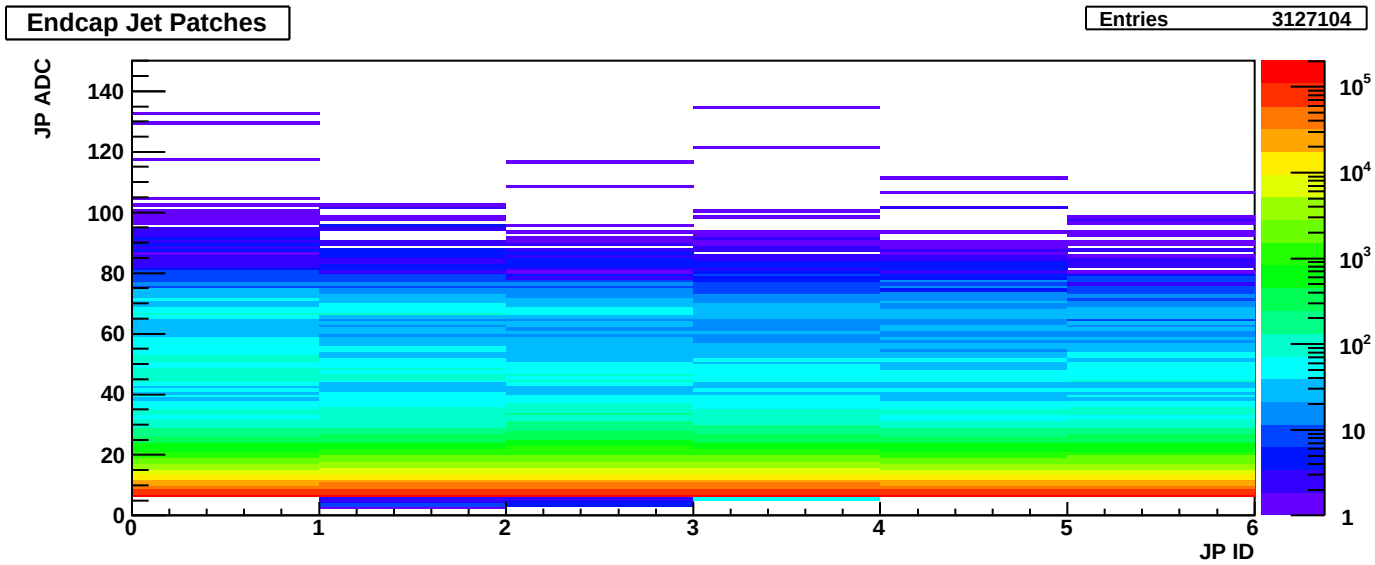

Barrel EMC L0 Input - High Tower
Entries 1.563552e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

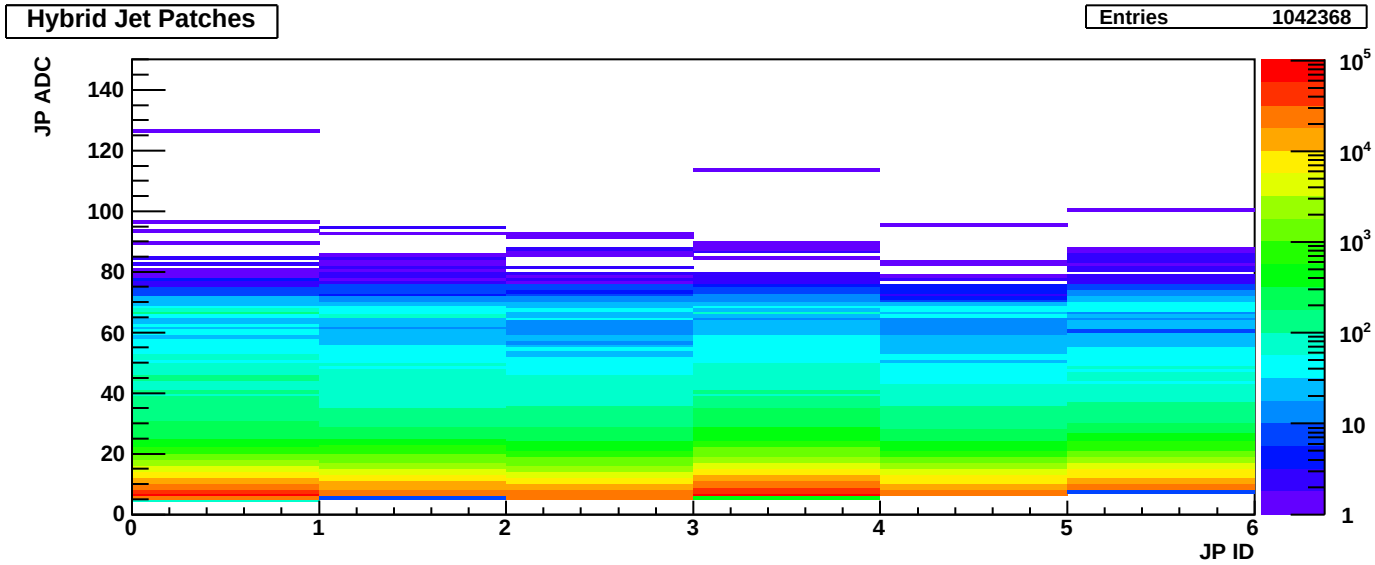

Barrel EMC L0 Input - Patch Sum
Entries 1.563552e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

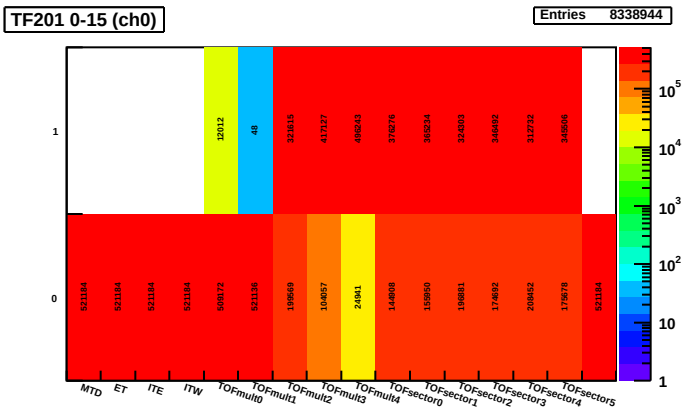

Endcap EMC L0 Input - High Tower
Entries 4.690656e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

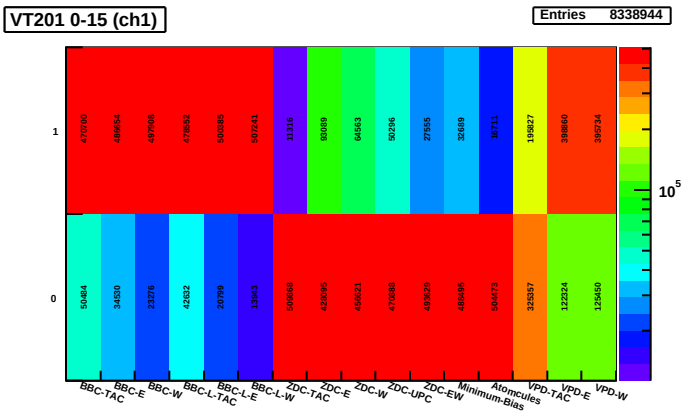

Endcap EMC L0 Input - Patch Sum
Entries 4.690656e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

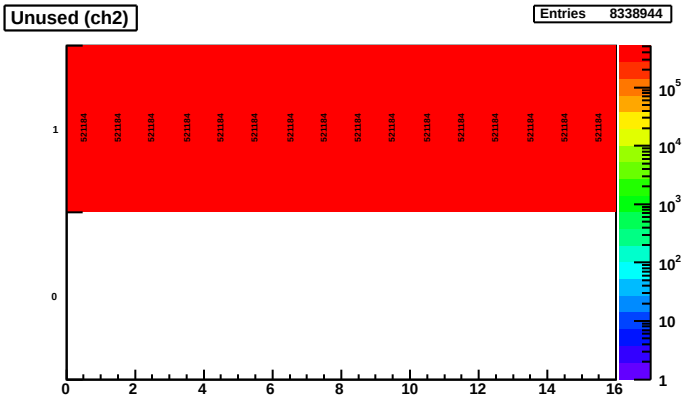

Barrel Jet Patches
Entries 9381312
JP ADC
JP ID

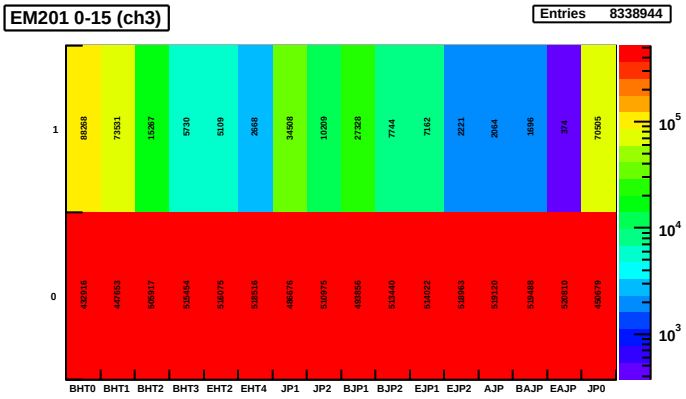

Endcap Jet Patches
Entries 3127104
JP ADC
JP ID

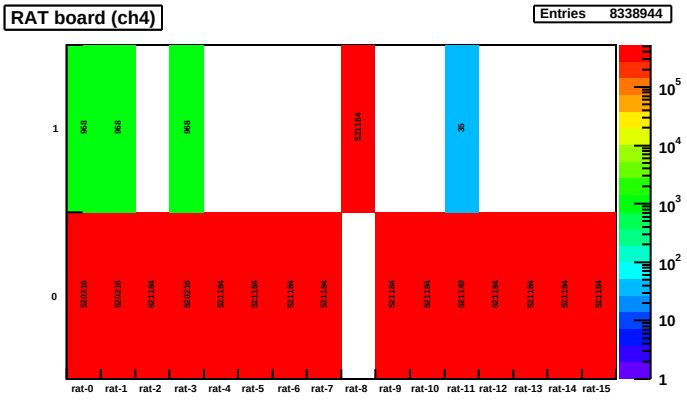

Hybrid Jet Patches
Entries 1042368
JP ADC
JP ID

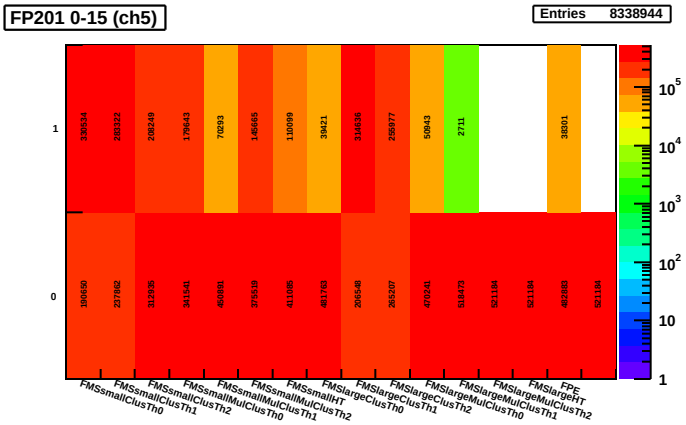

TF201 0-15 (ch0)
Entries 8338944
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

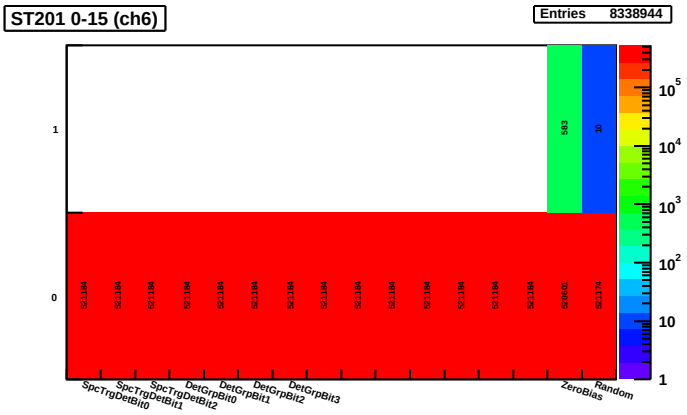

VT201 0-15 (ch1)
Entries 8338944
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

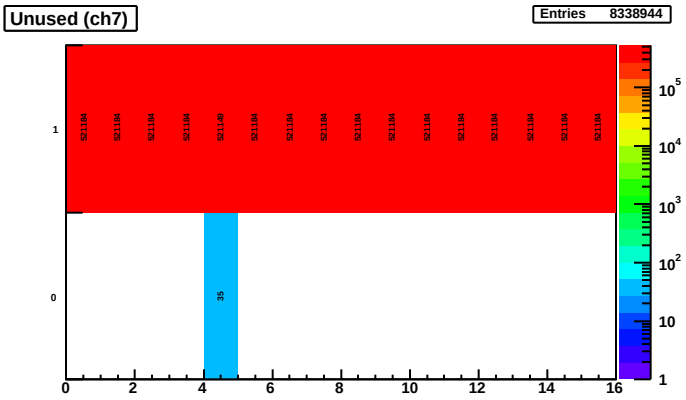

Unused (ch2)
Entries 8338944

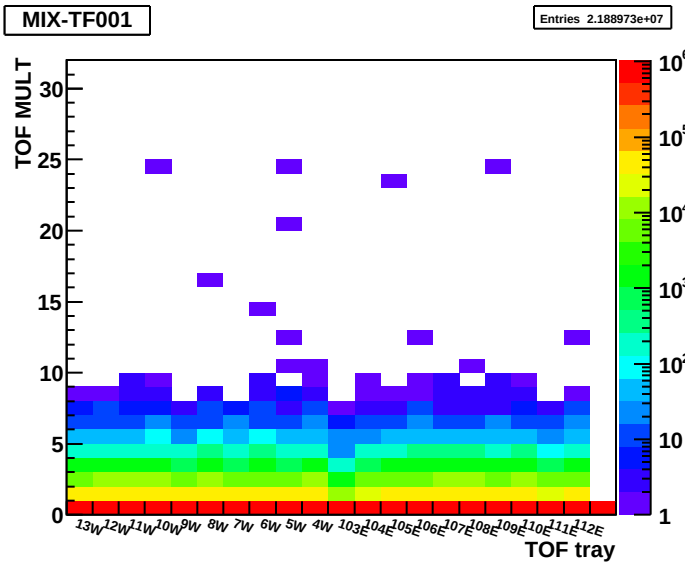

EM201 0-15 (ch3)
Entries 8338944
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

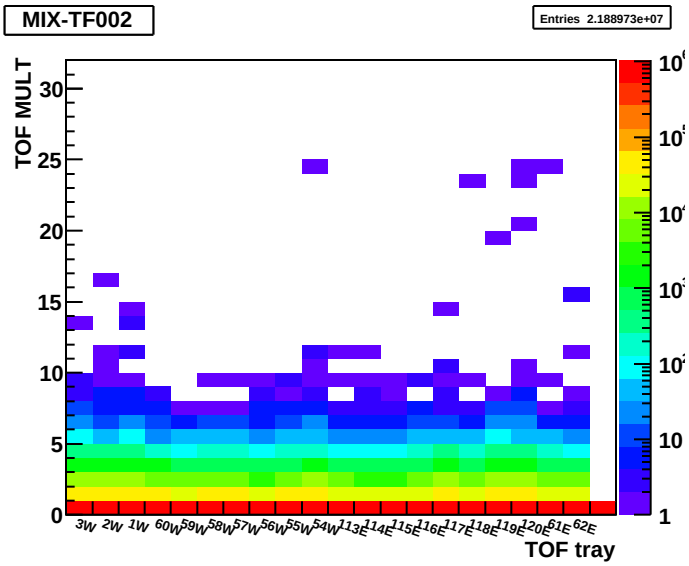

RAT board (ch4)
Entries 8338944
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

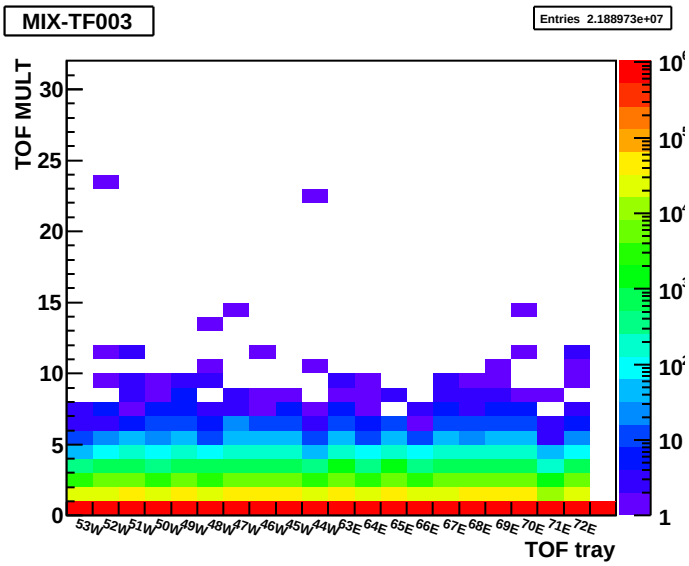

FP201 0-15 (ch5)
Entries 8338944
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

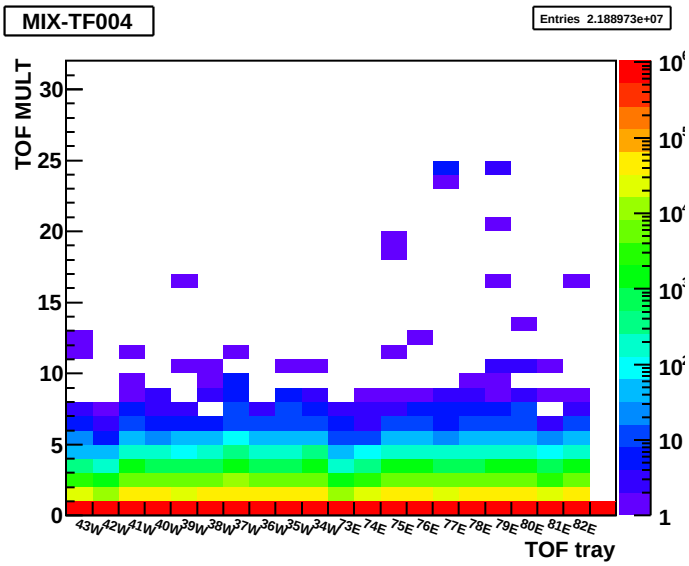

ST201 0-15 (ch6)
Entries 8338944
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

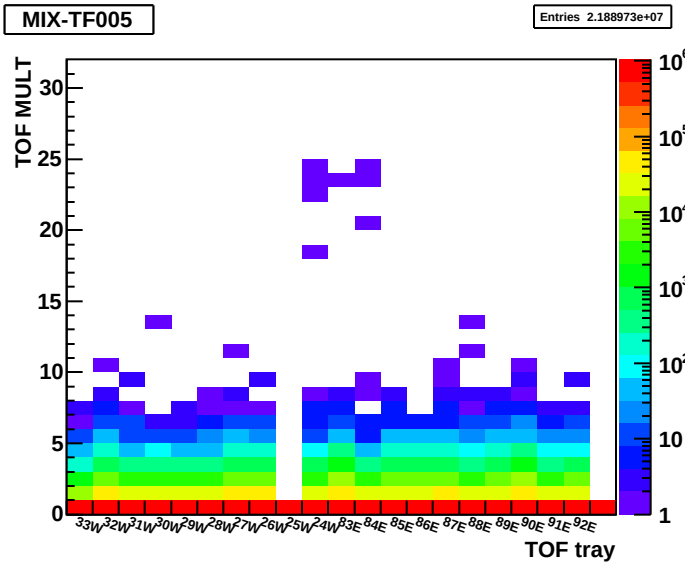

Unused (ch7)
Entries 8338944

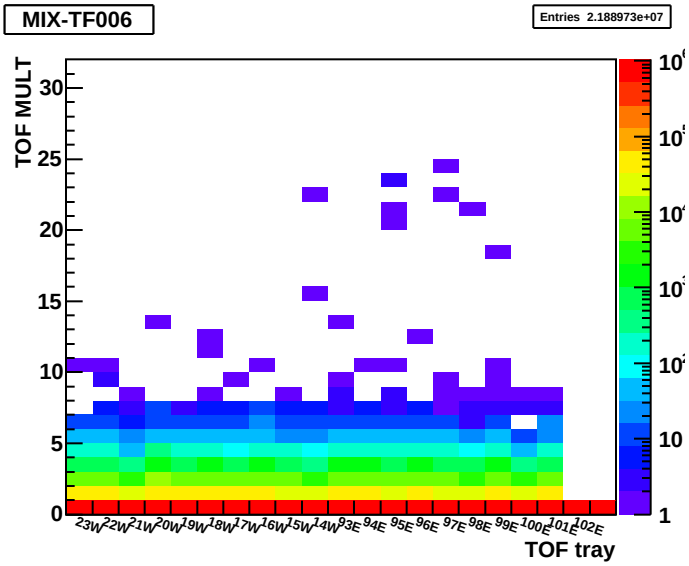

MIX-TF001
Entries 2.188973e+07
TOF MULT
TOF tray

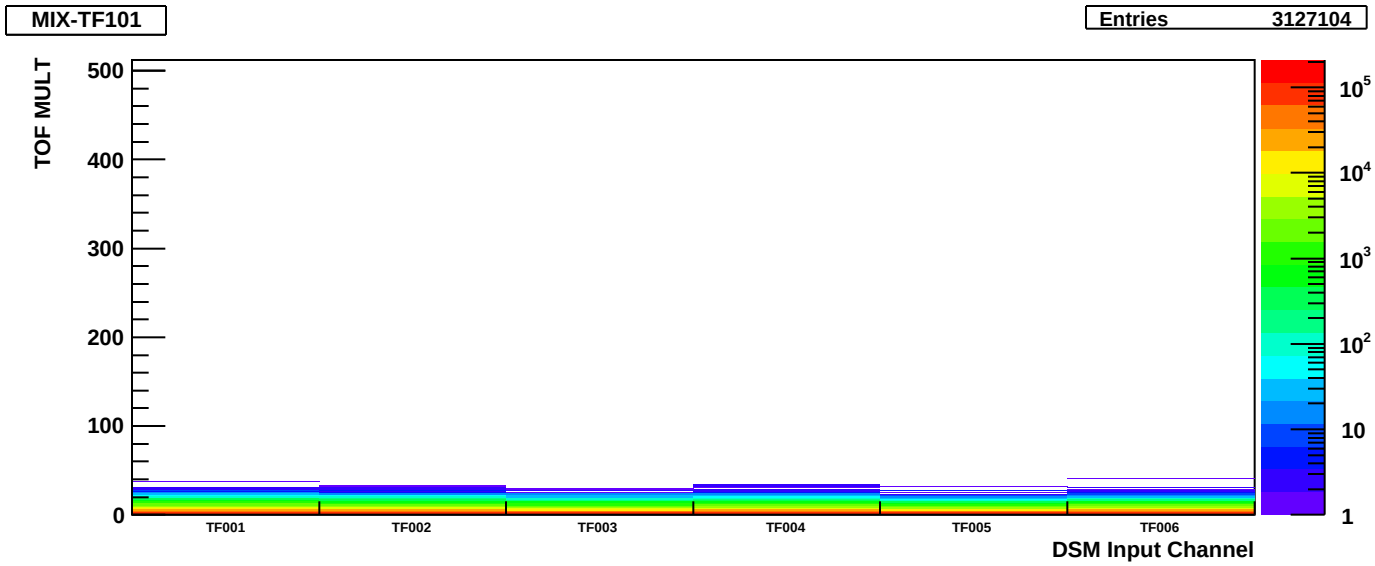

MIX-TF002
Entries 2.188973e+07
TOF MULT
TOF tray

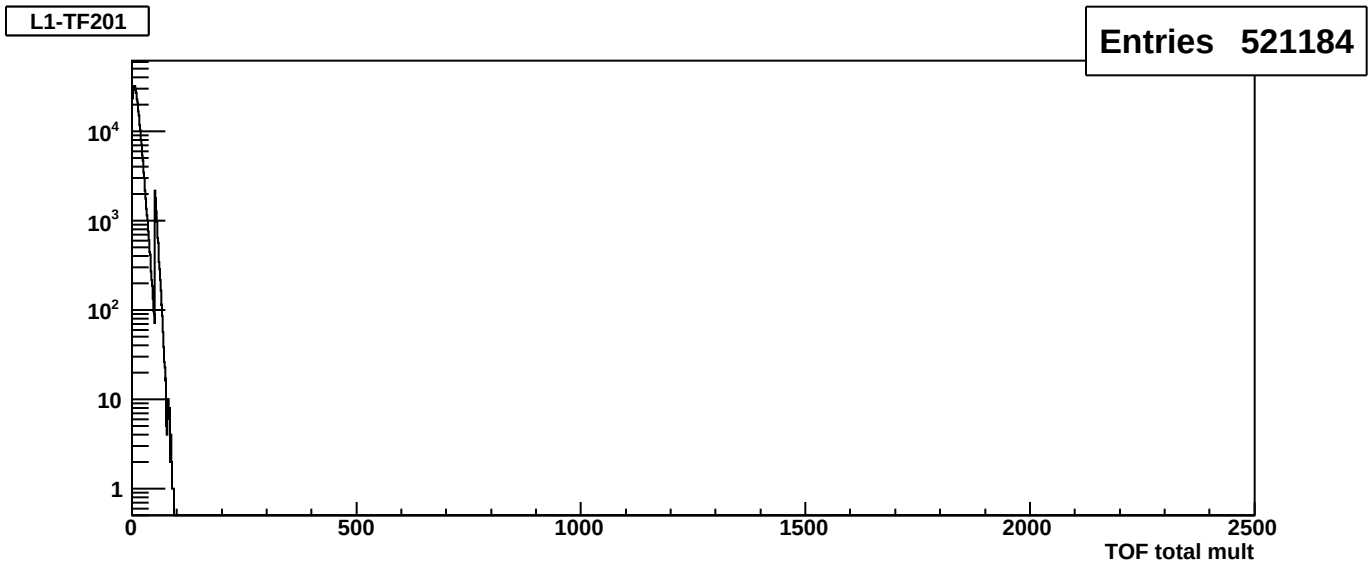

MIX-TF003
Entries 2.188973e+07
TOF MULT
TOF tray

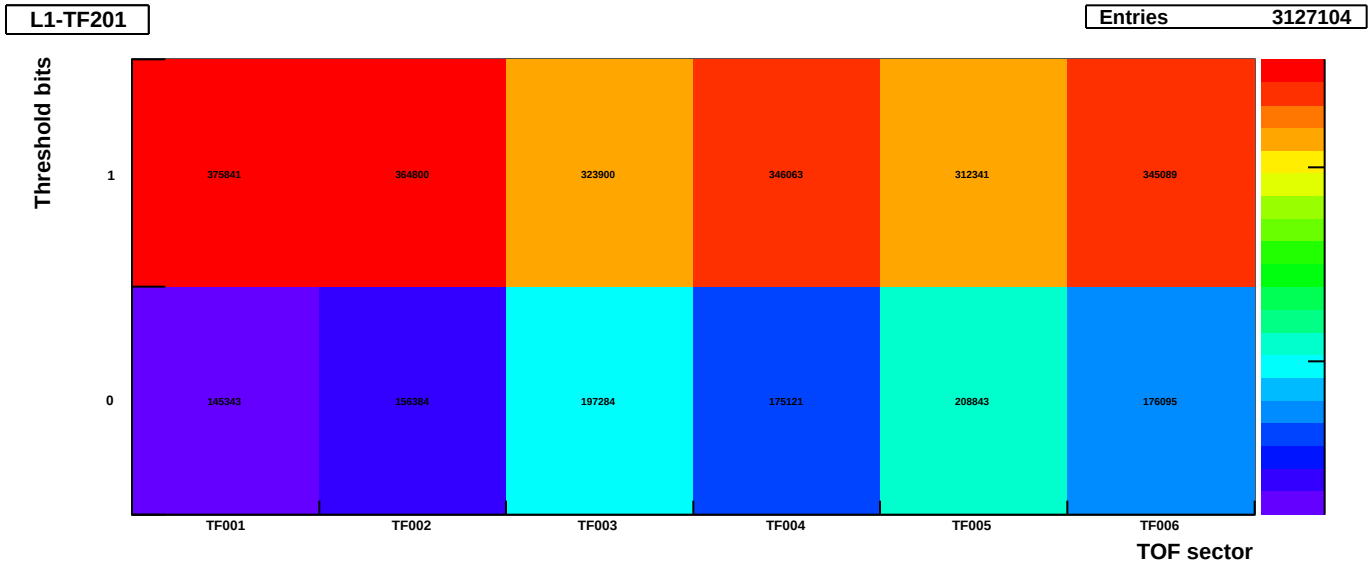

MIX-TF004
Entries 2.188973e+07
TOF MULT
TOF tray

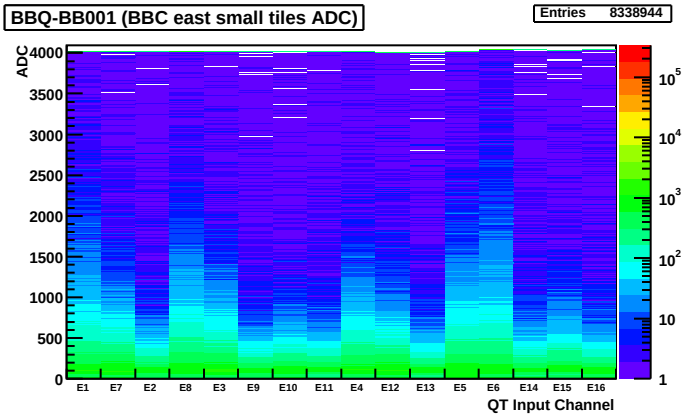

MIX-TF005
Entries 2.188973e+07
TOF MULT
TOF tray

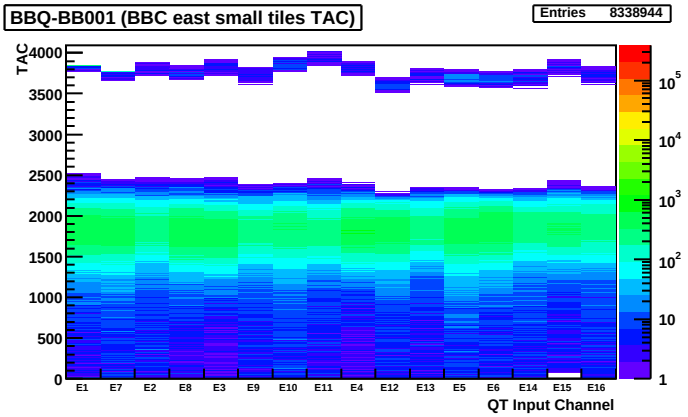

MIX-TF006
Entries 2.188973e+07
TOF MULT
TOF tray

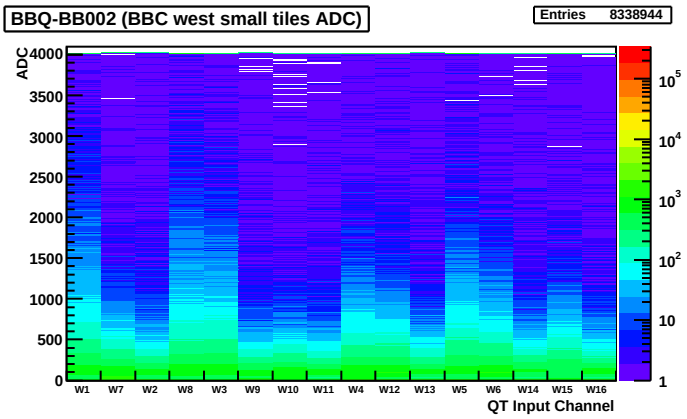

MIX-TF101
Entries 3127104
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

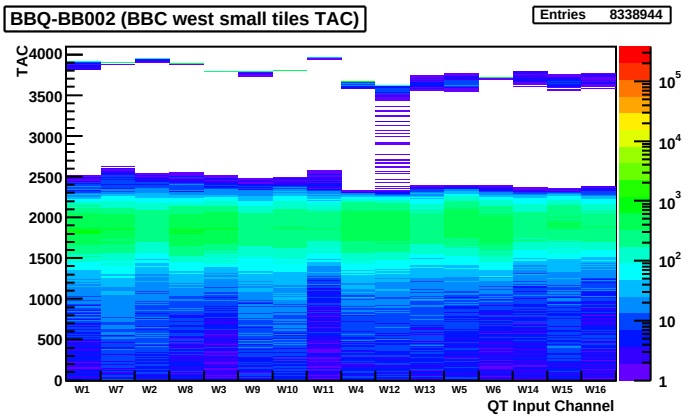

L1-TF201
Entries 521184
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

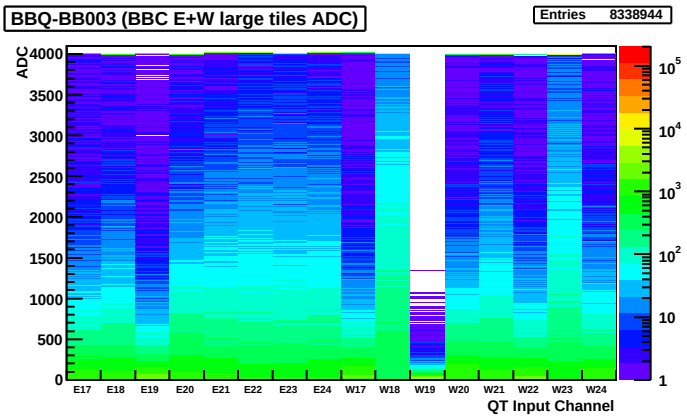

L1-TF201
Entries 3127104
Threshold bits
1
0
375841
364800
323900
346063
312341
345089
145343
156384
197284
175121
208843
176095
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

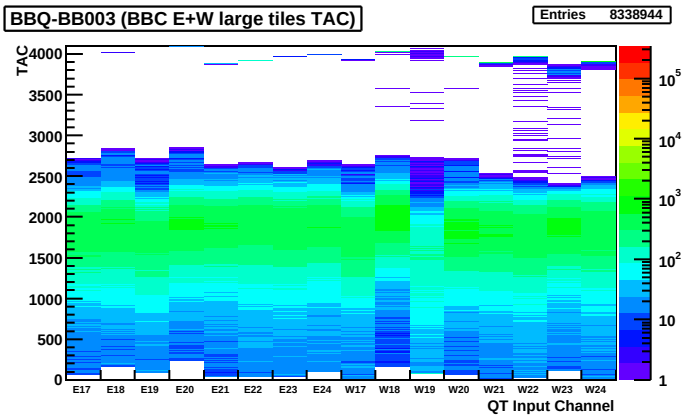

BBQ-BB001 (BBC east small tiles ADC)
Entries 8338944
ADC
QT Input Channel

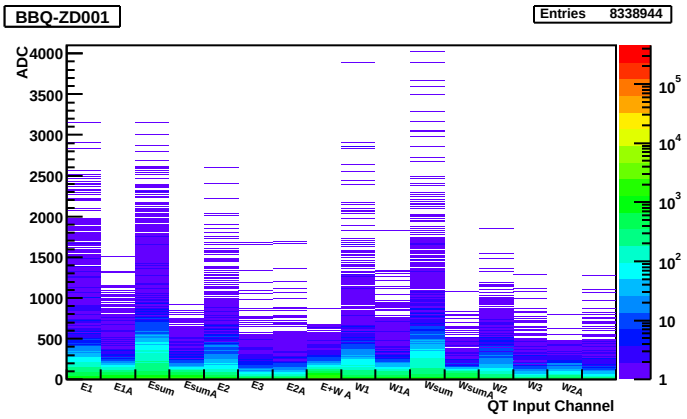

BBQ-BB001 (BBC east small tiles TAC)
Entries 8338944
TAC
QT Input Channel

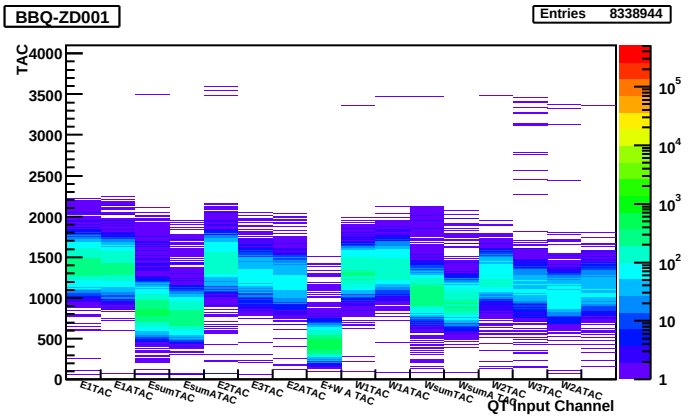

BBQ-BB002 (BBC west small tiles ADC)
Entries 8338944
ADC
QT Input Channel

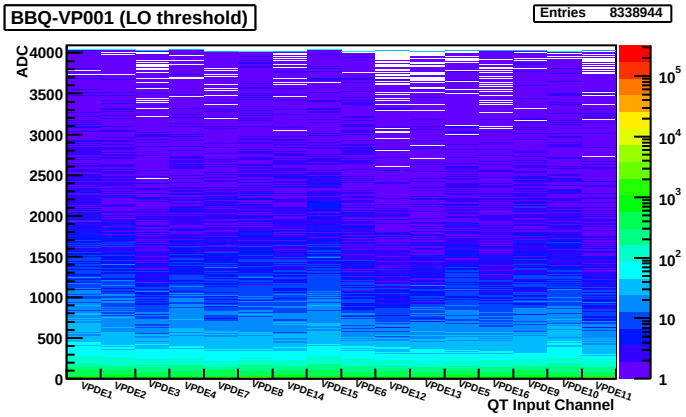

BBQ-BB002 (BBC west small tiles TAC)
Entries 8338944
TAC
QT Input Channel

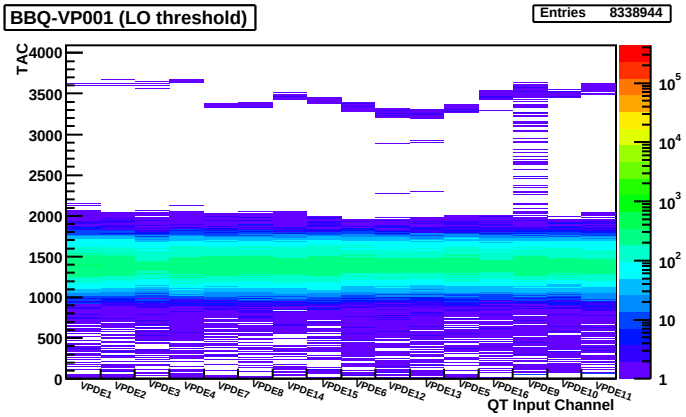

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8338944
ADC
QT Input Channel

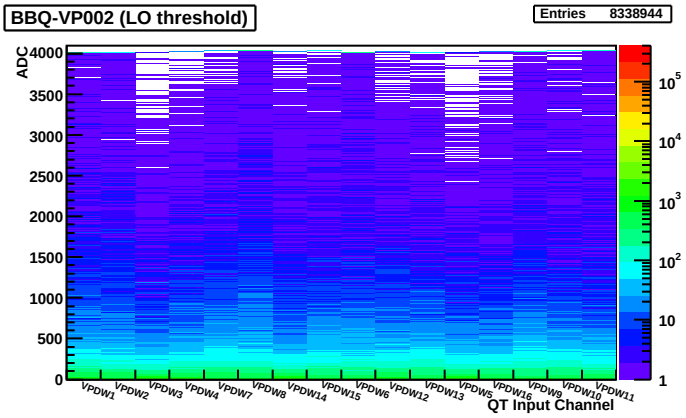

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8338944
TAC
QT Input Channel

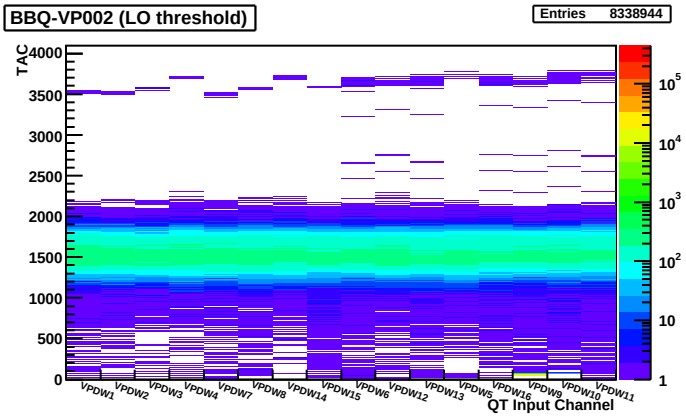

BBQ-ZD001
Entries 8338944
ADC
QT Input Channel

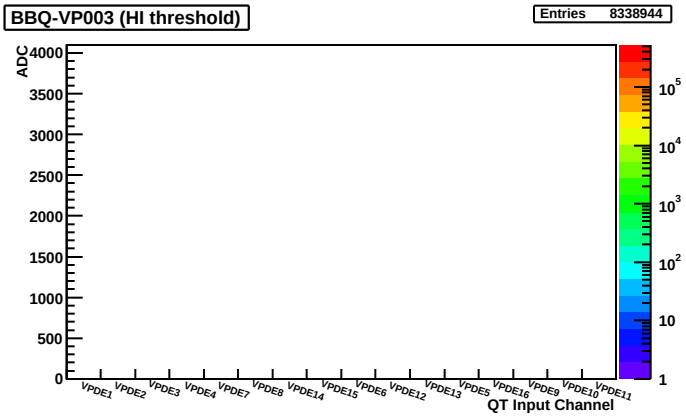

BBQ-ZD001
Entries 8338944
TAC
QT Input Channel

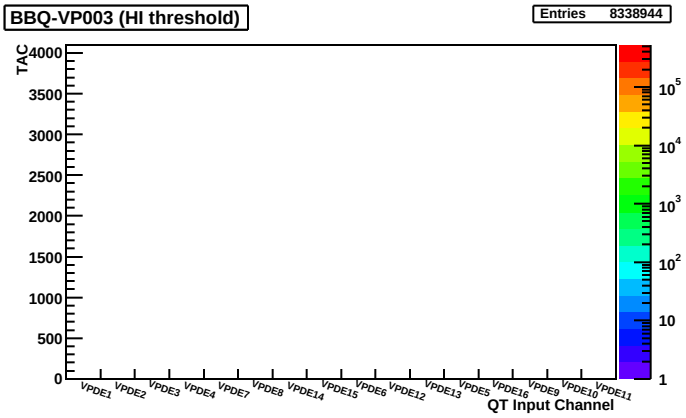

BBQ-VP001 (LO threshold)
Entries 8338944
ADC
QT Input Channel

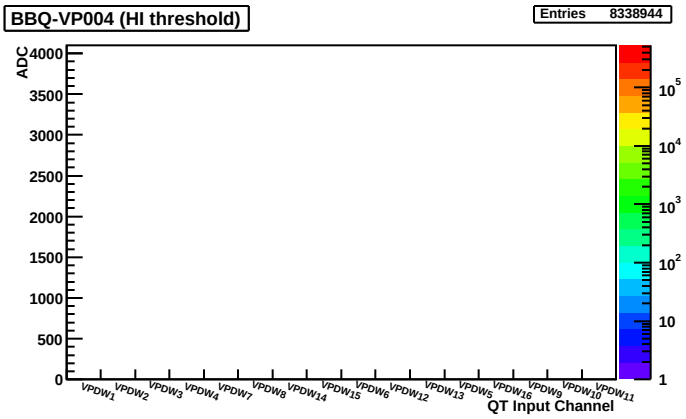

BBQ-VP001 (LO threshold)
Entries 8338944
TAC
QT Input Channel

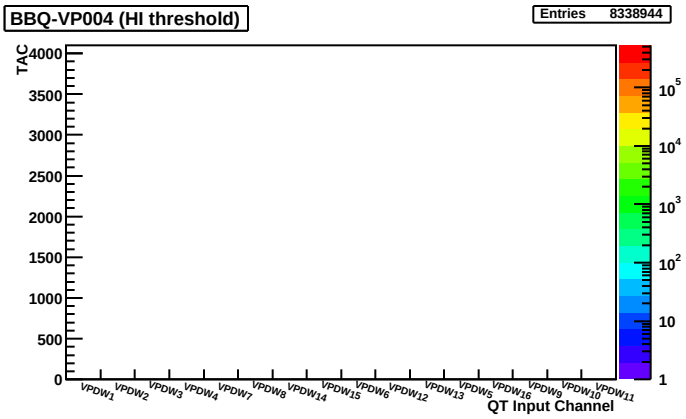

BBQ-VP002 (LO threshold)
Entries 8338944
ADC
QT Input Channel

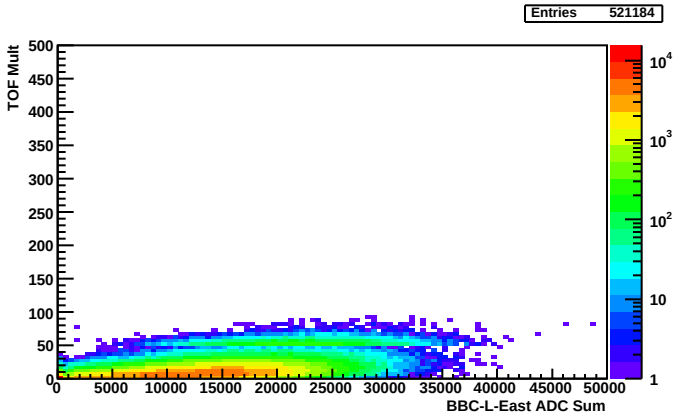

BBQ-VP002 (LO threshold)
Entries 8338944
TAC
QT Input Channel

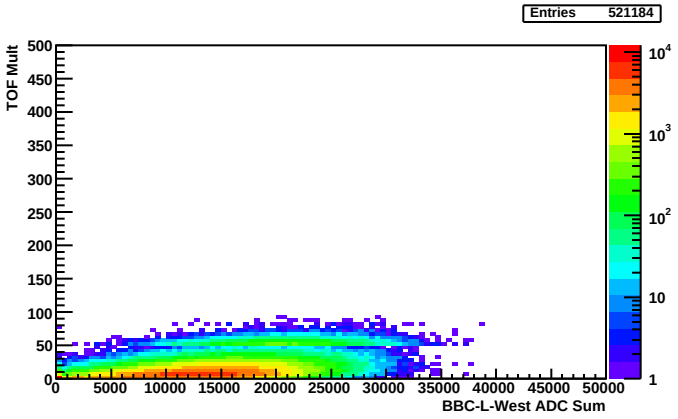

BBQ-VP003 (HI threshold)
Entries 8338944
ADC
QT Input Channel

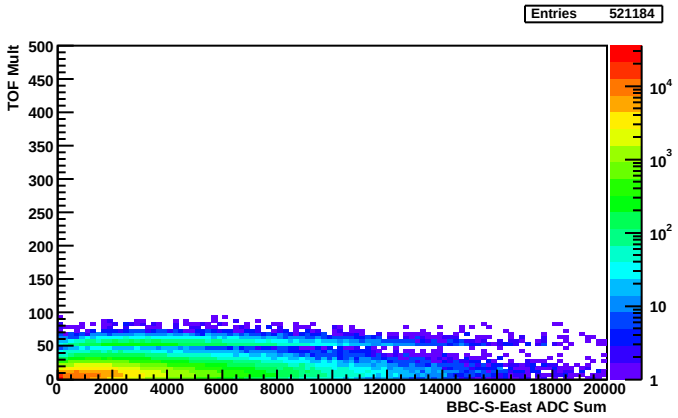

BBQ-VP003 (HI threshold)
Entries 8338944
TAC
QT Input Channel

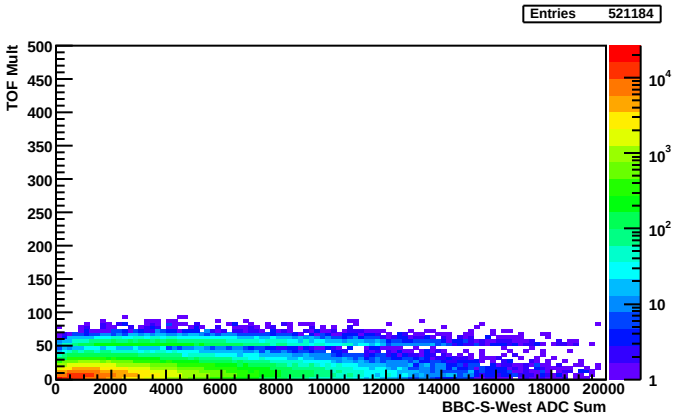

BBQ-VP004 (HI threshold)
Entries 8338944
ADC
QT Input Channel

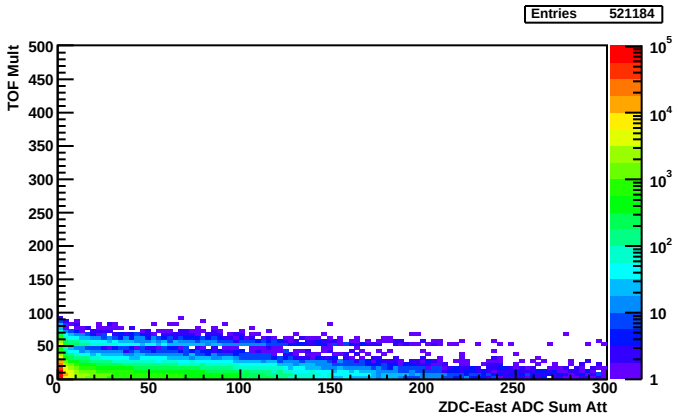

BBQ-VP004 (HI threshold)
Entries 8338944
TAC
QT Input Channel

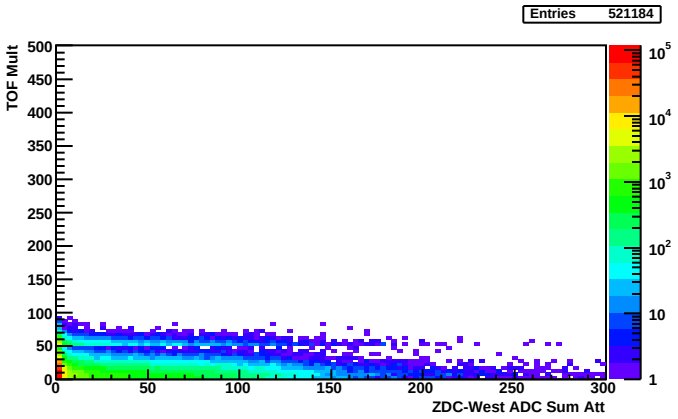

Entries 521184
TOF Mult
BBC-L-East ADC Sum

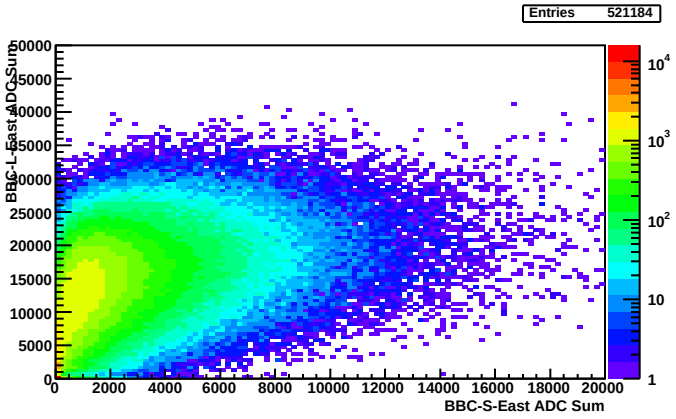

Entries 521184
TOF Mult
BBC-L-West ADC Sum

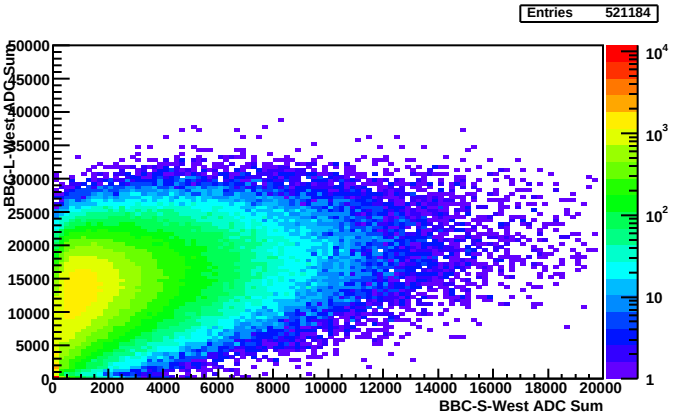

Entries 521184
TOF Mult
BBC-S-East ADC Sum

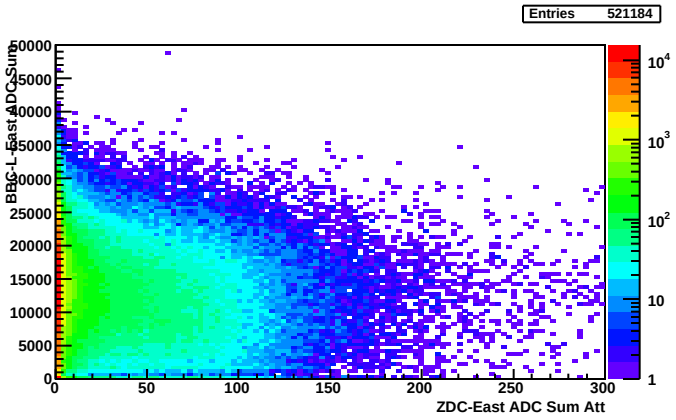

Entries 521184
TOF Mult
BBC-S-West ADC Sum

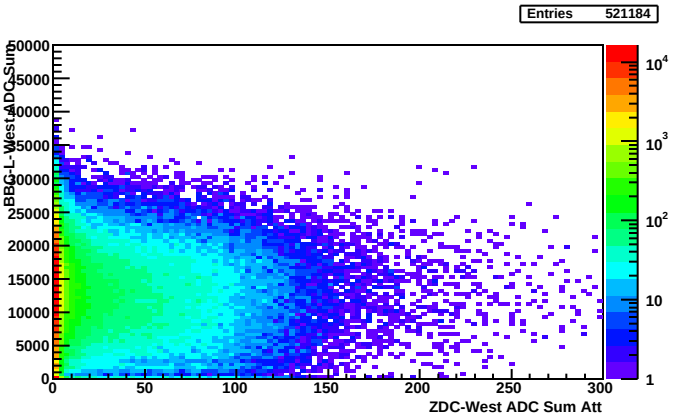

Entries 521184
TOF Mult
ZDC-East ADC Sum Att

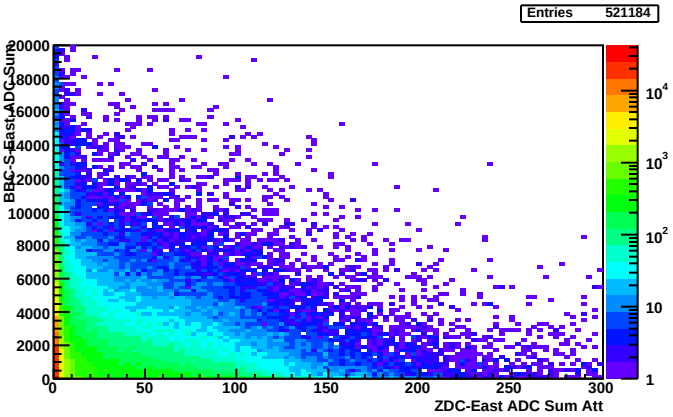

Entries 521184
TOF Mult
ZDC-West ADC Sum Att

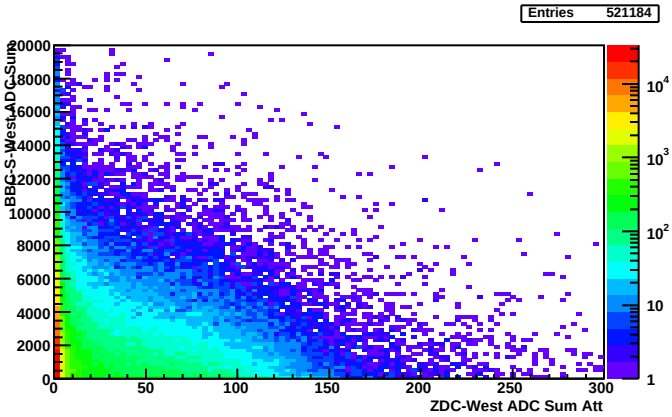

Entries 521184
BBC-L-East ADC Sum
BBC-S-East ADC Sum

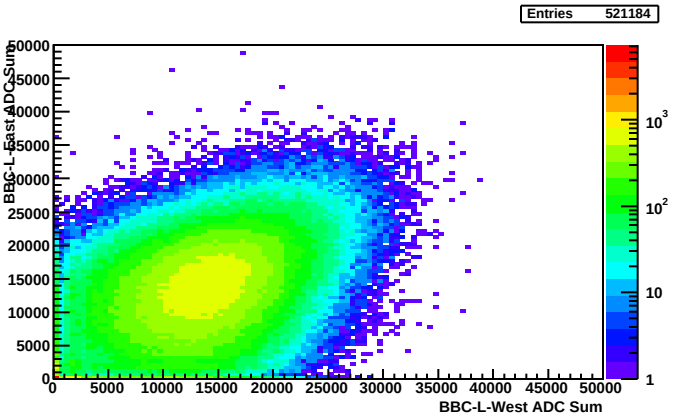

Entries 521184
BBC-L-West ADC Sum
BBC-S-West ADC Sum

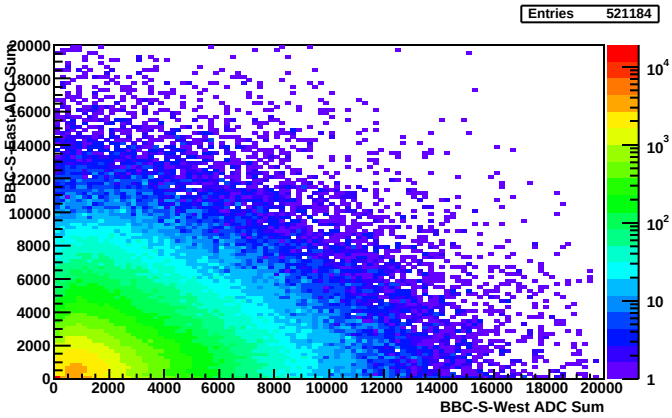

Entries 521184
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

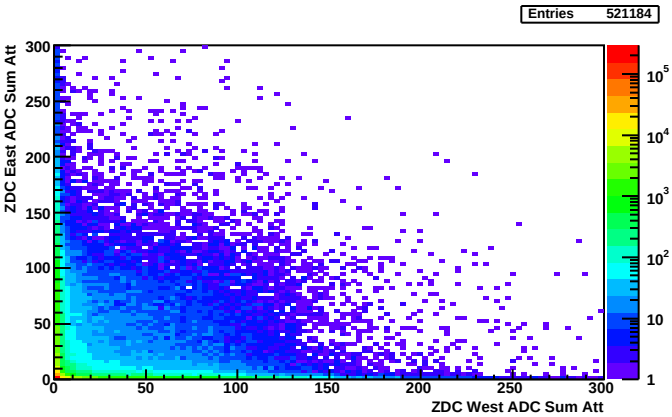

Entries 521184
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

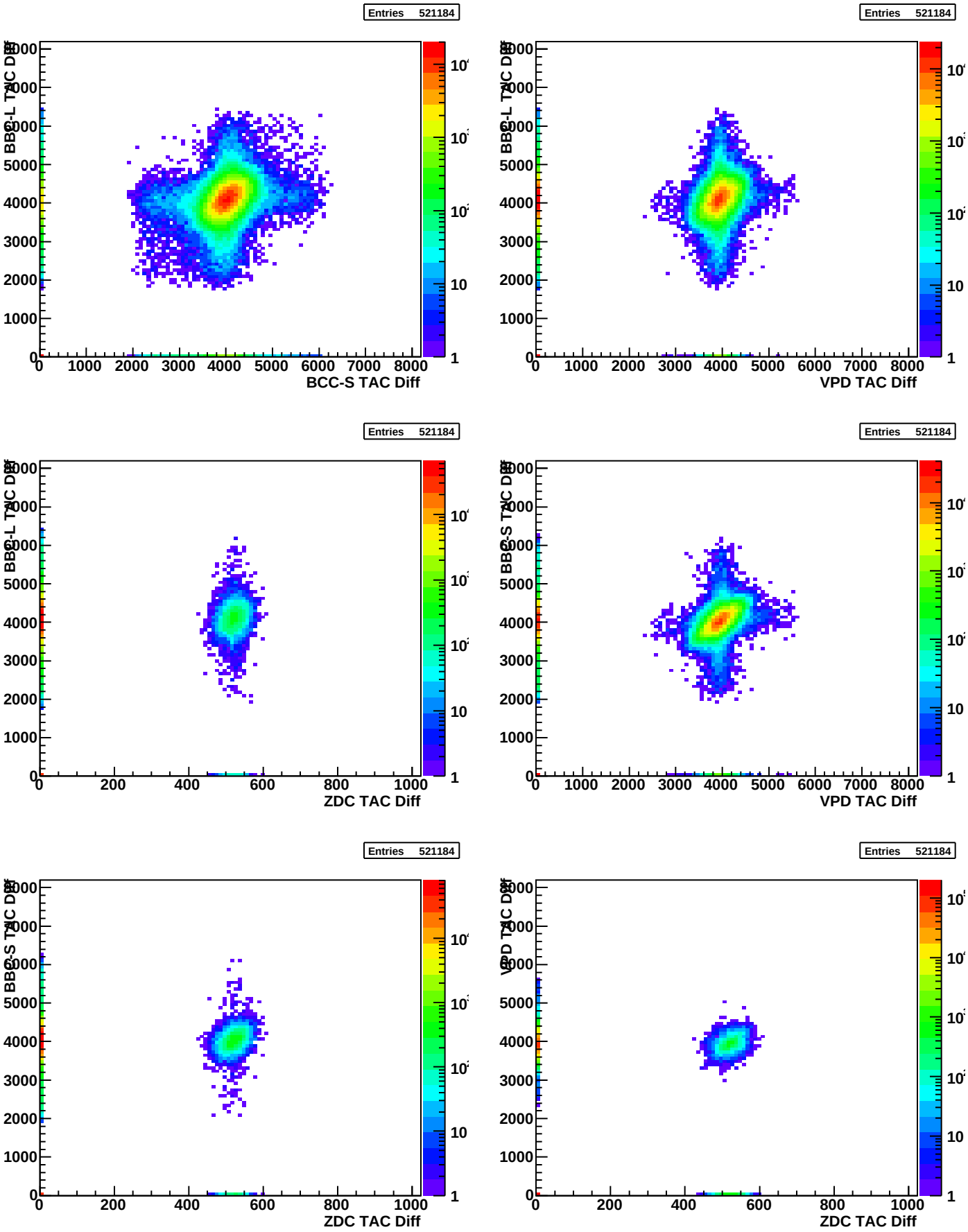

Entries 521184
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

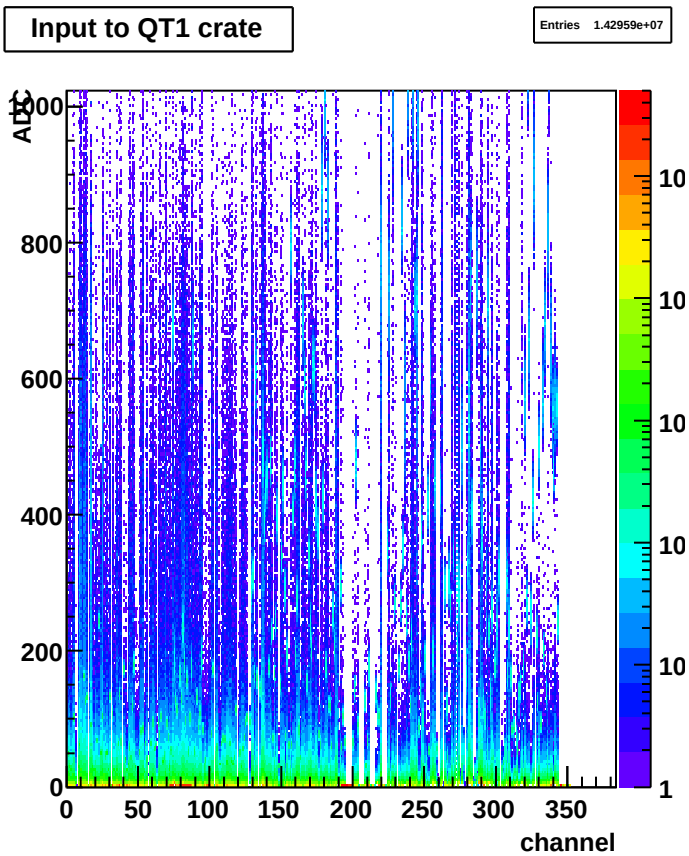

Entries 521184
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

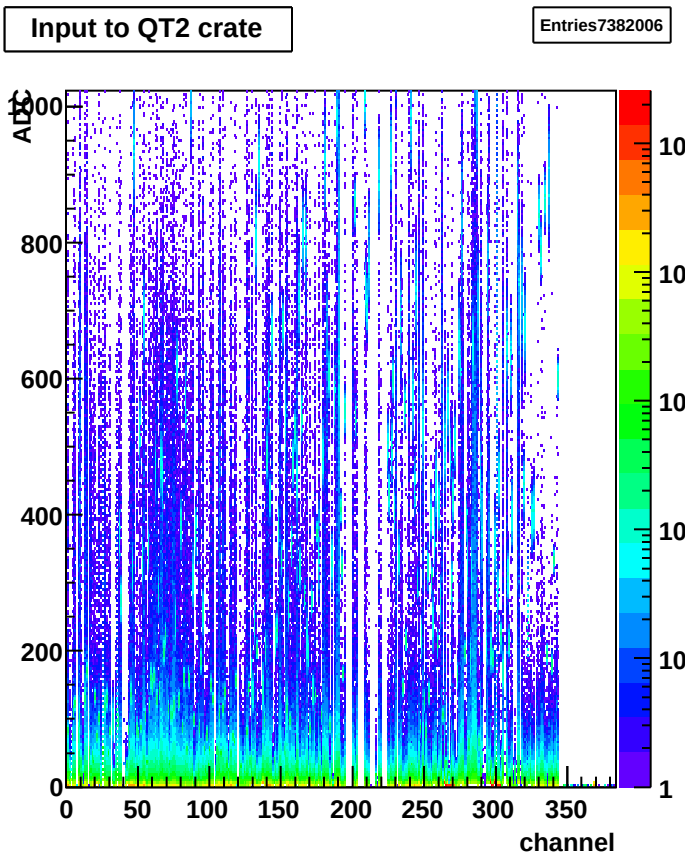

Entries 521184
BBC-L-East ADC Sum
BBC-L-West ADC Sum

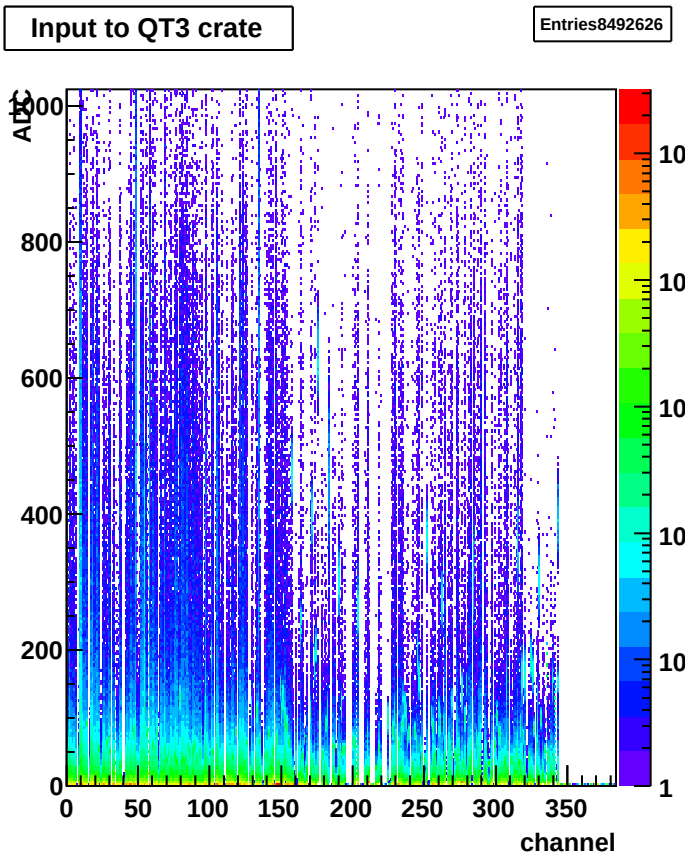

Entries 521184
BBC-S-East ADC Sum
BBC-S-West ADC Sum

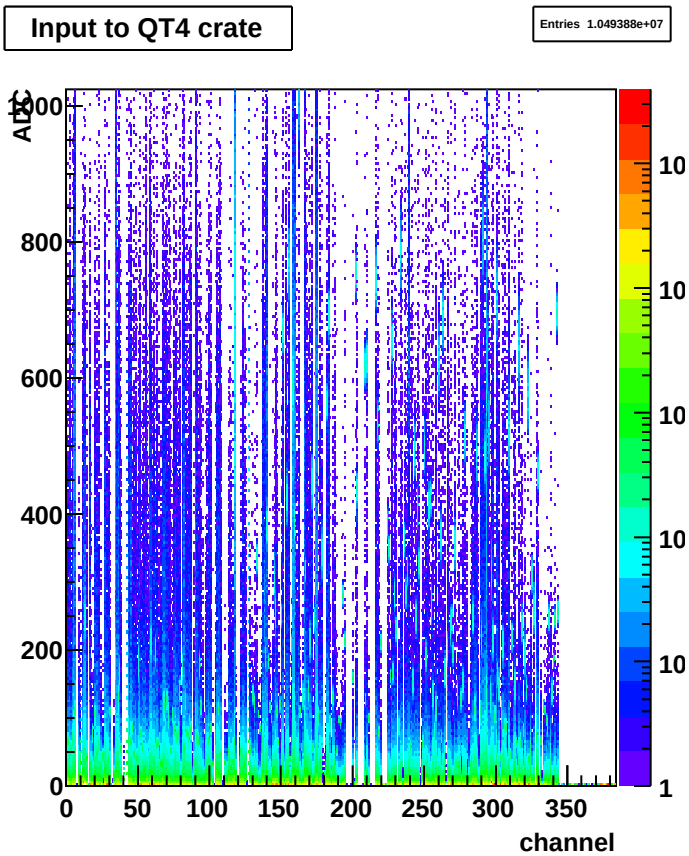

Entries 521184
ZDC East ADC Sum Att
ZDC West ADC Sum Att

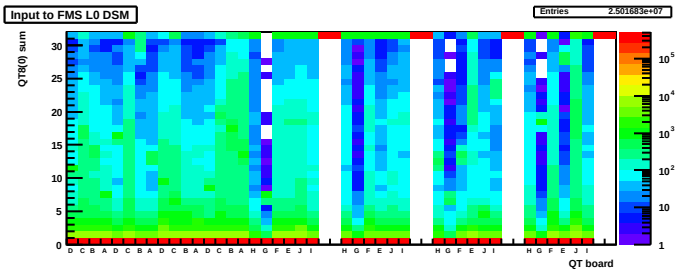

Entries 521184
BBC-L TAC Diff
BCC-S TAC Diff
Entries 521184
BBC-L TAC Diff
VPD TAC Diff
Entries 521184
BBC-L TAC Diff
ZDC TAC Diff
Entries 521184
BBC-S TAC Diff
VPD TAC Diff
Entries 521184
BBC-S TAC Diff
ZDC TAC Diff
Entries 521184
VPD TAC Diff
ZDC TAC Diff

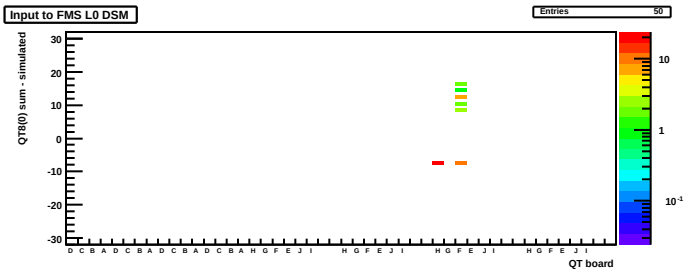

Input to QT1 crate
Entries 1.42959e+07
ADC
channel

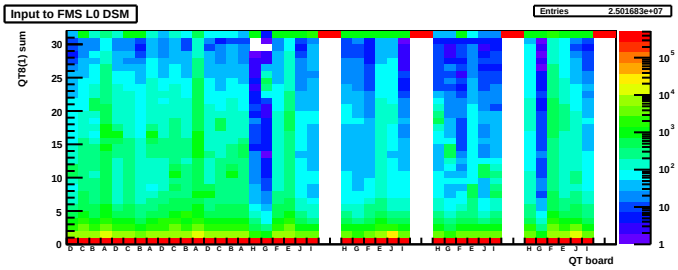

Input to QT2 crate
Entries7382006
ADC
channel

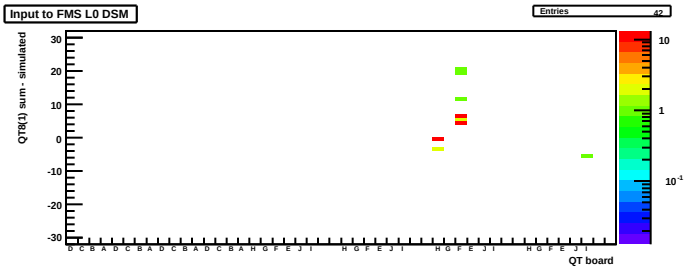

Input to QT3 crate
Entries8492626
ADC
channel

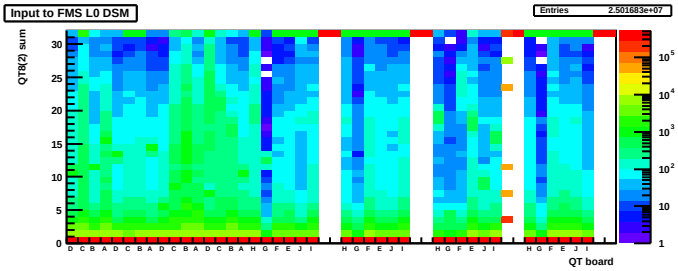

Input to QT4 crate
Entries 1.049388e+07
ADC
channel

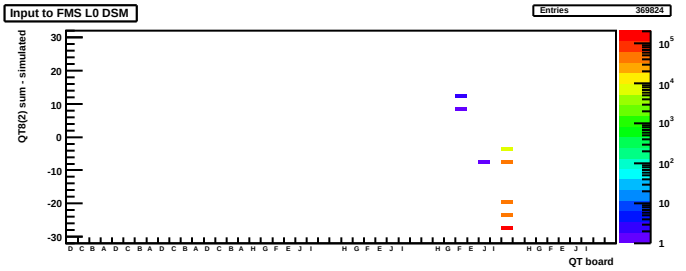

Input to FMS L0 DSM
Entries 2.501683e+07
QT8(0) sum
QT board

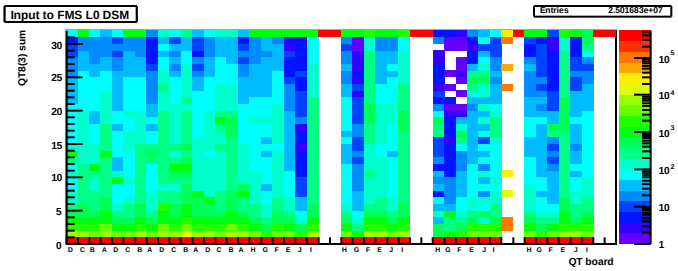

Input to FMS L0 DSM
Entries 50
QT8(0) sum - simulated
QT board

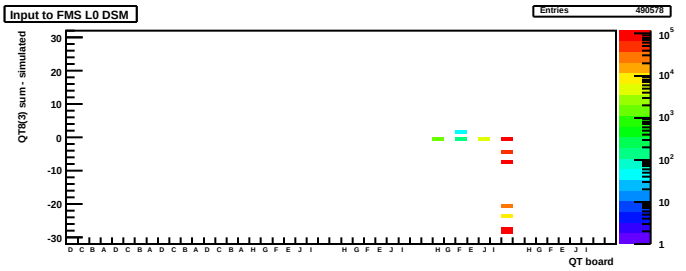

Input to FMS L0 DSM
Entries 2.501683e+07
QT8(1) sum
QT board

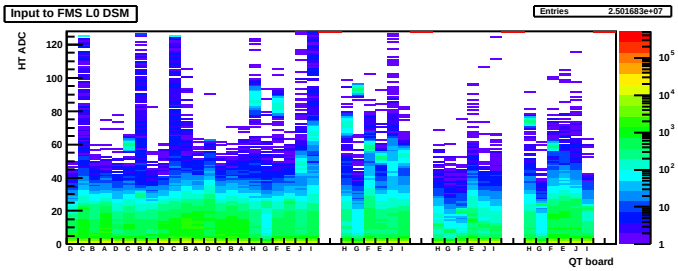

Input to FMS L0 DSM
Entries 42
QT8(1) sum - simulated
QT board

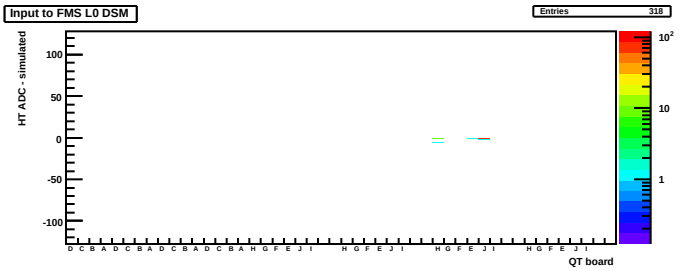

Input to FMS L0 DSM
Entries 2.501683e+07
QT8(2) sum
QT board

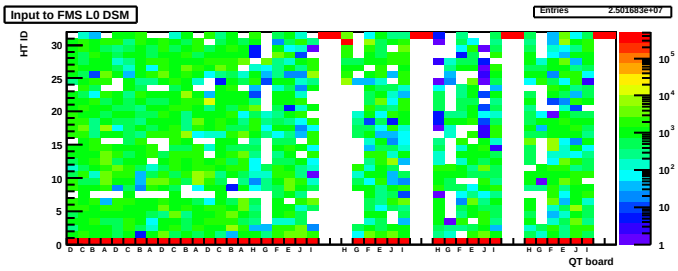

Input to FMS L0 DSM
Entries 369824
QT8(2) sum - simulated
QT board

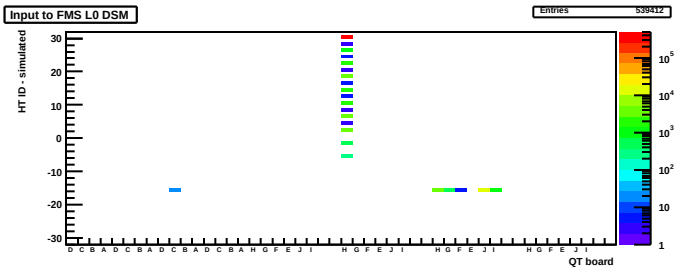

Input to FMS L0 DSM
Entries 2.501683e+07
QT8(3) sum
QT board

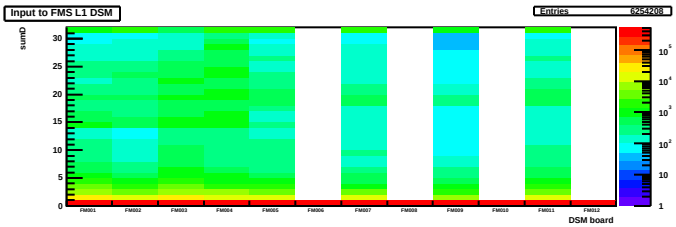

Input to FMS L0 DSM
Entries 490578
QT8(3) sum - simulated
QT board

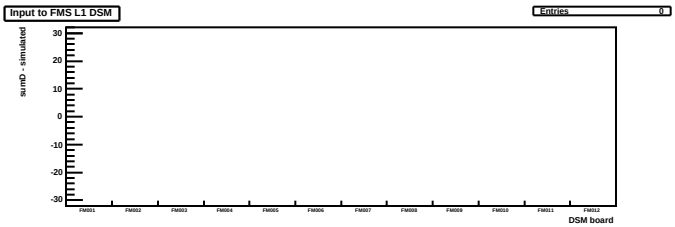

Input to FMS L0 DSM
Entries 2.501683e+07
HT ADC
QT board

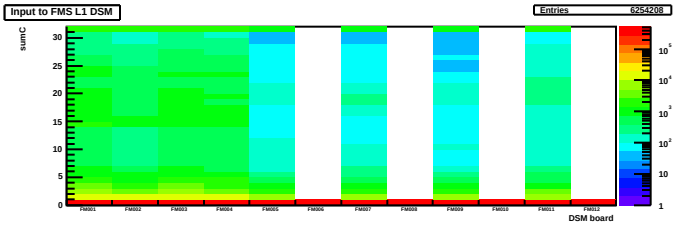

Input to FMS L0 DSM
Entries 318
HT ADC - simulated
QT board

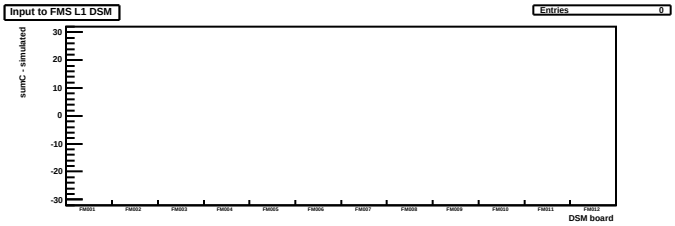

Input to FMS L0 DSM
Entries 2.501683e+07
HT ID
QT board

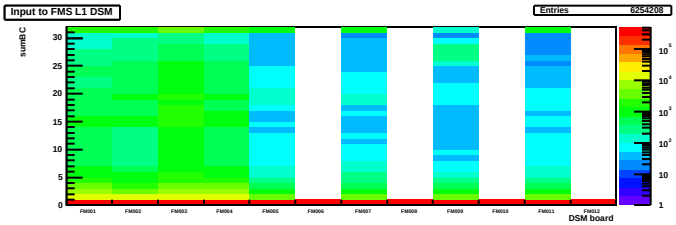

Input to FMS L0 DSM
Entries 539412
HT ID - simulated
QT board

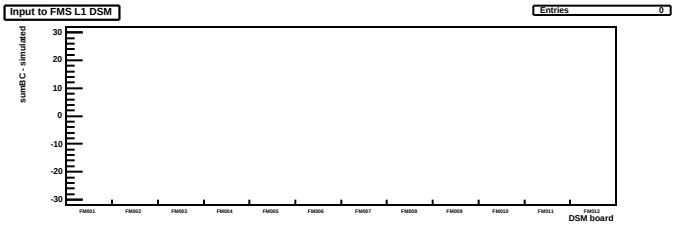

Input to FMS L1 DSM
Entries 6254208
sumD
DSM board

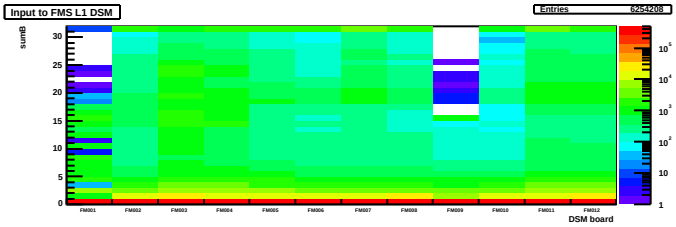

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

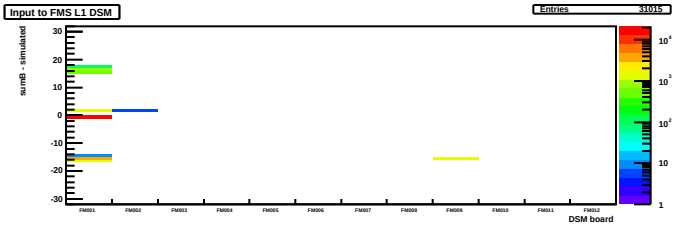

Input to FMS L1 DSM
Entries 6254208
sumC
DSM board

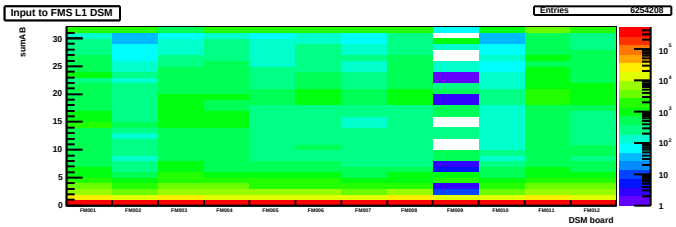

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

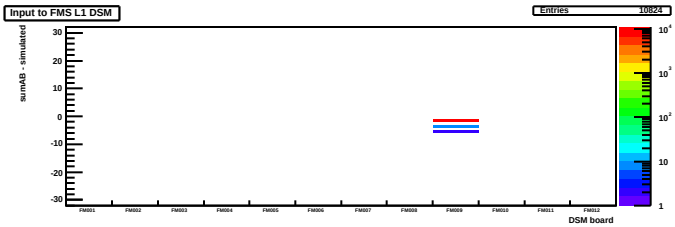

Input to FMS L1 DSM
Entries 6254208
sumBC
DSM board

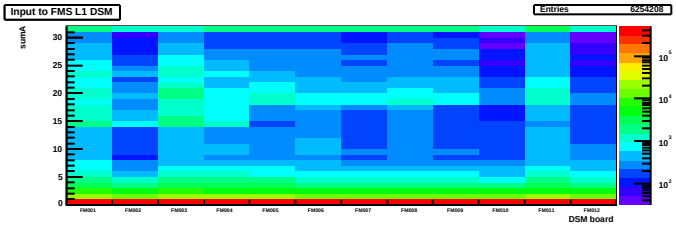

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

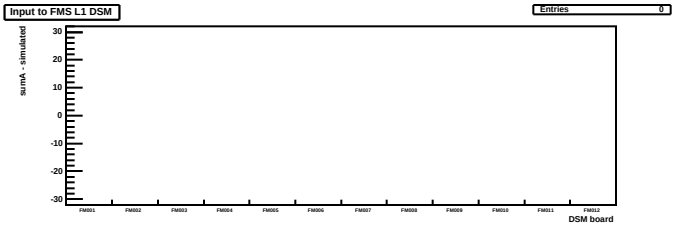

Input to FMS L1 DSM
Entries 6254208
sumB
DSM board

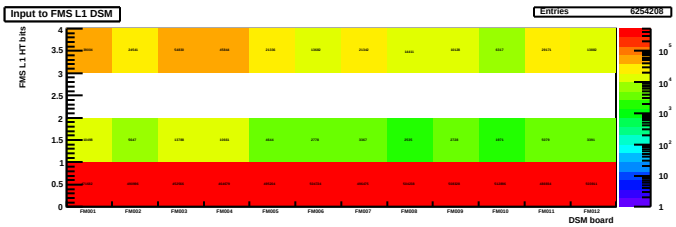

Input to FMS L1 DSM
Entries 31015
sumB - simulated
DSM board

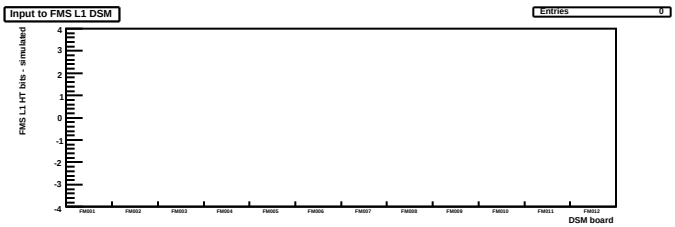

Input to FMS L1 DSM
Entries 6254208
sumAB
DSM board

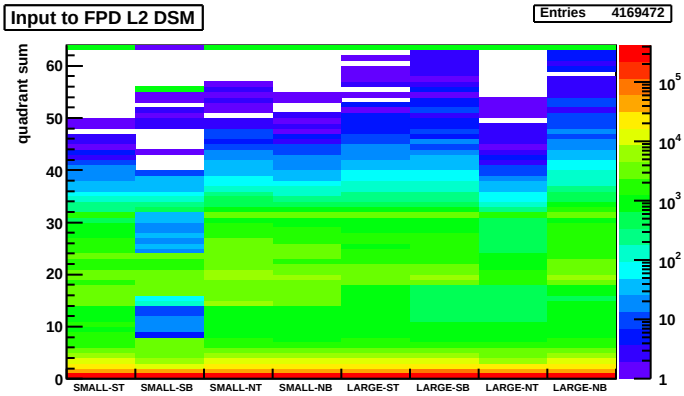

Input to FMS L1 DSM
Entries 10824
sumAB - simulated
DSM board

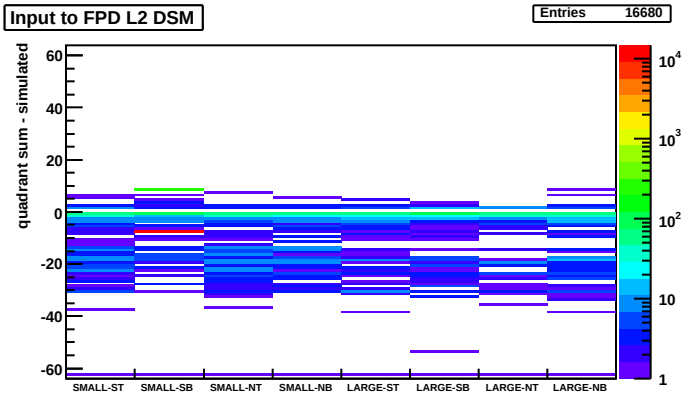

Input to FMS L1 DSM
Entries 6254208
sumA
DSM board

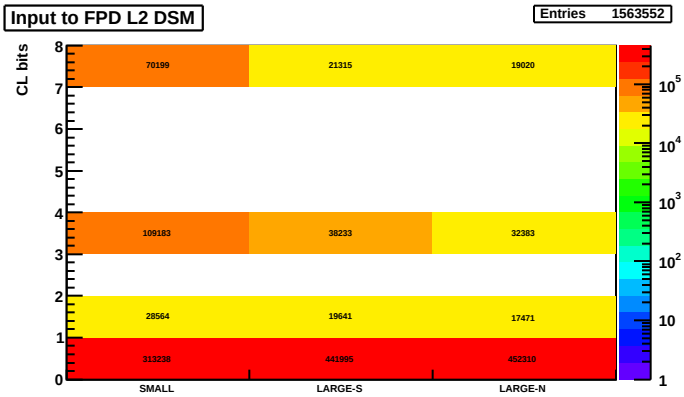

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

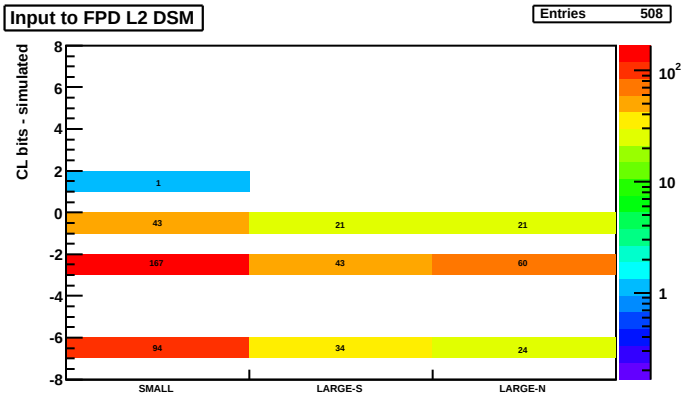

Input to FMS L1 DSM
Entries 6254208
FMS L1 HT bits
DSM board

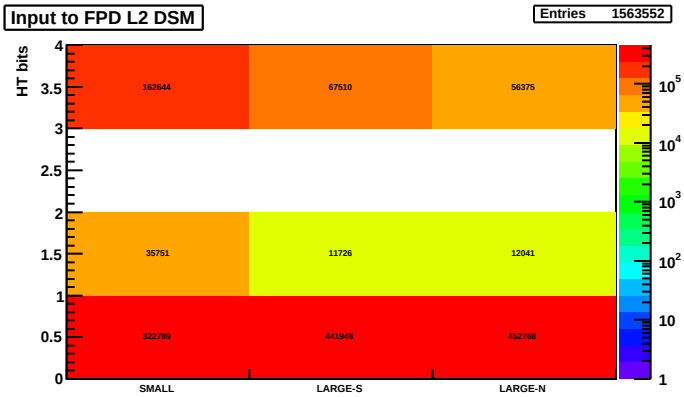

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

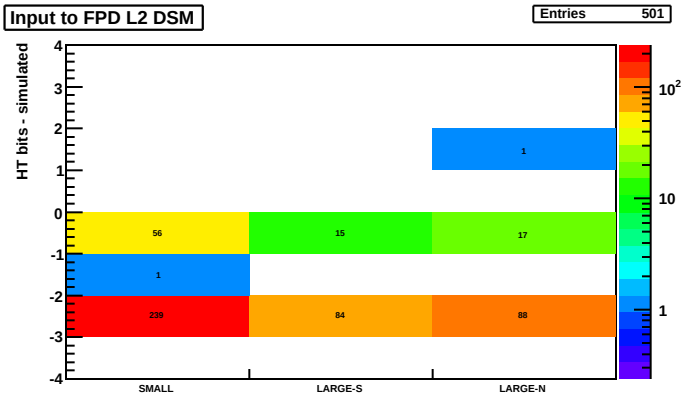

Input to FPD L2 DSM
Entries 4169472
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

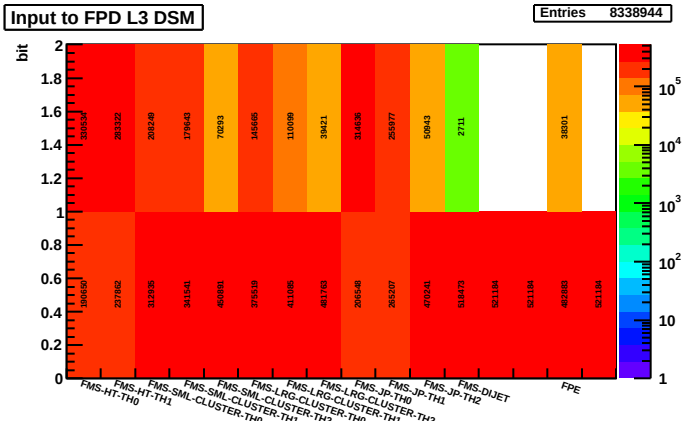

Input to FPD L2 DSM
Entries 16680
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

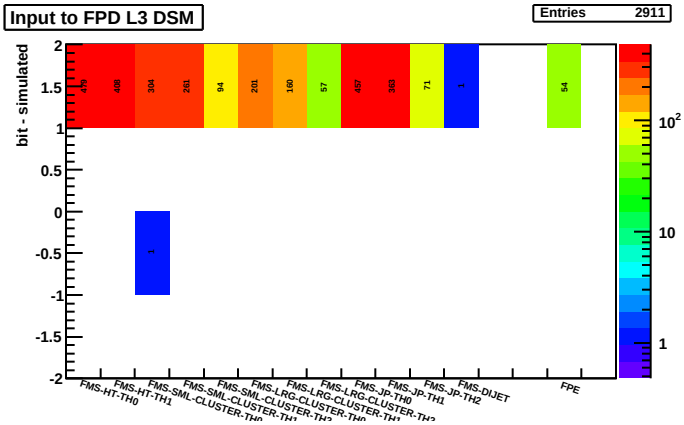

Input to FPD L2 DSM
Entries 1563552
CL bits
70199
21315
19020
109183
38233
32383
28564
19641
17471
313238
441995
452310
SMALL
LARGE-S
LARGE-N

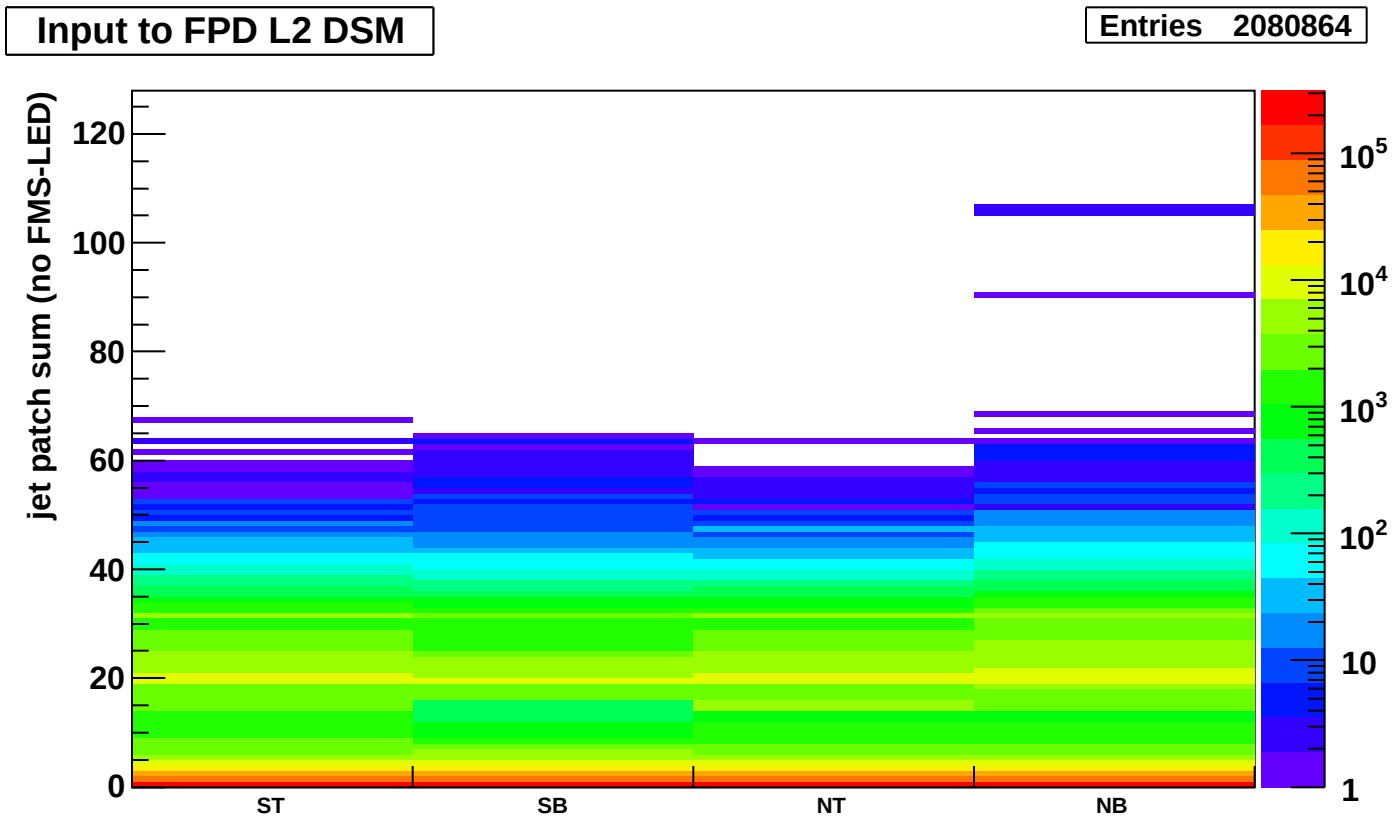

Input to FPD L2 DSM
Entries 508
CL bits - simulated
1
43
21
21
167
43
60
94
34
24
SMALL
LARGE-S
LARGE-N

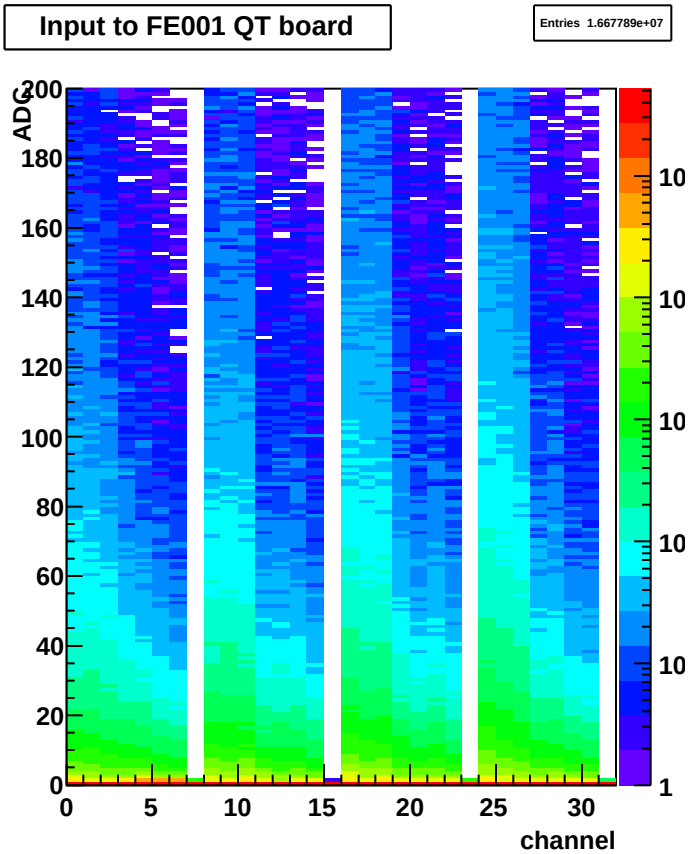

Input to FPD L2 DSM
Entries 1563552
HT bits
162644
67510
56375
35751
11726
12041
322789
441948
452768
SMALL
LARGE-S
LARGE-N

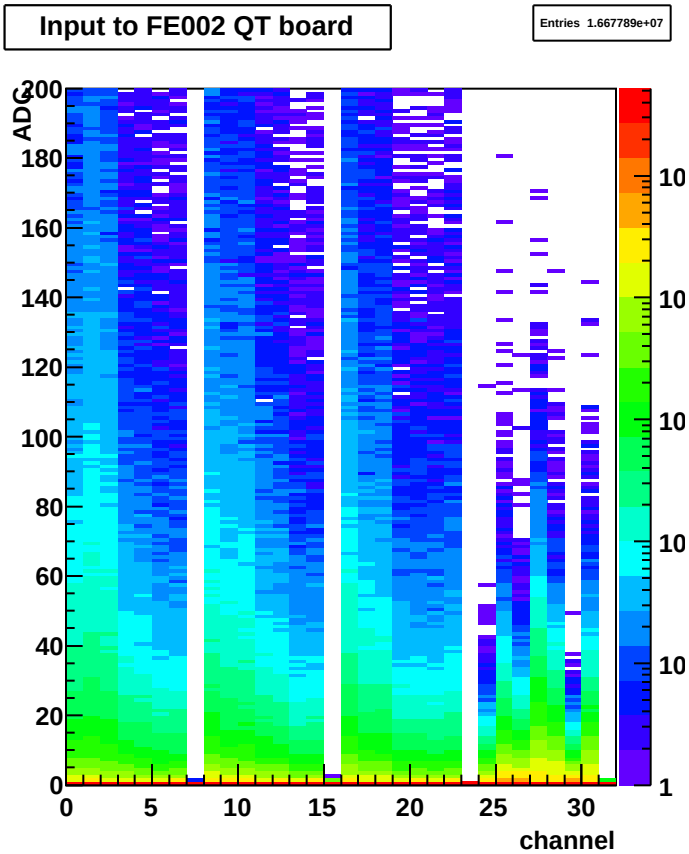

Input to FPD L2 DSM
Entries 501
HT bits - simulated
1
56
15
17
1
239
84
88
SMALL
LARGE-S
LARGE-N

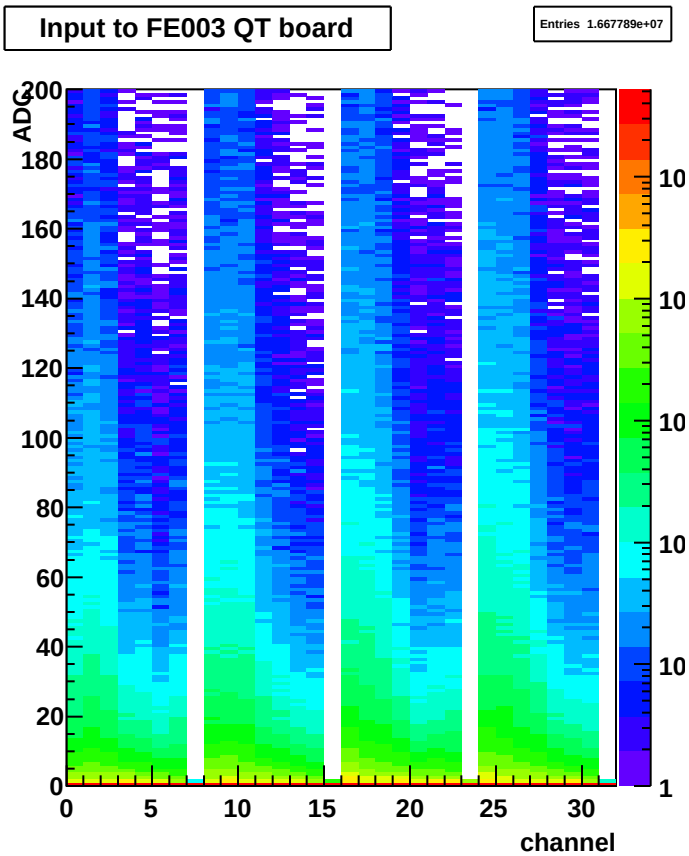

Input to FPD L3 DSM
Entries 8338944
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

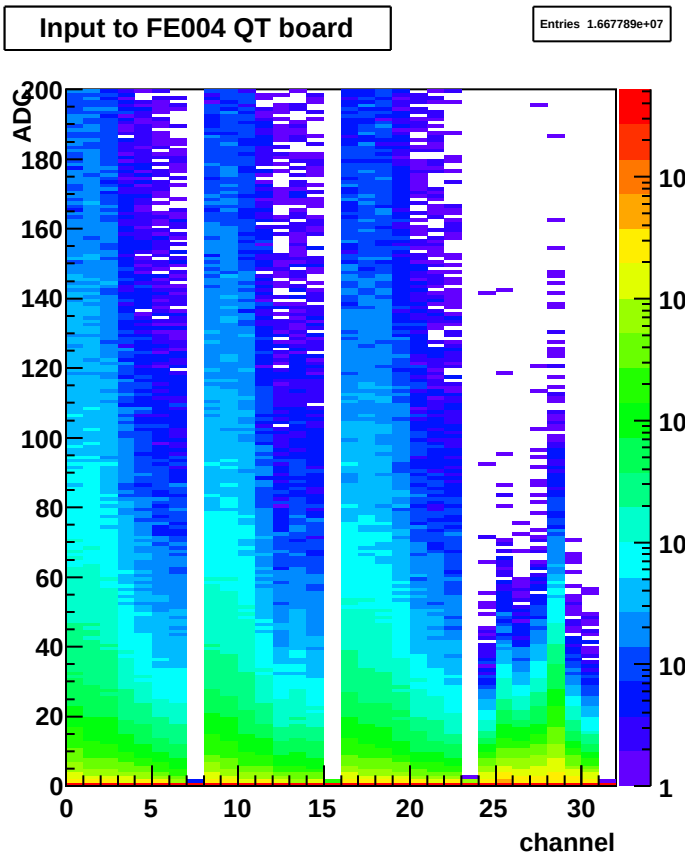

Input to FPD L3 DSM
Entries 2911
bit - simulated
479
408
394
261
94
201
160
57
457
363
71
1
54
1
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

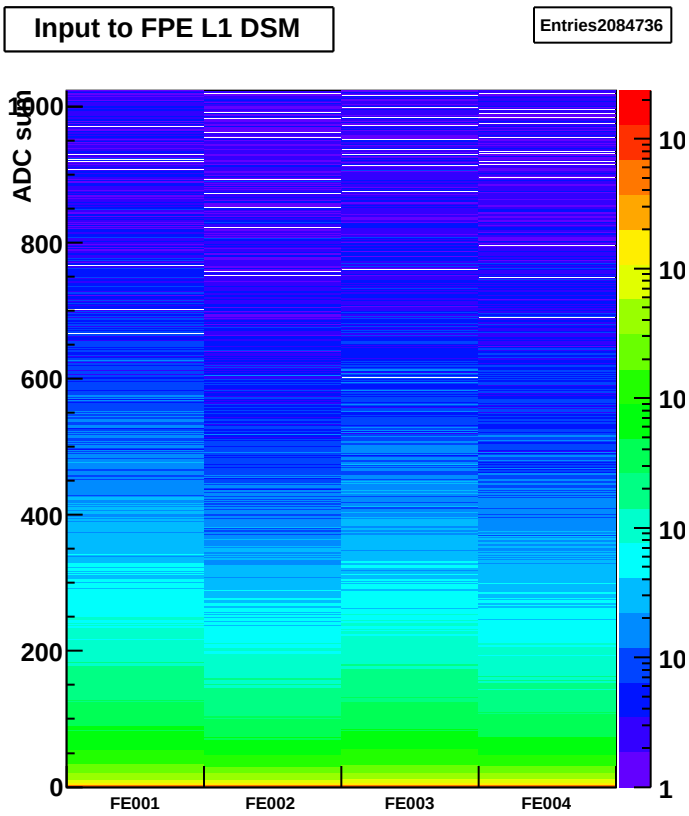

Input to FPD L2 DSM
Entries 2080864
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

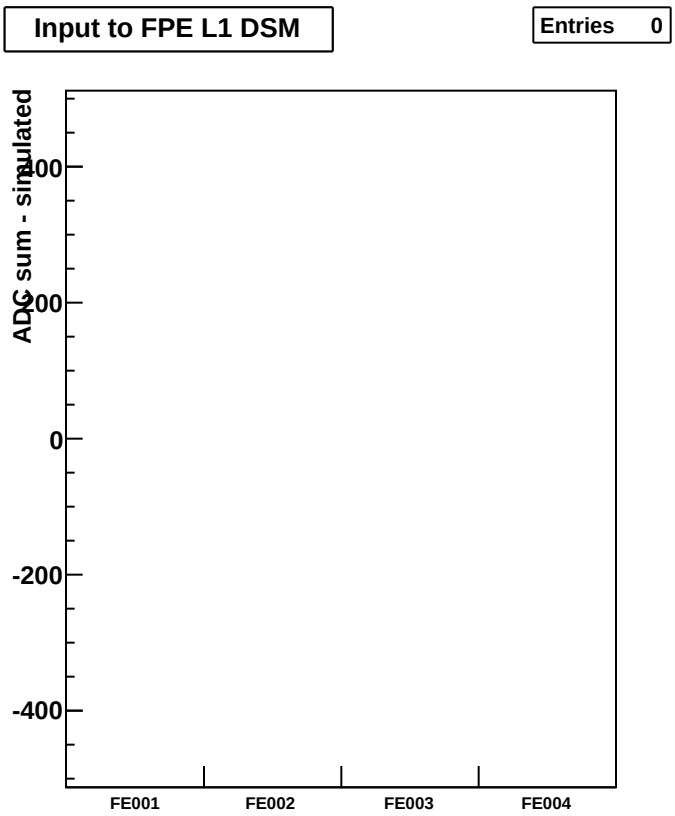

Input to FE001 QT board
Entries 1.667789e+07
ADC
channel

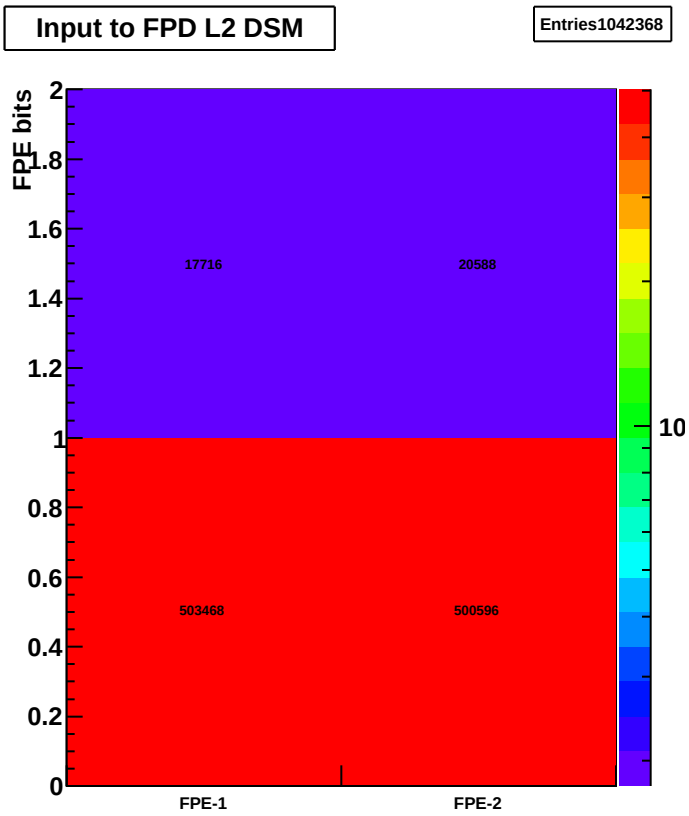

Input to FE002 QT board
Entries 1.667789e+07
ADC
channel

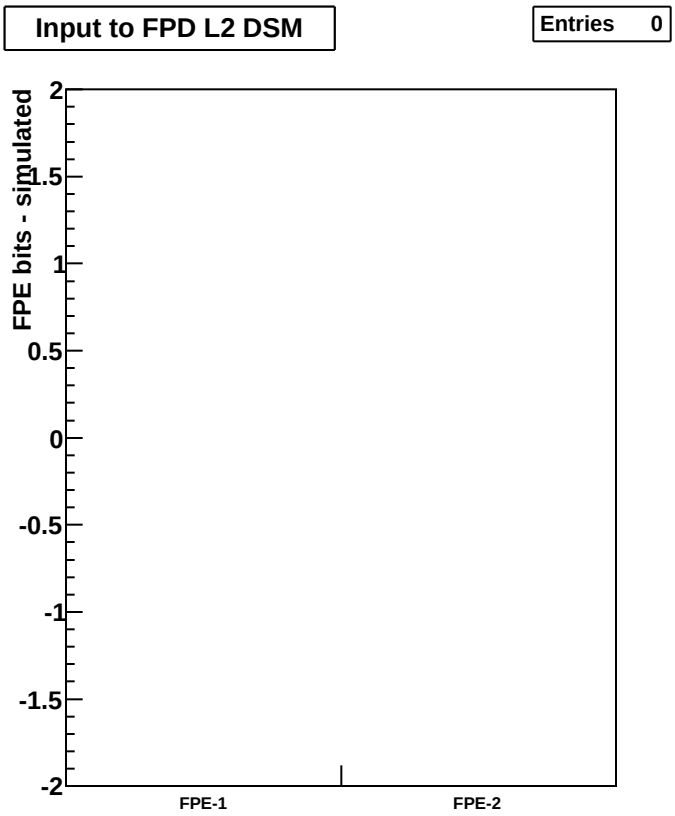

Input to FE003 QT board
Entries 1.667789e+07
ADC
channel


Input to FE004 QT board
Entries 1.667789e+07
ADC
channel


Input to FPE L1 DSM
Entries2084736
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1042368
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
17716
20588
503468
500596
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2