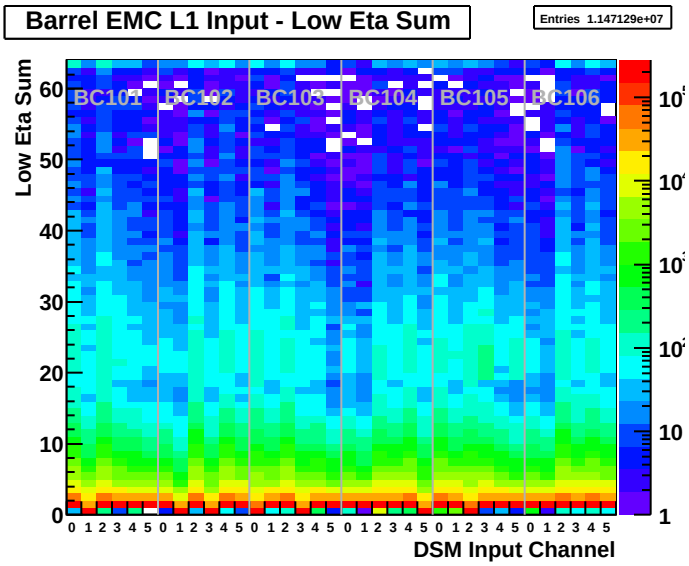
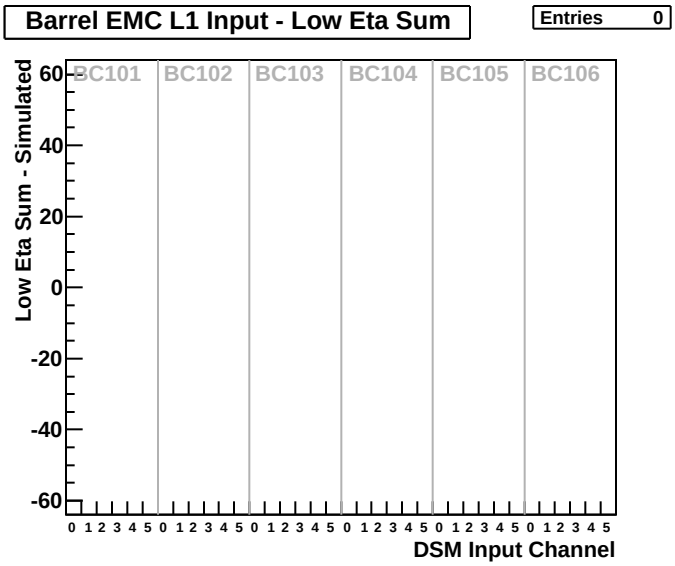
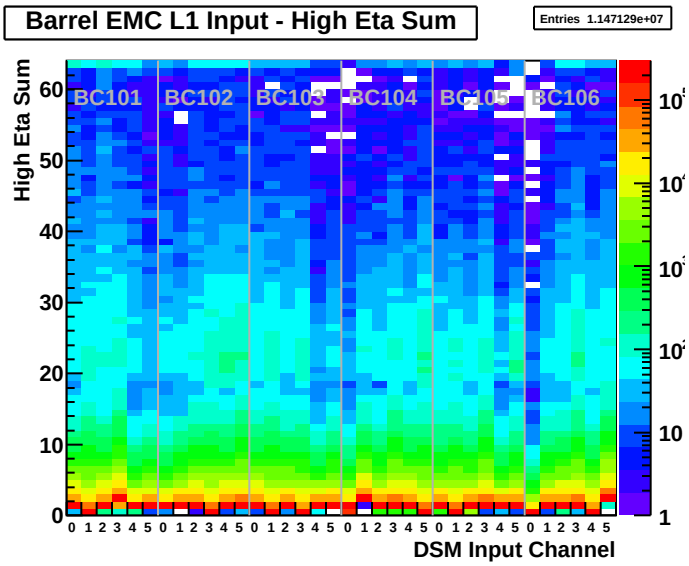
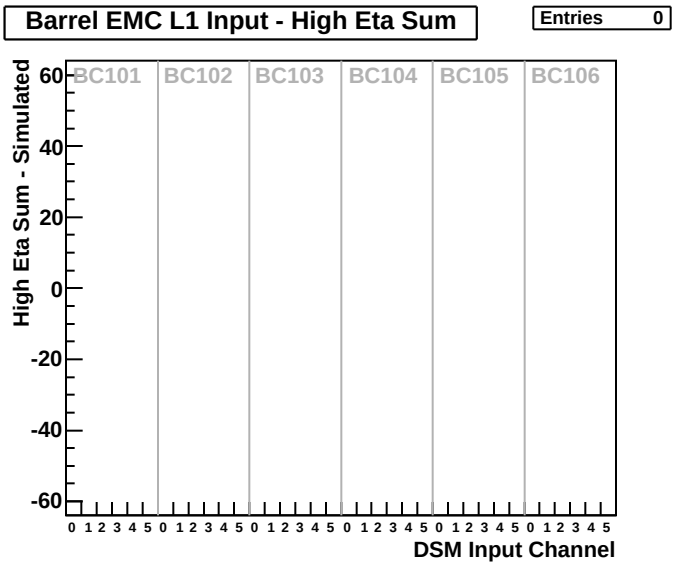
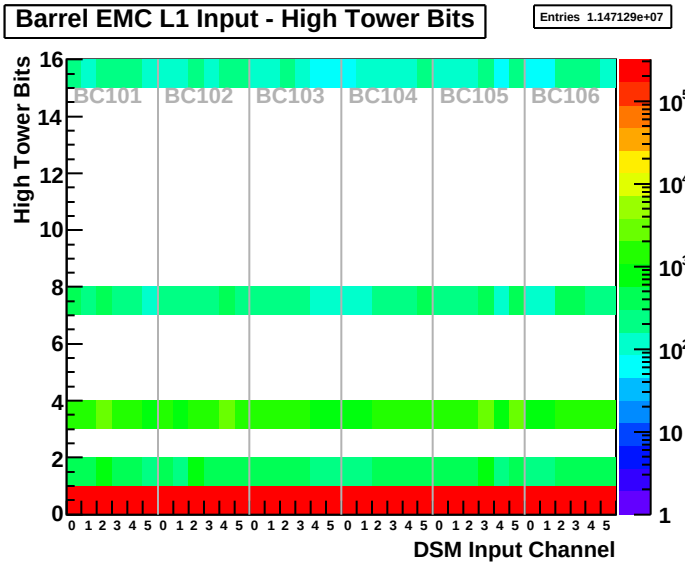
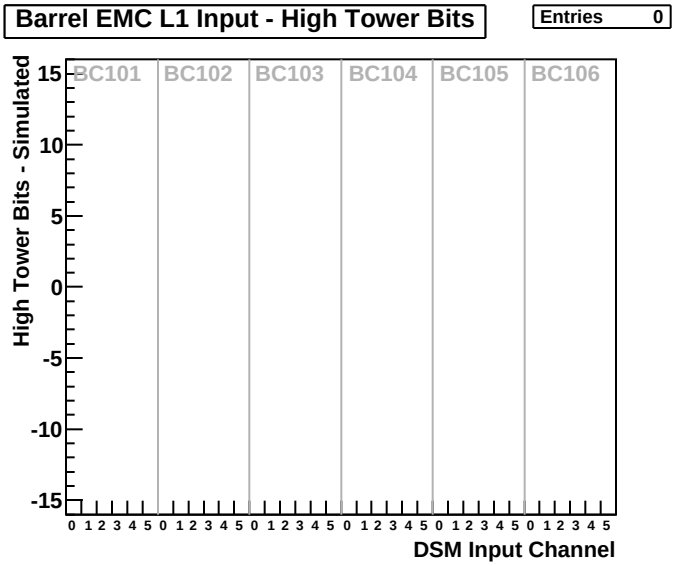

Barrel EMC L1 Input - Low Eta Sum
Entries 1.147129e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

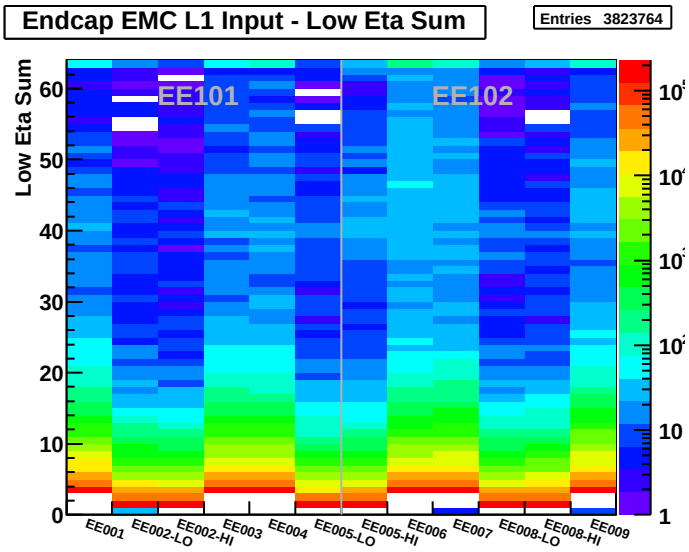

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

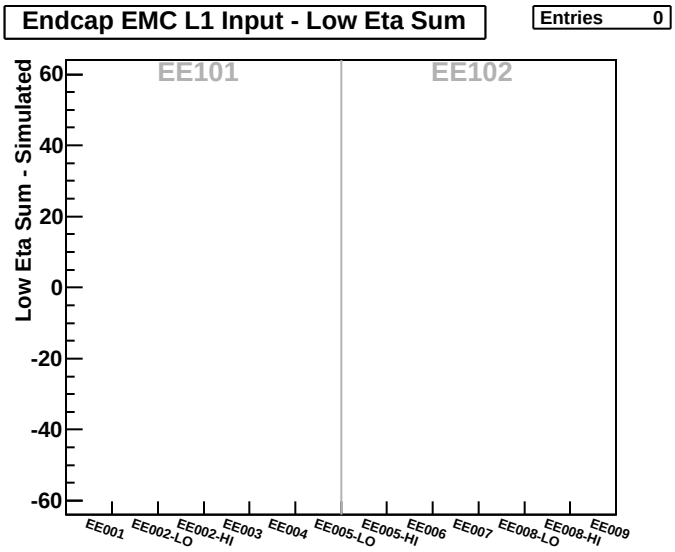

Barrel EMC L1 Input - High Eta Sum
Entries 1.147129e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

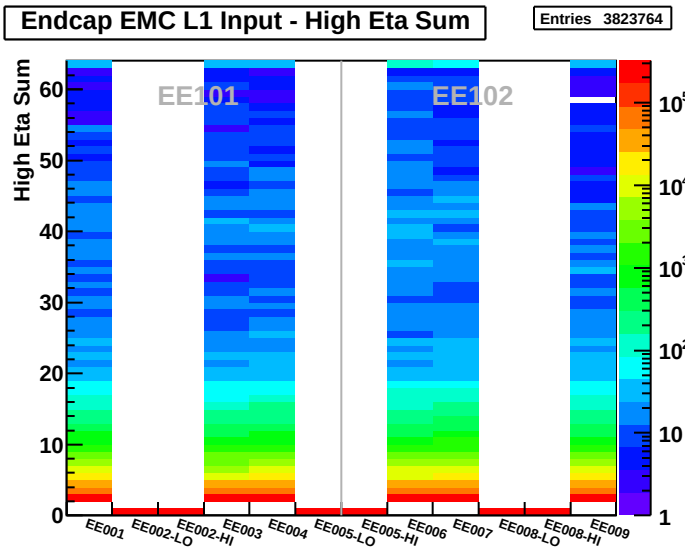

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

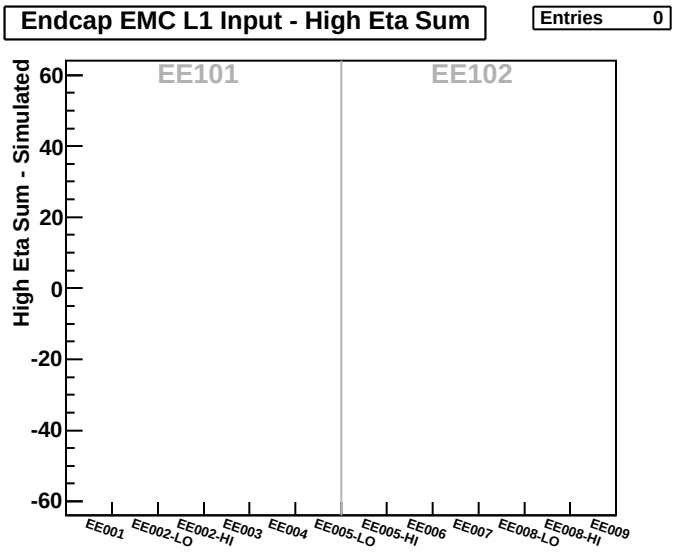

Barrel EMC L1 Input - High Tower Bits
Entries 1.147129e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

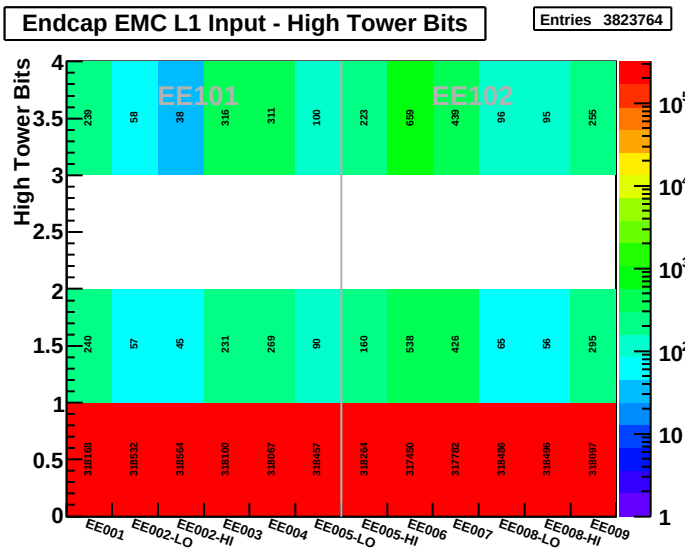

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

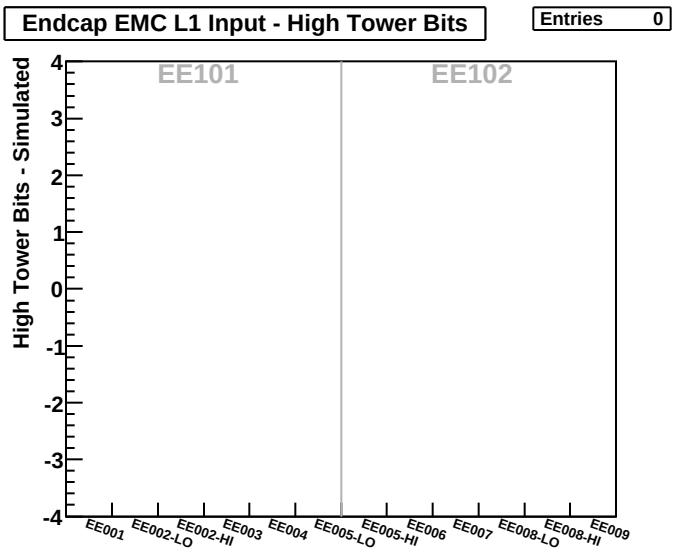

Endcap EMC L1 Input - Low Eta Sum
Entries 3823764
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

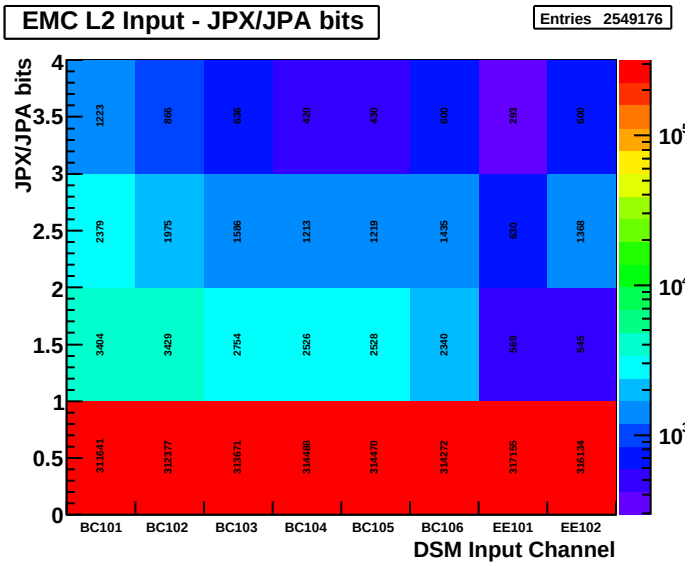

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

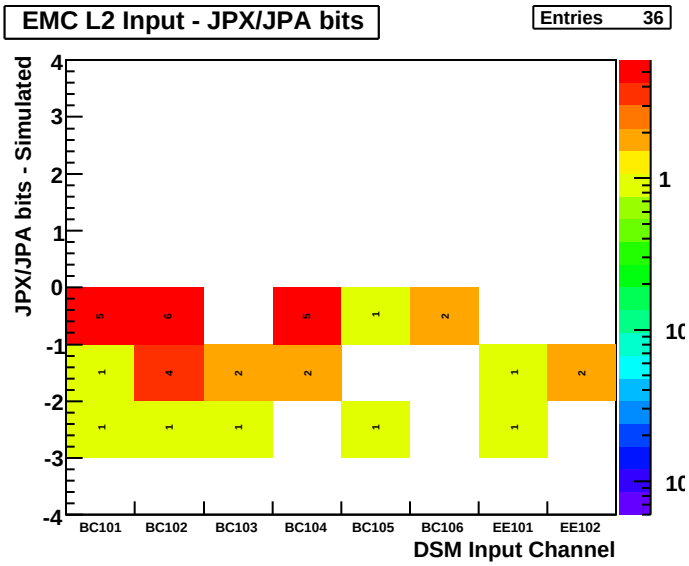

Endcap EMC L1 Input - High Eta Sum
Entries 3823764
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

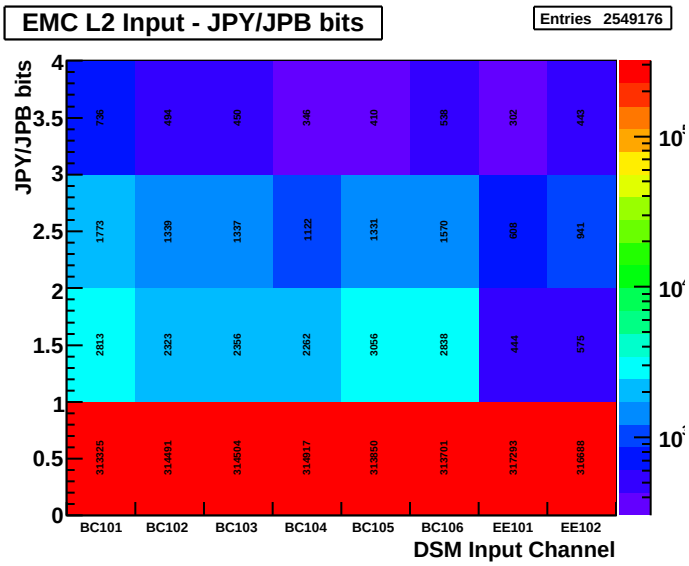

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

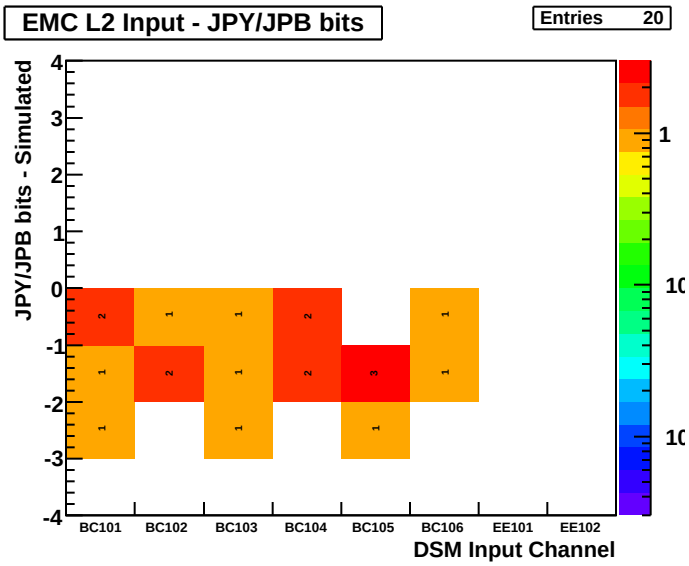

Endcap EMC L1 Input - High Tower Bits
Entries 3823764
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

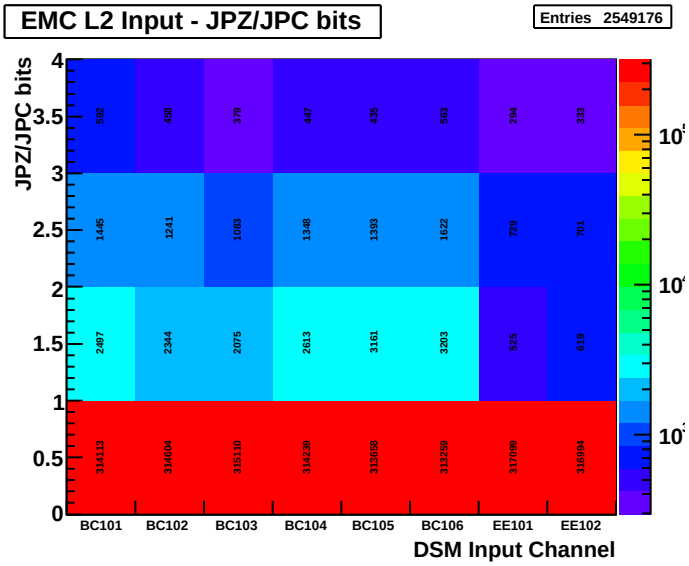

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

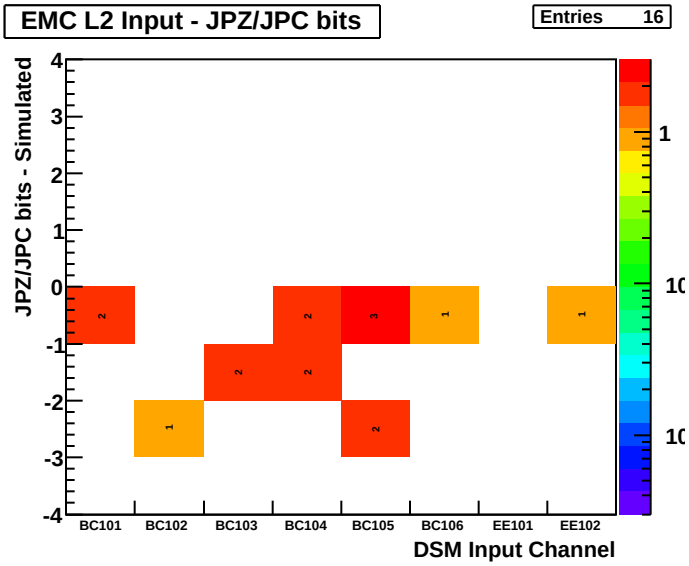

EMC L2 Input - JPX/JPA bits
Entries 2549176
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1223 866 636 420 430 600 293 600
2379 1975 1586 1213 1219 1435 630 1368
3404 3429 2754 2526 2528 2340 569 545
311641 312377 312671 314488 314470 314272 317195 316134

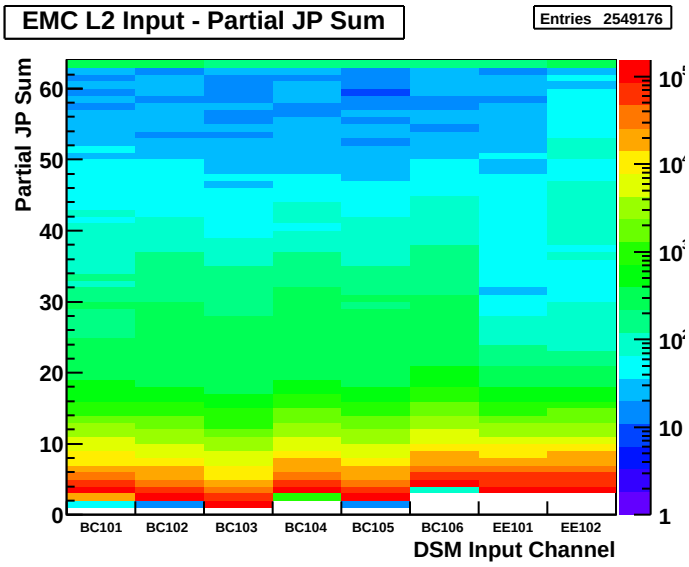

EMC L2 Input - JPX/JPA bits
Entries 36
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

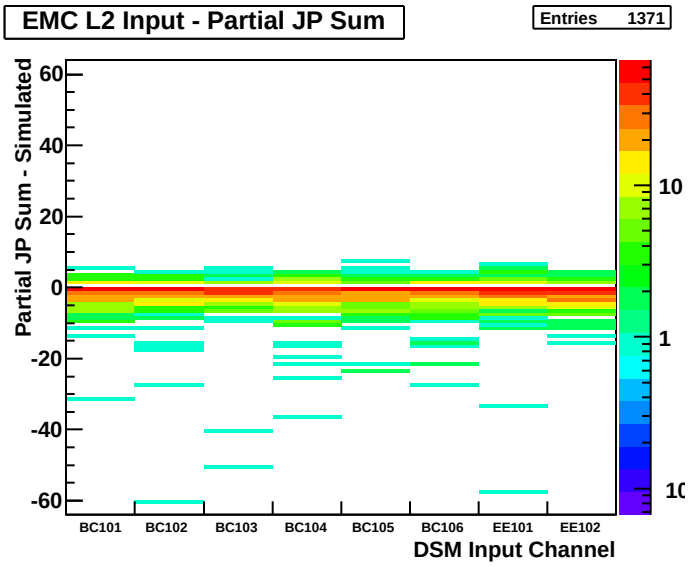

EMC L2 Input - JPY/JPB bits
Entries 2549176
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
736 494 450 346 410 538 302 443
1773 1339 1337 1122 1331 1570 608 941
2813 2323 2356 2362 3056 2638 444 575
313325 314491 314504 314917 313850 313701 317293 316688

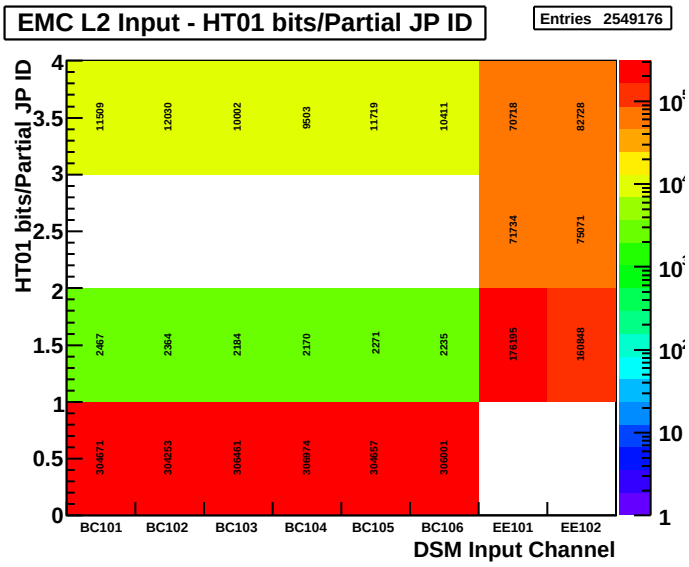

EMC L2 Input - JPY/JPB bits
Entries 20
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

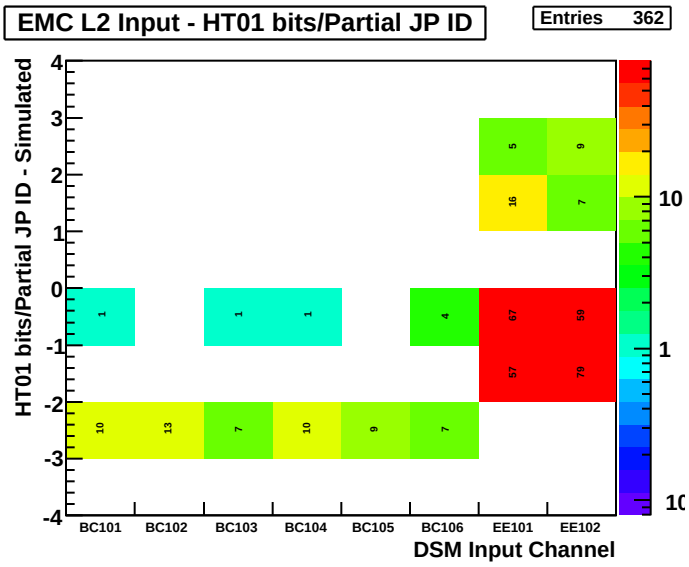

EMC L2 Input - JPZ/JPC bits
Entries 2549176
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
582 458 379 447 435 563 294 333
1445 1241 1083 1348 1393 1622 729 701
2487 2344 2075 2613 3161 3203 525 619
314113 314604 315110 314239 313598 313299 317099 316994

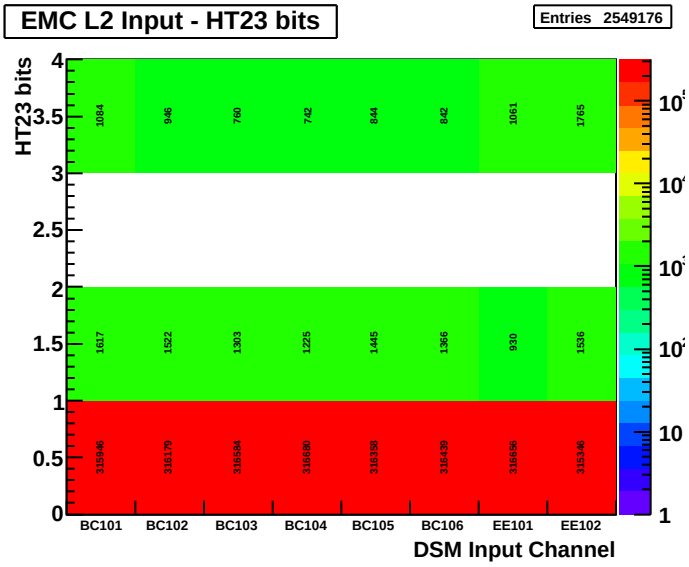

EMC L2 Input - JPZ/JPC bits
Entries 16
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

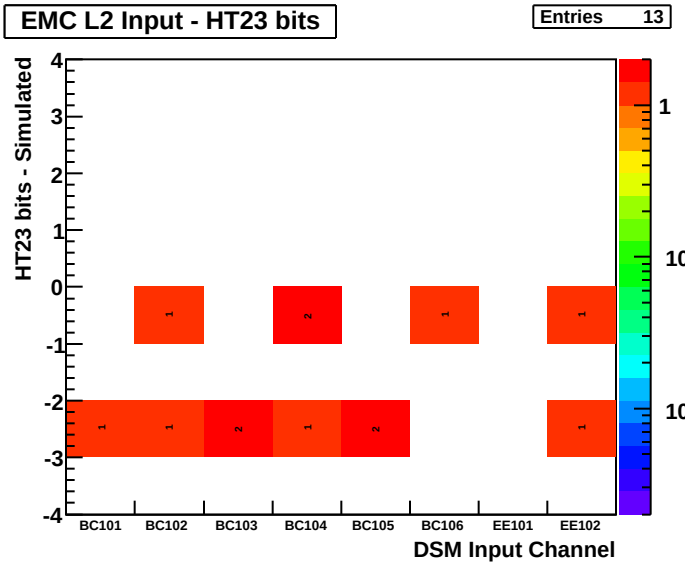

EMC L2 Input - Partial JP Sum
Entries 2549176
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

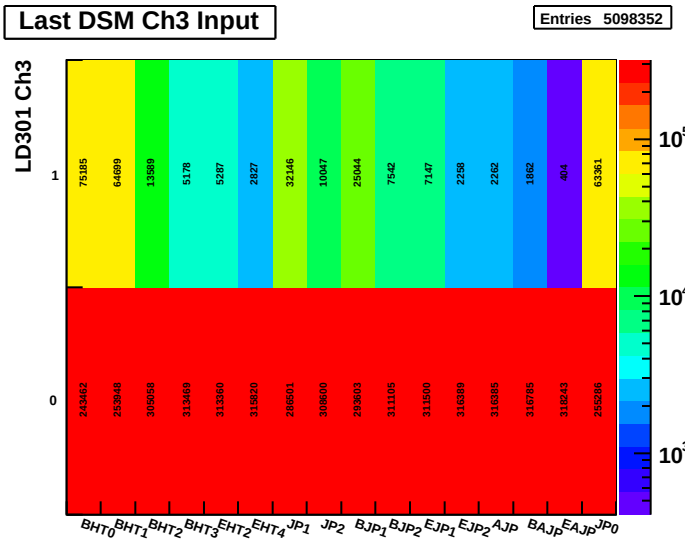

EMC L2 Input - Partial JP Sum
Entries 1371
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

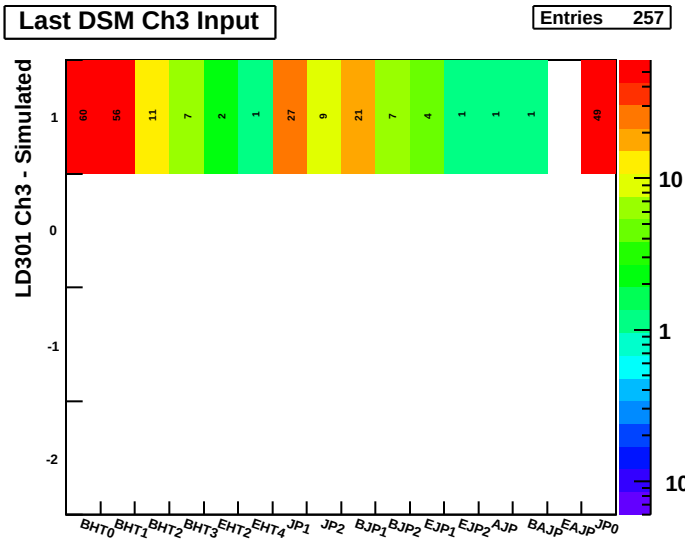

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2549176
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

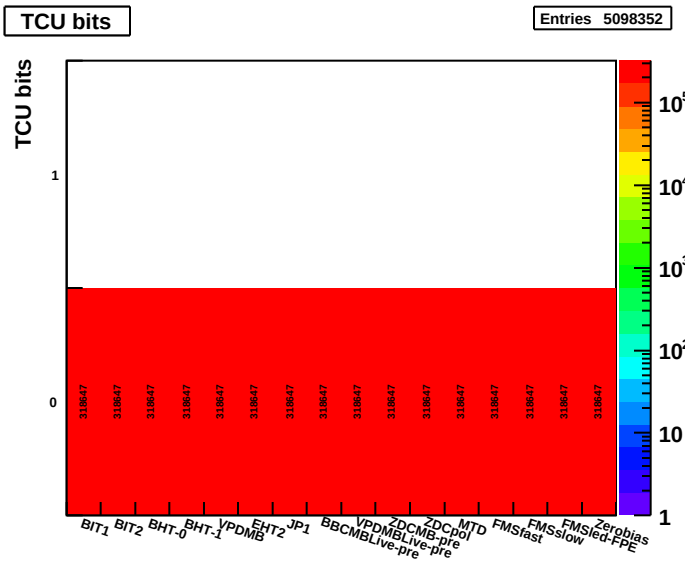

EMC L2 Input - HT01 bits/Partial JP ID
Entries 362
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

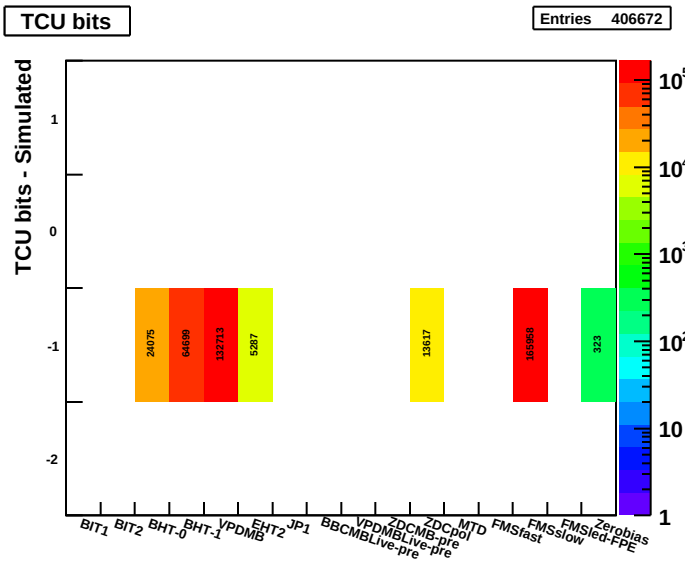

EMC L2 Input - HT23 bits
Entries 2549176
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

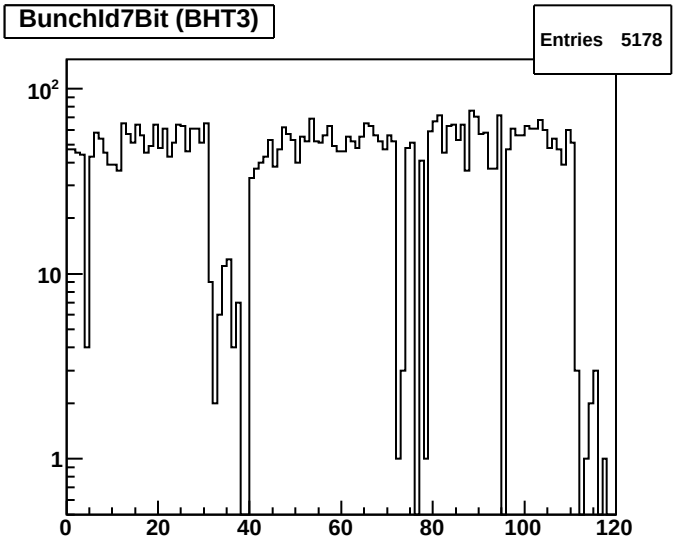

EMC L2 Input - HT23 bits
Entries 13
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

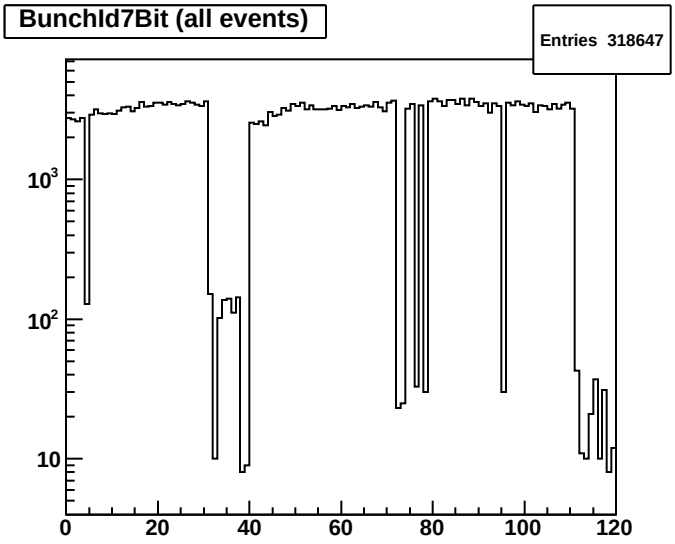

Last DSM Ch3 Input
Entries 5098352
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

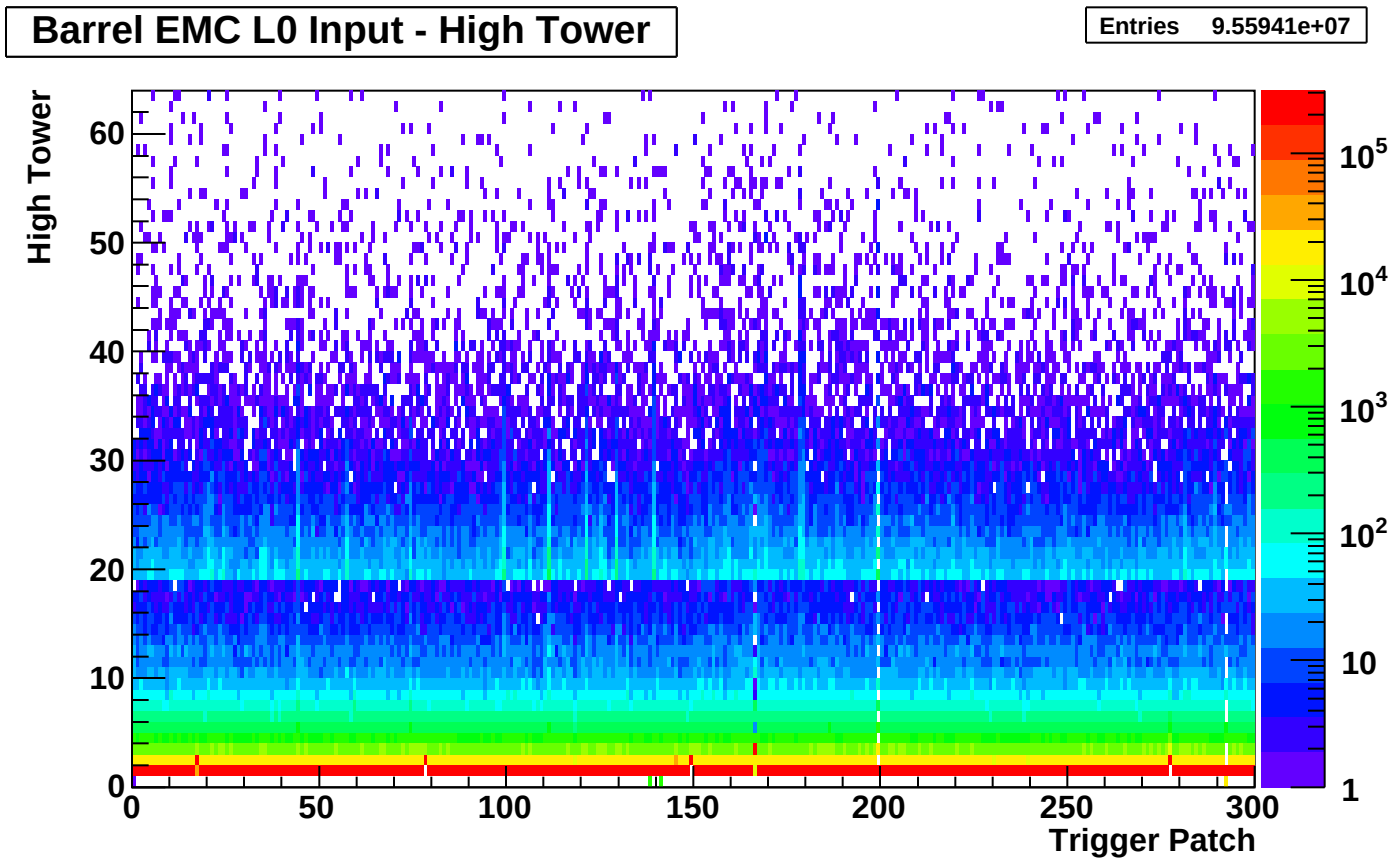

Last DSM Ch3 Input
Entries 257
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

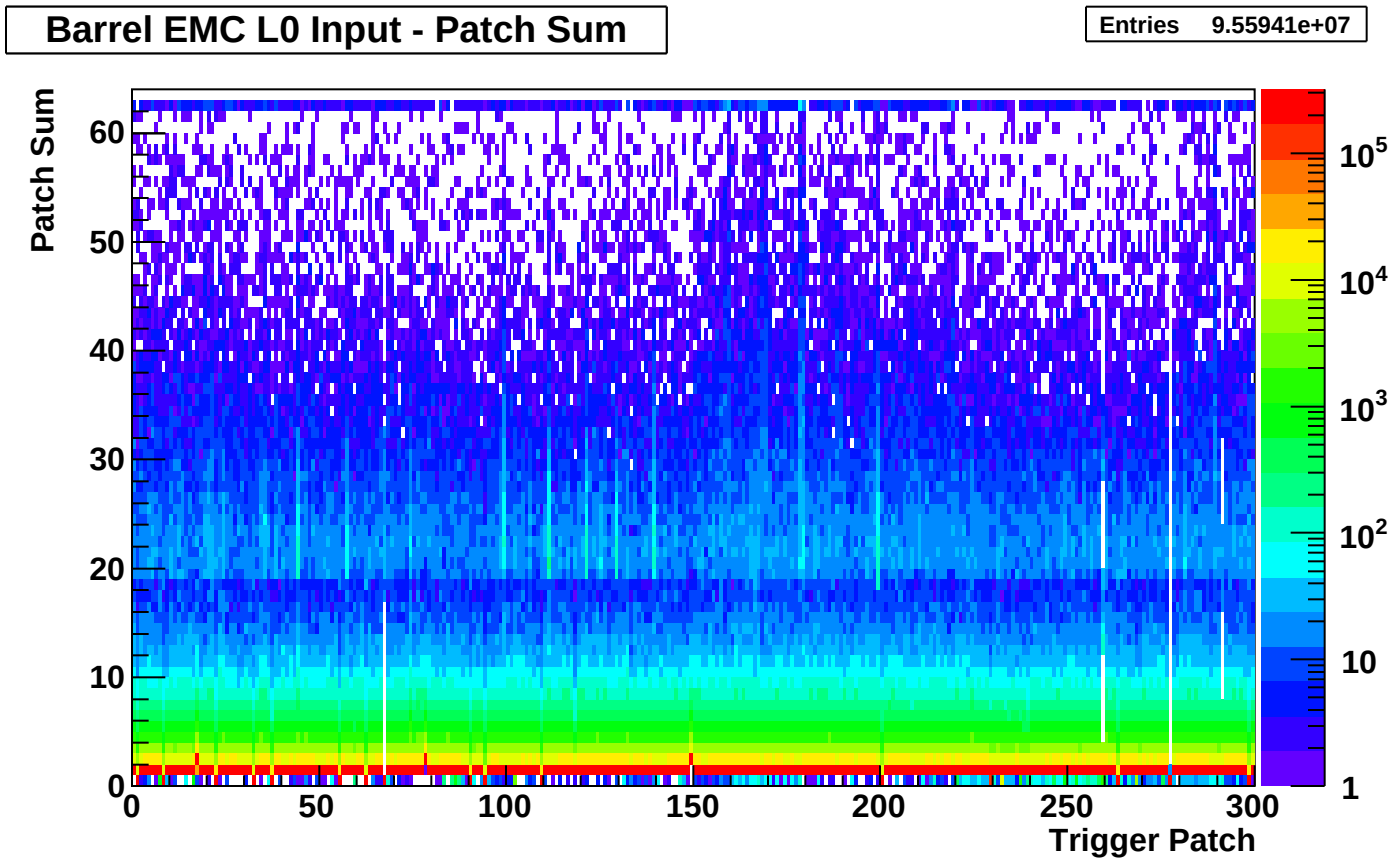

TCU bits
Entries 5098352
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

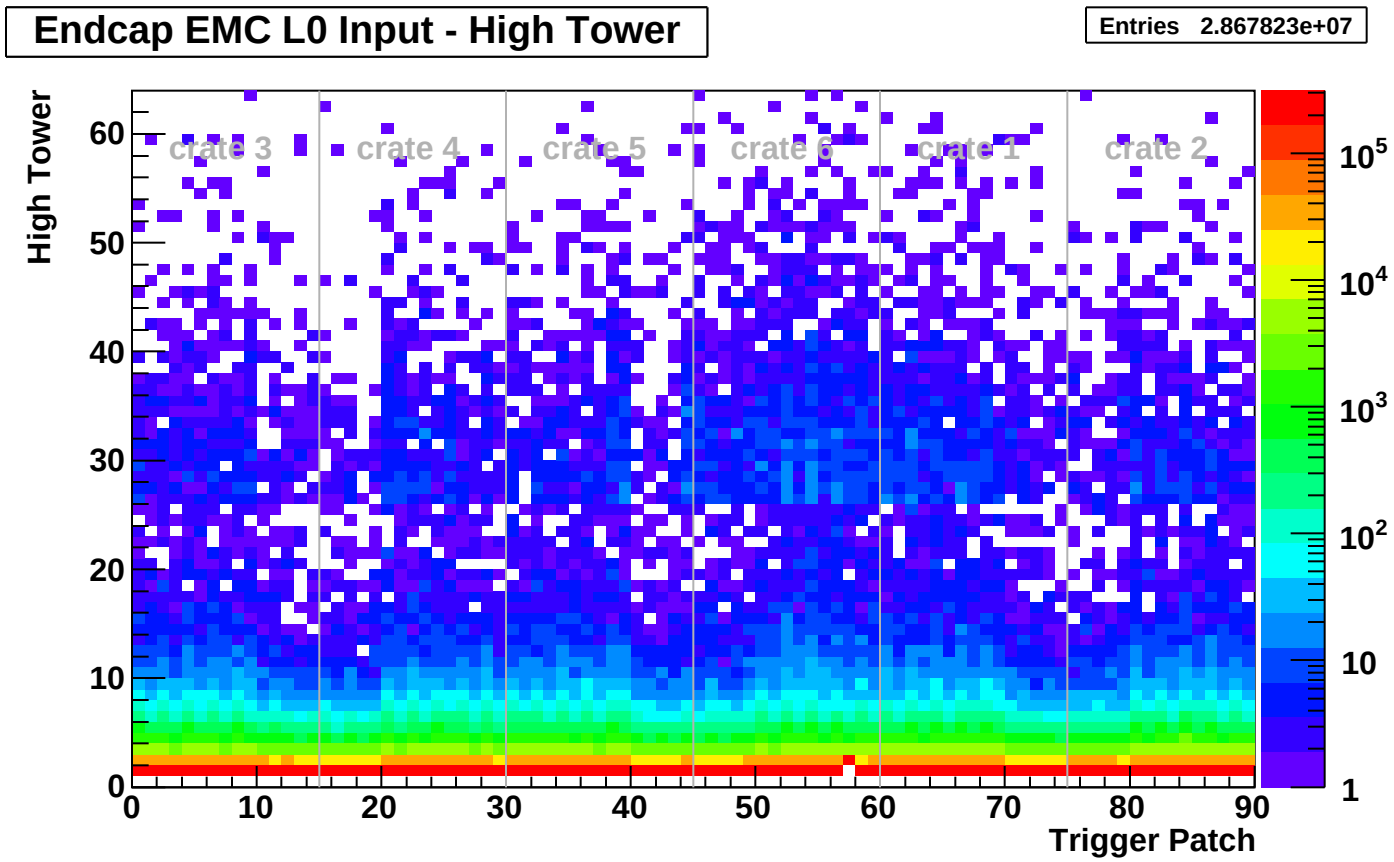

TCU bits
Entries 406672
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

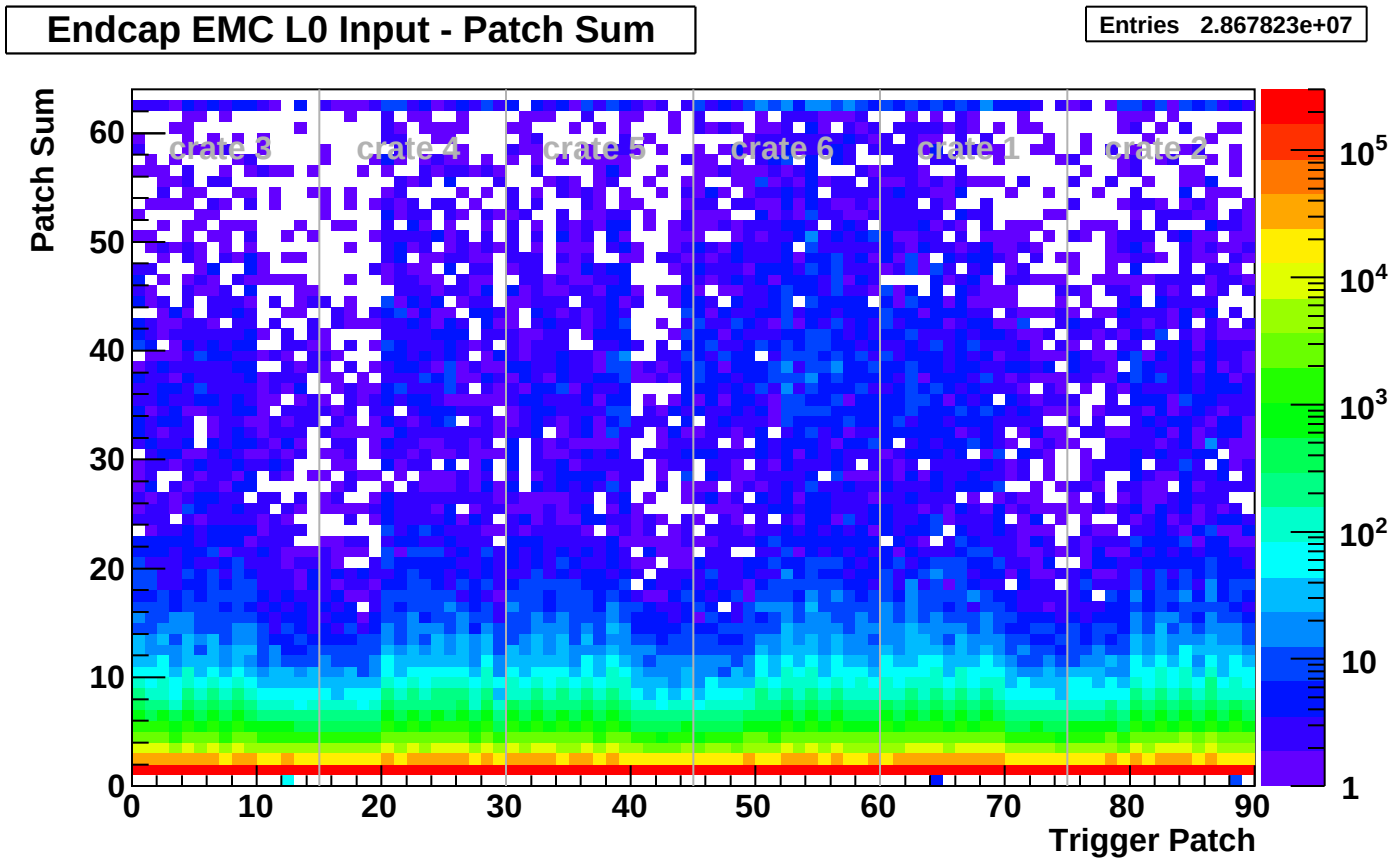

BunchId7Bit (BHT3)
Entries 5178

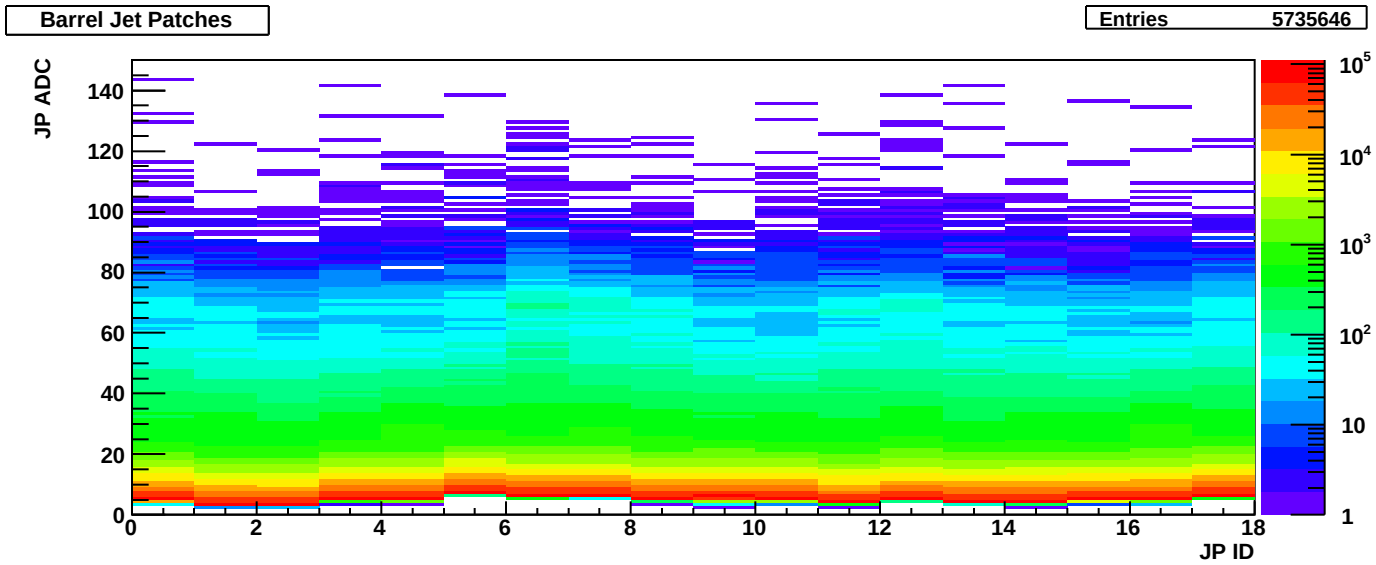

BunchId7Bit (all events)
Entries 318647

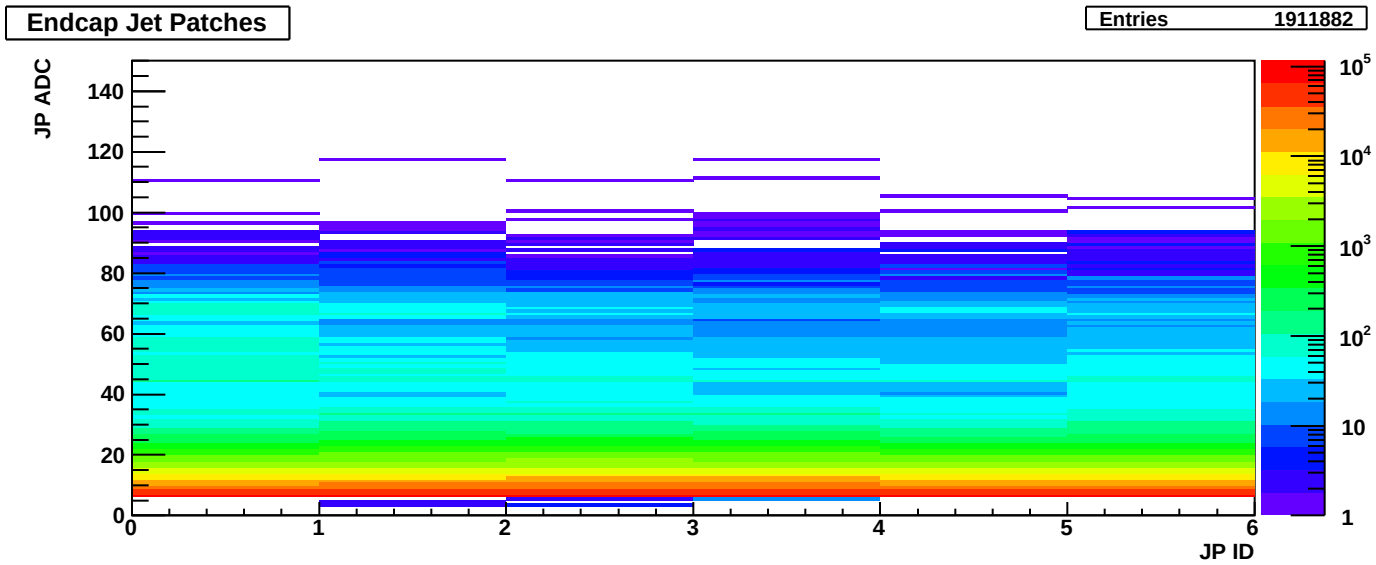

Barrel EMC L0 Input - High Tower
Entries 9.55941e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

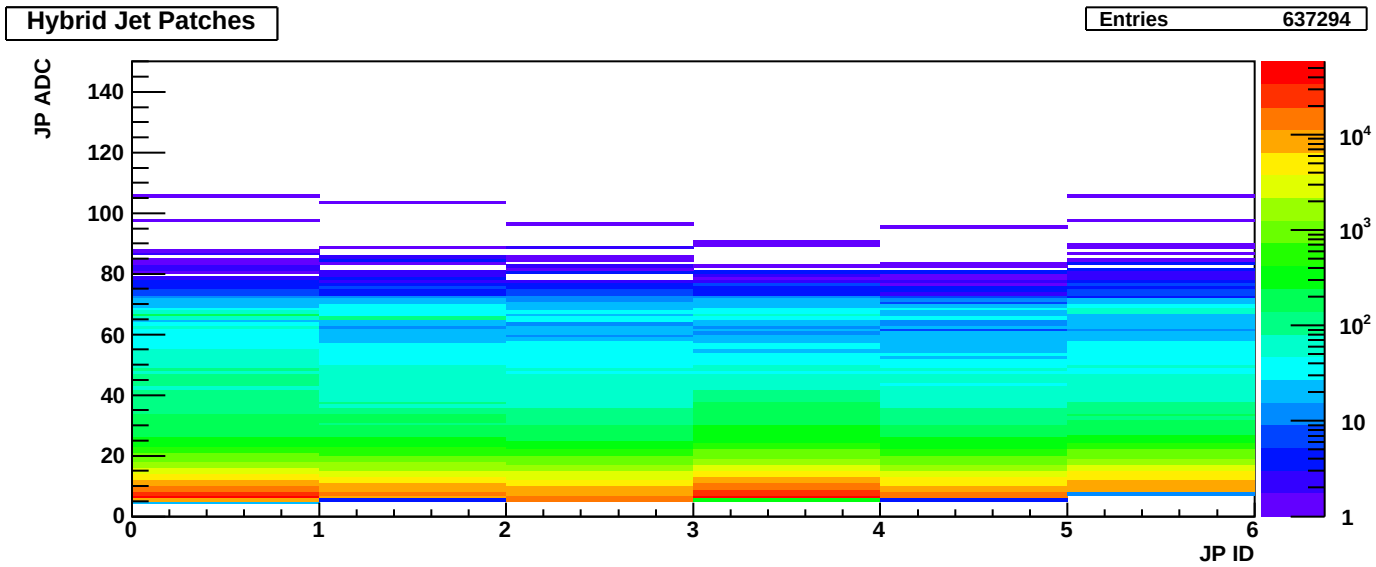

Barrel EMC L0 Input - Patch Sum
Entries 9.55941e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

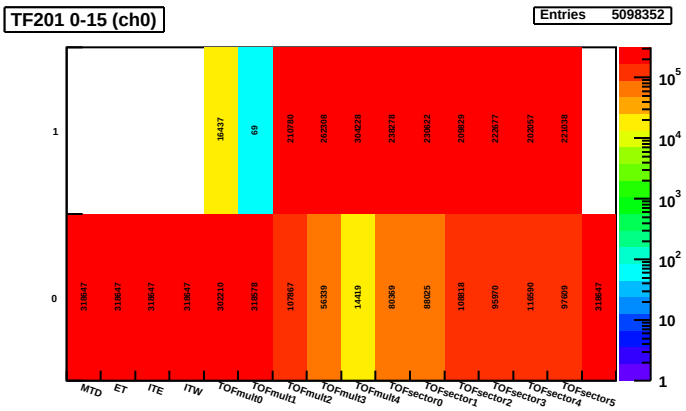

Endcap EMC L0 Input - High Tower
Entries 2.867823e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

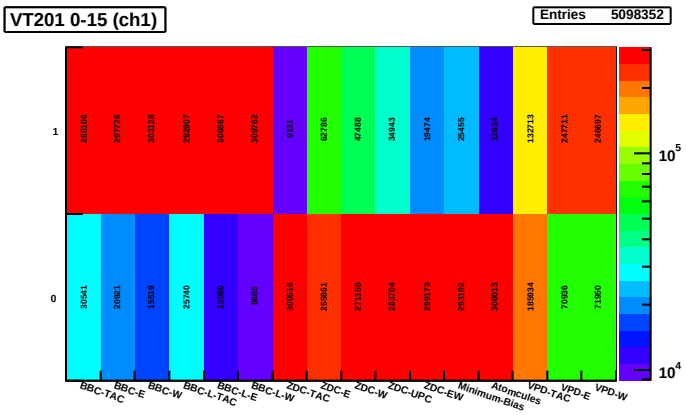

Endcap EMC L0 Input - Patch Sum
Entries 2.867823e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

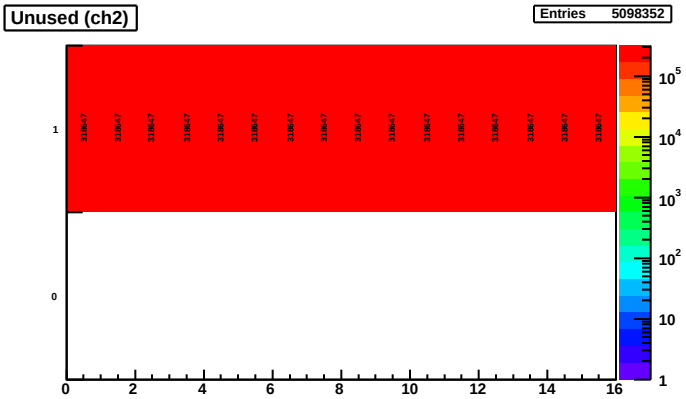

Barrel Jet Patches
Entries 5735646
JP ADC
JP ID

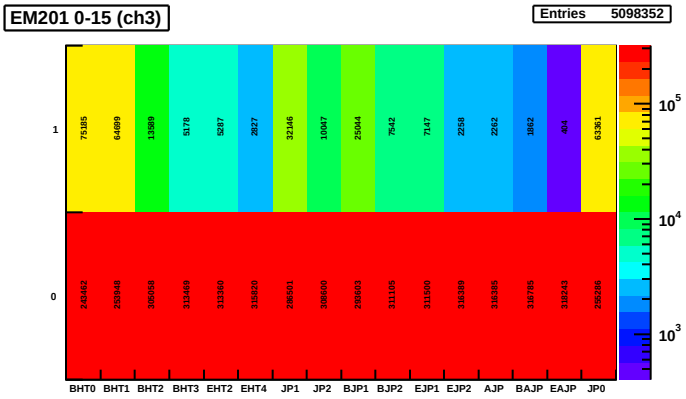

Endcap Jet Patches
Entries 1911882
JP ADC
JP ID

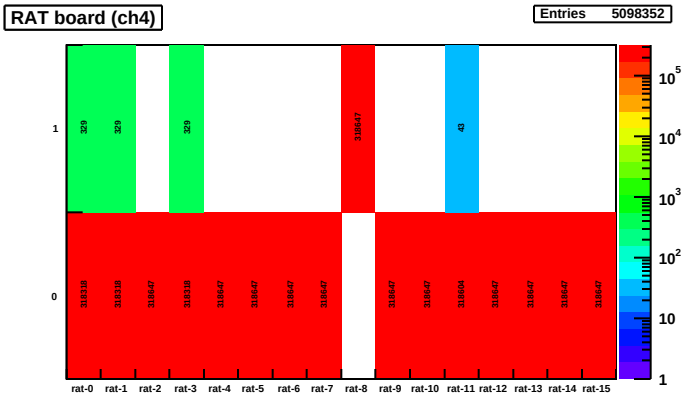

Hybrid Jet Patches
Entries 637294
JP ADC
JP ID

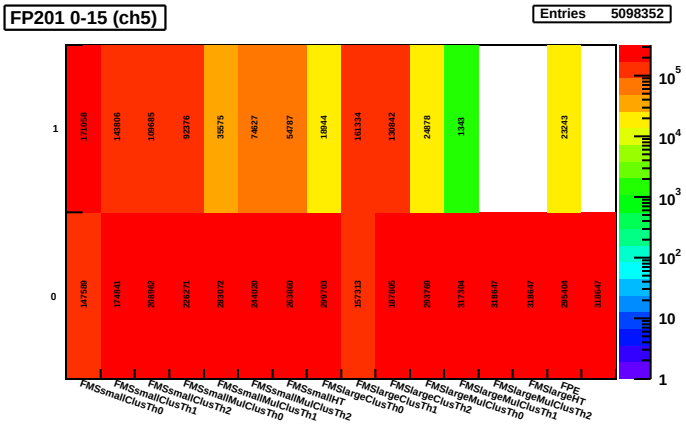

TF201 0-15 (ch0)
Entries 5098352
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

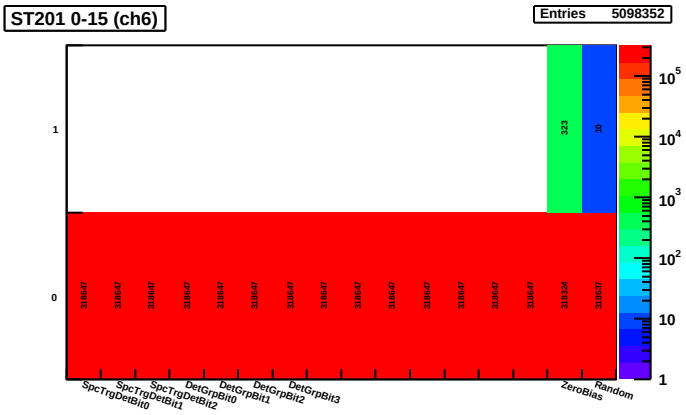

VT201 0-15 (ch1)
Entries 5098352
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

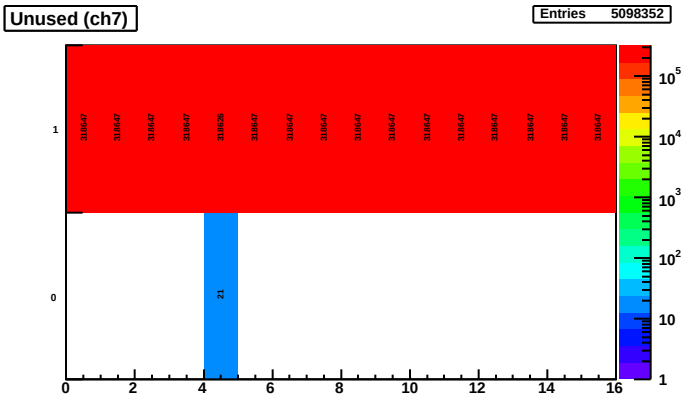

Unused (ch2)
Entries 5098352

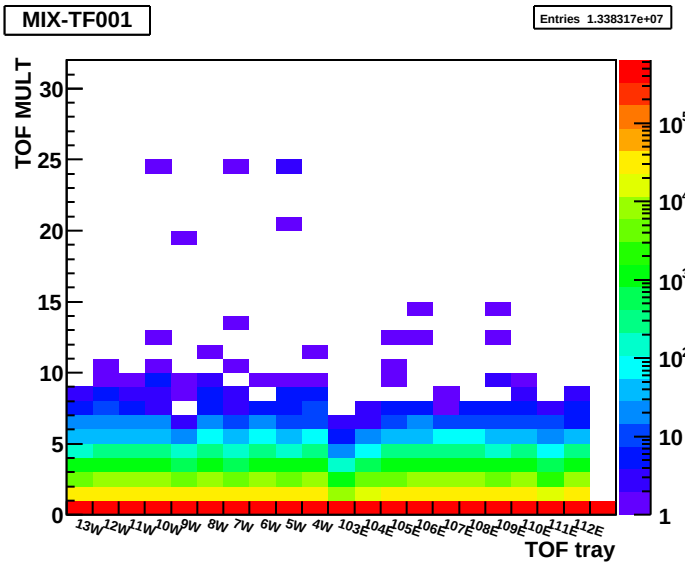

EM201 0-15 (ch3)
Entries 5098352
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

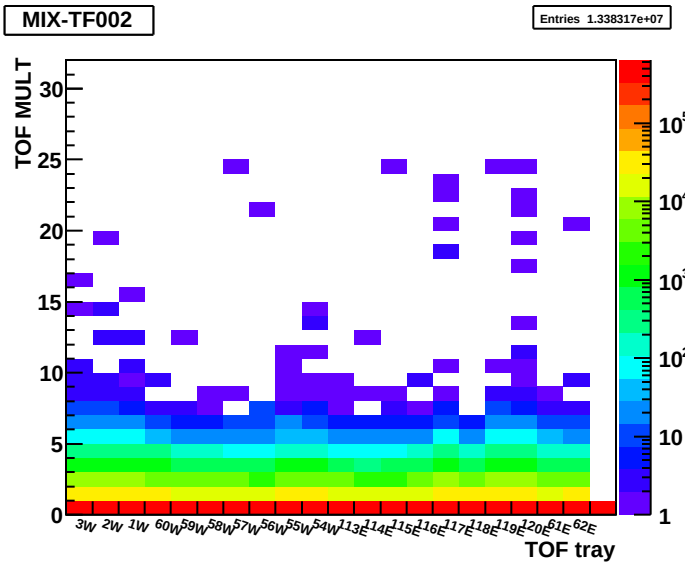

RAT board (ch4)
Entries 5098352
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

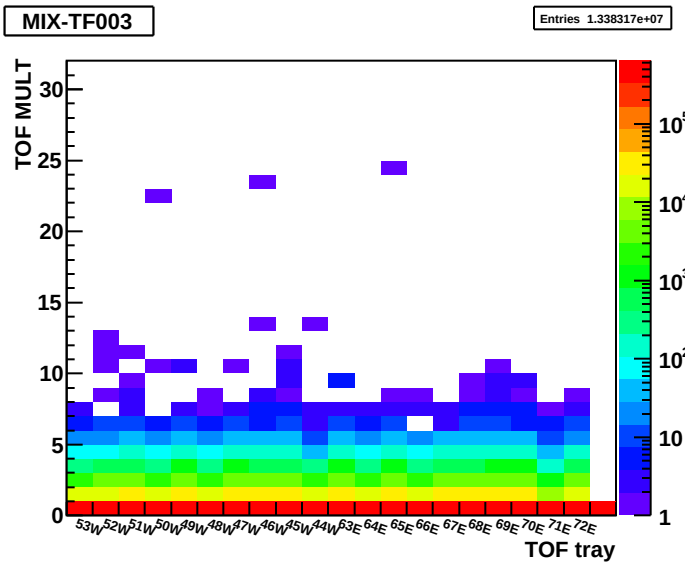

FP201 0-15 (ch5)
Entries 5098352
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

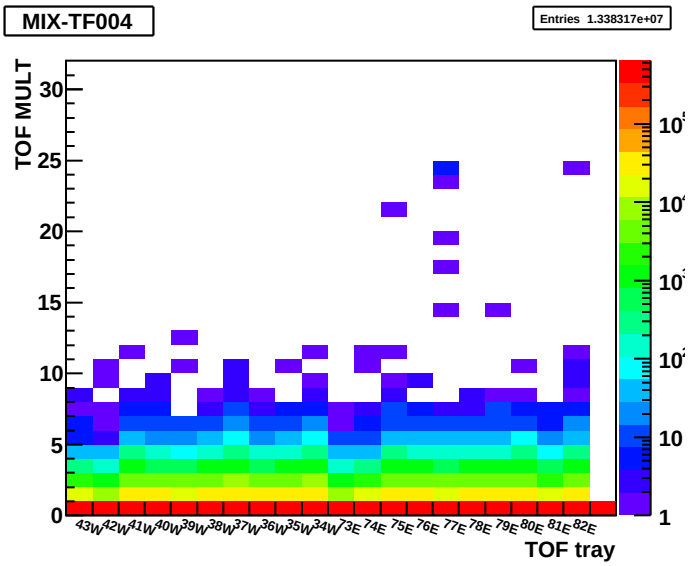

ST201 0-15 (ch6)
Entries 5098352
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

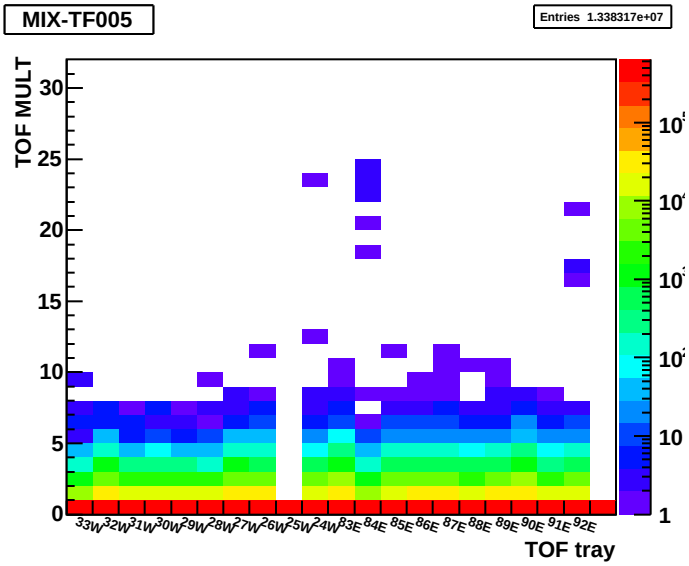

Unused (ch7)
Entries 5098352

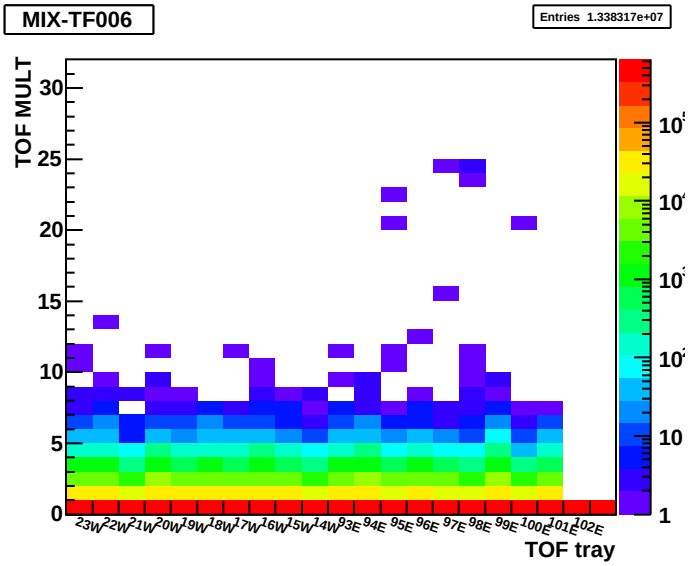

MIX-TF001
Entries 1.338317e+07
TOF MULT
TOF tray

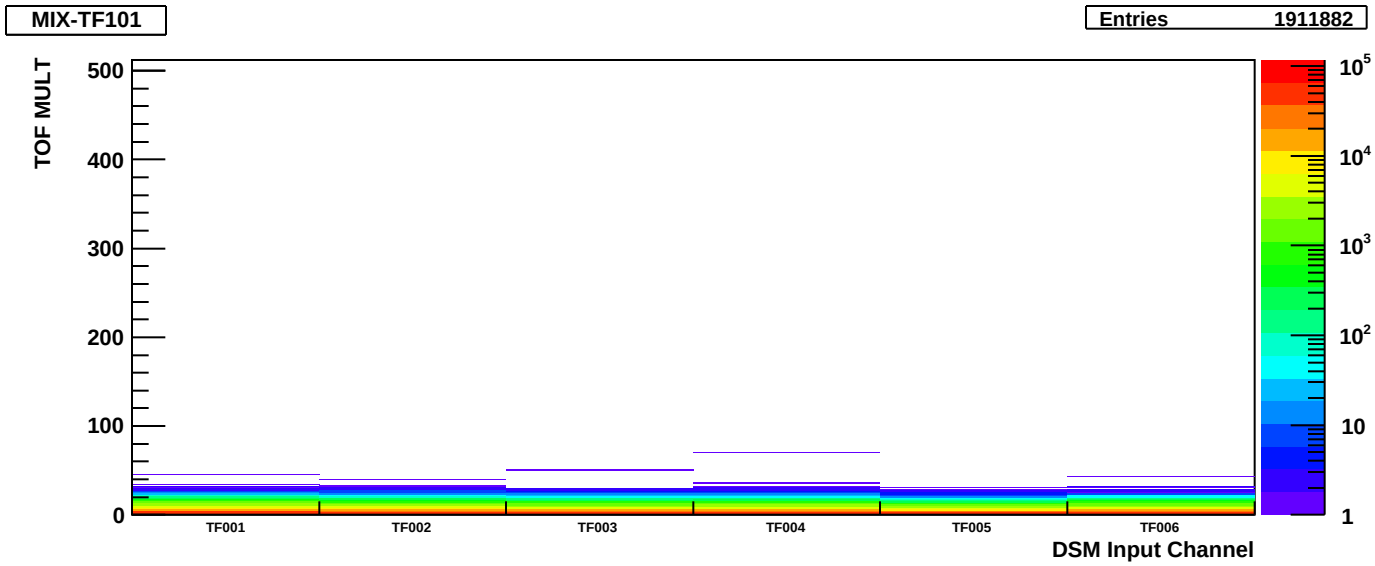

MIX-TF002
Entries 1.338317e+07
TOF MULT
TOF tray

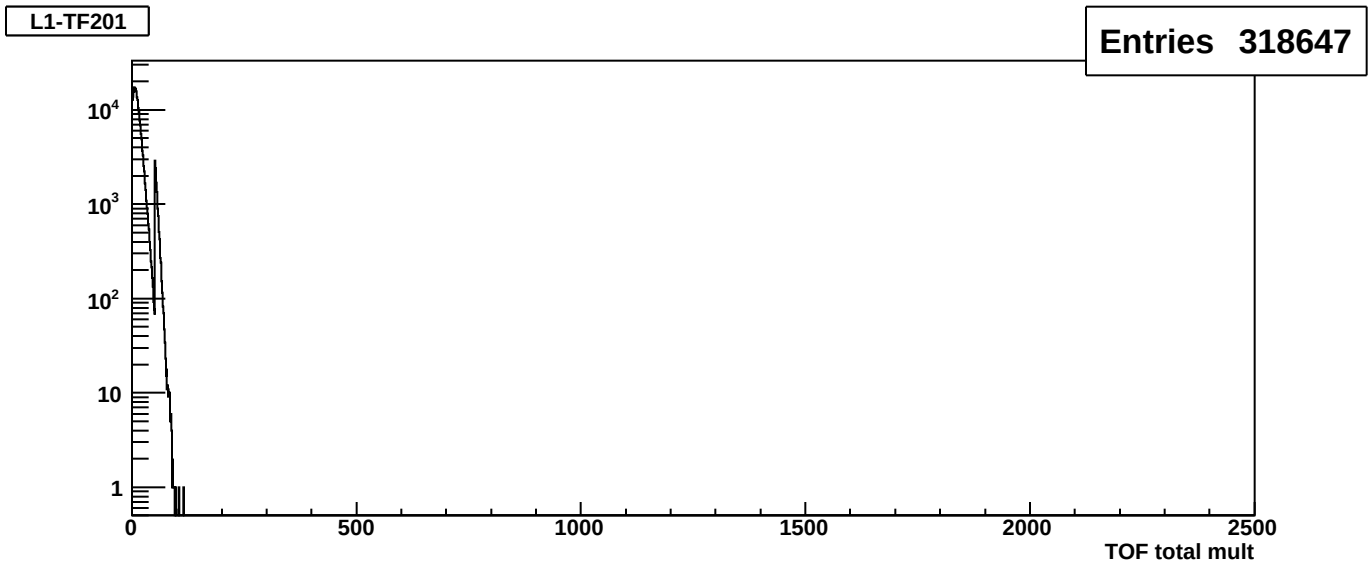

MIX-TF003
Entries 1.338317e+07
TOF MULT
TOF tray

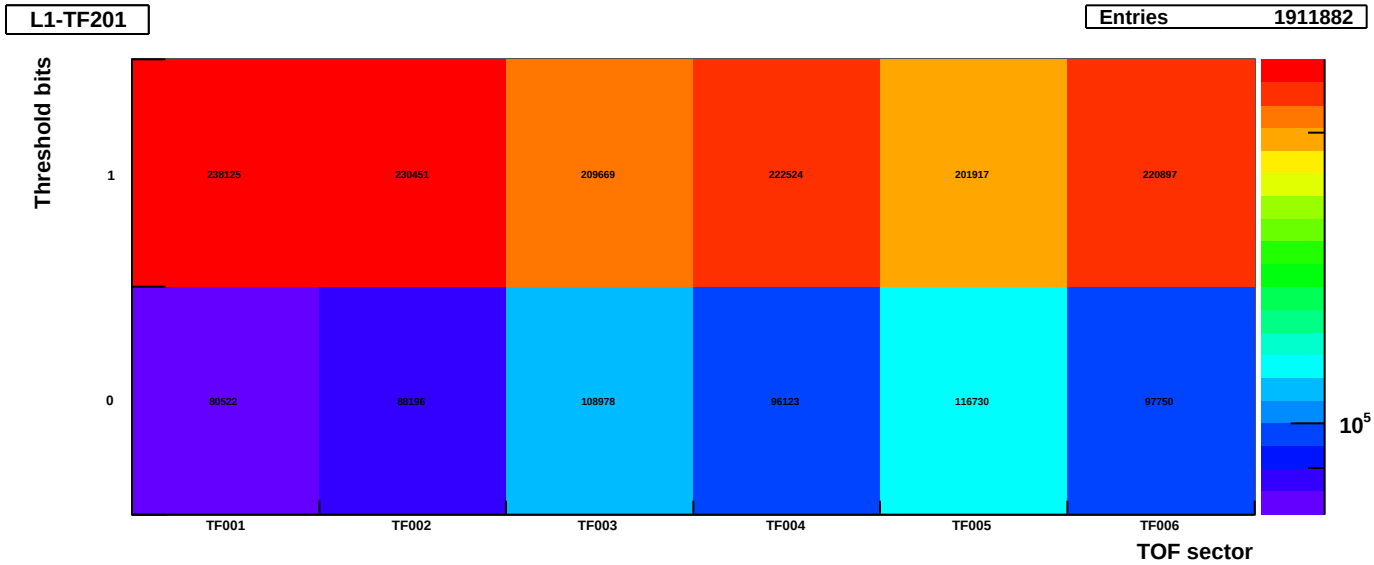

MIX-TF004
Entries 1.338317e+07
TOF MULT
TOF tray

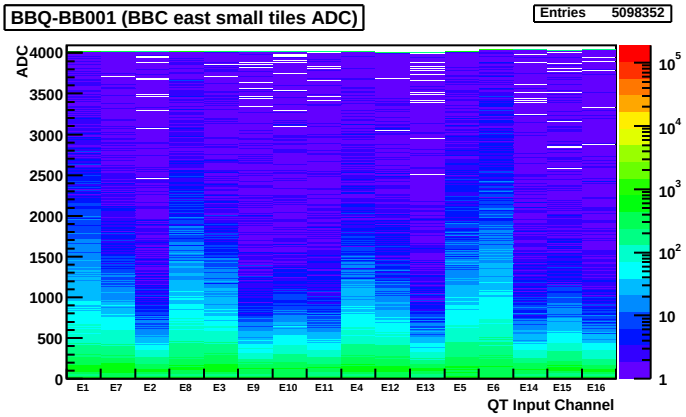

MIX-TF005
Entries 1.338317e+07
TOF MULT
TOF tray

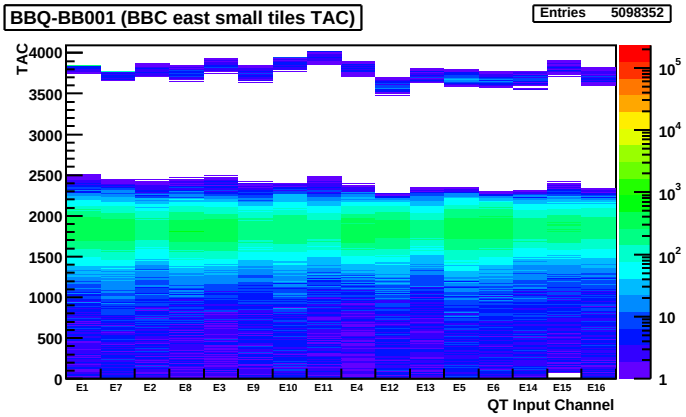

MIX-TF006
Entries 1.338317e+07
TOF MULT
TOF tray

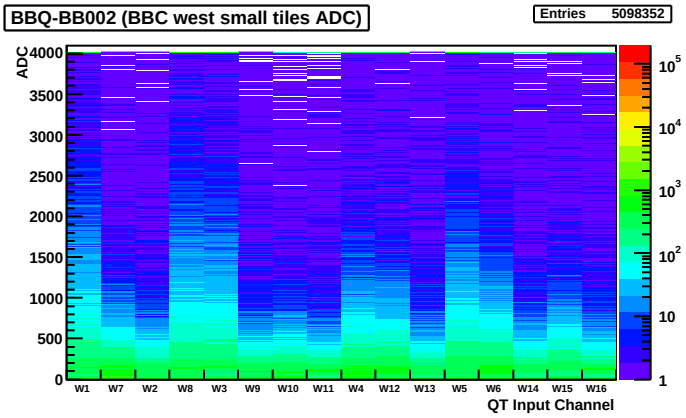

MIX-TF101
Entries 1911882
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

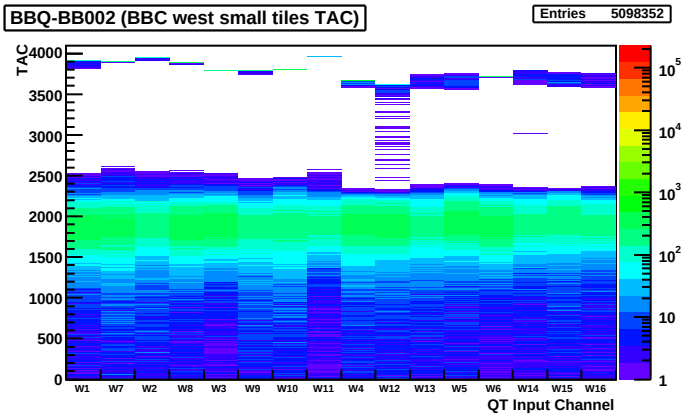

L1-TF201
Entries 318647
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

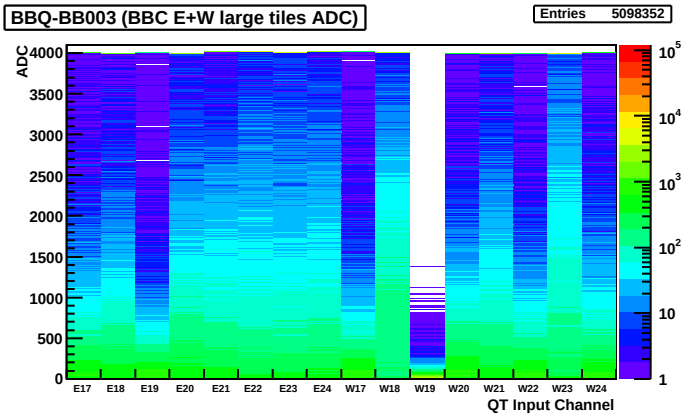

L1-TF201
Entries 1911882
Threshold bits
1
0
238125
230451
209669
222524
201917
220897
80522
88196
108978
96123
116730
97750
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

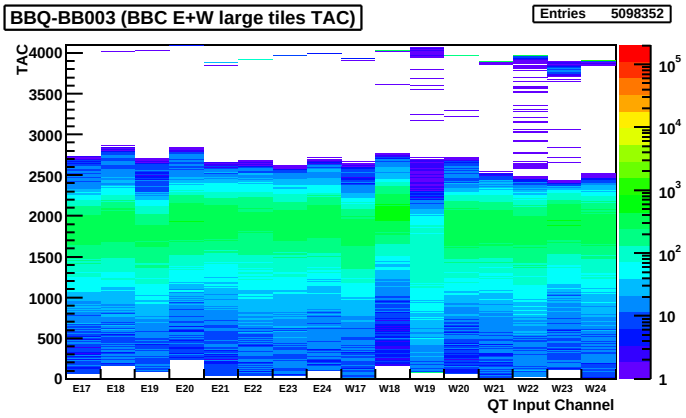

BBQ-BB001 (BBC east small tiles ADC)
Entries 5098352
ADC
QT Input Channel

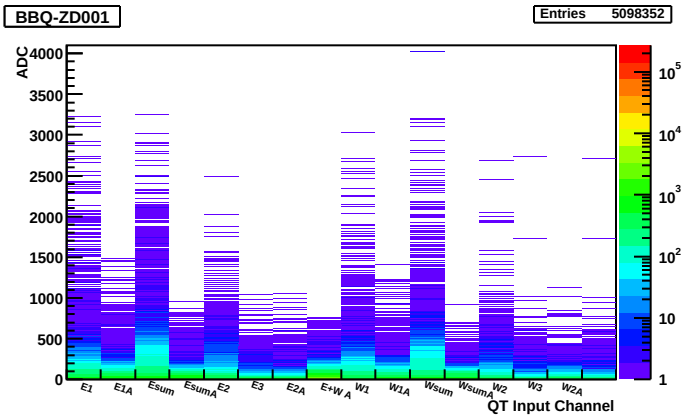

BBQ-BB001 (BBC east small tiles TAC)
Entries 5098352
TAC
QT Input Channel

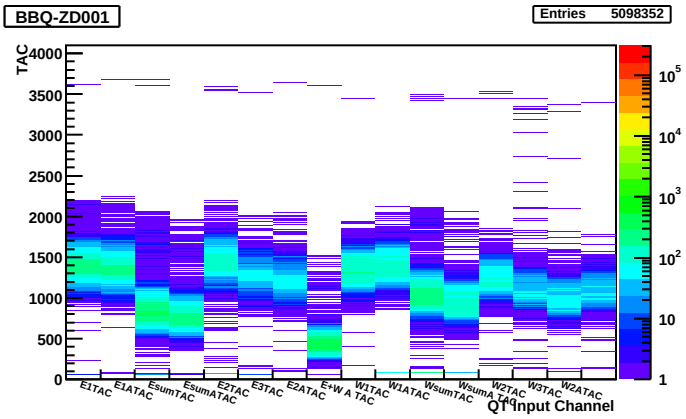

BBQ-BB002 (BBC west small tiles ADC)
Entries 5098352
ADC
QT Input Channel

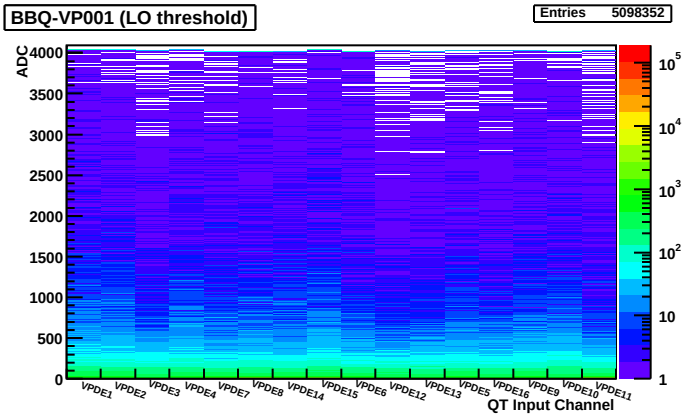

BBQ-BB002 (BBC west small tiles TAC)
Entries 5098352
TAC
QT Input Channel

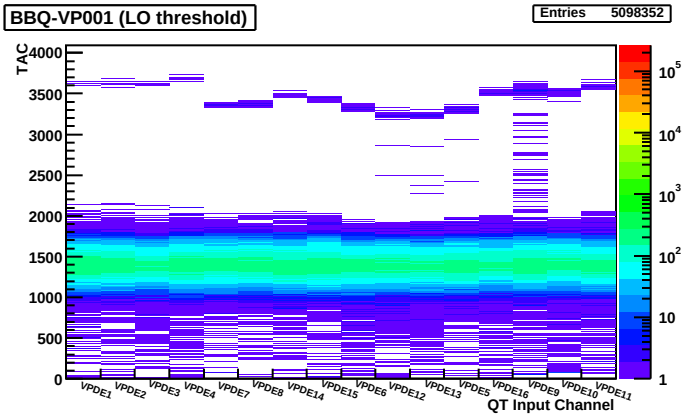

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5098352
ADC
QT Input Channel

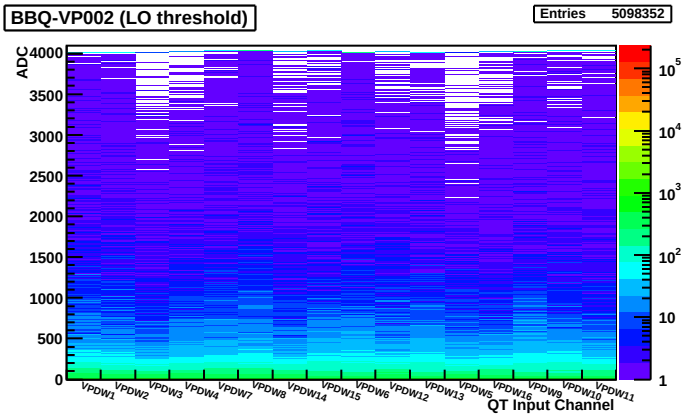

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5098352
TAC
QT Input Channel

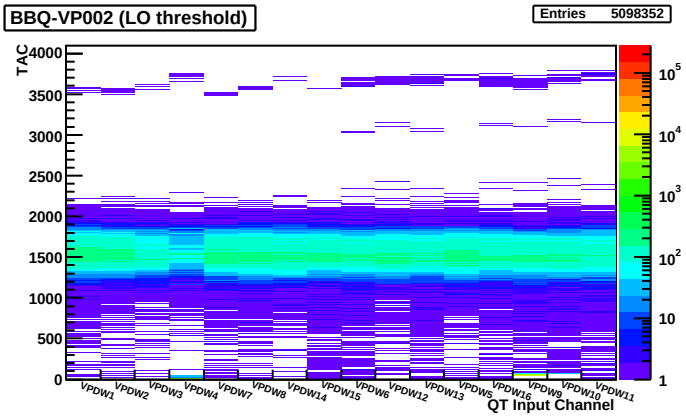

BBQ-ZD001
Entries 5098352
ADC
QT Input Channel

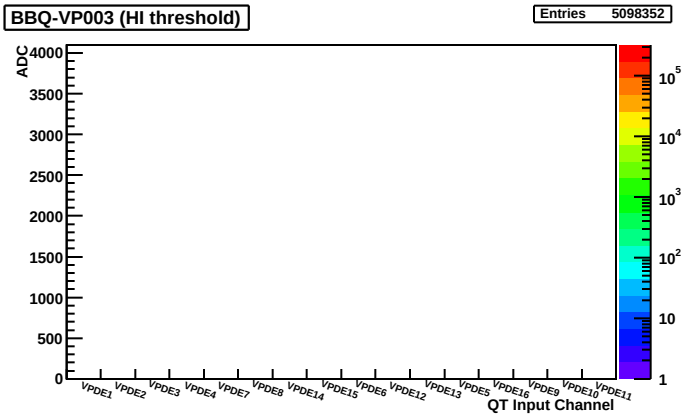

BBQ-ZD001
Entries 5098352
TAC
QT Input Channel

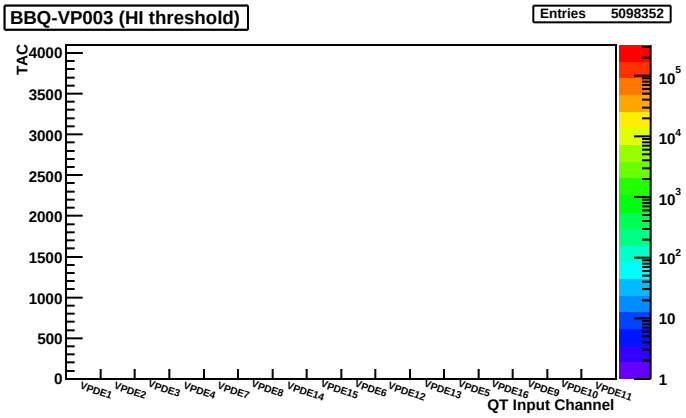

BBQ-VP001 (LO threshold)
Entries 5098352
ADC
QT Input Channel

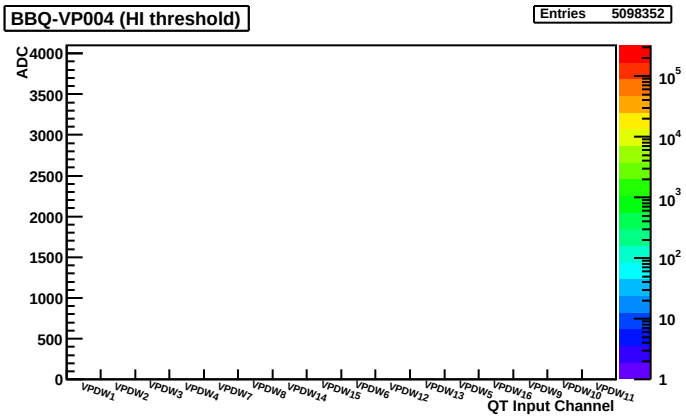

BBQ-VP001 (LO threshold)
Entries 5098352
TAC
QT Input Channel

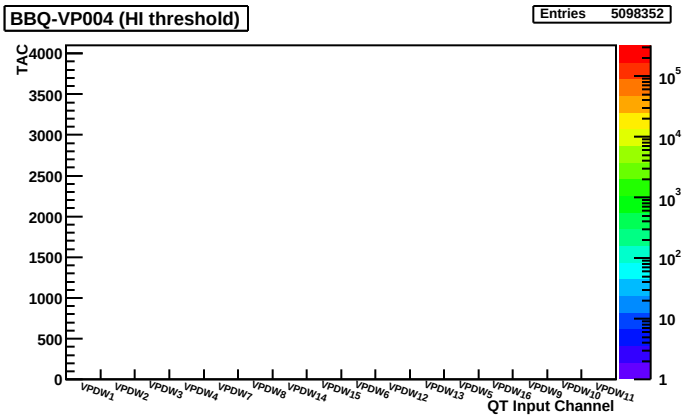

BBQ-VP002 (LO threshold)
Entries 5098352
ADC
QT Input Channel

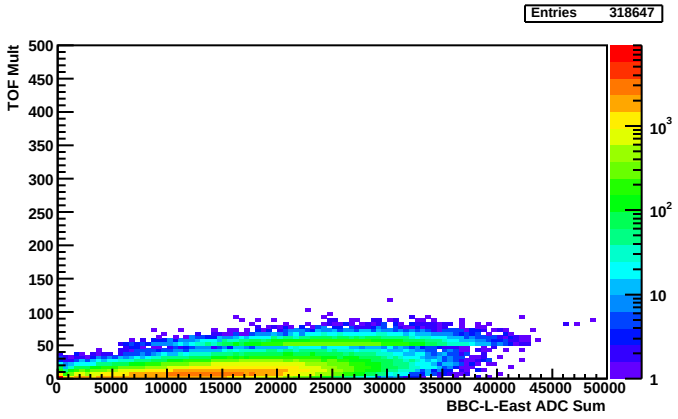

BBQ-VP002 (LO threshold)
Entries 5098352
TAC
QT Input Channel

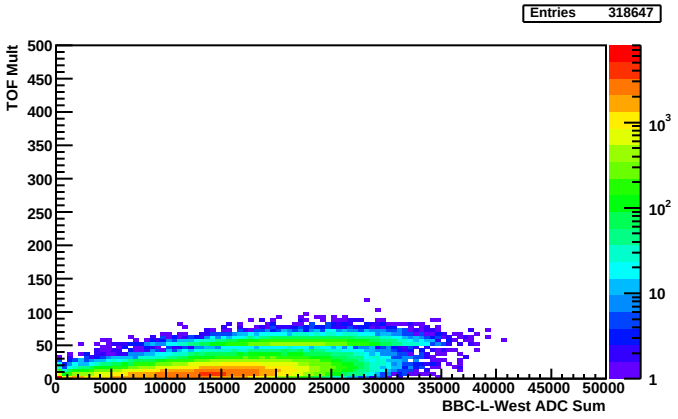

BBQ-VP003 (HI threshold)
Entries 5098352
ADC
QT Input Channel

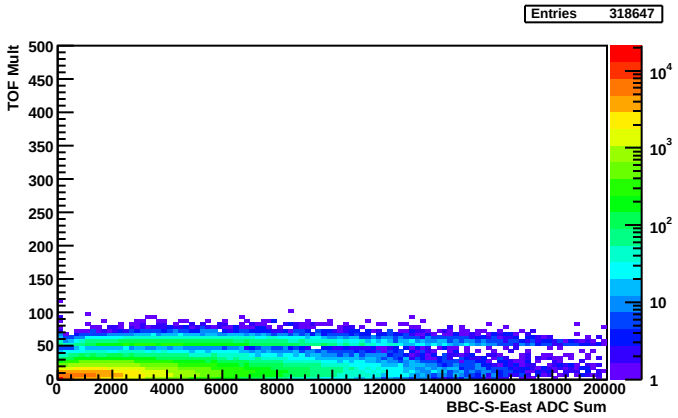

BBQ-VP003 (HI threshold)
Entries 5098352
TAC
QT Input Channel

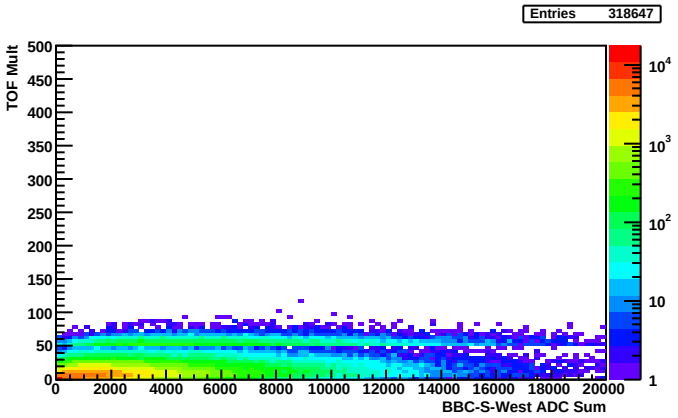

BBQ-VP004 (HI threshold)
Entries 5098352
ADC
QT Input Channel

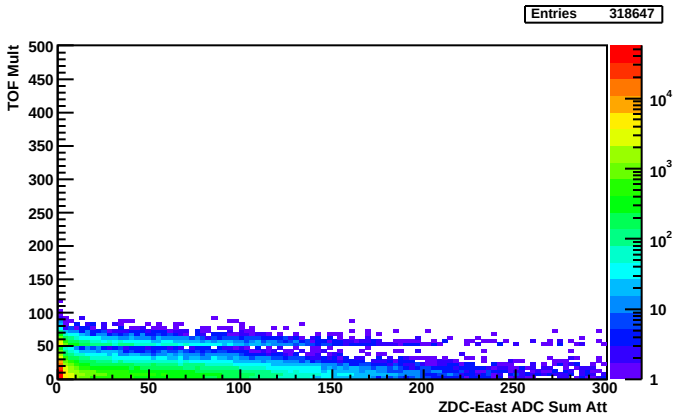

BBQ-VP004 (HI threshold)
Entries 5098352
TAC
QT Input Channel

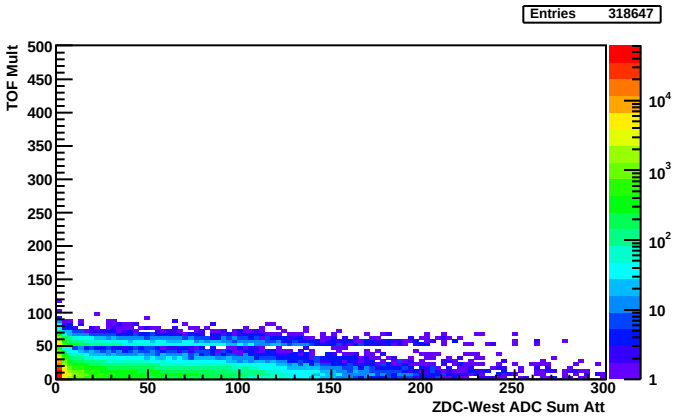

Entries 318647
TOF Mult
BBC-L-East ADC Sum

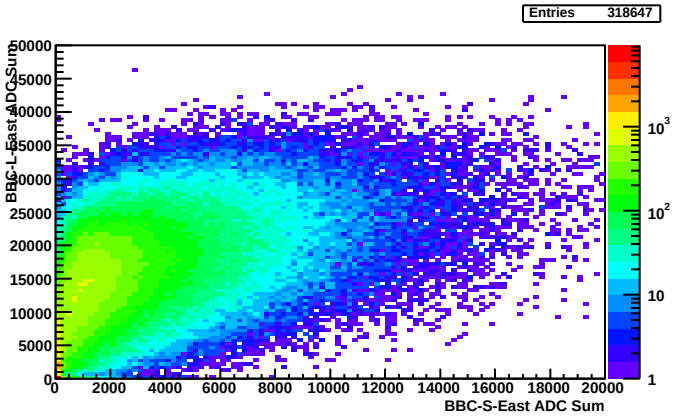

Entries 318647
TOF Mult
BBC-L-West ADC Sum

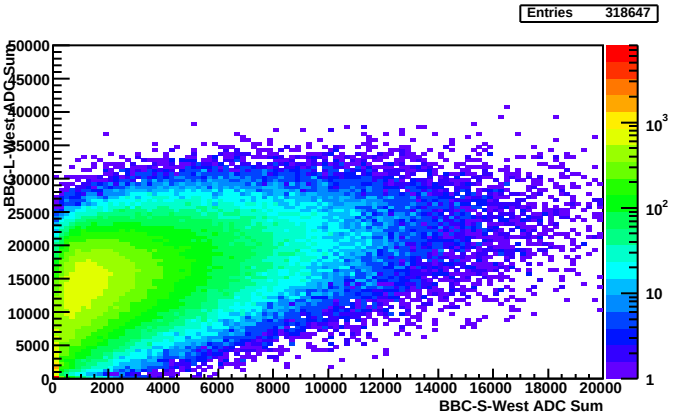

Entries 318647
TOF Mult
BBC-S-East ADC Sum

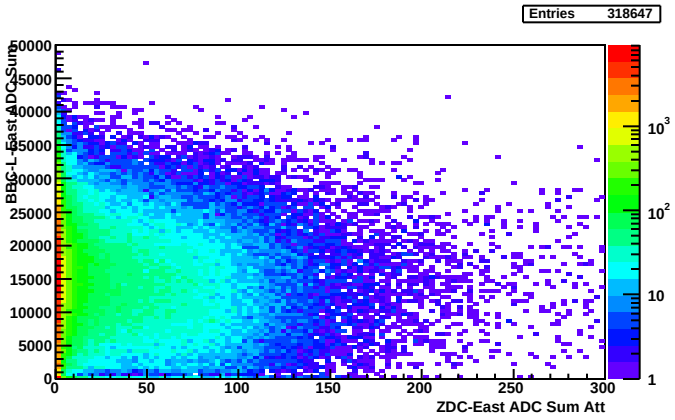

Entries 318647
TOF Mult
BBC-S-West ADC Sum

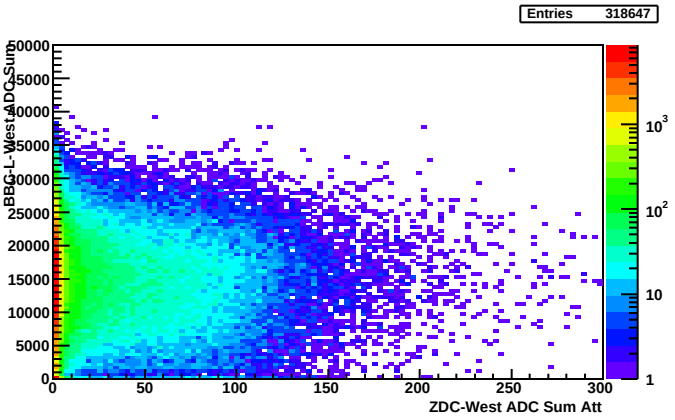

Entries 318647
TOF Mult
ZDC-East ADC Sum Att

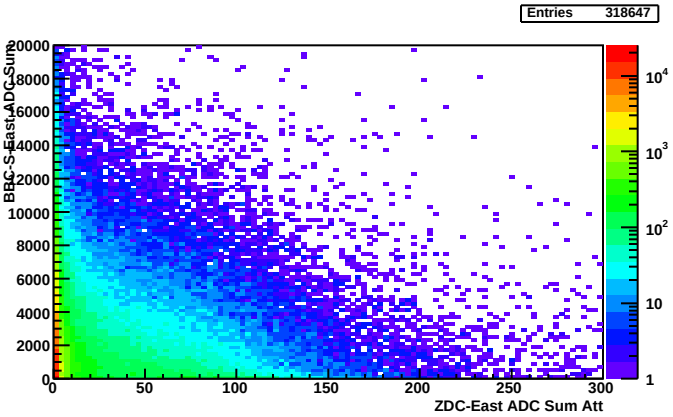

Entries 318647
TOF Mult
ZDC-West ADC Sum Att

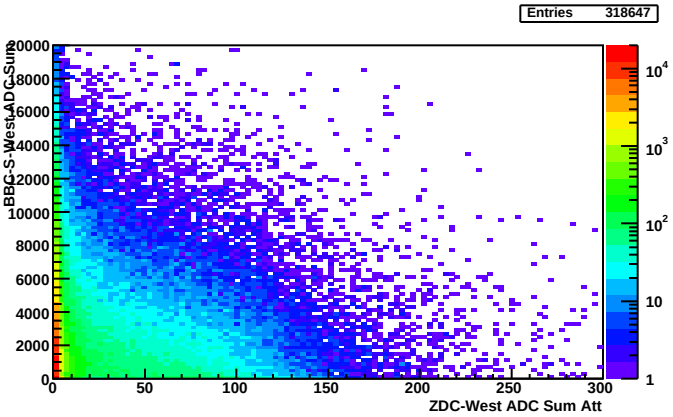

Entries 318647
BBC-L-East ADC Sum
BBC-S-East ADC Sum

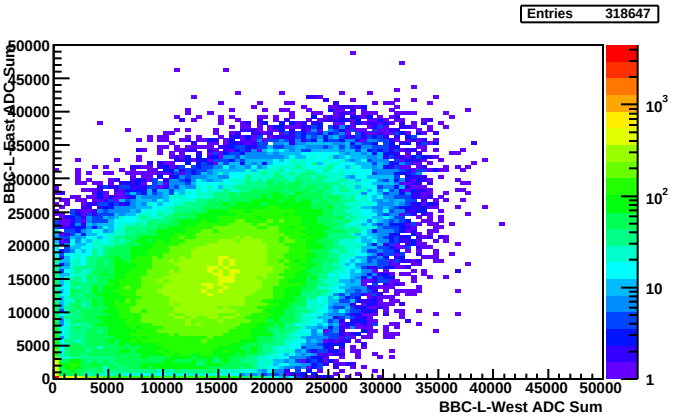

Entries 318647
BBC-L-West ADC Sum
BBC-S-West ADC Sum

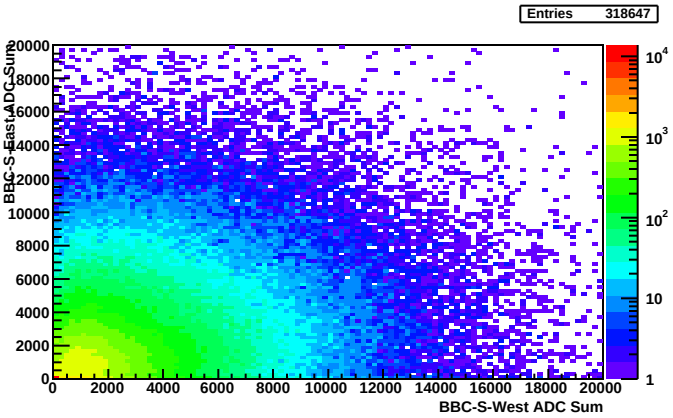

Entries 318647
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

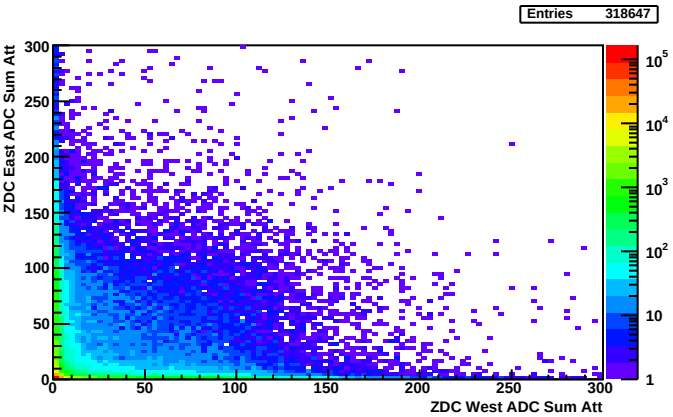

Entries 318647
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

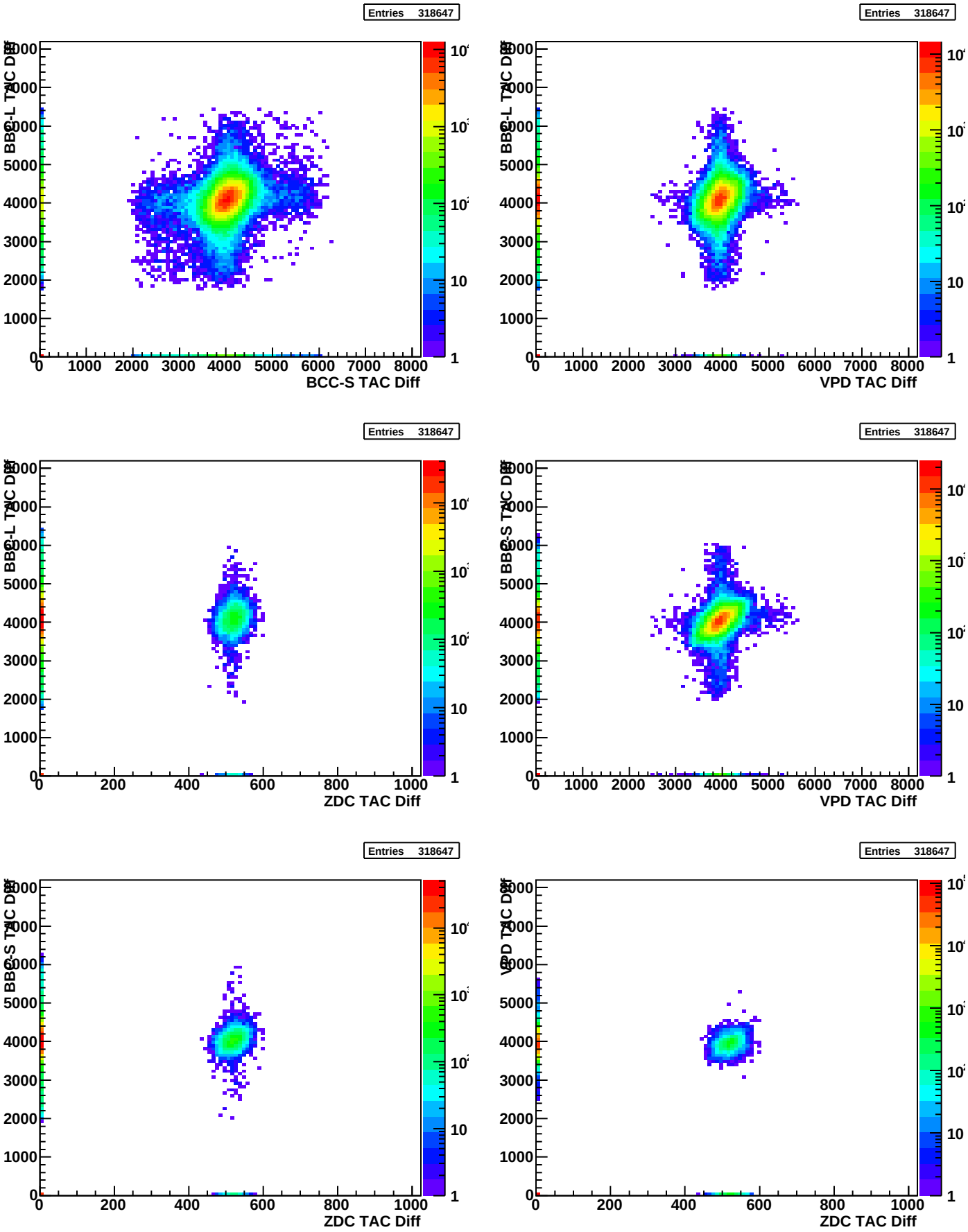

Entries 318647
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

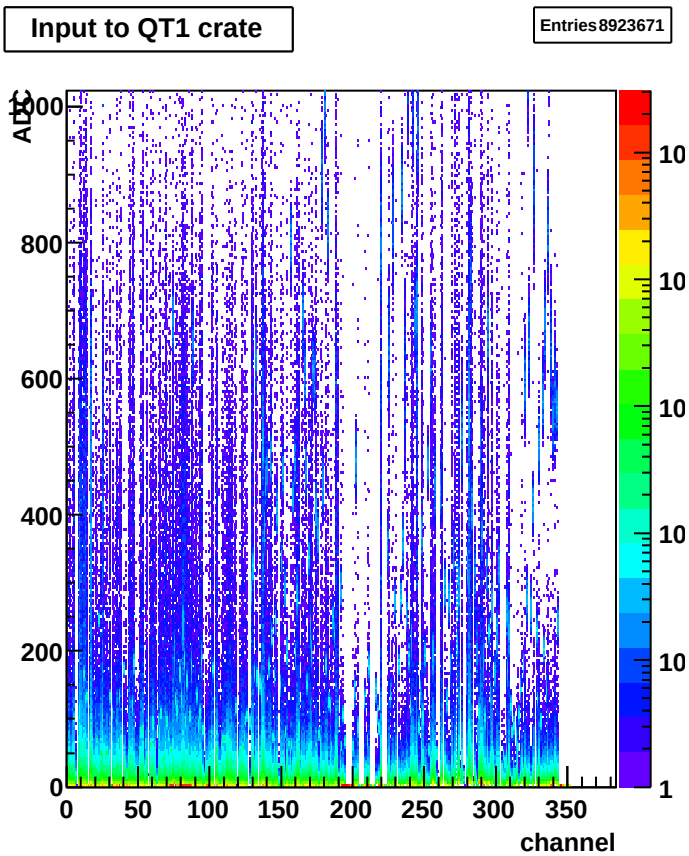

Entries 318647
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

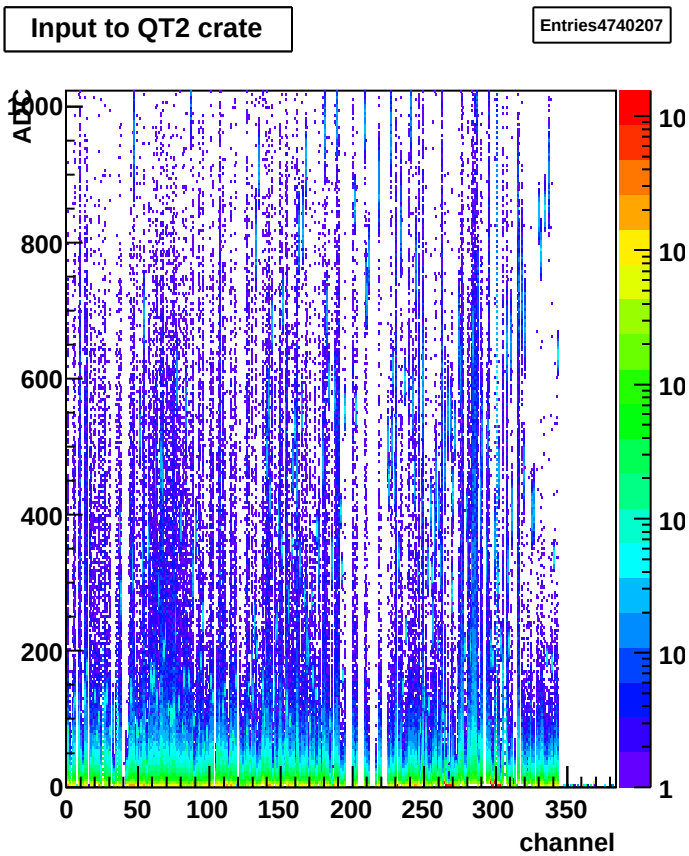

Entries 318647
BBC-L-East ADC Sum
BBC-L-West ADC Sum

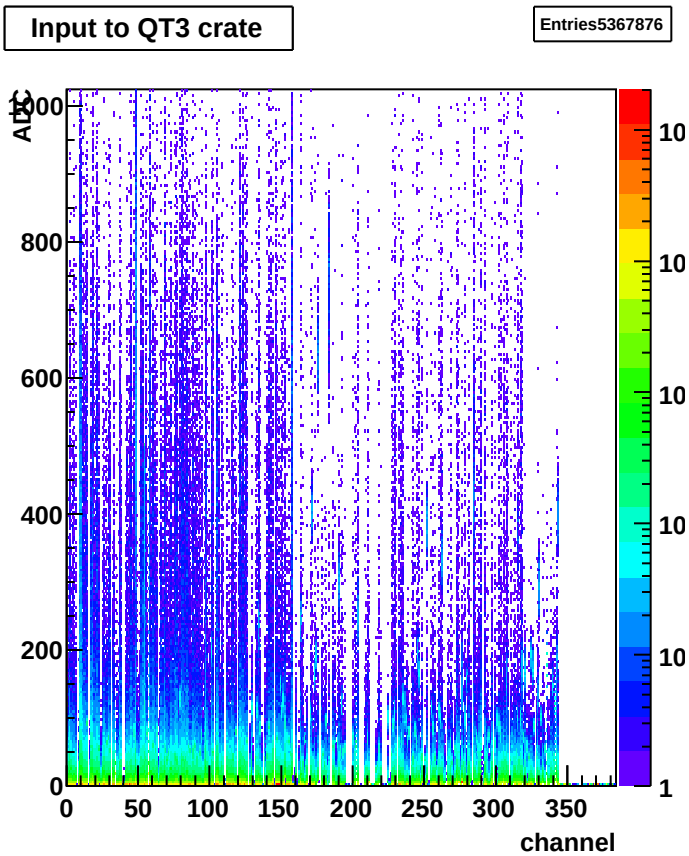

Entries 318647
BBC-S-East ADC Sum
BBC-S-West ADC Sum

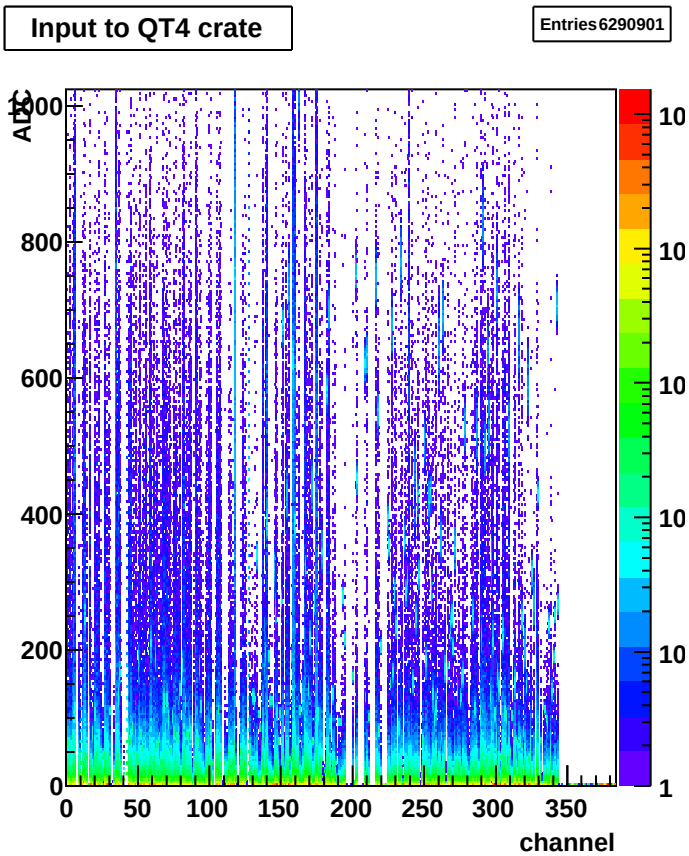

Entries 318647
ZDC East ADC Sum Att
ZDC West ADC Sum Att

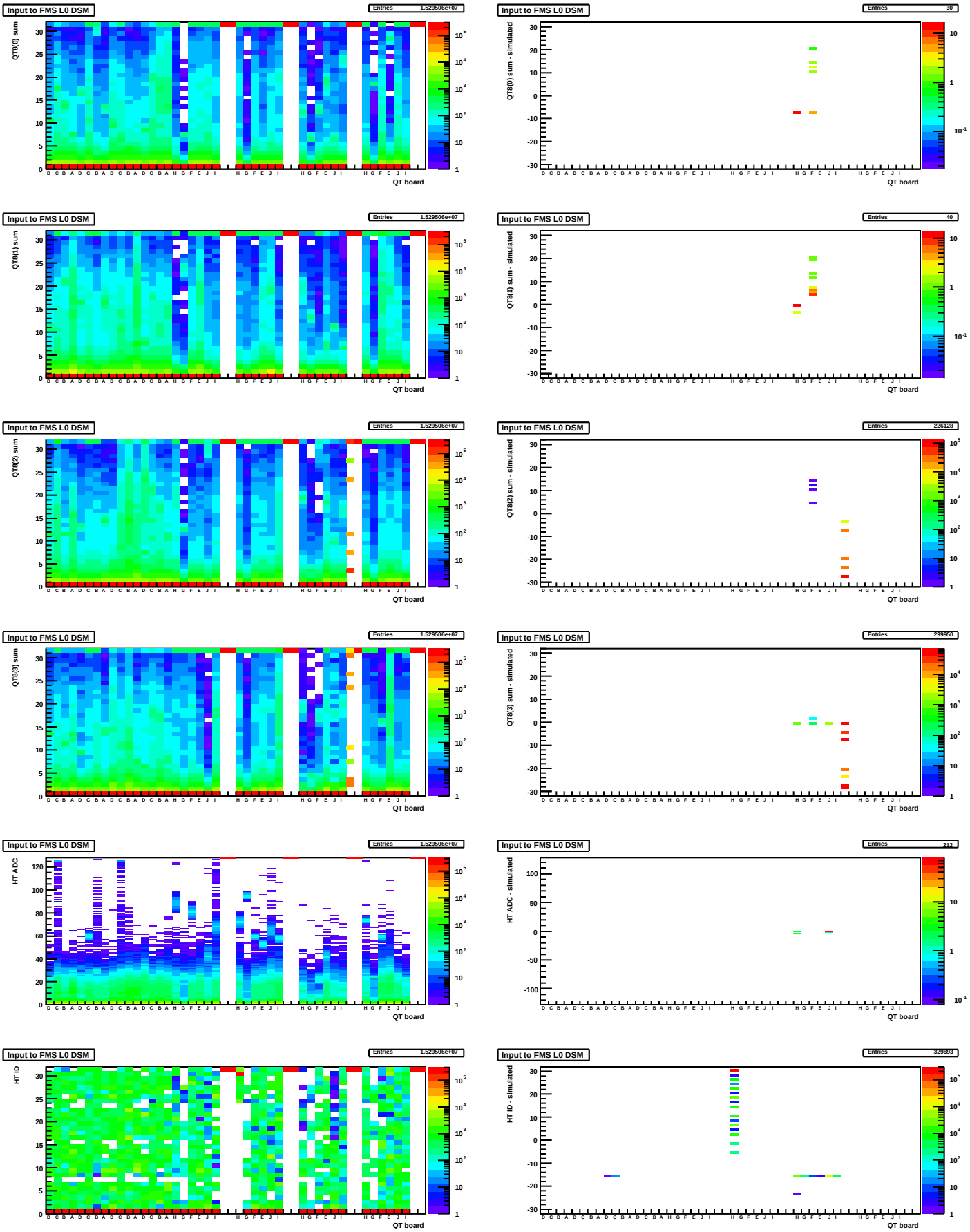

Entries 318647
BBC-L TAC Diff
BCC-S TAC Diff
Entries 318647
BBC-L TAC Diff
VPD TAC Diff
Entries 318647
BBC-L TAC Diff
ZDC TAC Diff
Entries 318647
BBC-S TAC Diff
VPD TAC Diff
Entries 318647
BBC-S TAC Diff
ZDC TAC Diff
Entries 318647
VPD TAC Diff
ZDC TAC Diff

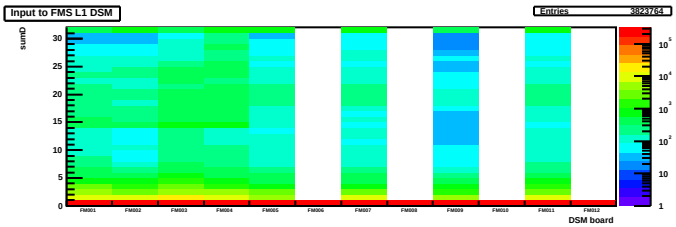

Input to QT1 crate
Entries 8923671
ADC
channel

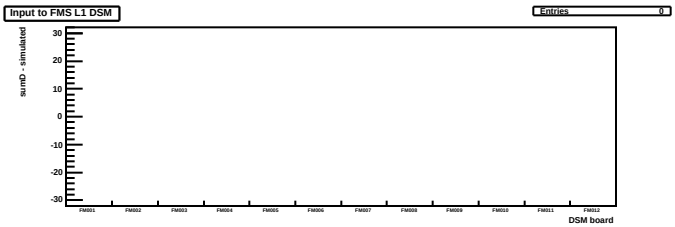

Input to QT2 crate
Entries 4740207
ADC
channel

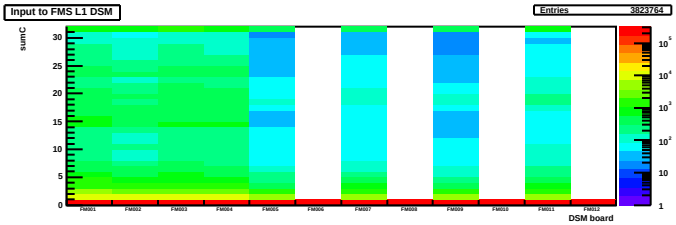

Input to QT3 crate
Entries 5367876
ADC
channel

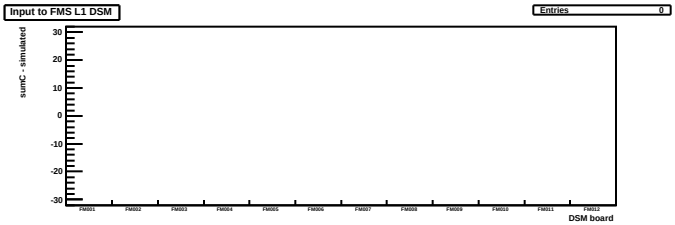

Input to QT4 crate
Entries 6290901
ADC
channel

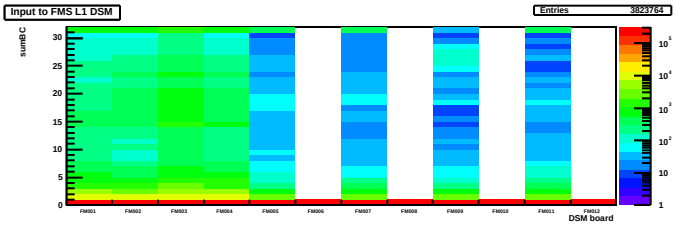

Input to FMS L0 DSM
Entries 1.529506e+07
QT8(0) sum
QT8(1) sum
QT8(2) sum
QT8(3) sum
HT ADC
HT ID
Entries 30
Entries 40
Entries 226128
Entries 299950
Entries 212
Entries 329893
QT8(0) sum - simulated
QT8(1) sum - simulated
QT8(2) sum - simulated
QT8(3) sum - simulated
HT ADC - simulated
HT ID - simulated
QT board

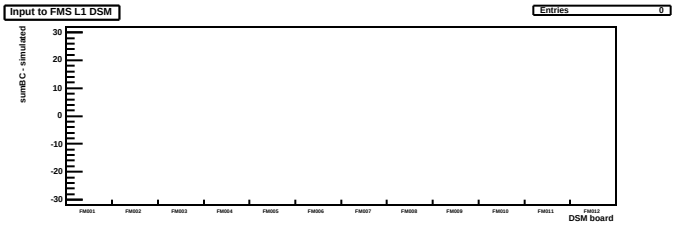

Input to FMS L1 DSM
Entries 3823764
sumD
DSM board

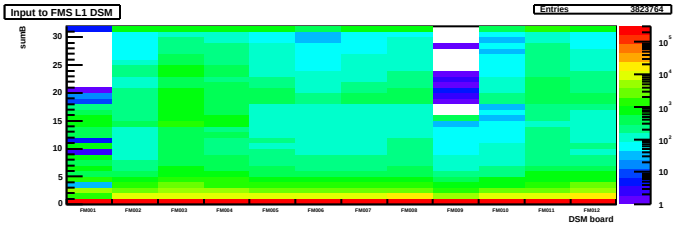

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

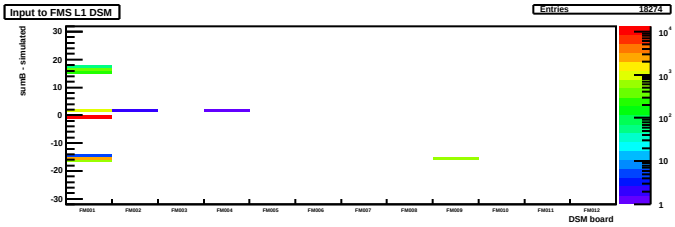

Input to FMS L1 DSM
Entries 3823764
sumC
DSM board

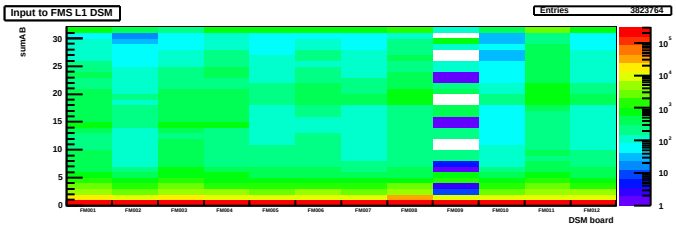

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

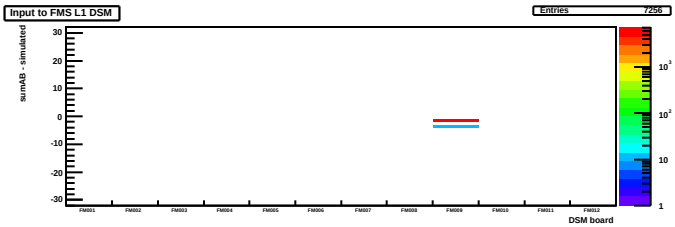

Input to FMS L1 DSM
Entries 3823764
sumBC
DSM board

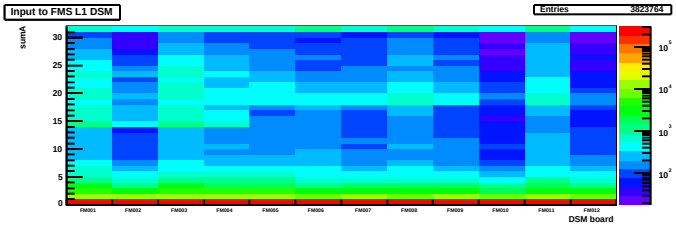

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

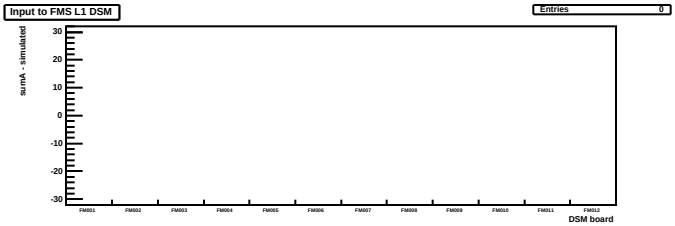

Input to FMS L1 DSM
Entries 3823764
sumB
DSM board

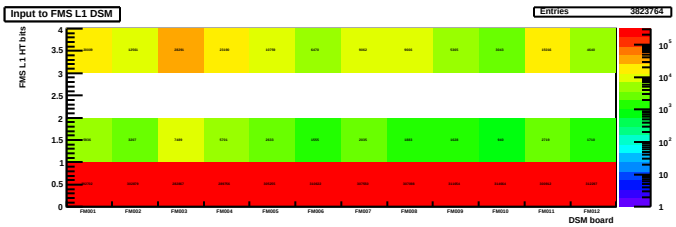

Input to FMS L1 DSM
Entries 18274
sumB - simulated
DSM board

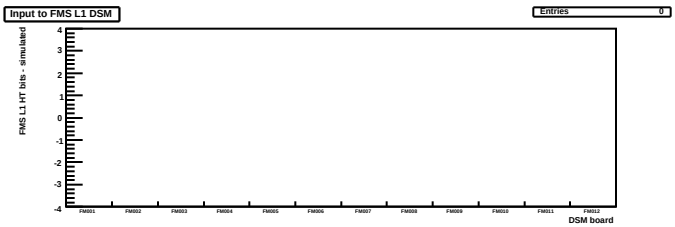

Input to FMS L1 DSM
Entries 3823764
sumAB
DSM board

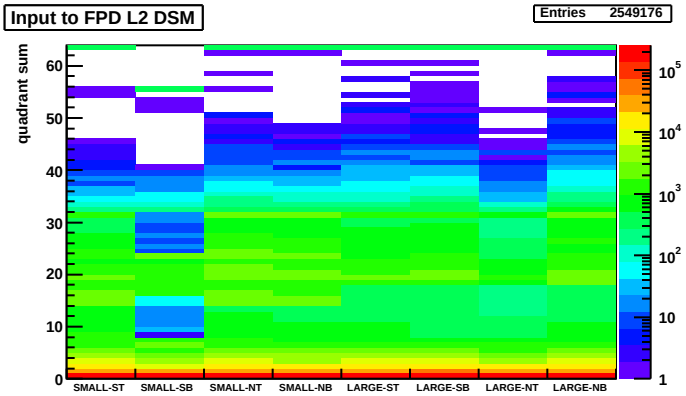

Input to FMS L1 DSM
Entries 7256
sumAB - simulated
DSM board

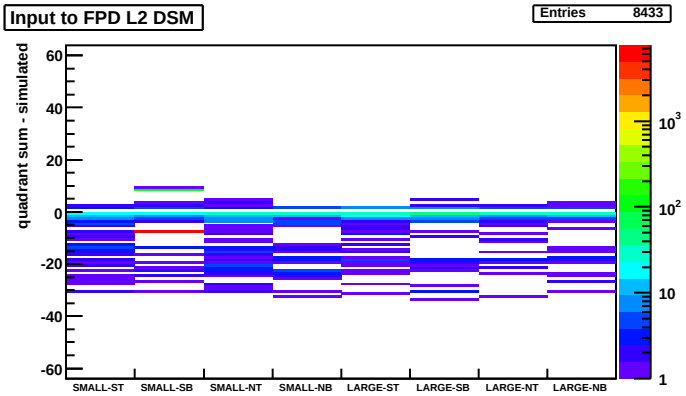

Input to FMS L1 DSM
Entries 3823764
sumA
DSM board

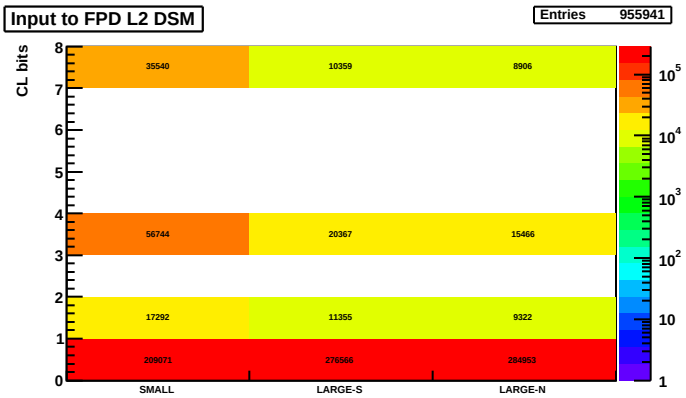

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

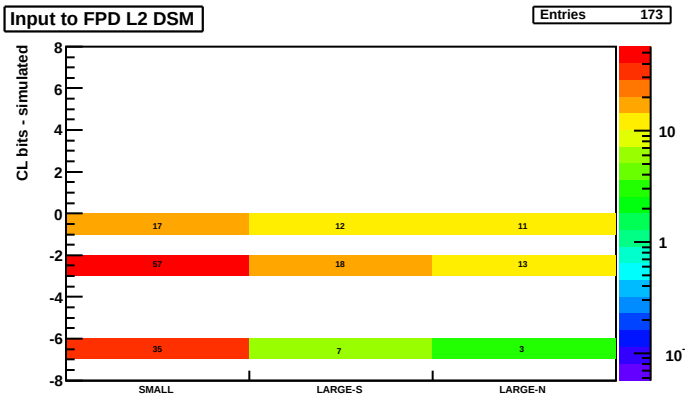

Input to FMS L1 DSM
Entries 3823764
FMS L1 HT bits
DSM board

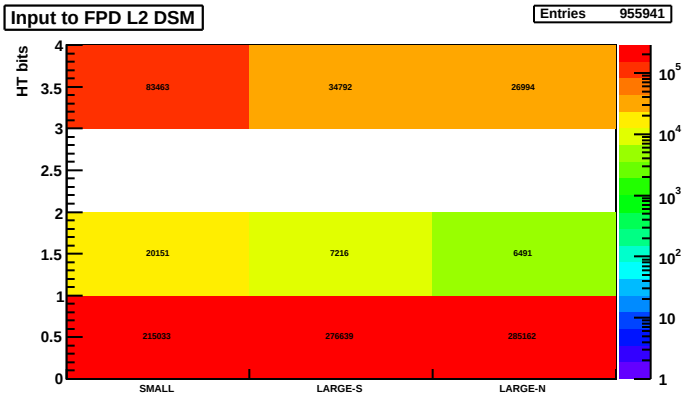

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

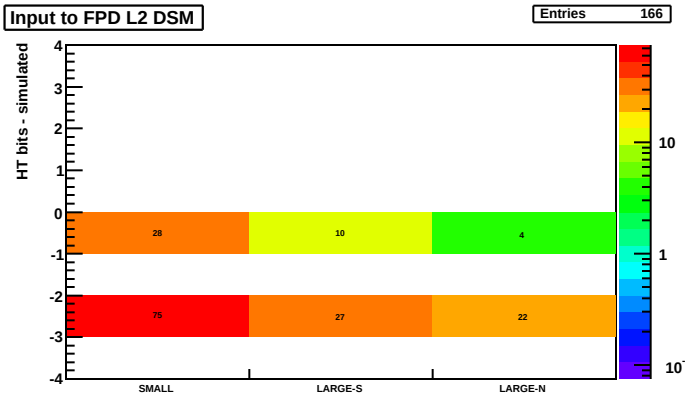

Input to FPD L2 DSM
Entries 2549176
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

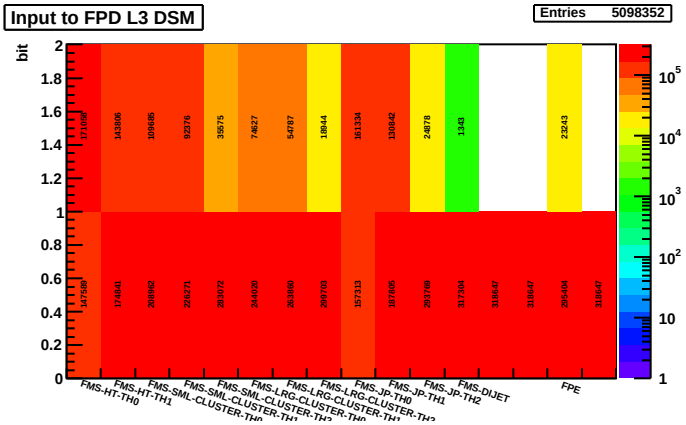

Input to FPD L2 DSM
Entries 8433
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

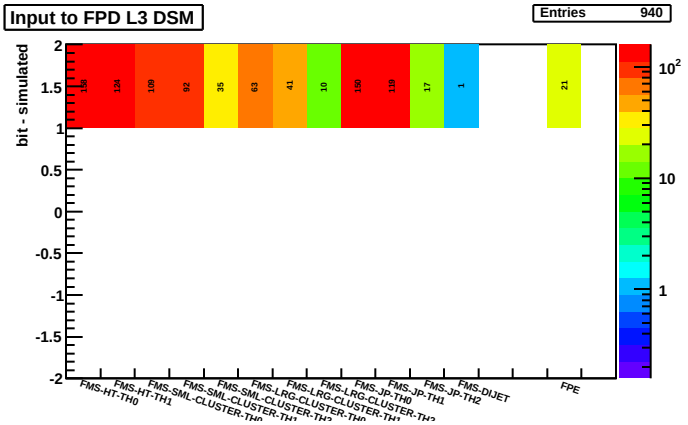

Input to FPD L2 DSM
Entries 955941
CL bits
35540
10359
8906
56744
20367
15466
17292
11355
9322
209071
276566
284953
SMALL
LARGE-S
LARGE-N

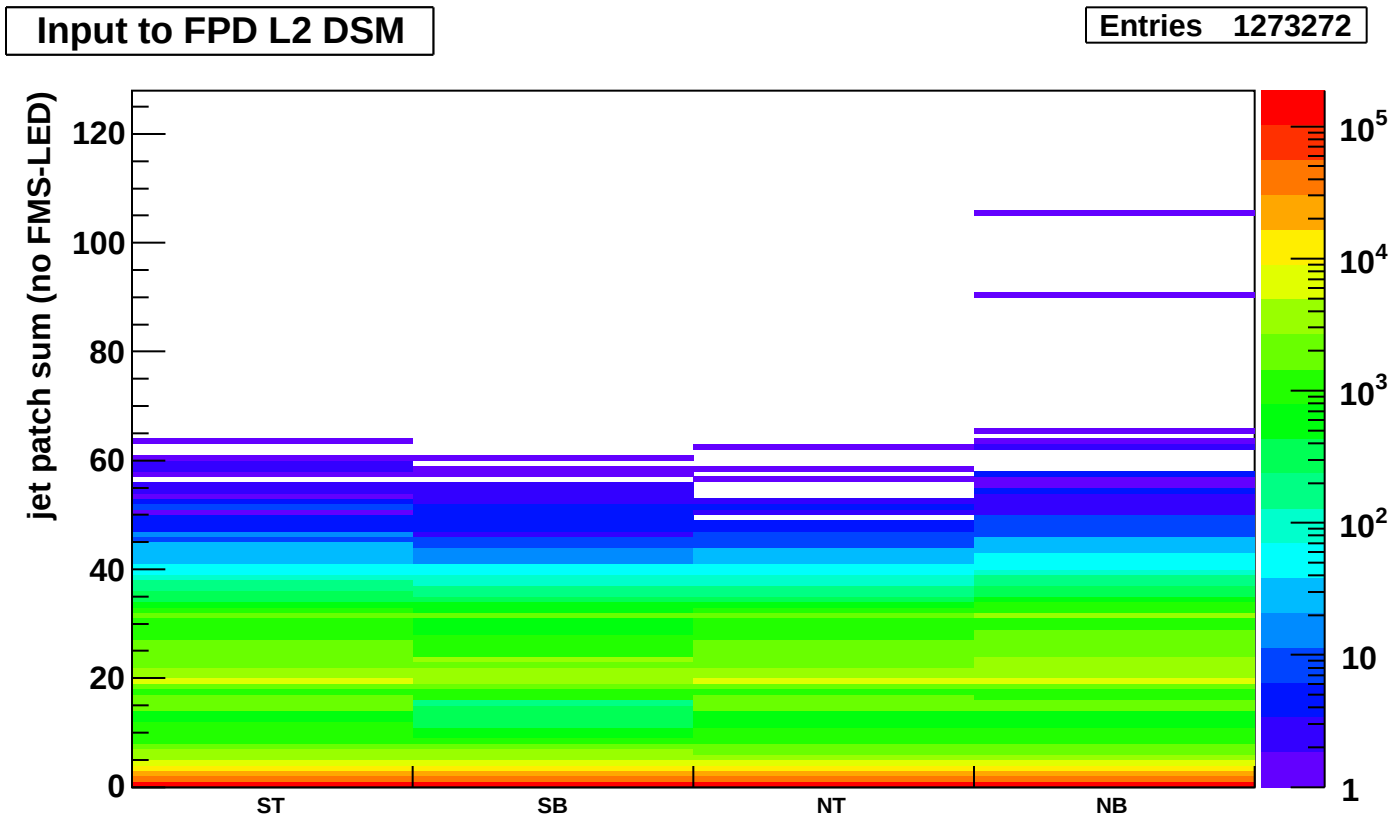

Input to FPD L2 DSM
Entries 173
CL bits - simulated
17
12
11
57
18
13
35
7
3
SMALL
LARGE-S
LARGE-N

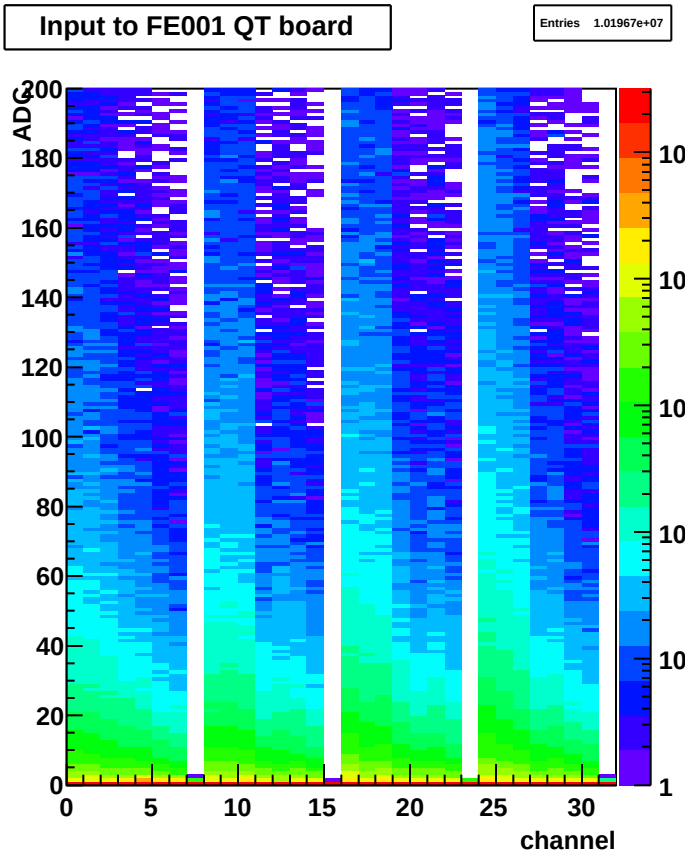

Input to FPD L2 DSM
Entries 955941
HT bits
83463
34792
26994
20151
7216
6491
215033
276639
285162
SMALL
LARGE-S
LARGE-N

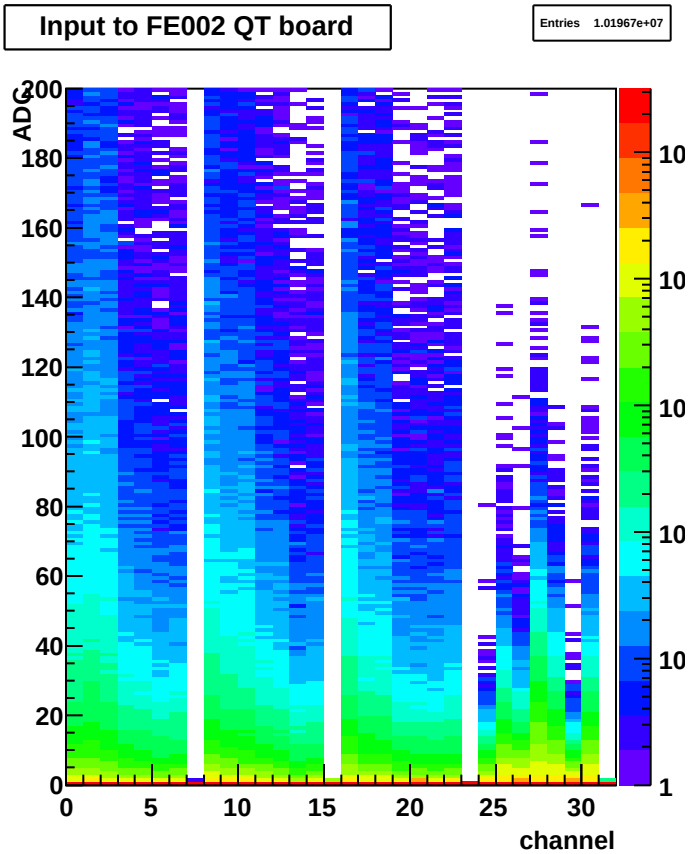

Input to FPD L2 DSM
Entries 166
HT bits - simulated
28
10
4
75
27
22
SMALL
LARGE-S
LARGE-N

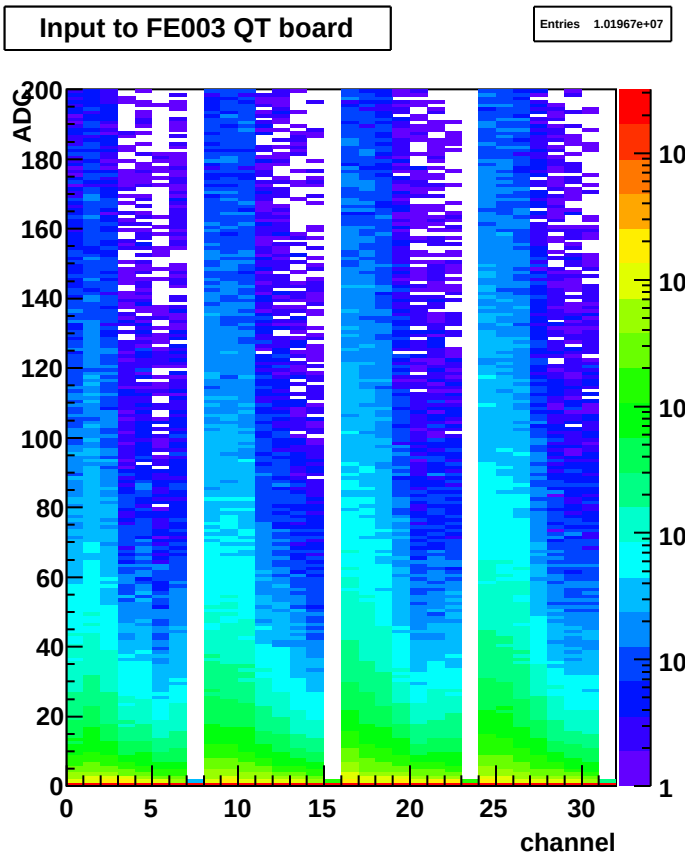

Input to FPD L3 DSM
Entries 5098352
bit
171057
143806
109685
92376
35575
74627
54797
18944
161334
120642
24878
1343
23243
147589
174841
209962
226271
283072
244020
263860
299703
157313
187805
293769
317204
310647
318647
295404
318647
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

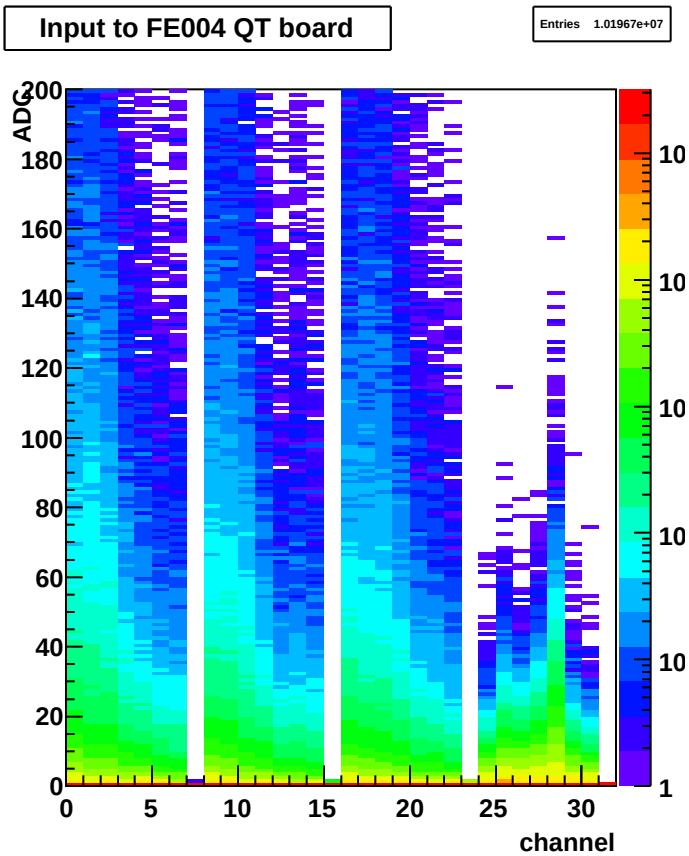

Input to FPD L3 DSM
Entries 940
bit - simulated
158
124
109
92
35
63
41
10
150
119
17
1
21
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

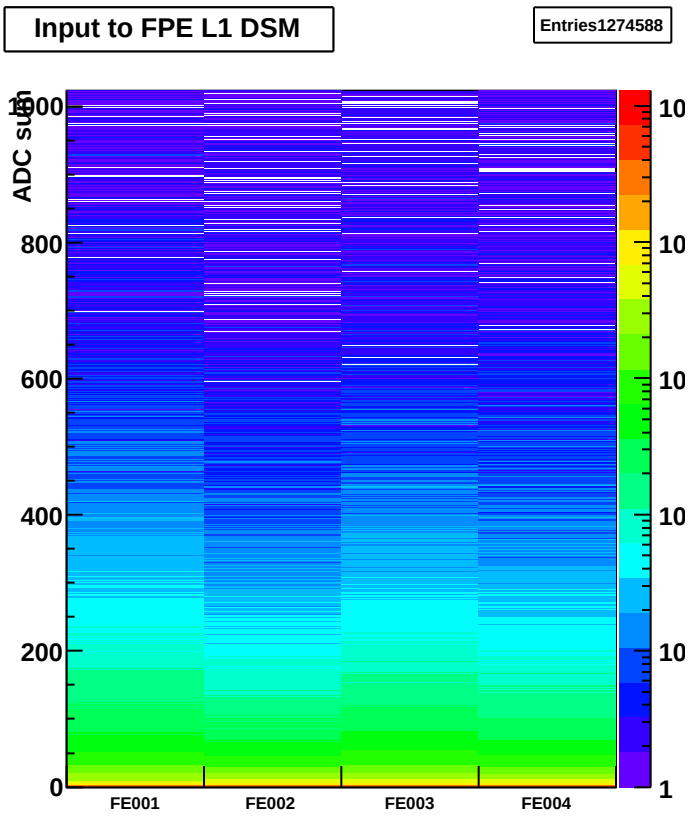

Input to FPD L2 DSM
Entries 1273272
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

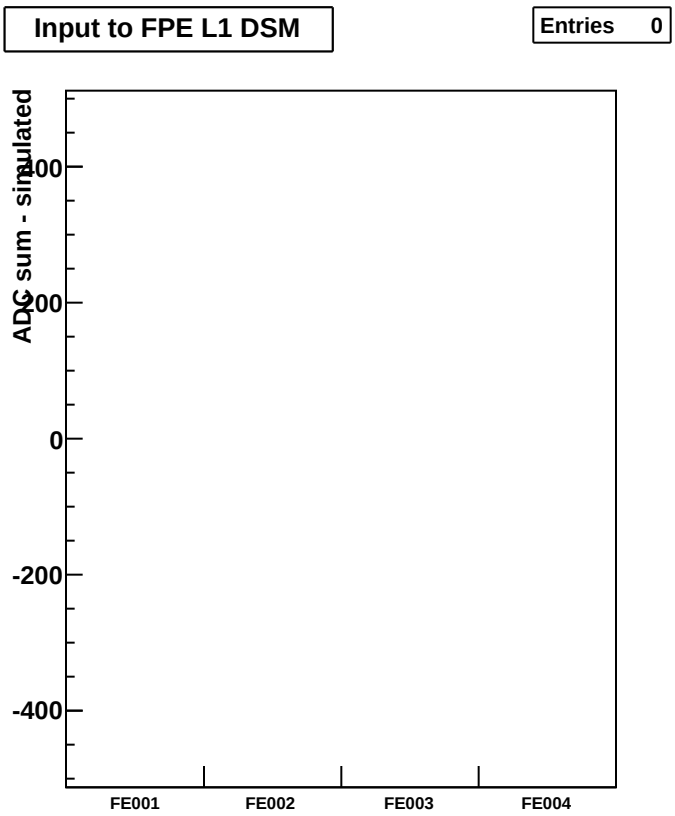

Input to FE001 QT board
Entries 1.01967e+07
ADC
channel

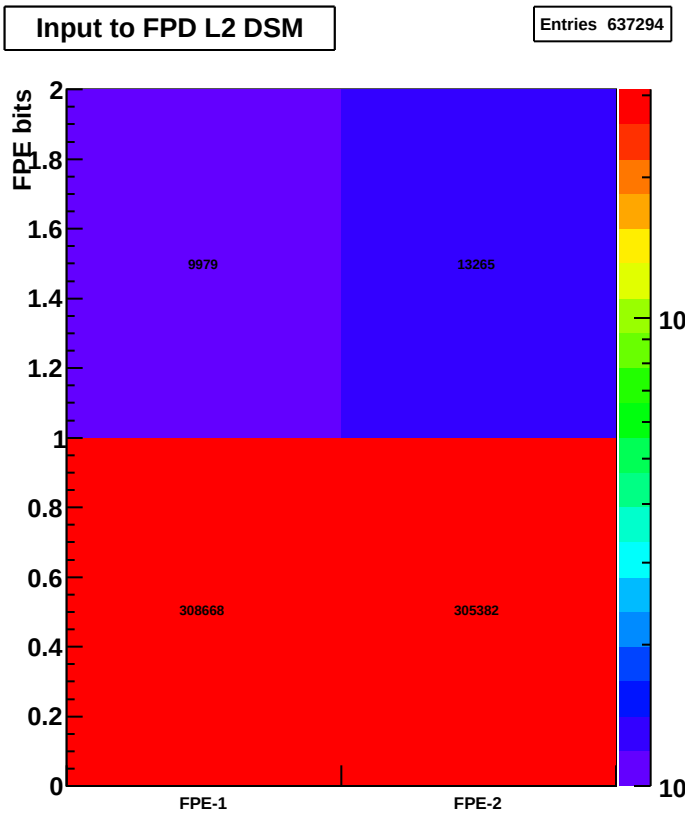

Input to FE002 QT board
Entries 1.01967e+07
ADC
channel

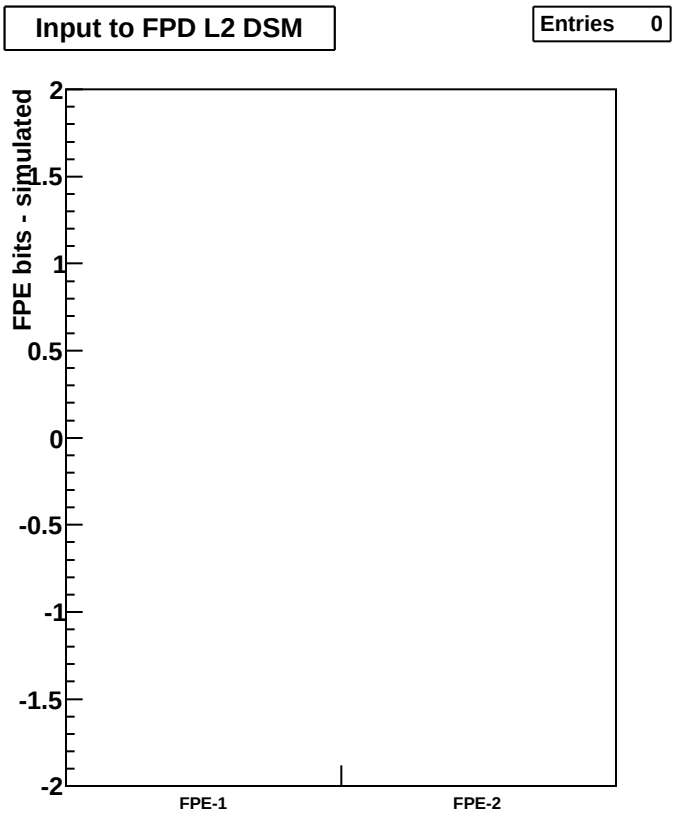

Input to FE003 QT board
Entries 1.01967e+07
ADC
channel


Input to FE004 QT board
Entries 1.01967e+07
ADC
channel


Input to FPE L1 DSM
Entries1274588
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 637294
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
9979
13265
308668
305382
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2