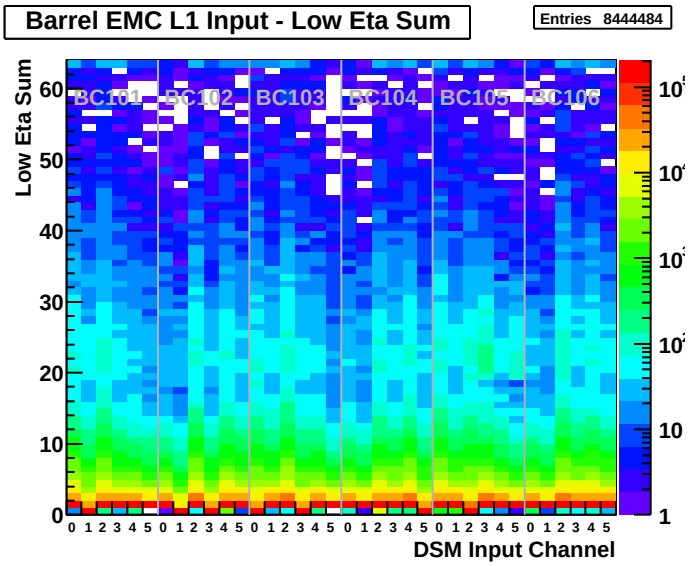
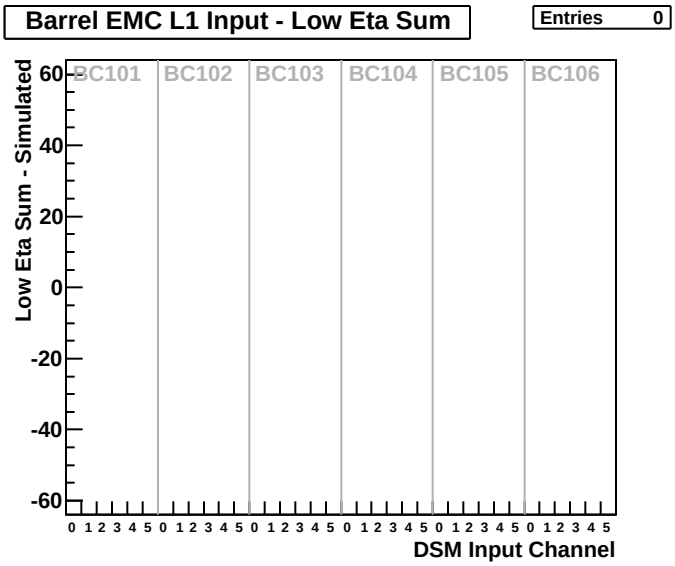
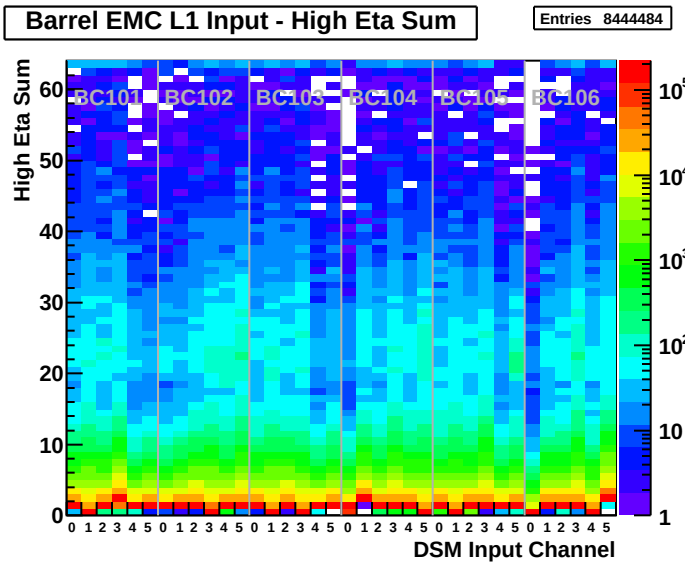
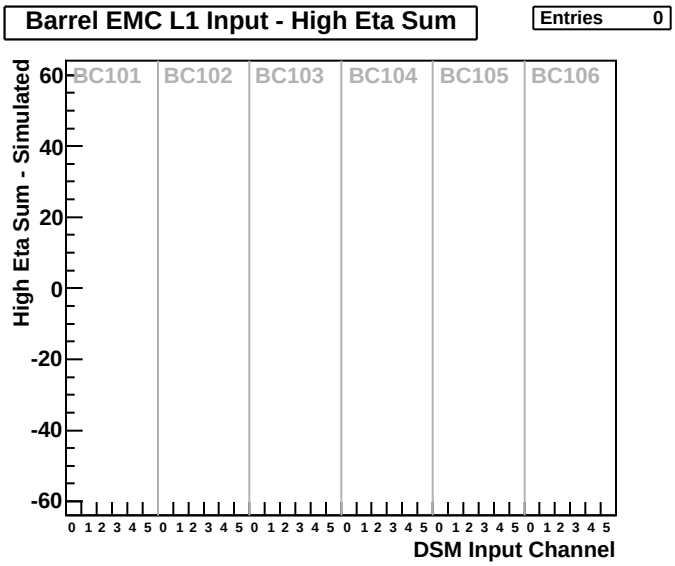
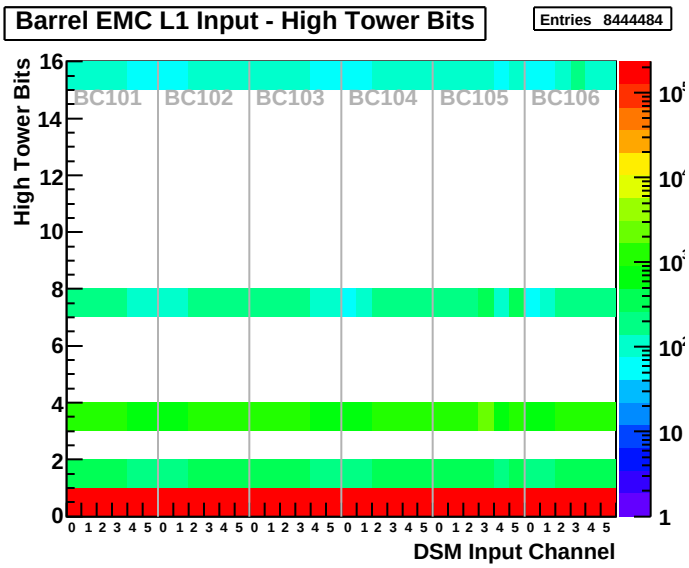
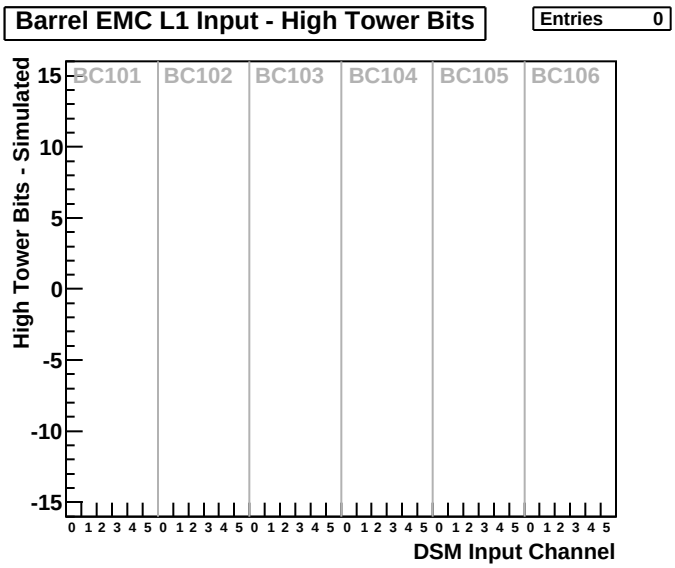

Barrel EMC L1 Input - Low Eta Sum
Entries 8444484
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

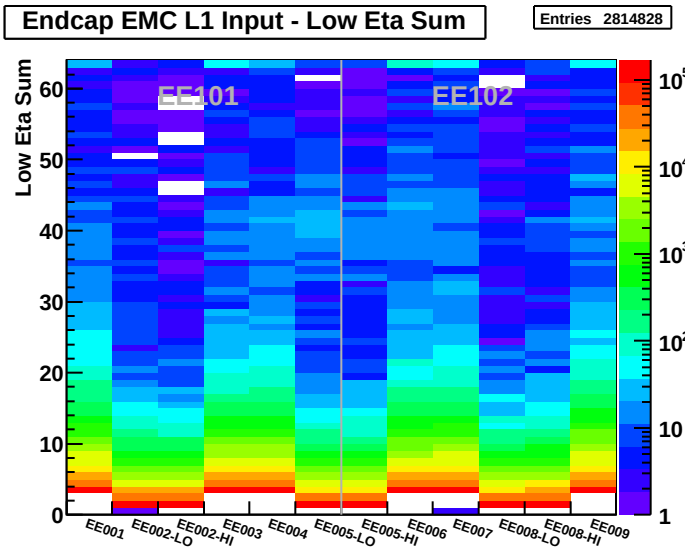

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

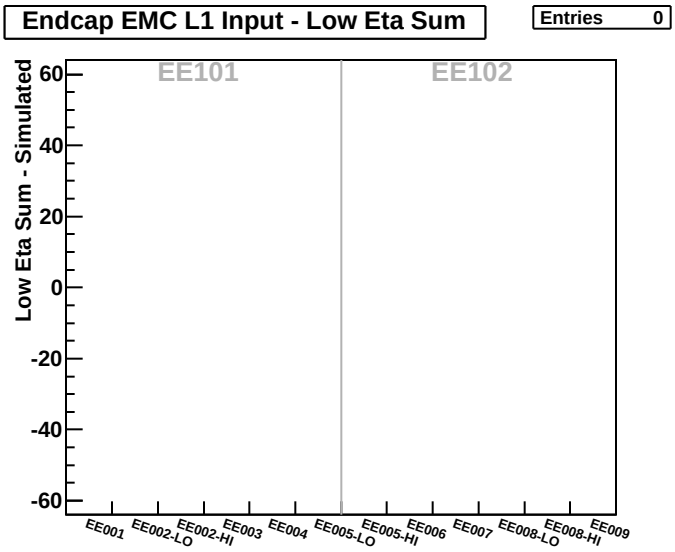

Barrel EMC L1 Input - High Eta Sum
Entries 8444484
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

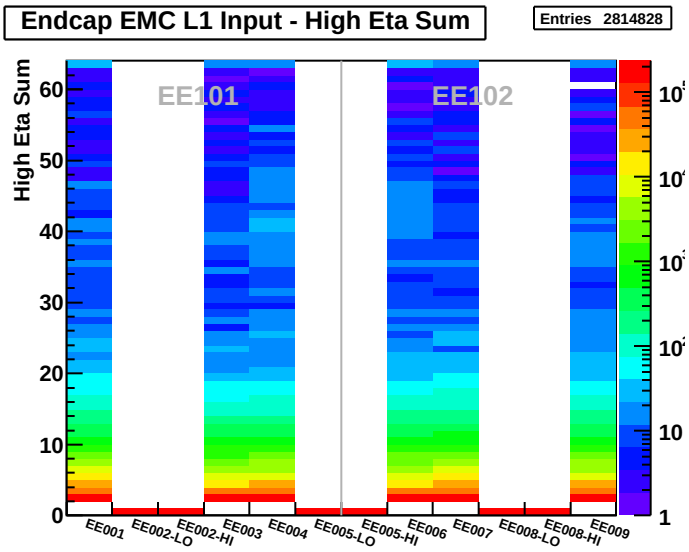

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

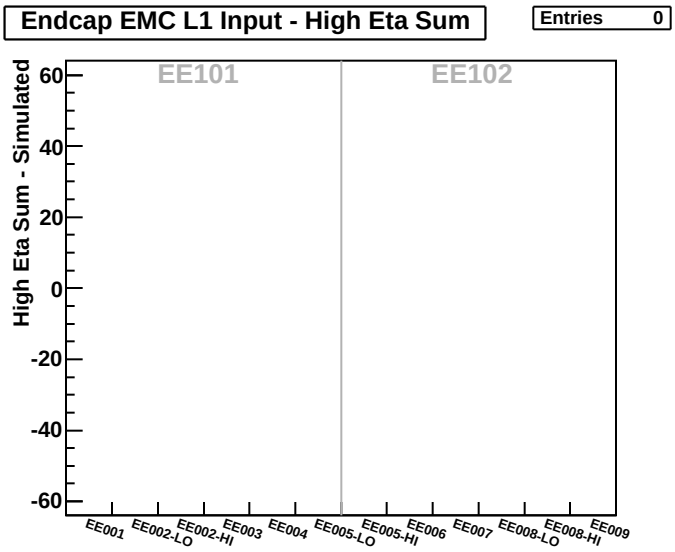

Barrel EMC L1 Input - High Tower Bits
Entries 8444484
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

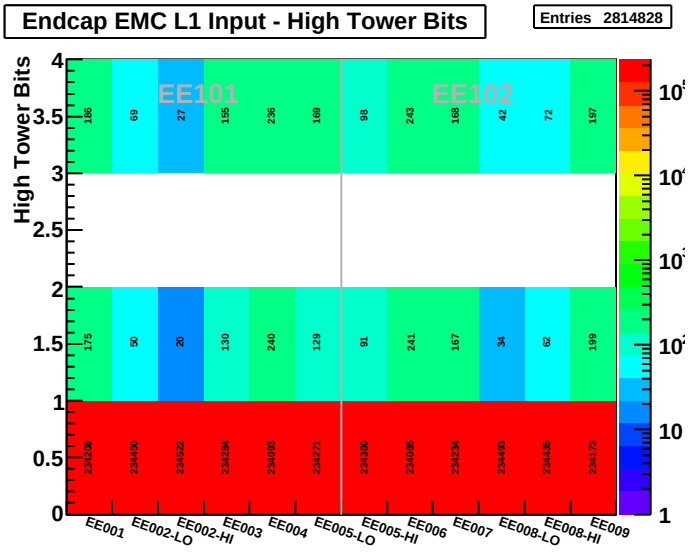

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

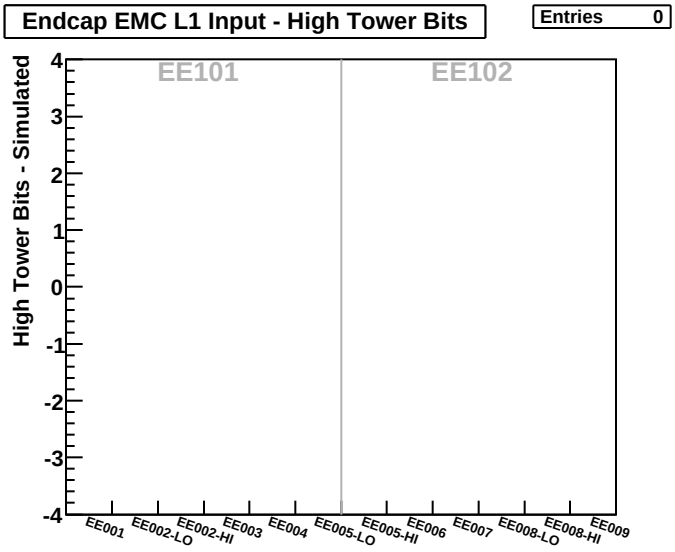

Endcap EMC L1 Input - Low Eta Sum
Entries 2814828
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

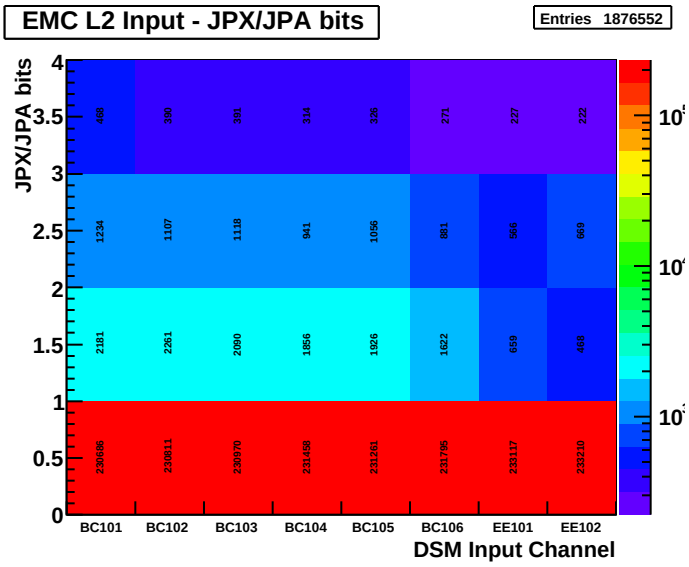

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

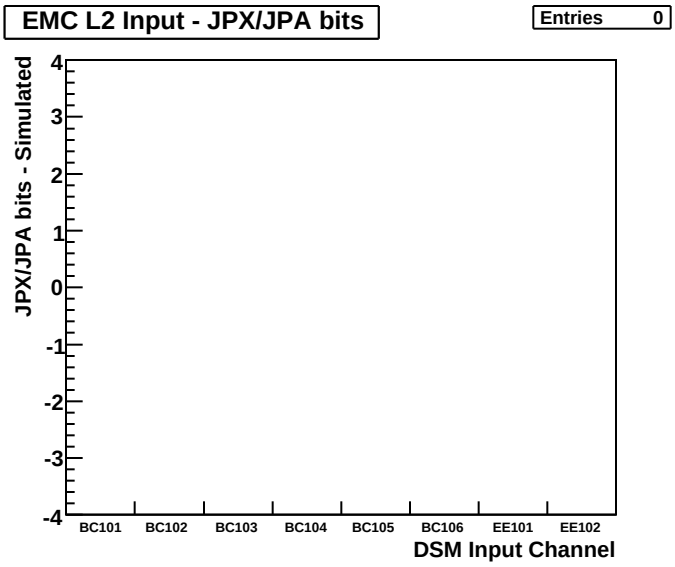

Endcap EMC L1 Input - High Eta Sum
Entries 2814828
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

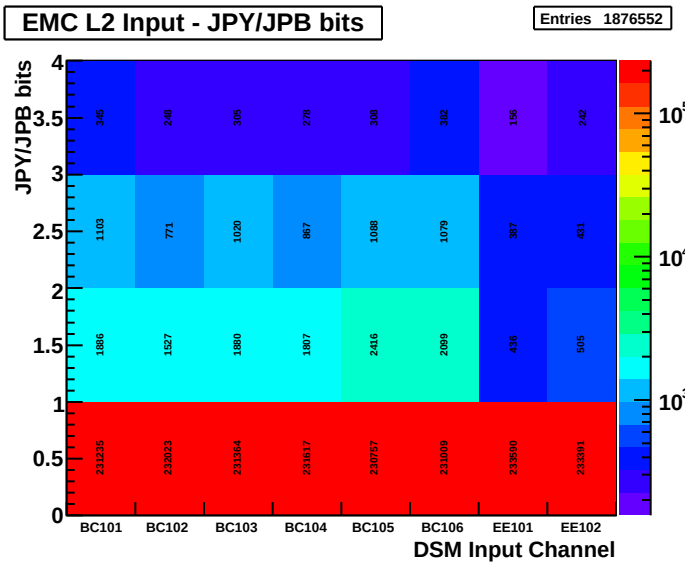

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

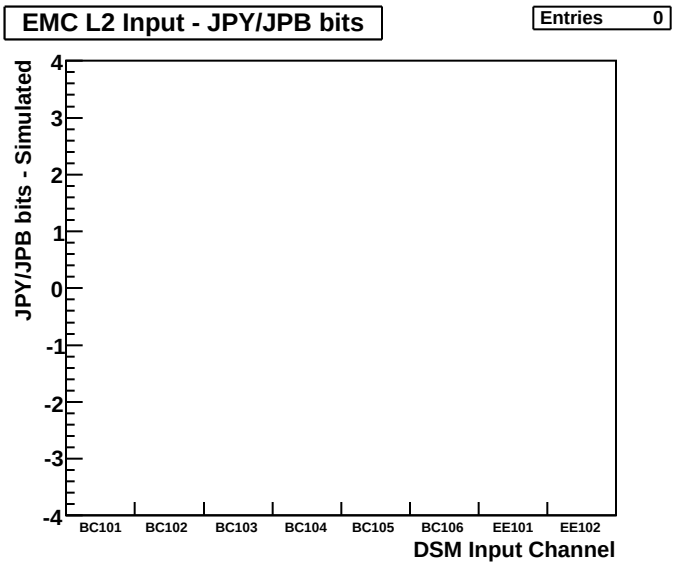

Endcap EMC L1 Input - High Tower Bits
Entries 2814828
High Tower Bits
EE101
EE102
186 69 27 155 236 169 98 243 168 42 72 197
175 50 20 130 240 129 91 241 167 34 62 199
234208 234450 234522 234284 234093 234271 234380 234085 234234 234493 234435 234173
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

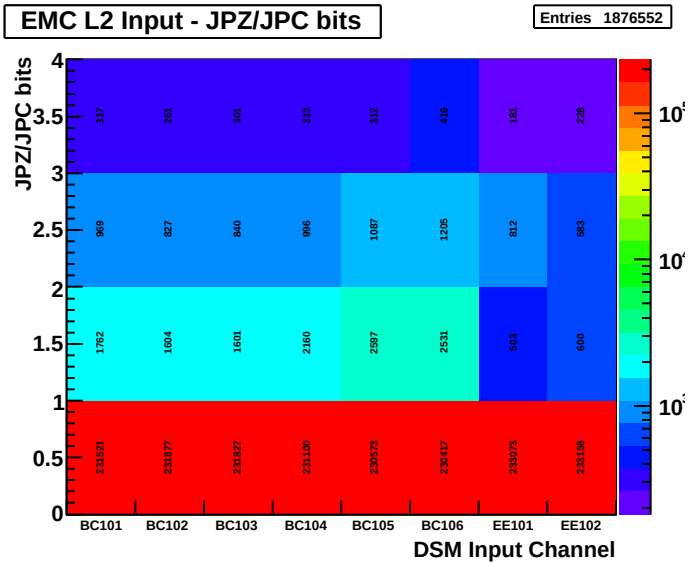

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

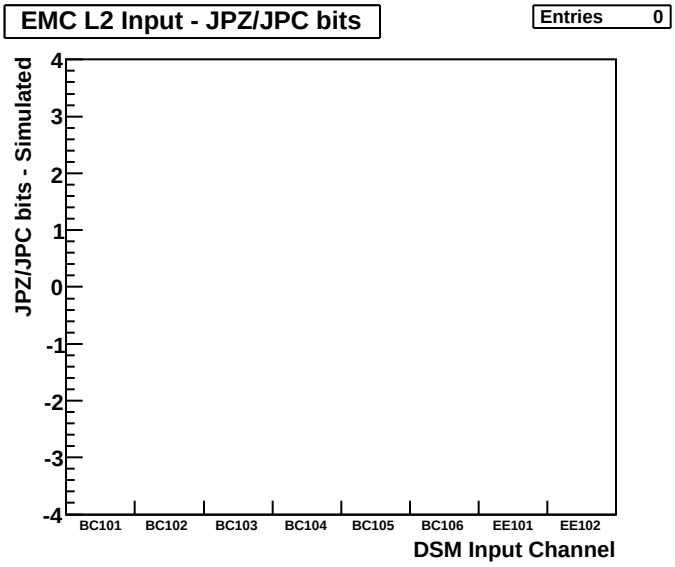

EMC L2 Input - JPX/JPA bits
Entries 1876552
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
468 390 391 314 326 271 227 222
1234 1107 1118 941 1056 881 566 669
2181 2261 2090 1856 1926 1622 659 468
230686 230811 230970 231458 231261 231795 233117 233210

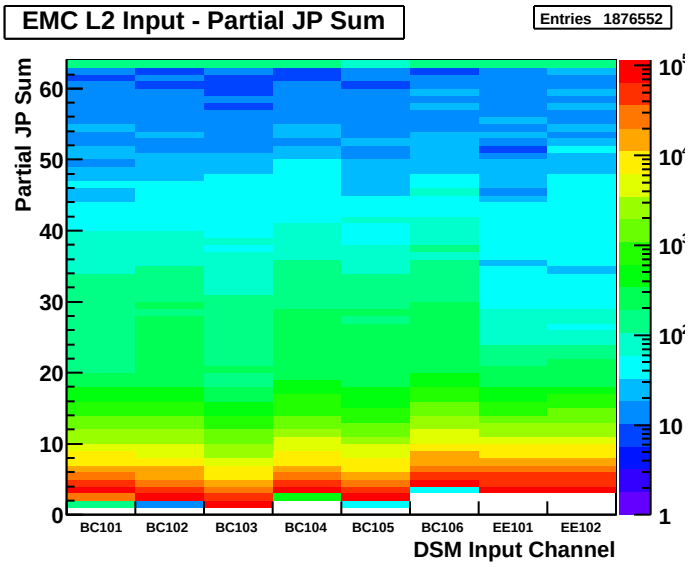

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

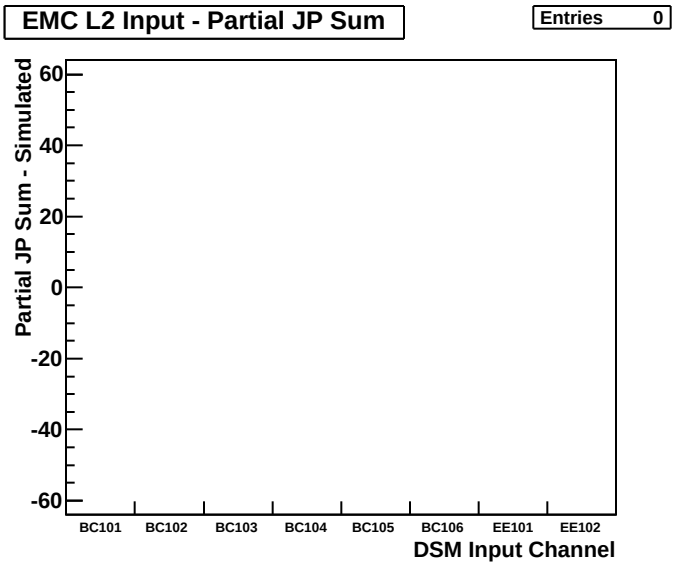

EMC L2 Input - JPY/JPB bits
Entries 1876552
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
345 248 305 278 308 382 156 242
1103 771 1020 867 1088 1079 387 431
1886 1527 1980 1907 2416 2099 436 505
231235 233023 231364 231617 230757 231009 233590 233391

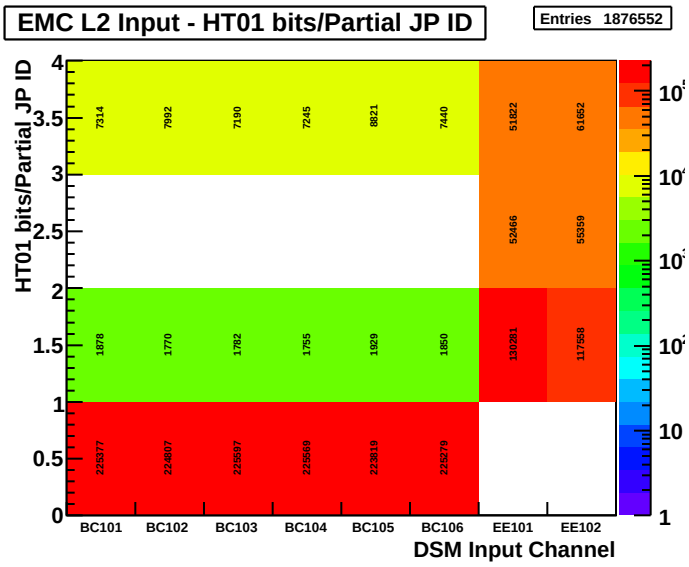

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

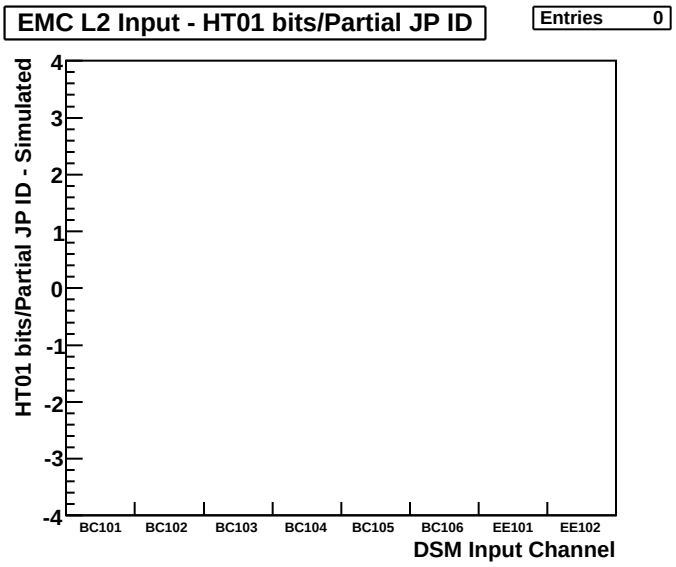

EMC L2 Input - JPZ/JPC bits
Entries 1876552
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
317 261 301 313 312 416 181 228
969 827 840 996 1087 1205 812 583
1762 1604 1601 2160 2597 2531 503 600
231521 231877 231827 231100 230573 230417 233073 233158

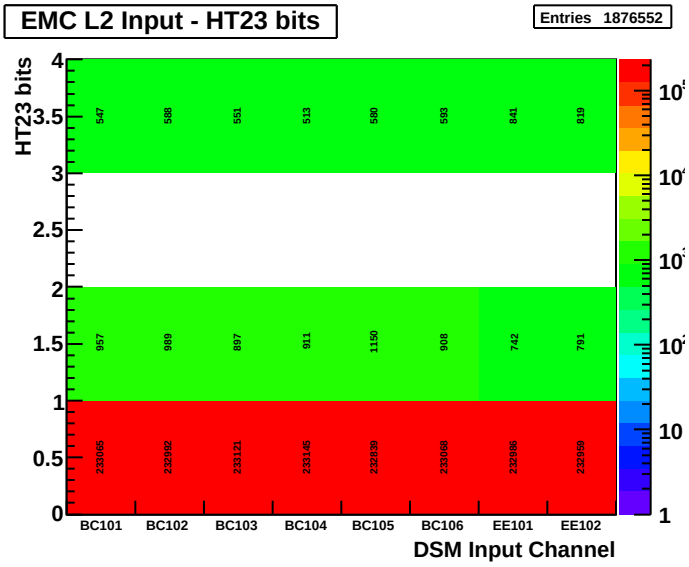

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

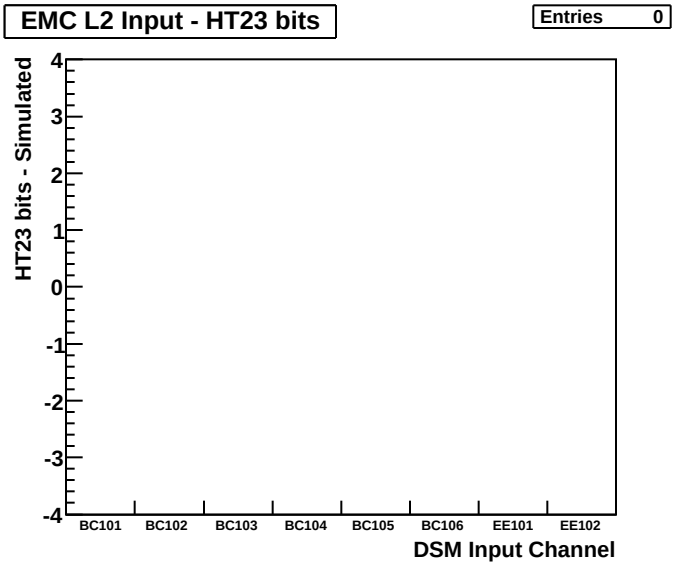

EMC L2 Input - Partial JP Sum
Entries 1876552
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

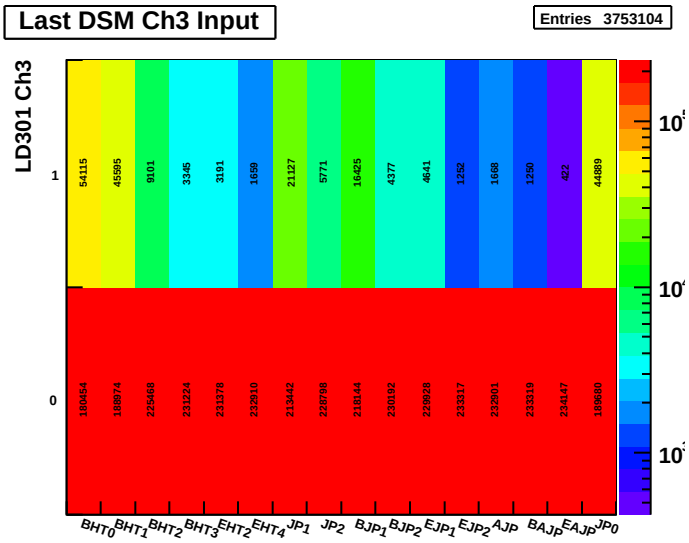

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

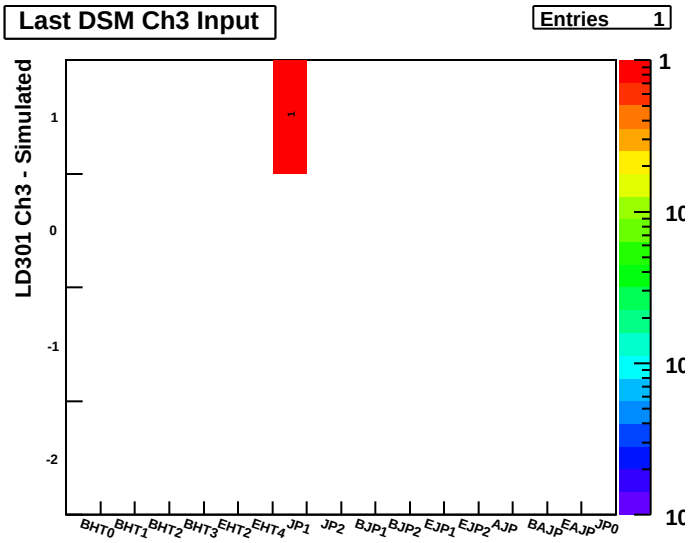

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1876552
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

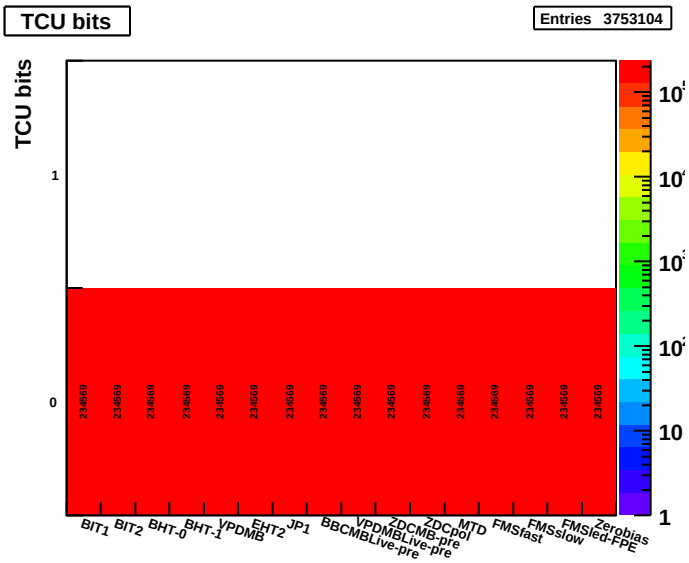

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

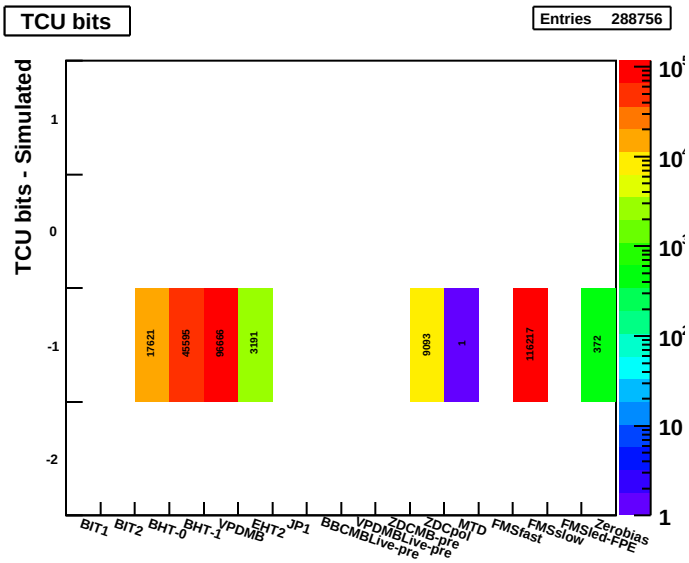

EMC L2 Input - HT23 bits
Entries 1876552
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

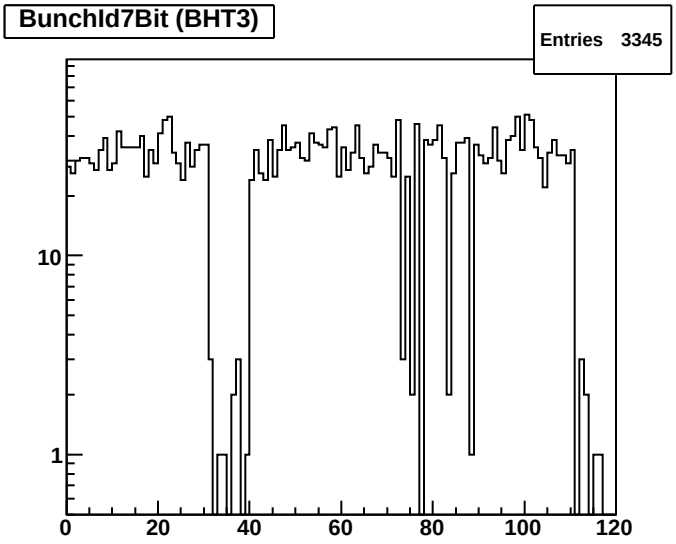

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

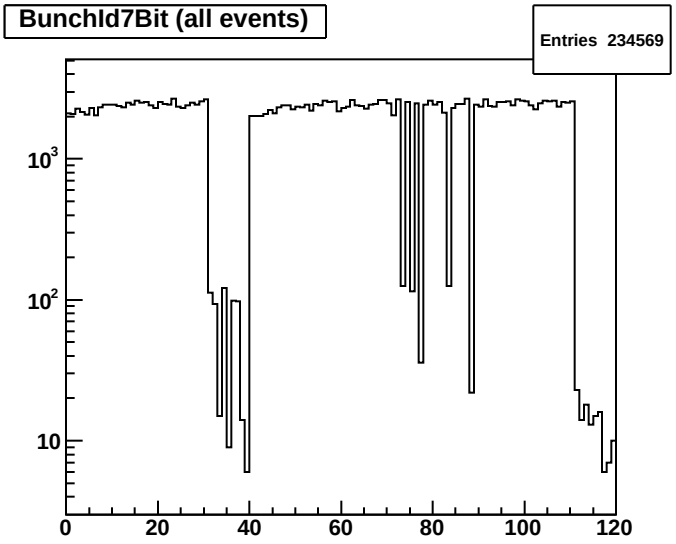

Last DSM Ch3 Input
Entries 3753104
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

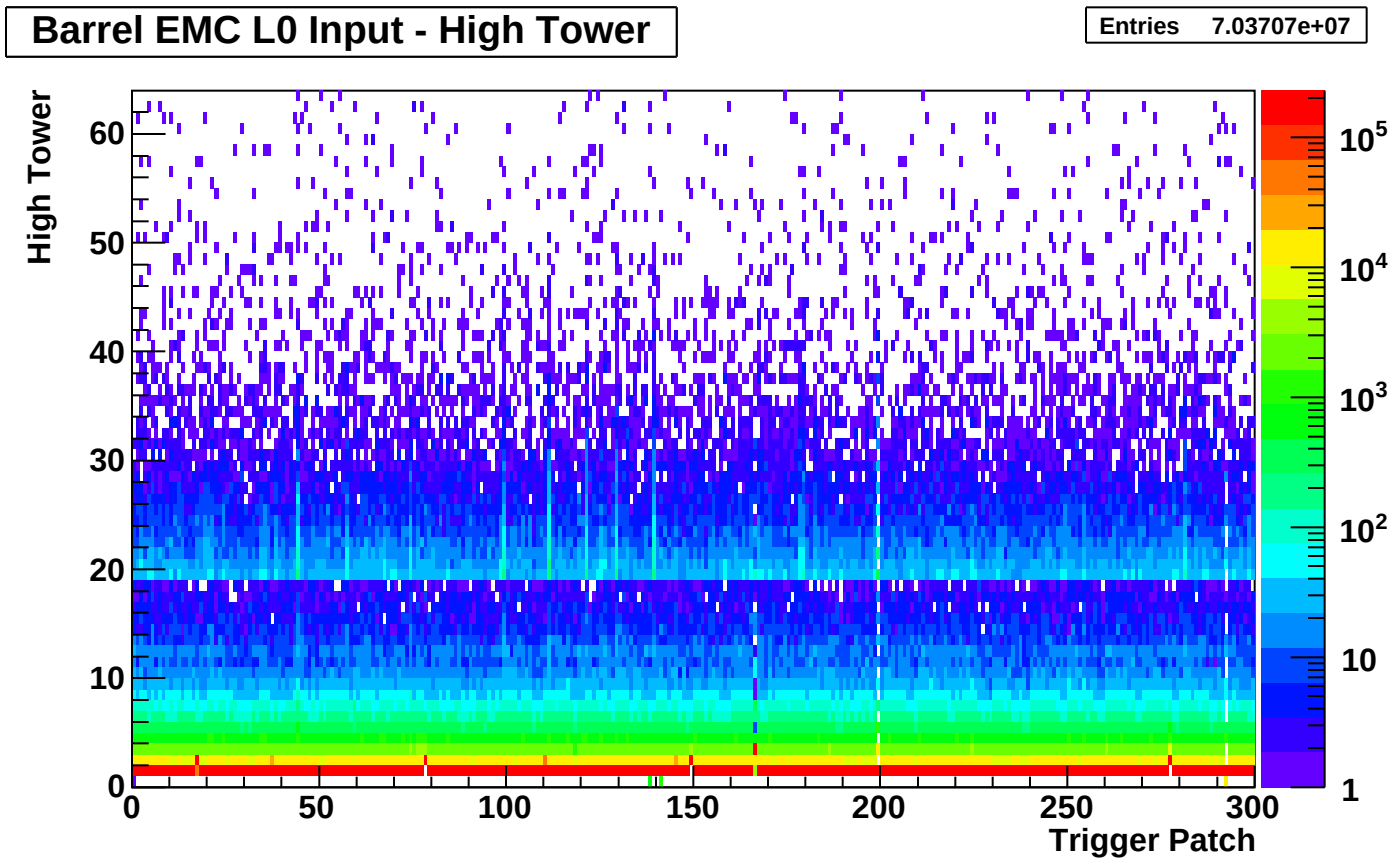

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

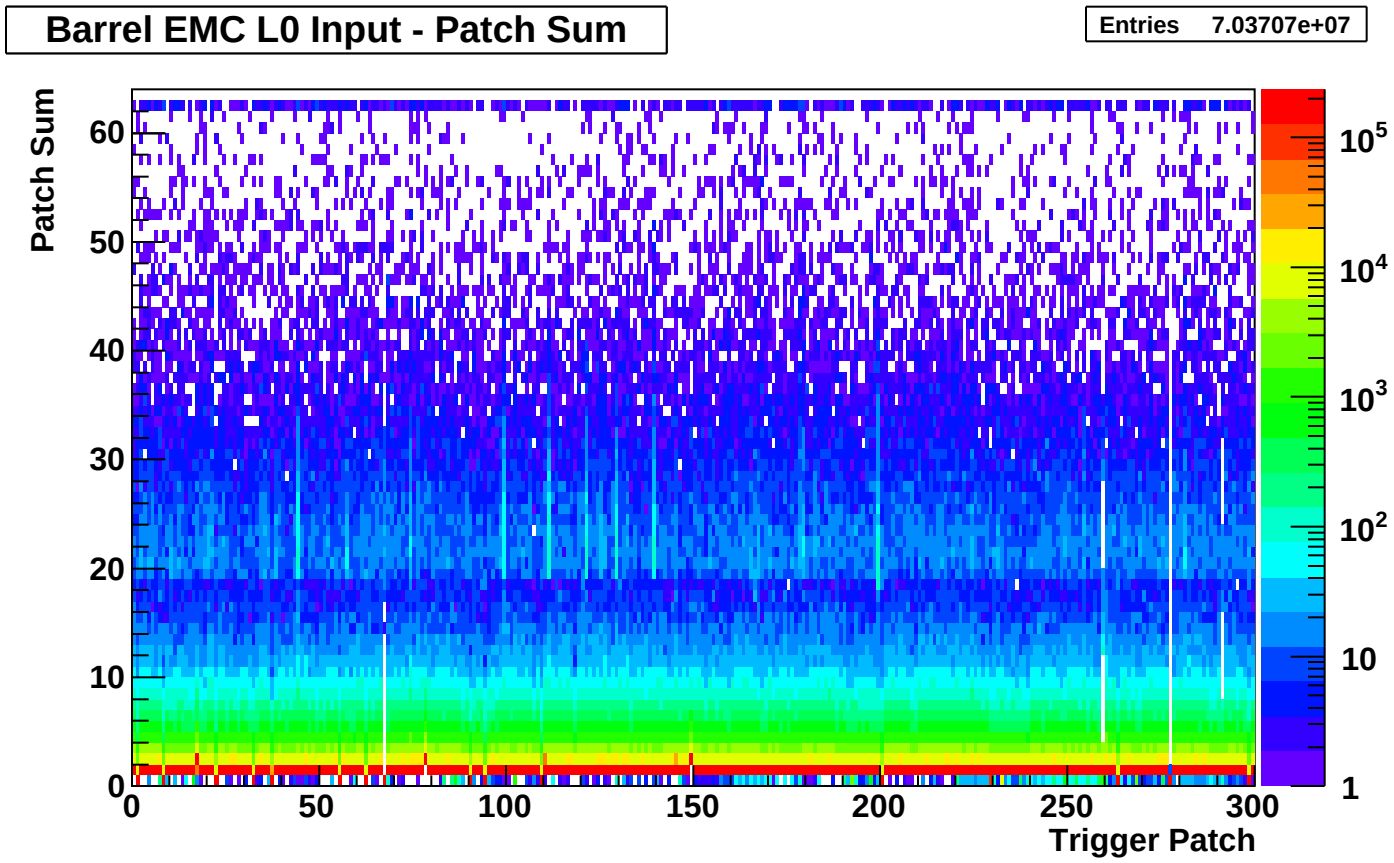

TCU bits
Entries 3753104
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

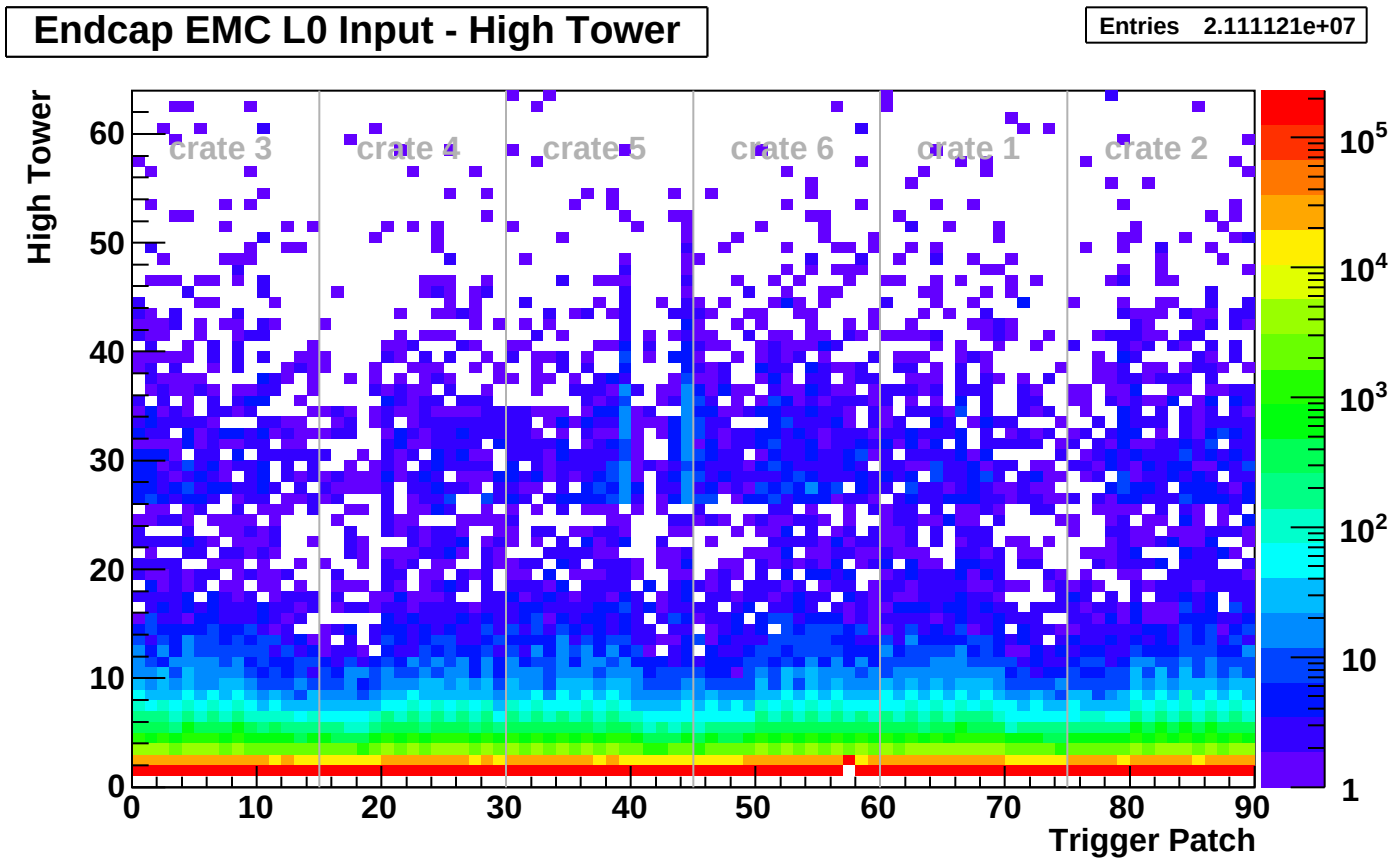

TCU bits
Entries 288756
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

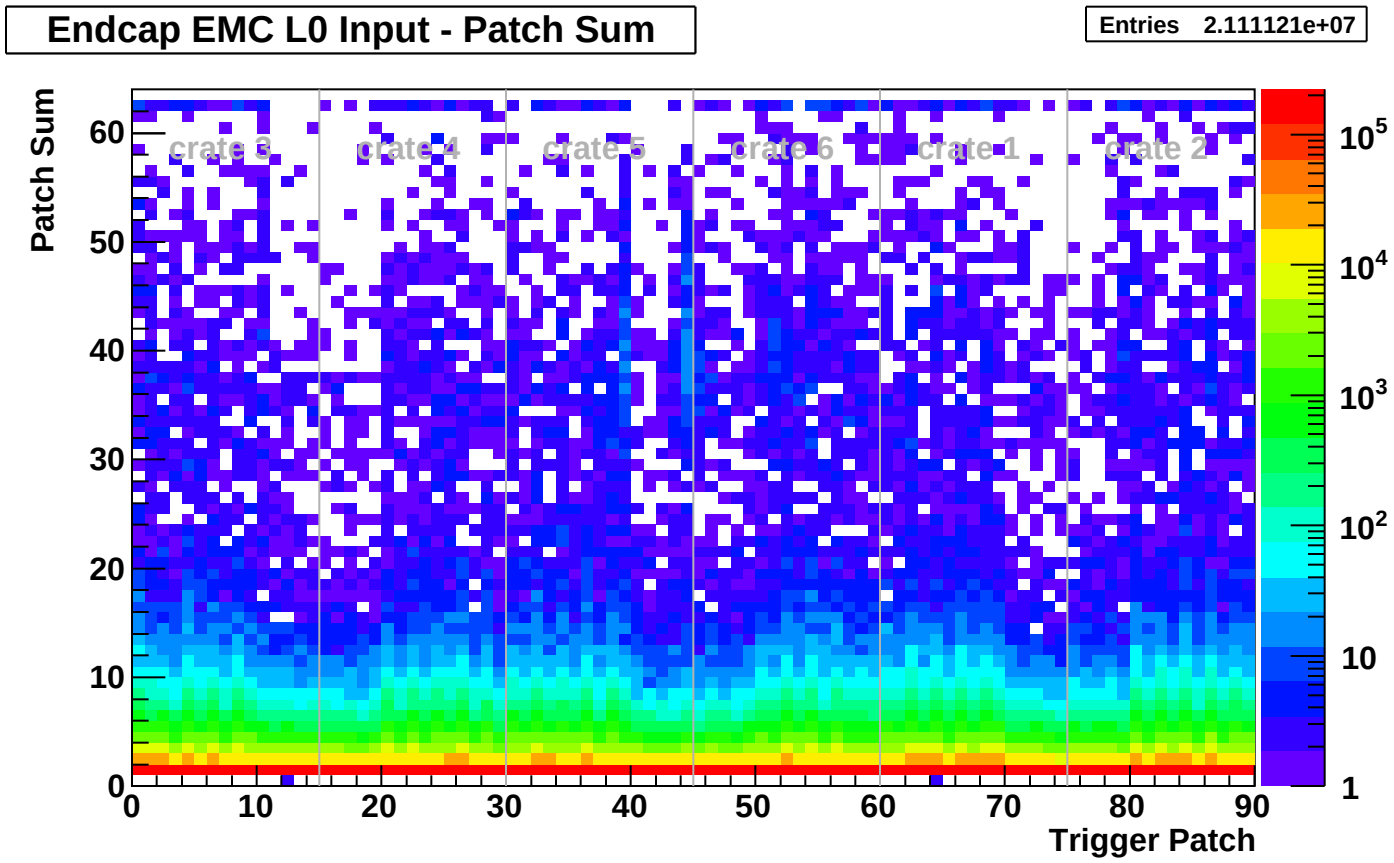

BunchId7Bit (BHT3)
Entries 3345

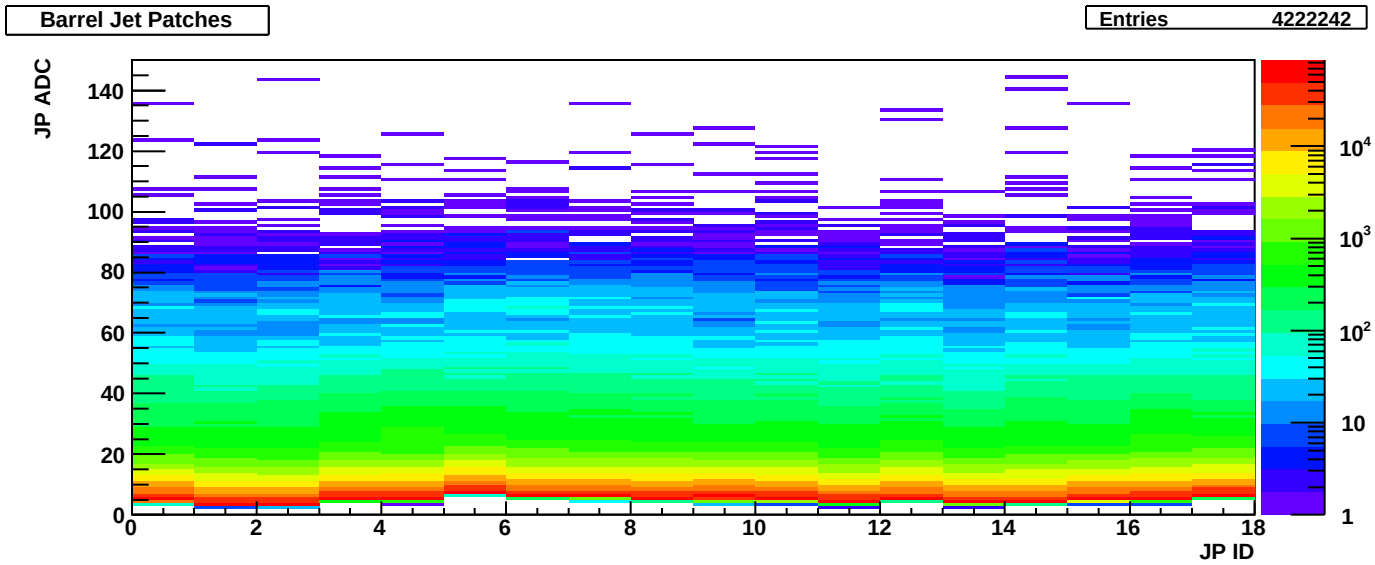

BunchId7Bit (all events)
Entries 234569

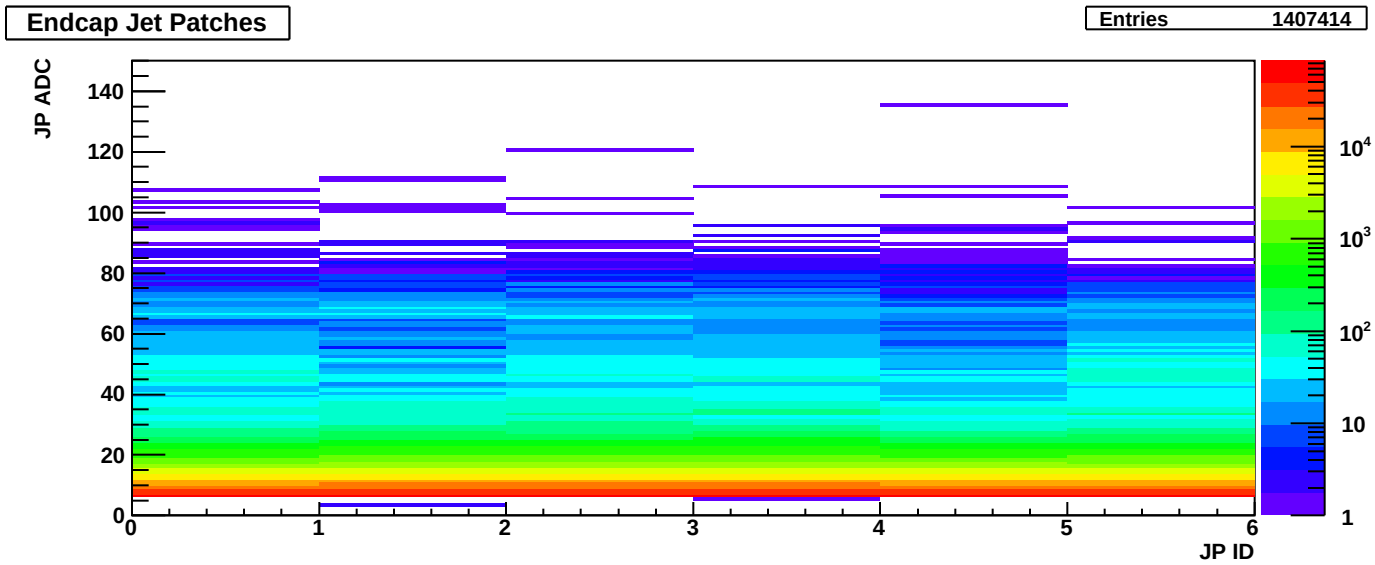

Barrel EMC L0 Input - High Tower
Entries 7.03707e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

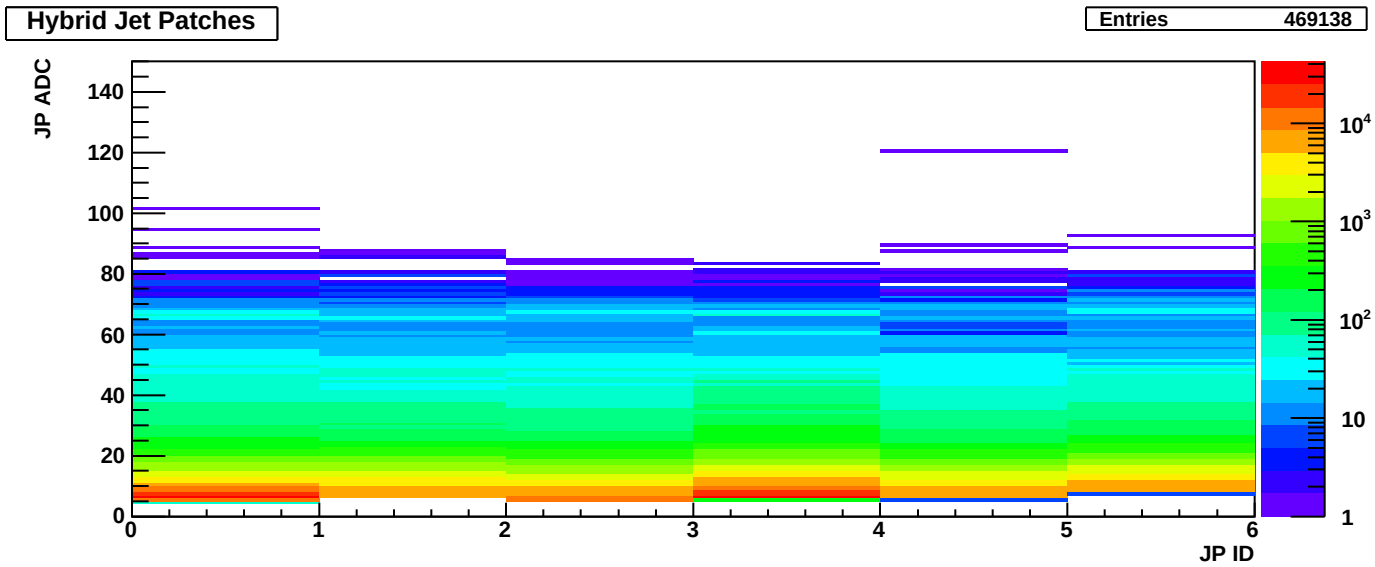

Barrel EMC L0 Input - Patch Sum
Entries 7.03707e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

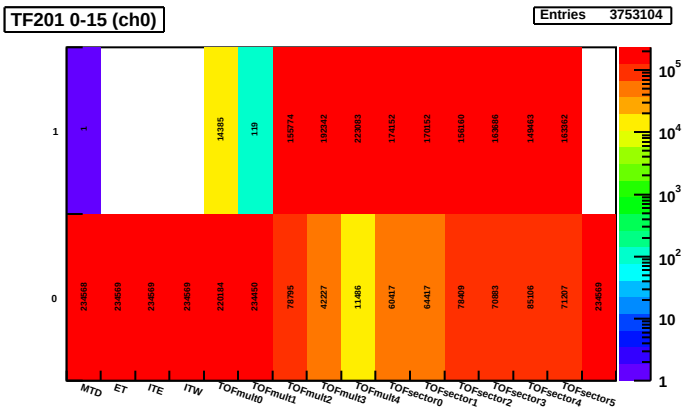

Endcap EMC L0 Input - High Tower
Entries 2.111121e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

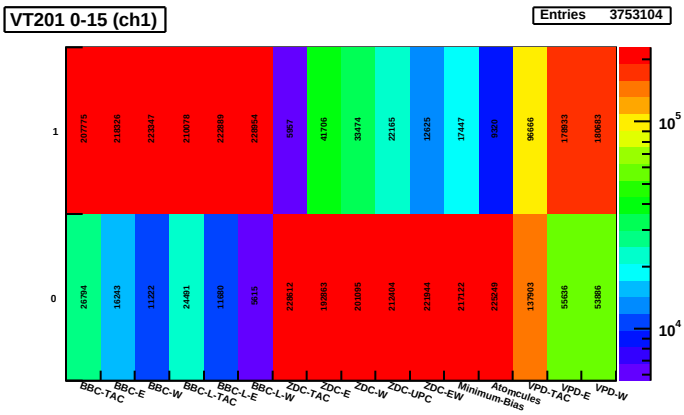

Endcap EMC L0 Input - Patch Sum
Entries 2.111121e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

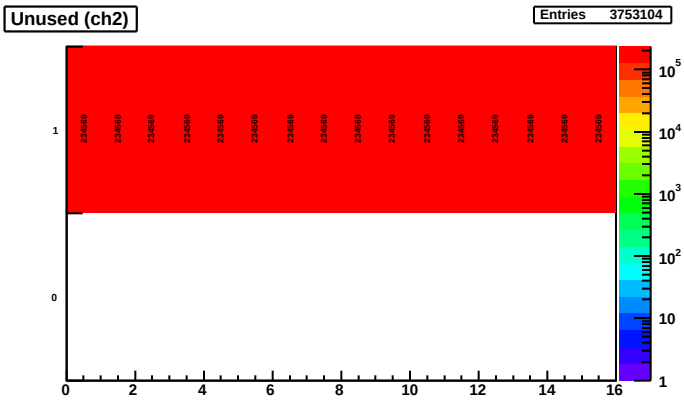

Barrel Jet Patches
Entries 4222242
JP ADC
JP ID

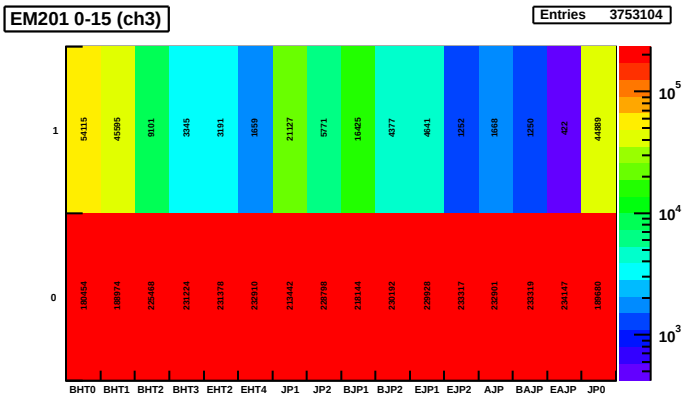

Endcap Jet Patches
Entries 1407414
JP ADC
JP ID

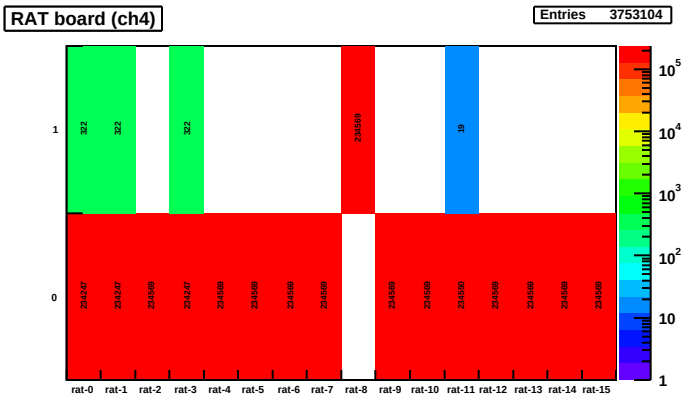

Hybrid Jet Patches
Entries 469138
JP ADC
JP ID

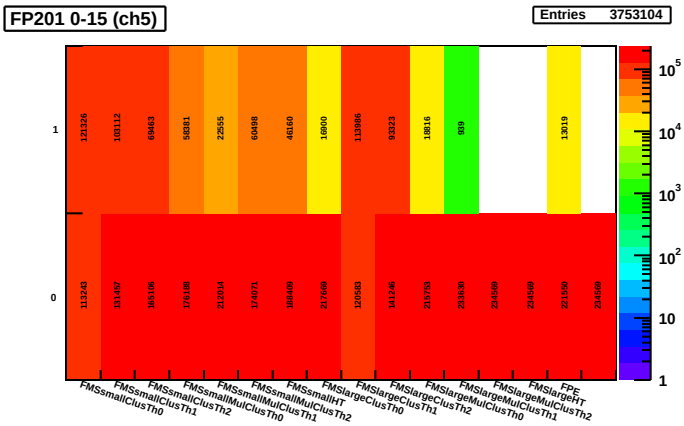

TF201 0-15 (ch0)
Entries 3753104
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

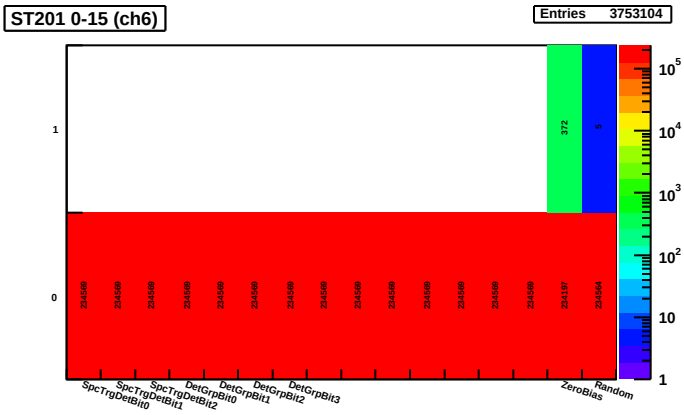

VT201 0-15 (ch1)
Entries 3753104
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

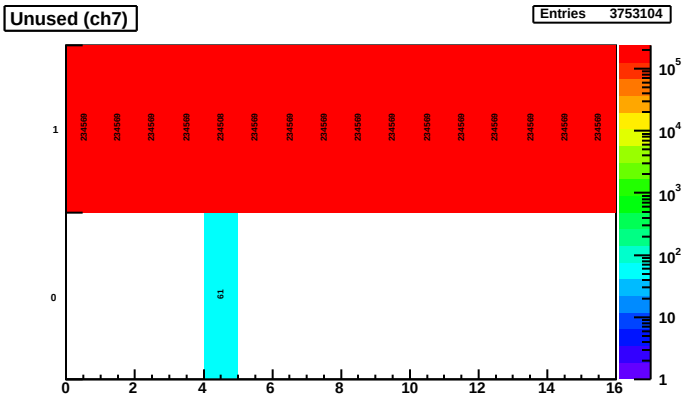

Unused (ch2)
Entries 3753104

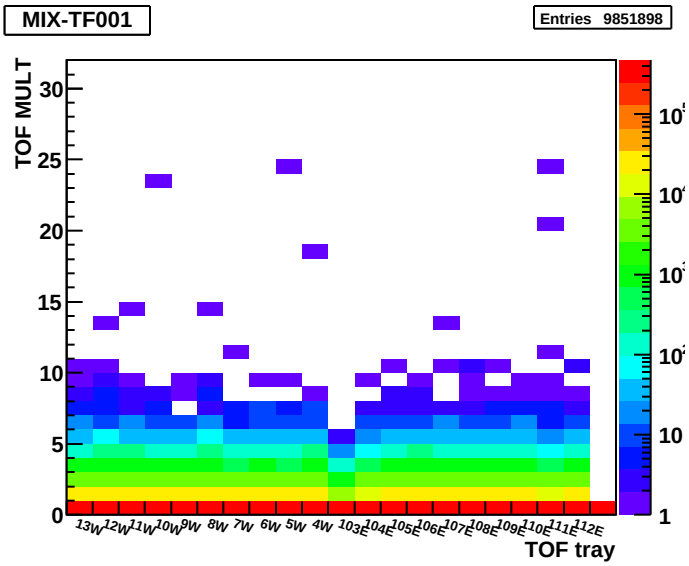

EM201 0-15 (ch3)
Entries 3753104
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

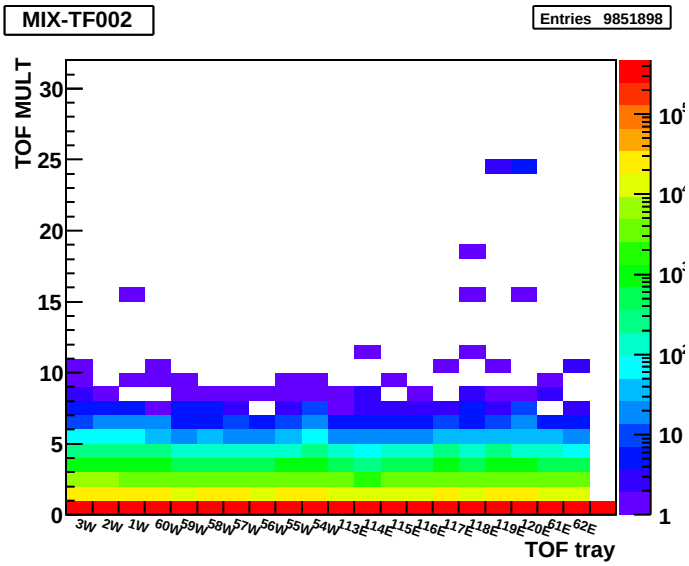

RAT board (ch4)
Entries 3753104
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

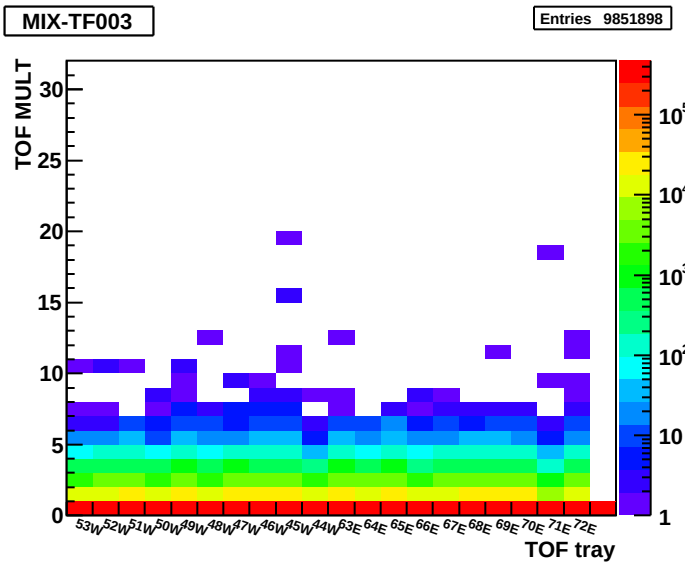

FP201 0-15 (ch5)
Entries 3753104
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

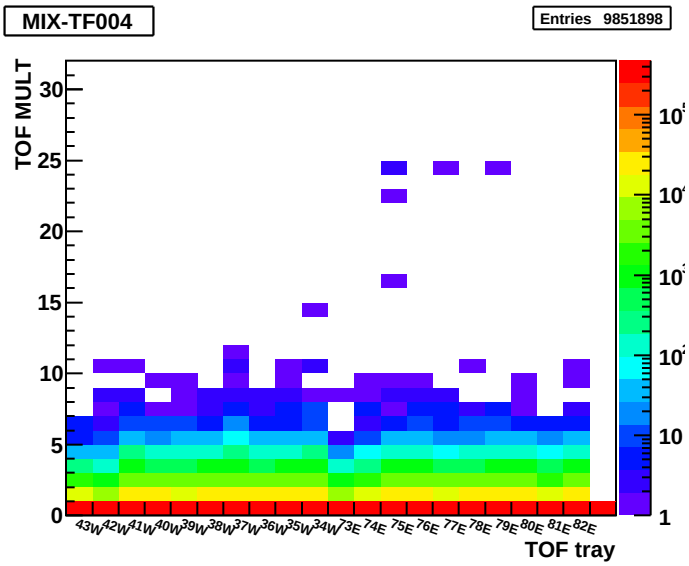

ST201 0-15 (ch6)
Entries 3753104
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

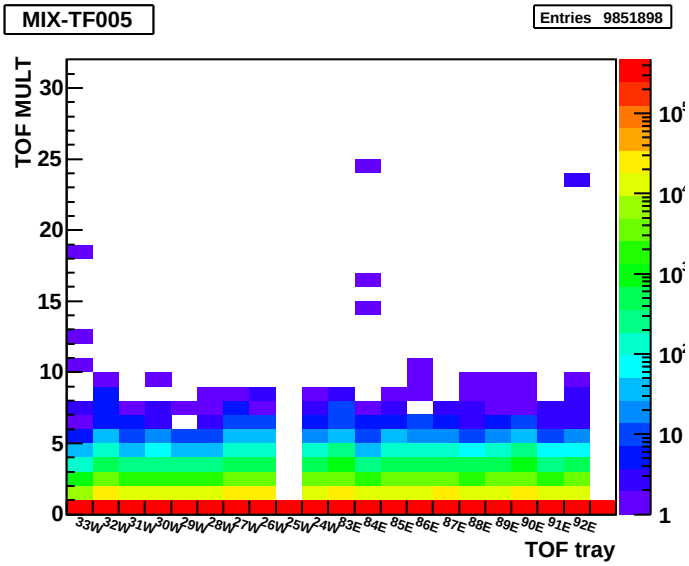

Unused (ch7)
Entries 3753104

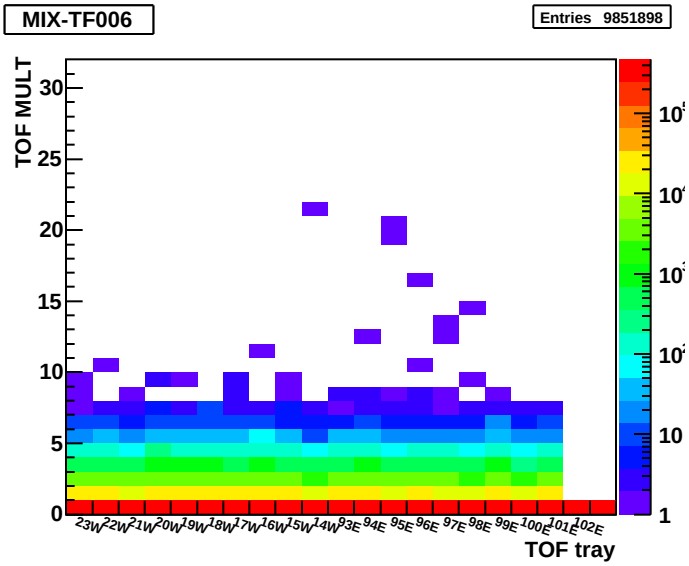

MIX-TF001
Entries 9851898
TOF MULT
TOF tray

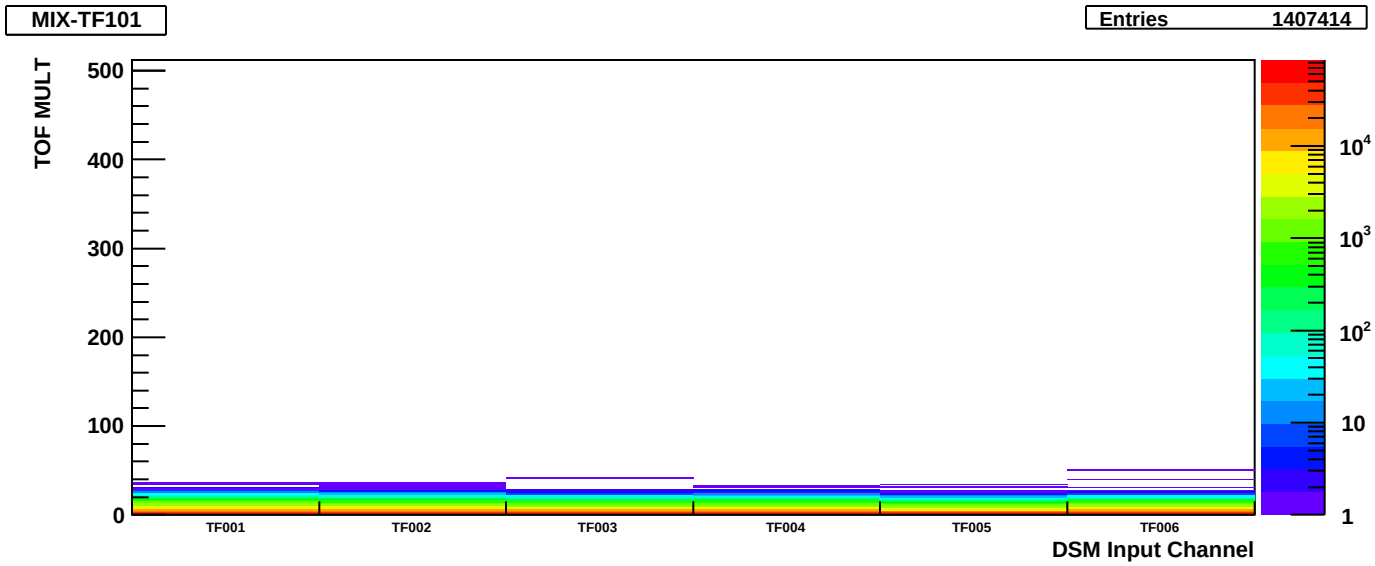

MIX-TF002
Entries 9851898
TOF MULT
TOF tray

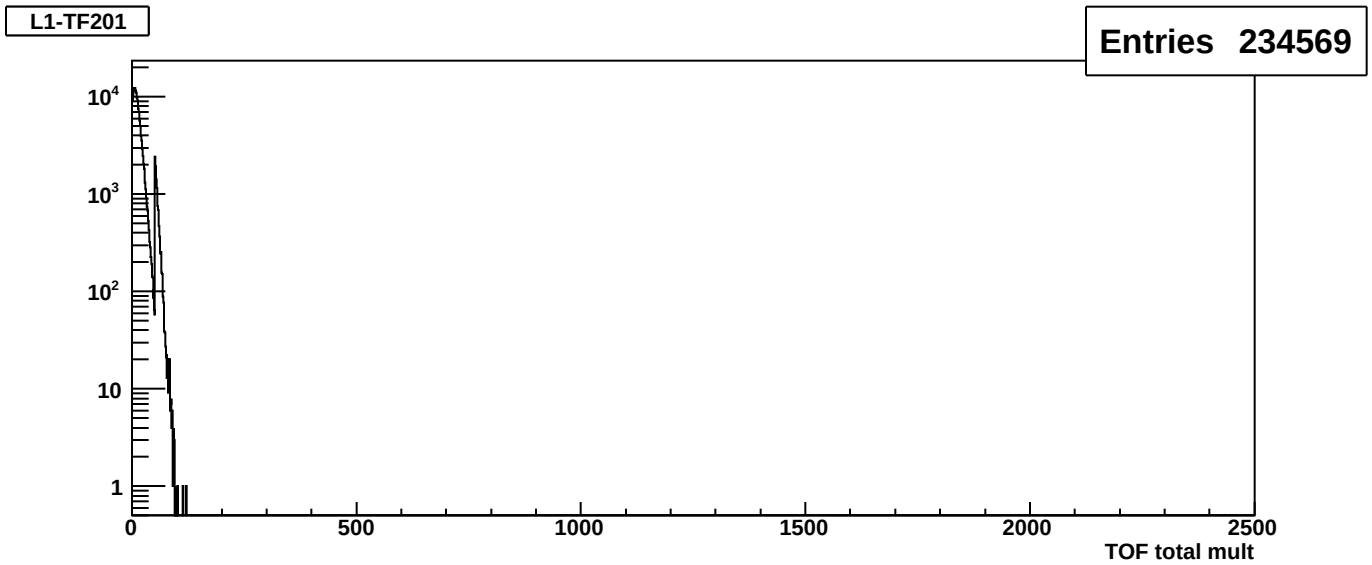

MIX-TF003
Entries 9851898
TOF MULT
TOF tray

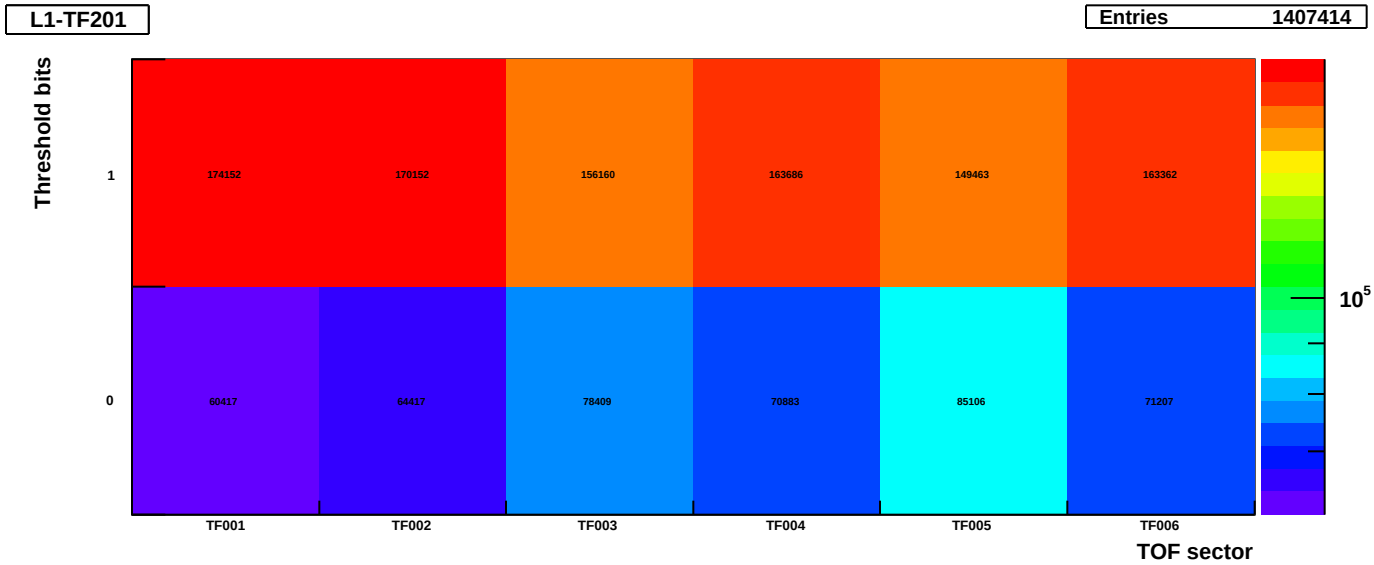

MIX-TF004
Entries 9851898
TOF MULT
TOF tray

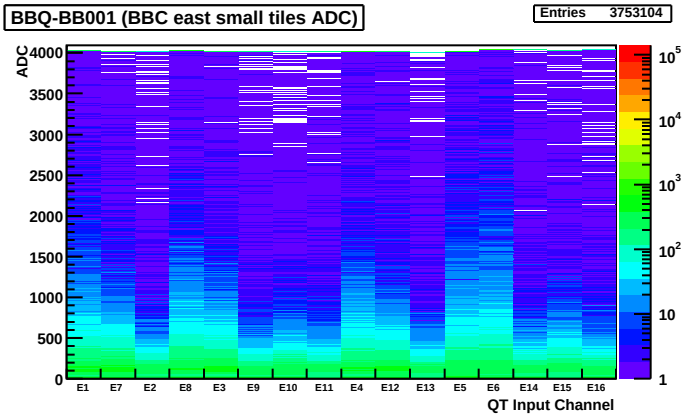

MIX-TF005
Entries 9851898
TOF MULT
TOF tray

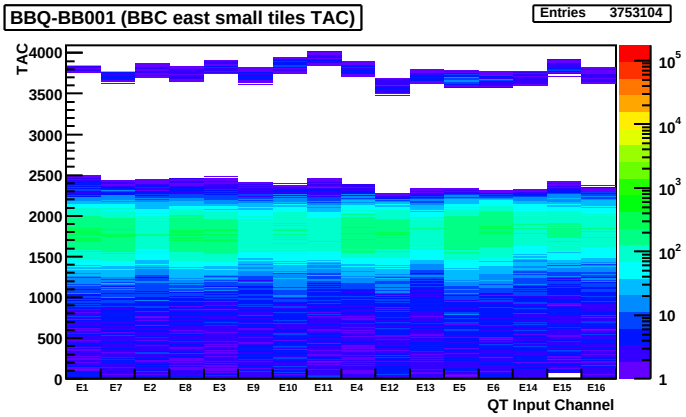

MIX-TF006
Entries 9851898
TOF MULT
TOF tray

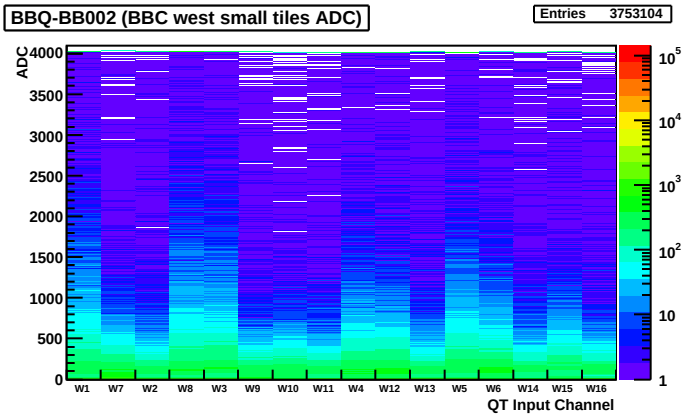

MIX-TF101
Entries 1407414
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

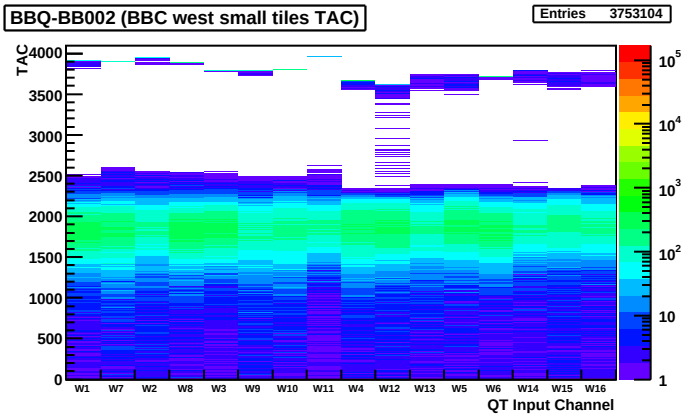

L1-TF201
Entries 234569
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

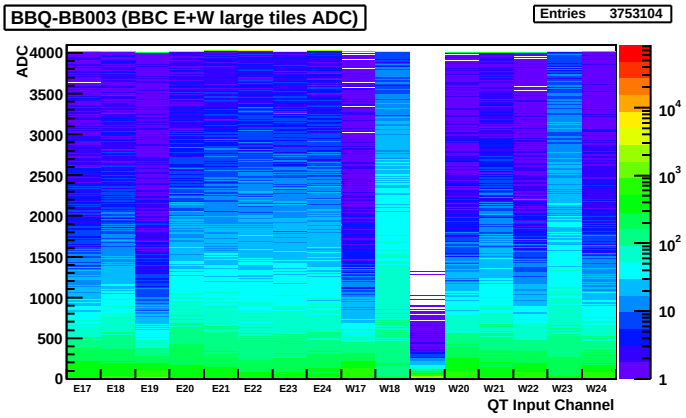

L1-TF201
Entries 1407414
Threshold bits
1
0
174152
170152
156160
163686
149463
163362
60417
64417
78409
70883
85106
71207
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

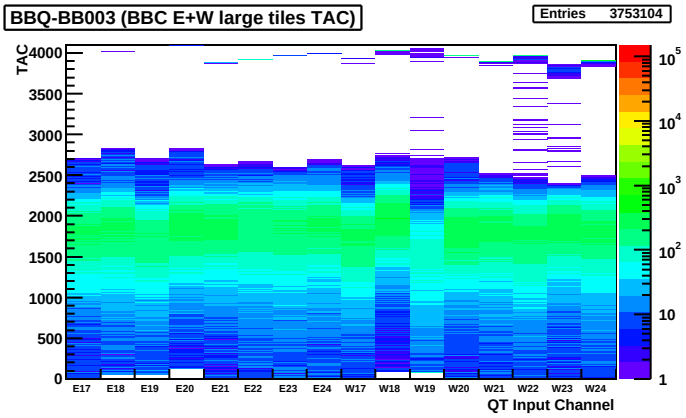

BBQ-BB001 (BBC east small tiles ADC)
Entries 3753104
ADC
QT Input Channel

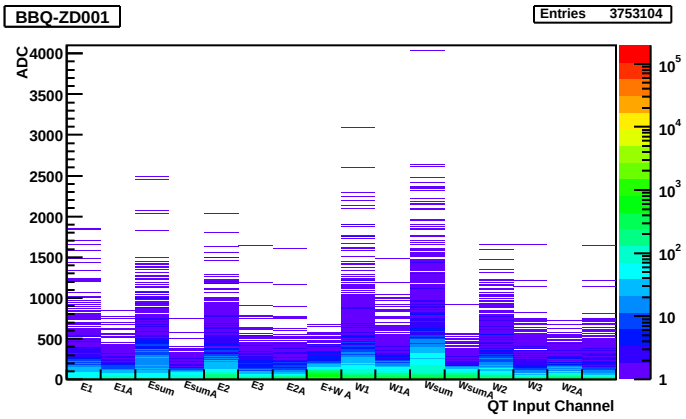

BBQ-BB001 (BBC east small tiles TAC)
Entries 3753104
TAC
QT Input Channel

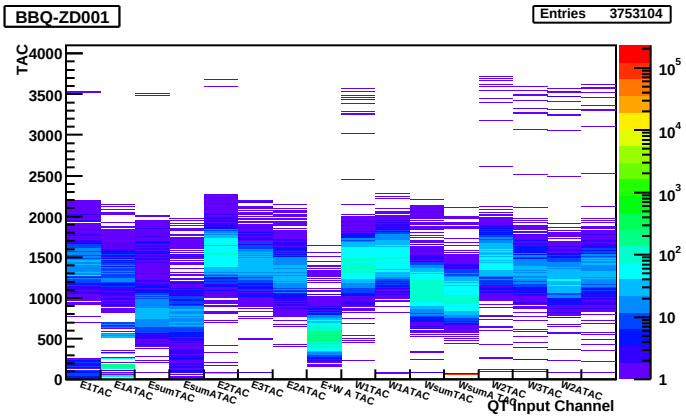

BBQ-BB002 (BBC west small tiles ADC)
Entries 3753104
ADC
QT Input Channel

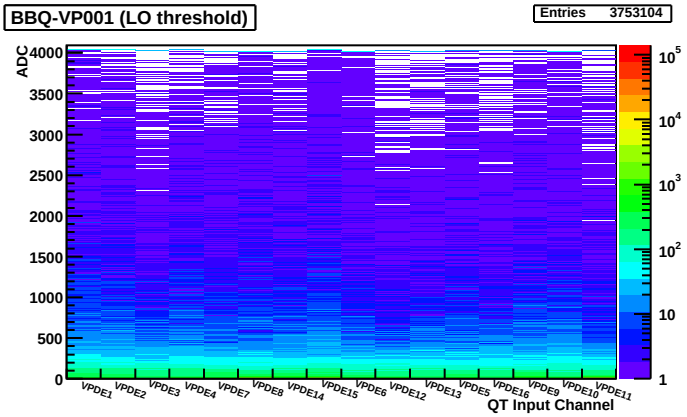

BBQ-BB002 (BBC west small tiles TAC)
Entries 3753104
TAC
QT Input Channel

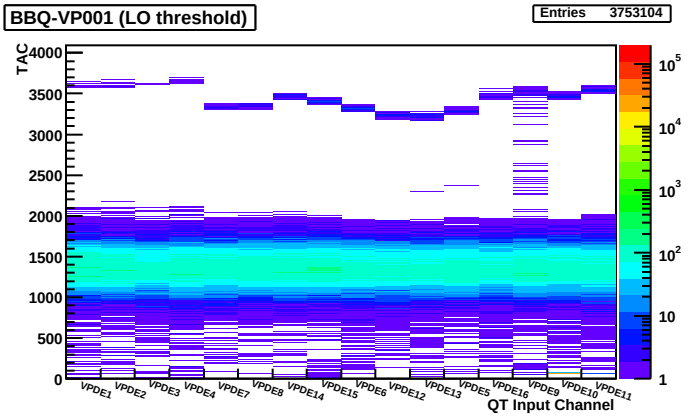

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3753104
ADC
QT Input Channel

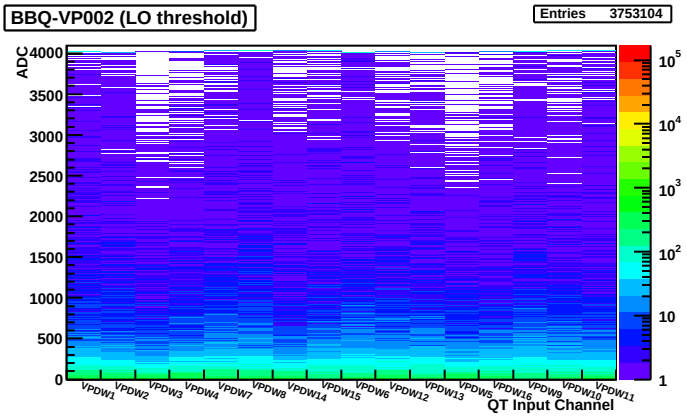

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3753104
TAC
QT Input Channel

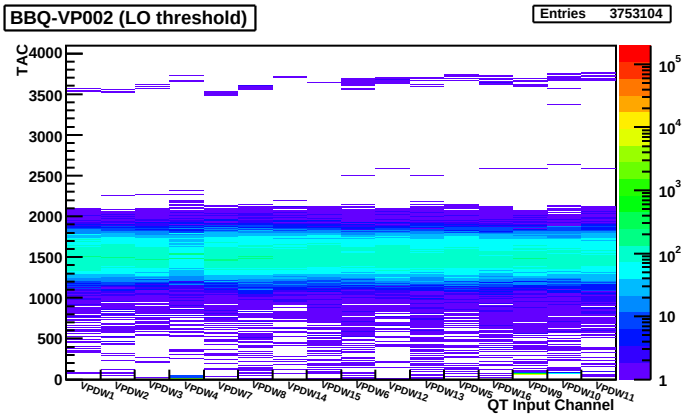

BBQ-ZD001
Entries 3753104
ADC
QT Input Channel

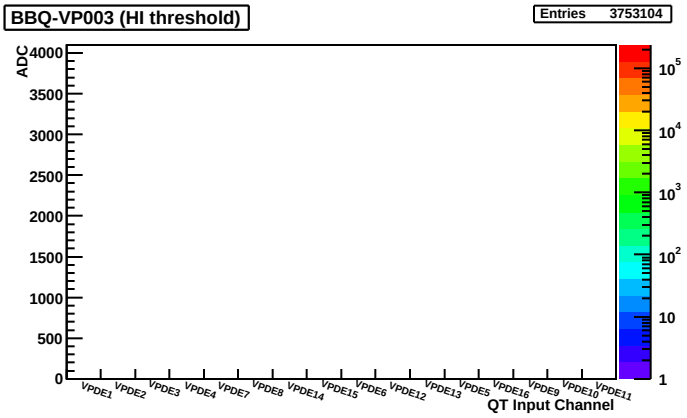

BBQ-ZD001
Entries 3753104
TAC
QT Input Channel

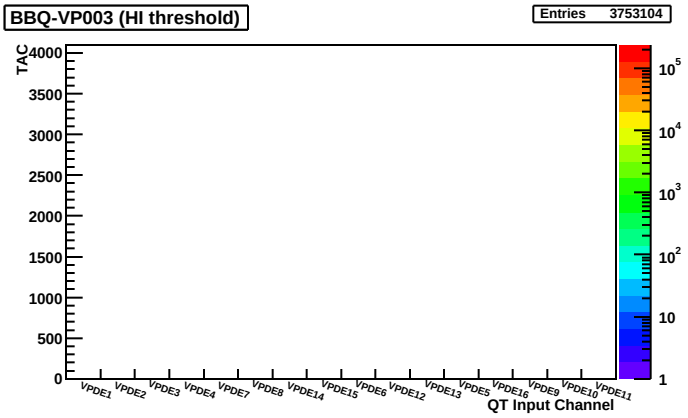

BBQ-VP001 (LO threshold)
Entries 3753104
ADC
QT Input Channel

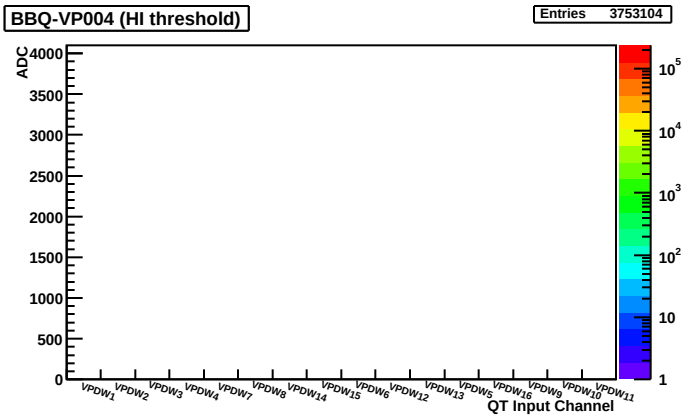

BBQ-VP001 (LO threshold)
Entries 3753104
TAC
QT Input Channel

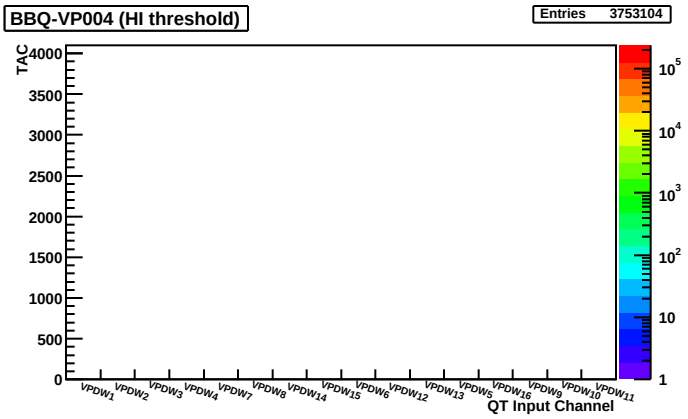

BBQ-VP002 (LO threshold)
Entries 3753104
ADC
QT Input Channel

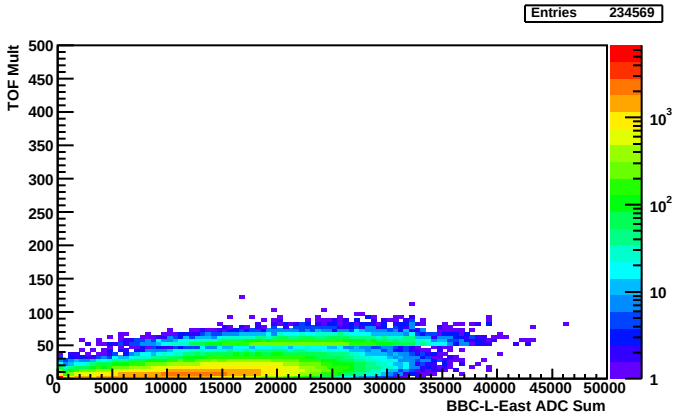

BBQ-VP002 (LO threshold)
Entries 3753104
TAC
QT Input Channel

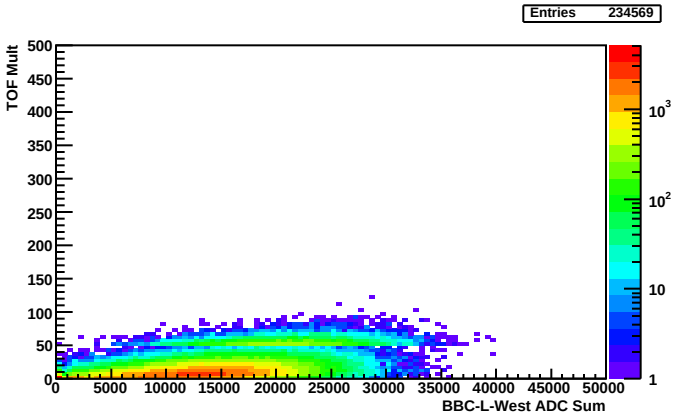

BBQ-VP003 (HI threshold)
Entries 3753104
ADC
QT Input Channel

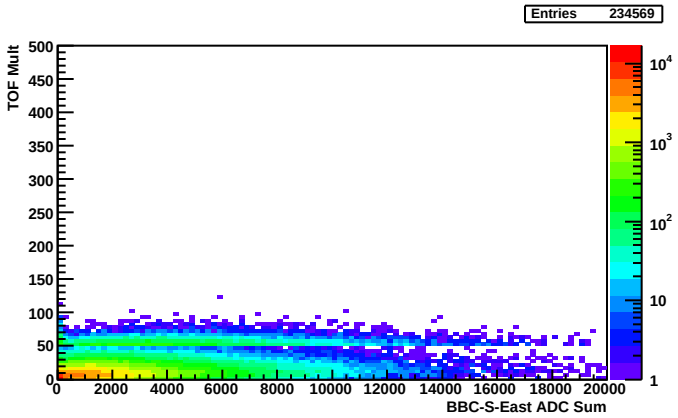

BBQ-VP003 (HI threshold)
Entries 3753104
TAC
QT Input Channel

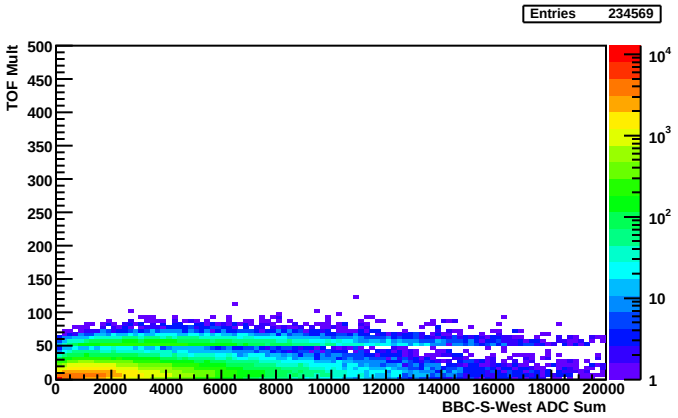

BBQ-VP004 (HI threshold)
Entries 3753104
ADC
QT Input Channel

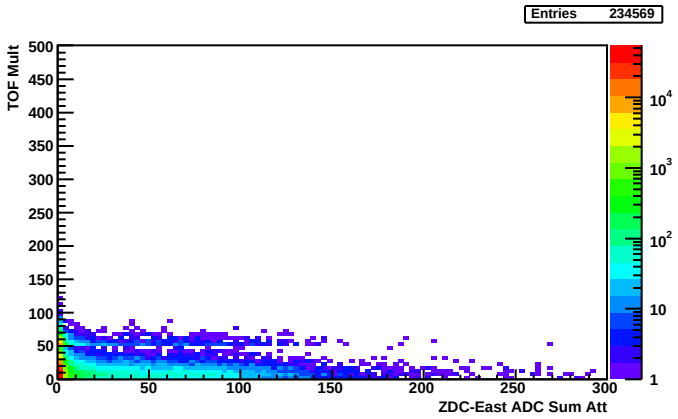

BBQ-VP004 (HI threshold)
Entries 3753104
TAC
QT Input Channel

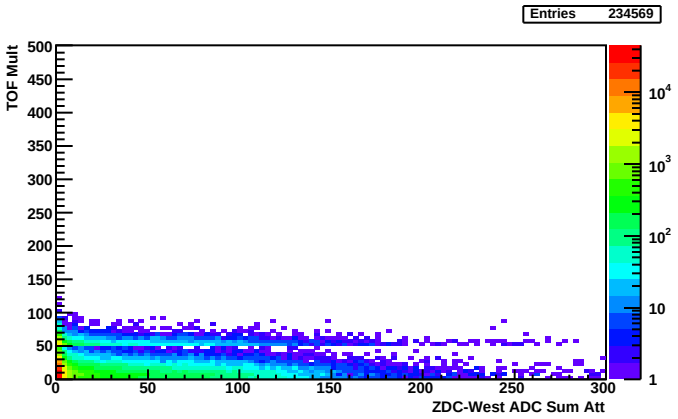

Entries 234569
TOF Mult
BBC-L-East ADC Sum

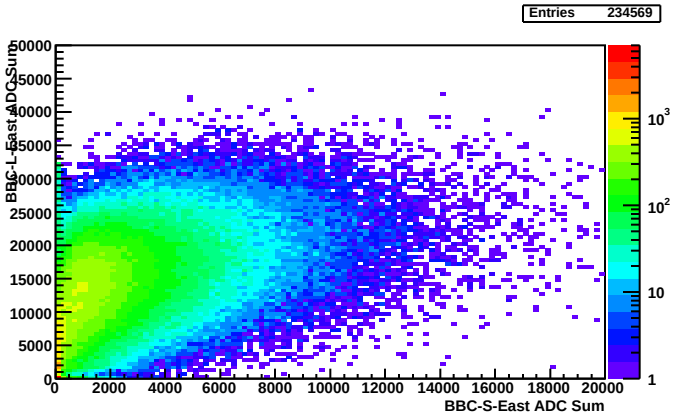

Entries 234569
TOF Mult
BBC-L-West ADC Sum

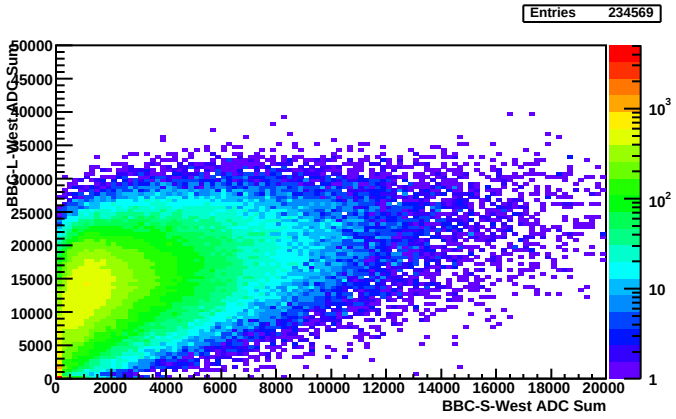

Entries 234569
TOF Mult
BBC-S-East ADC Sum

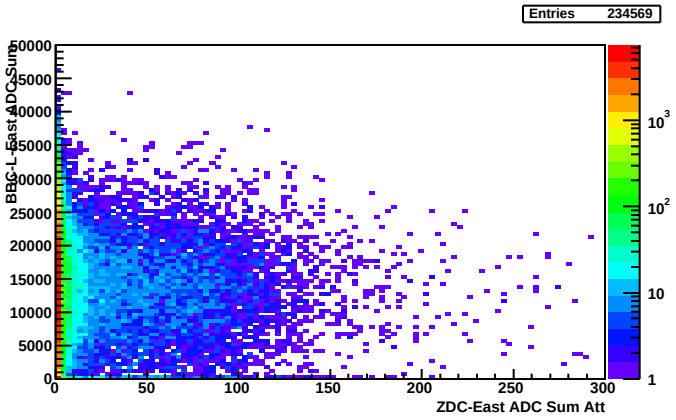

Entries 234569
TOF Mult
BBC-S-West ADC Sum

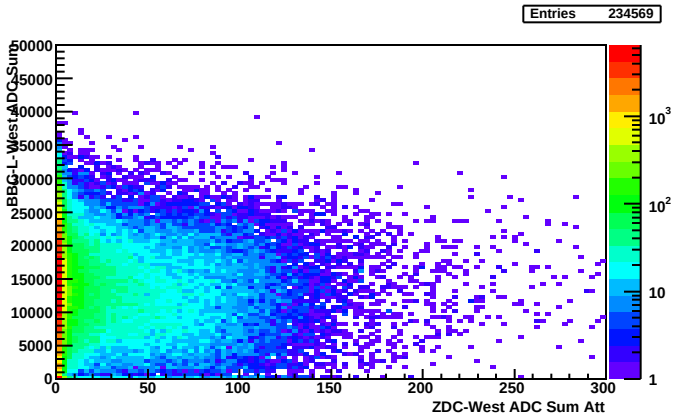

Entries 234569
TOF Mult
ZDC-East ADC Sum Att

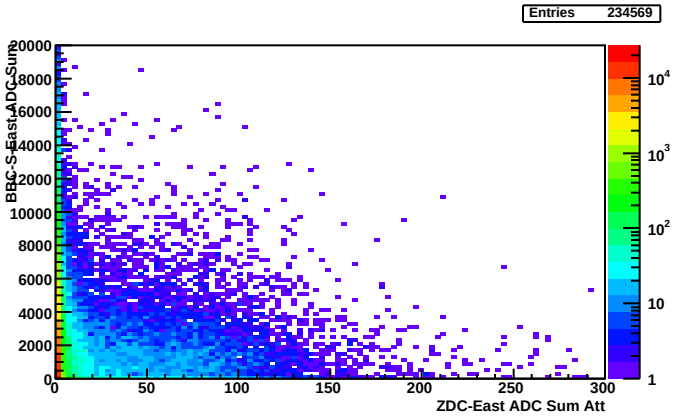

Entries 234569
TOF Mult
ZDC-West ADC Sum Att

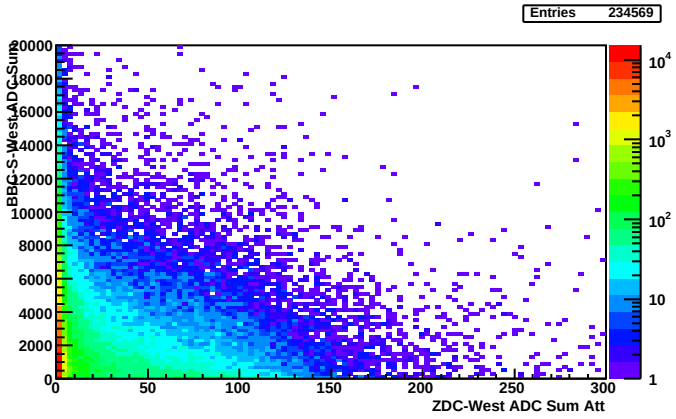

Entries 234569
BBC-L-East ADC Sum
BBC-S-East ADC Sum

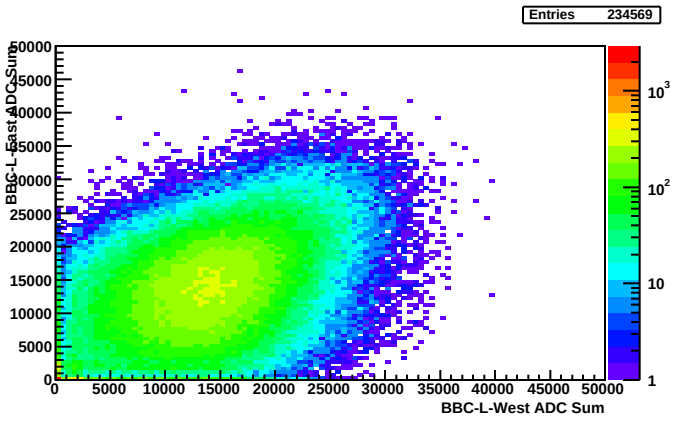

Entries 234569
BBC-L-West ADC Sum
BBC-S-West ADC Sum

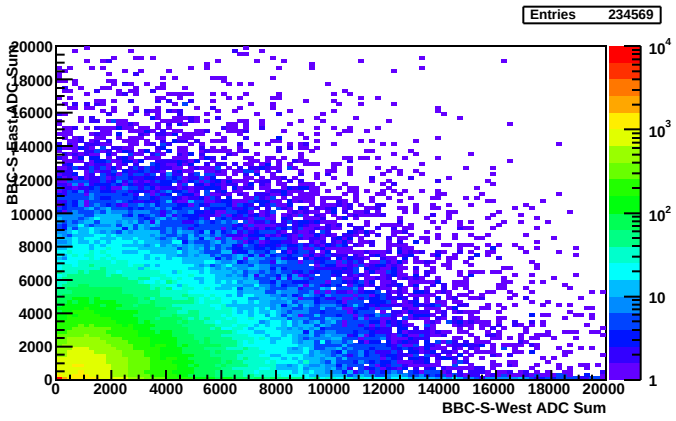

Entries 234569
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

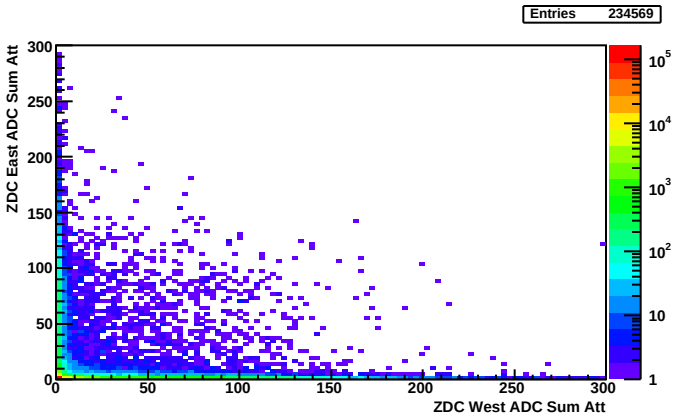

Entries 234569
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

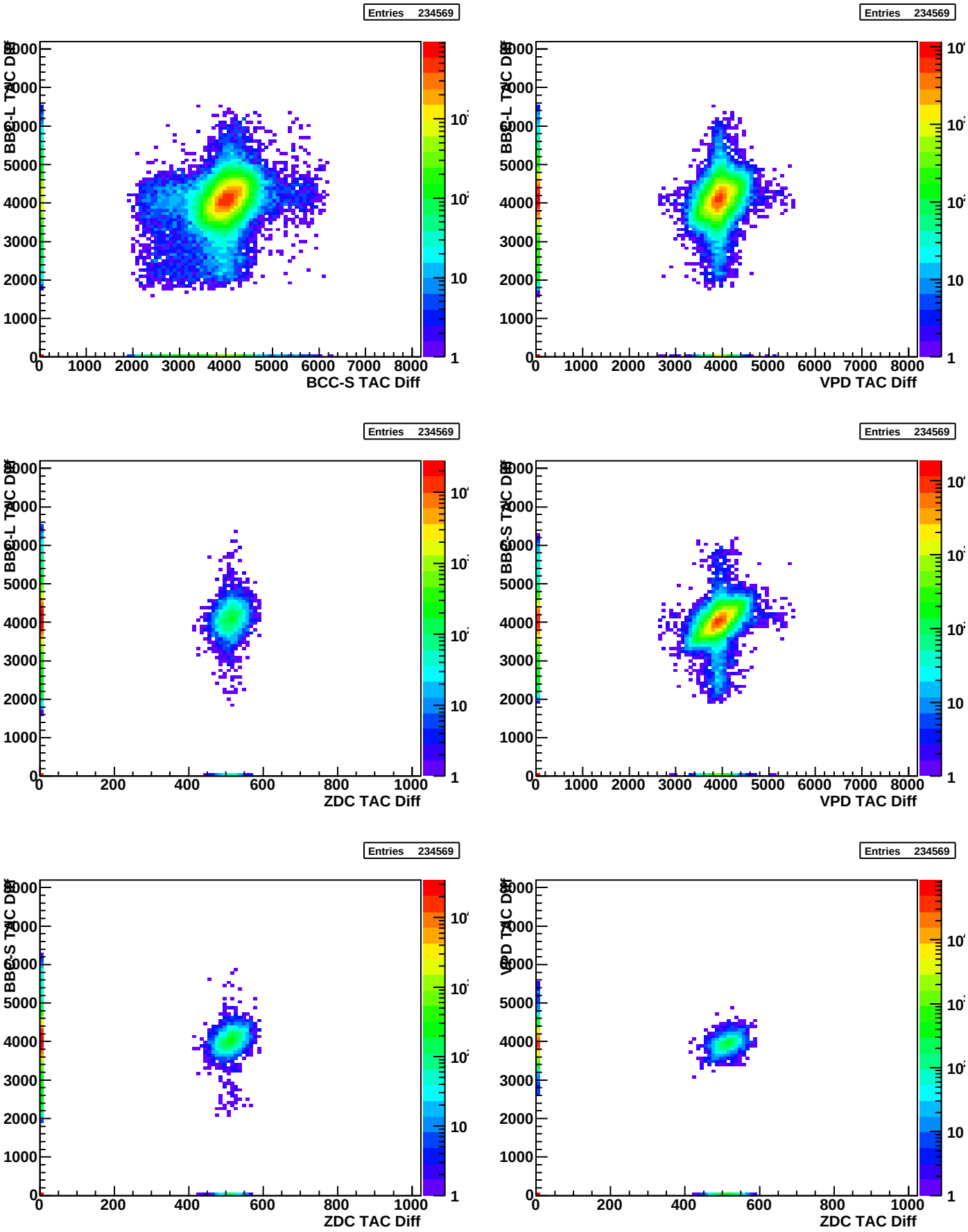

Entries 234569
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

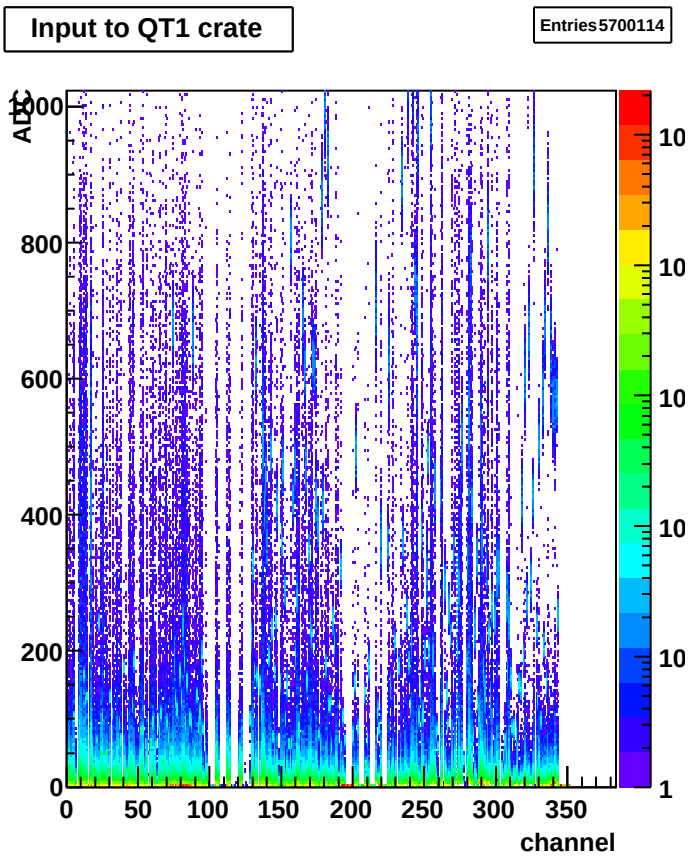

Entries 234569
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

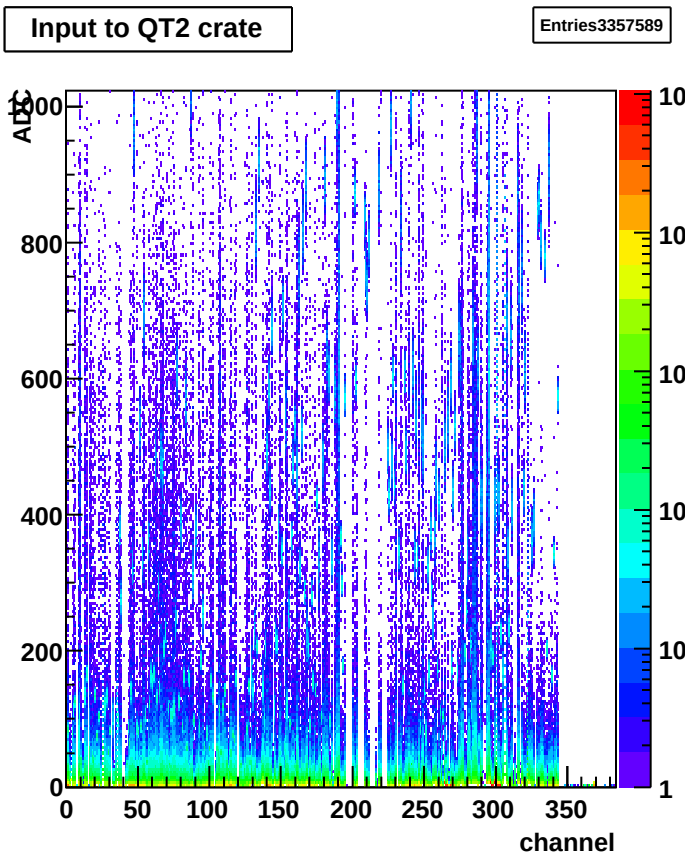

Entries 234569
BBC-L-East ADC Sum
BBC-L-West ADC Sum

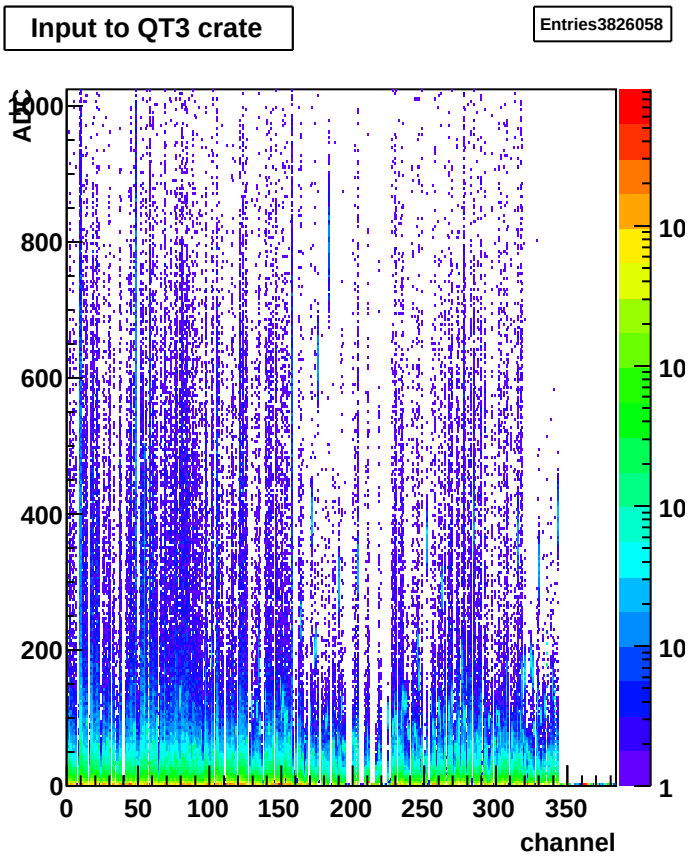

Entries 234569
BBC-S-East ADC Sum
BBC-S-West ADC Sum

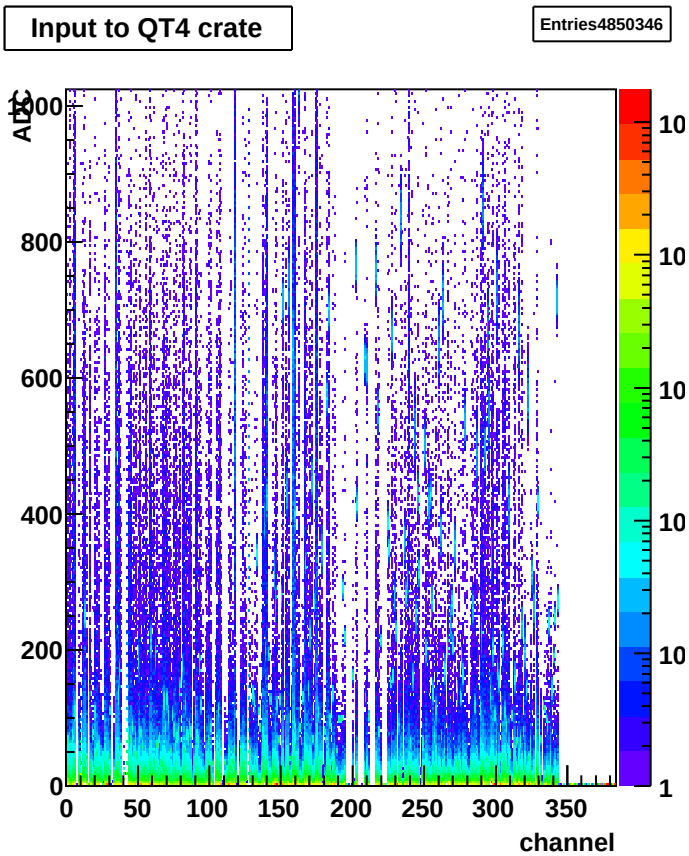

Entries 234569
ZDC East ADC Sum Att
ZDC West ADC Sum Att

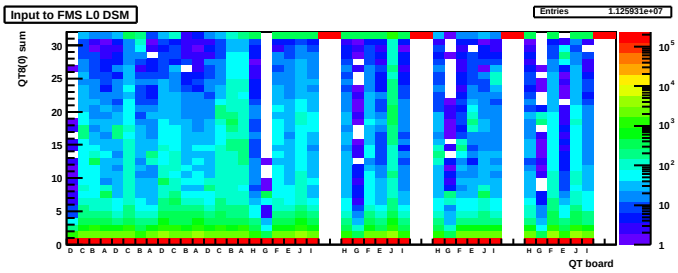

Entries 234569
BBC-L TAC Diff
BCC-S TAC Diff
Entries 234569
BBC-L TAC Diff
VPD TAC Diff
Entries 234569
BBC-L TAC Diff
ZDC TAC Diff
Entries 234569
BBC-S TAC Diff
VPD TAC Diff
Entries 234569
BBC-S TAC Diff
ZDC TAC Diff
Entries 234569
VPD TAC Diff
ZDC TAC Diff

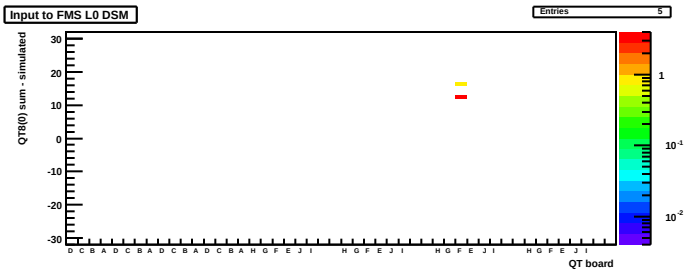

Input to QT1 crate
Entries5700114
ADC
channel

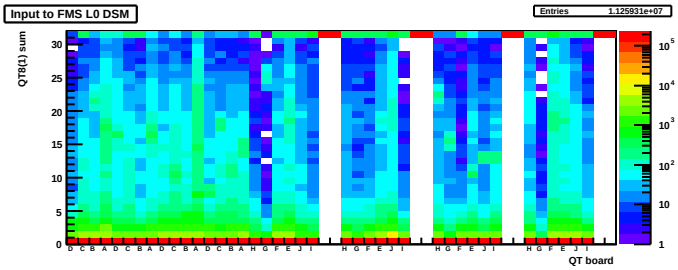

Input to QT2 crate
Entries3357589
ADC
channel

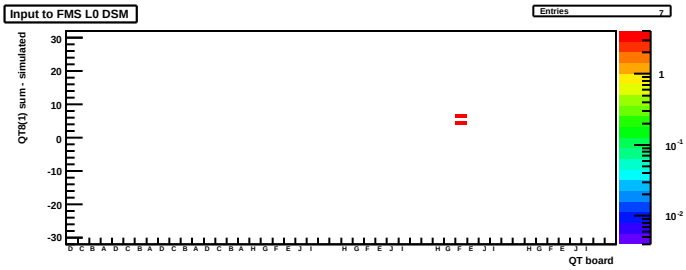

Input to QT3 crate
Entries3826058
ADC
channel

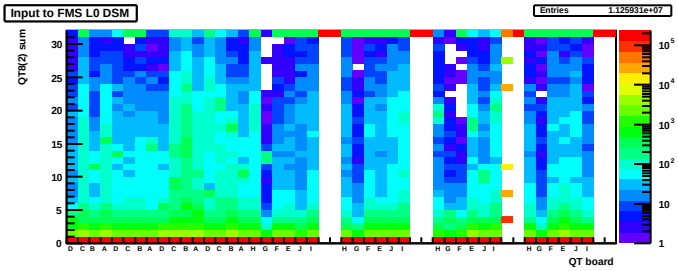

Input to QT4 crate
Entries4850346
ADC
channel

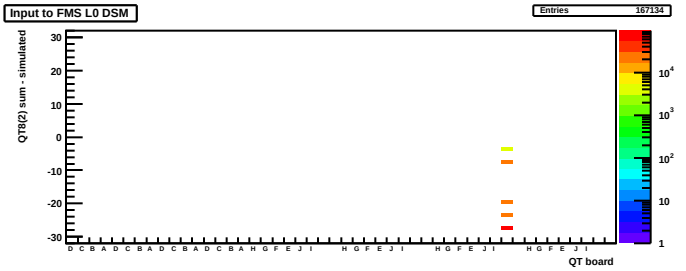

Input to FMS L0 DSM
Entries 1.125931e+07
QT8(0) sum
QT board

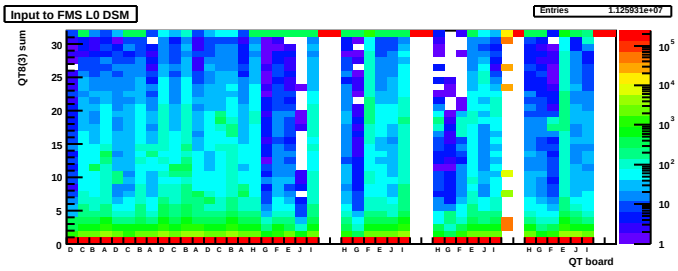

Input to FMS L0 DSM
Entries 5
QT8(0) sum - simulated
QT board

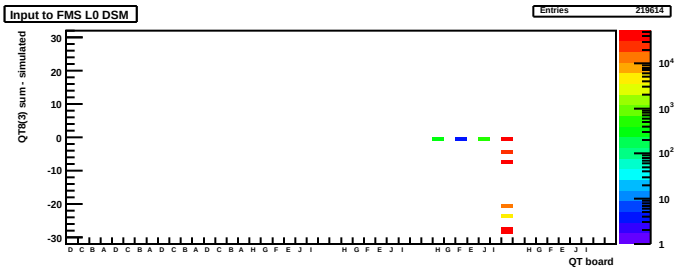

Input to FMS L0 DSM
Entries 1.125931e+07
QT8(1) sum
QT board

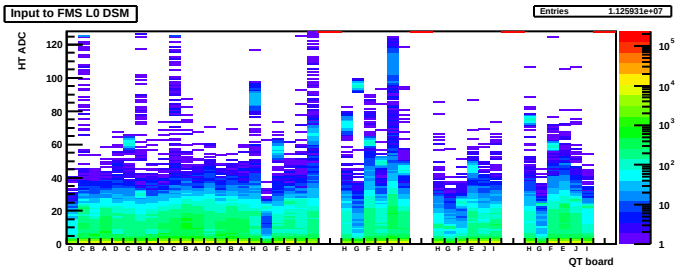

Input to FMS L0 DSM
Entries 7
QT8(1) sum - simulated
QT board

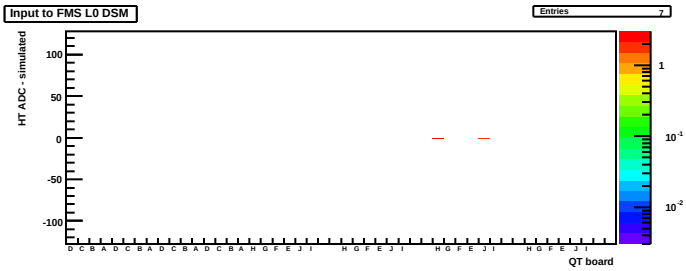

Input to FMS L0 DSM
Entries 1.125931e+07
QT8(2) sum
QT board

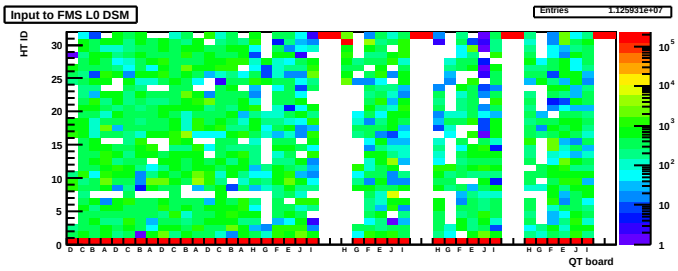

Input to FMS L0 DSM
Entries 167134
QT8(2) sum - simulated
QT board

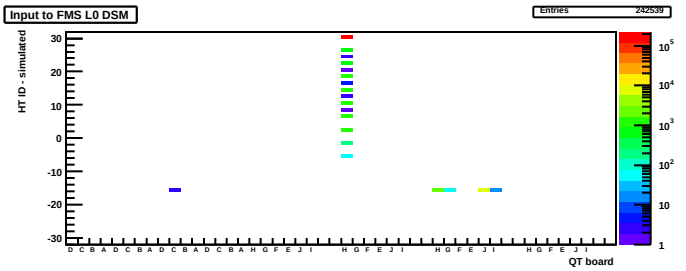

Input to FMS L0 DSM
Entries 1.125931e+07
QT8(3) sum
QT board

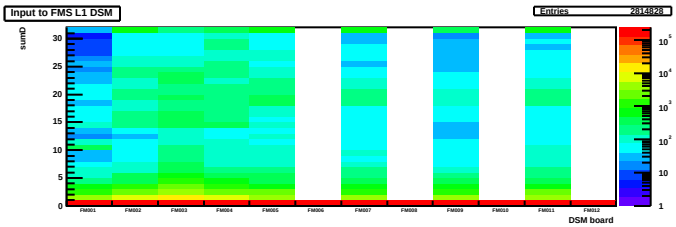

Input to FMS L0 DSM
Entries 219614
QT8(3) sum - simulated
QT board

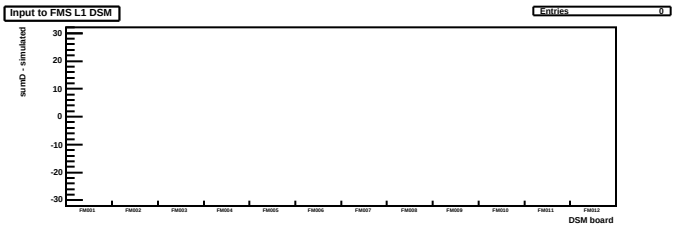

Input to FMS L0 DSM
Entries 1.125931e+07
HT ADC
QT board

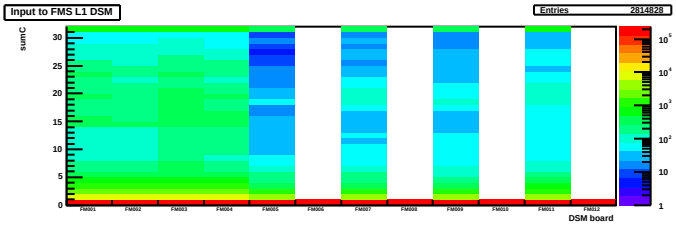

Input to FMS L0 DSM
Entries 7
HT ADC - simulated
QT board

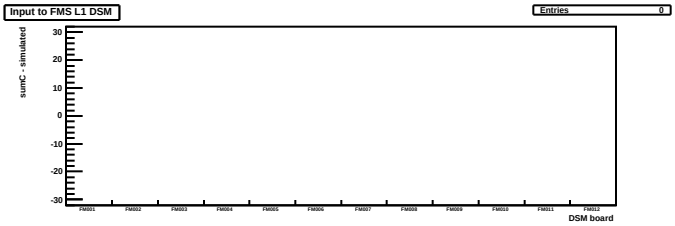

Input to FMS L0 DSM
Entries 1.125931e+07
HT ID
QT board

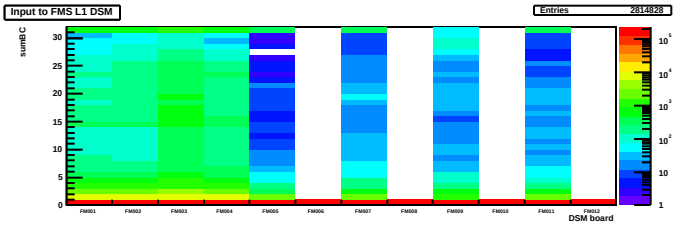

Input to FMS L0 DSM
Entries 242539
HT ID - simulated
QT board

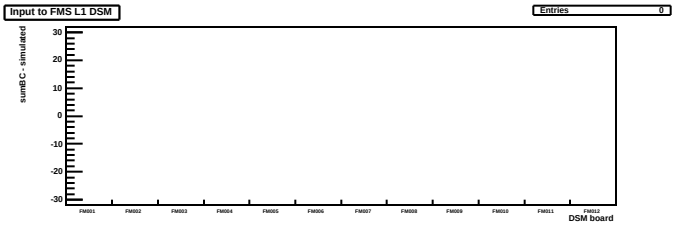

Input to FMS L1 DSM
Entries 2814828
sumD
DSM board

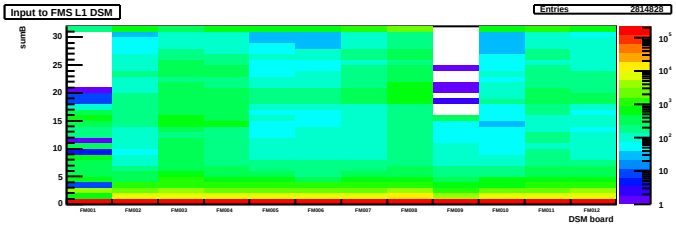

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

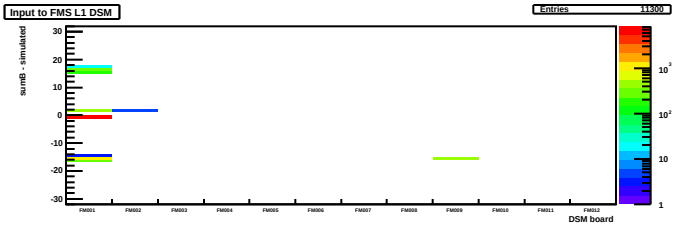

Input to FMS L1 DSM
Entries 2814828
sumC
DSM board

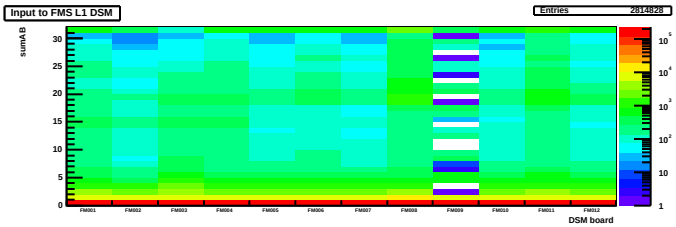

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

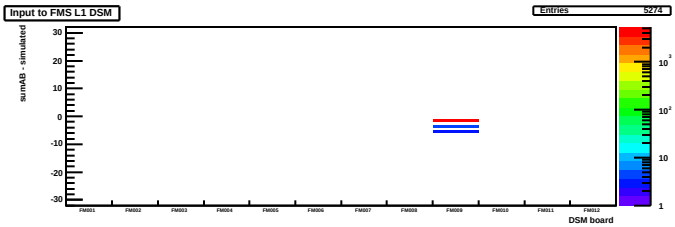

Input to FMS L1 DSM
Entries 2814828
sumBC
DSM board

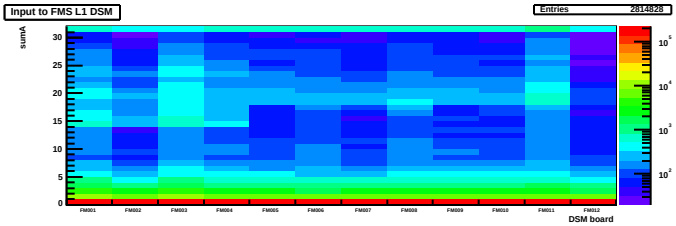

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

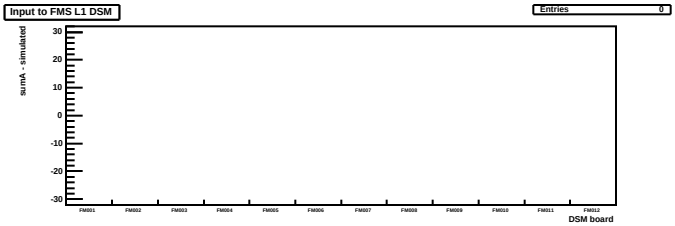

Input to FMS L1 DSM
Entries 2814828
sumB
DSM board

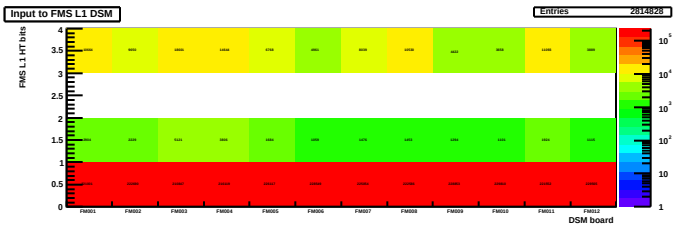

Input to FMS L1 DSM
Entries 11300
sumB - simulated
DSM board

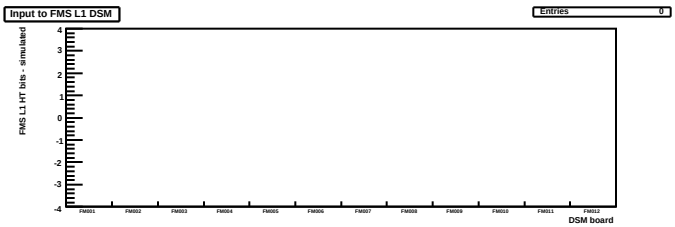

Input to FMS L1 DSM
Entries 2814828
sumAB
DSM board

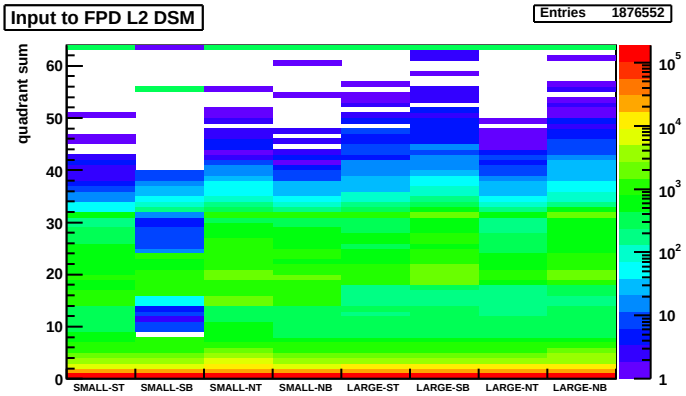

Input to FMS L1 DSM
Entries 5274
sumAB - simulated
DSM board

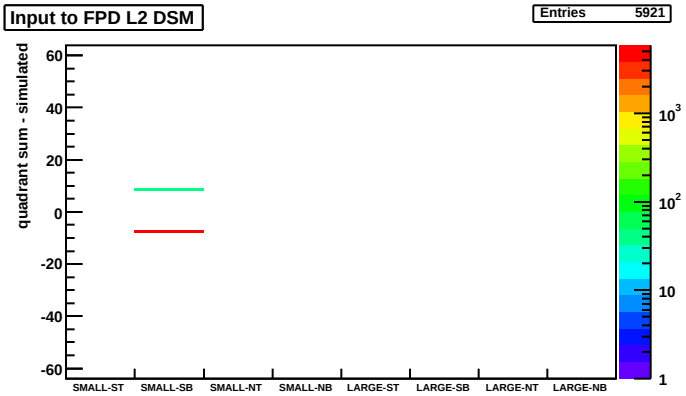

Input to FMS L1 DSM
Entries 2814828
sumA
DSM board

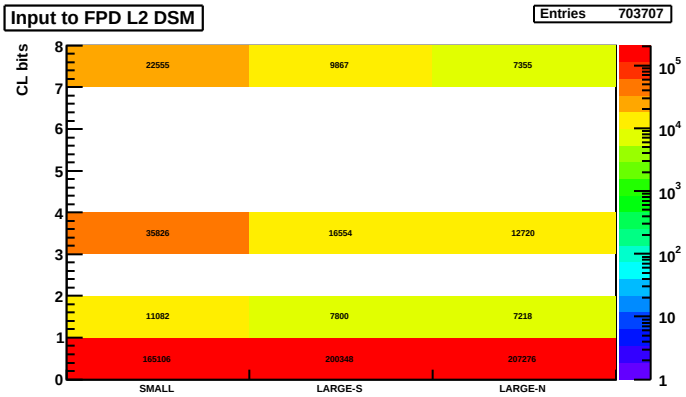

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

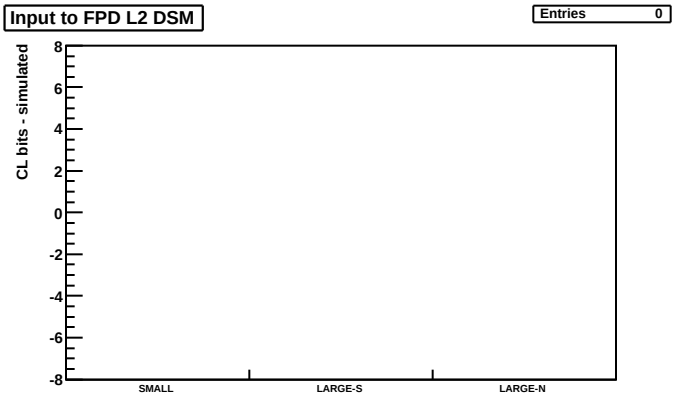

Input to FMS L1 DSM
Entries 2814828
FMS L1 HT bits
DSM board

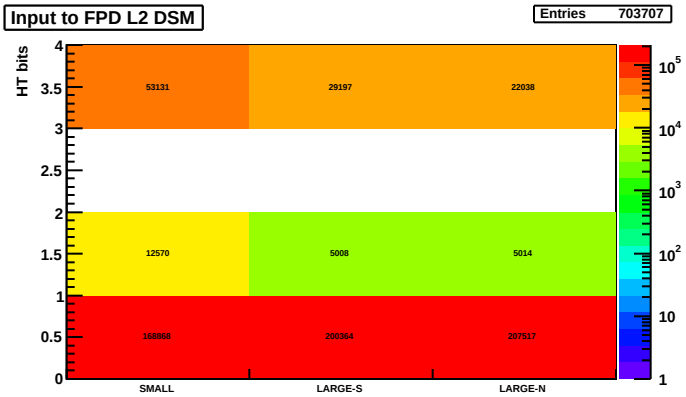

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

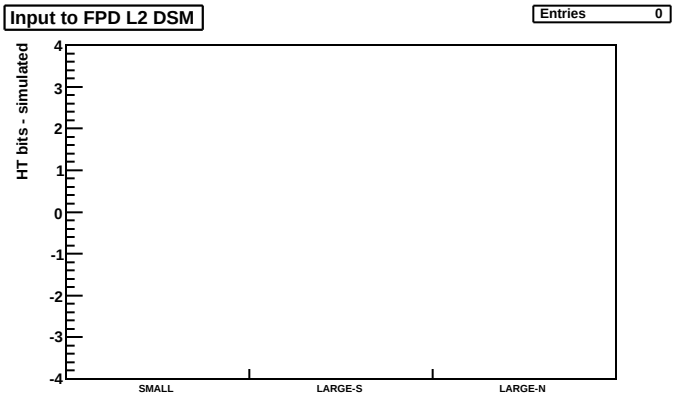

Input to FPD L2 DSM
Entries 1876552
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

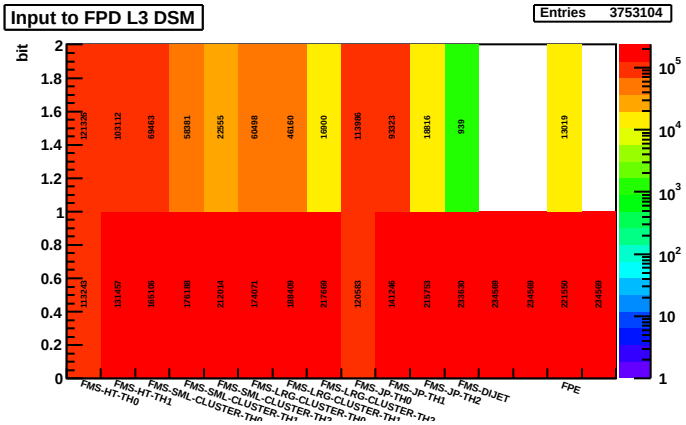

Input to FPD L2 DSM
Entries 5921
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

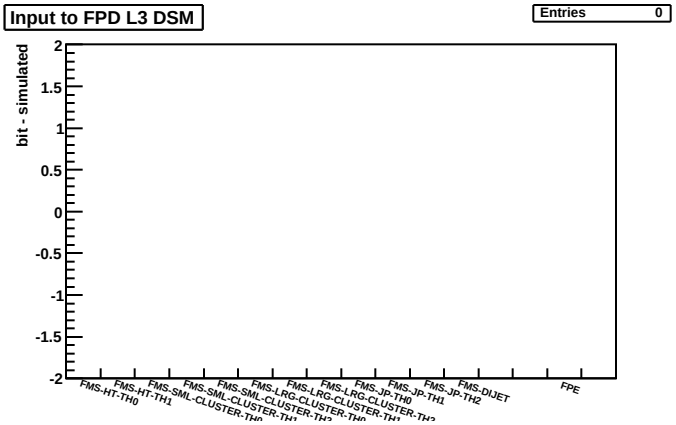

Input to FPD L2 DSM
Entries 703707
CL bits
22555
9867
7355
35826
16554
12720
11082
7800
7218
165106
200348
207276
SMALL
LARGE-S
LARGE-N

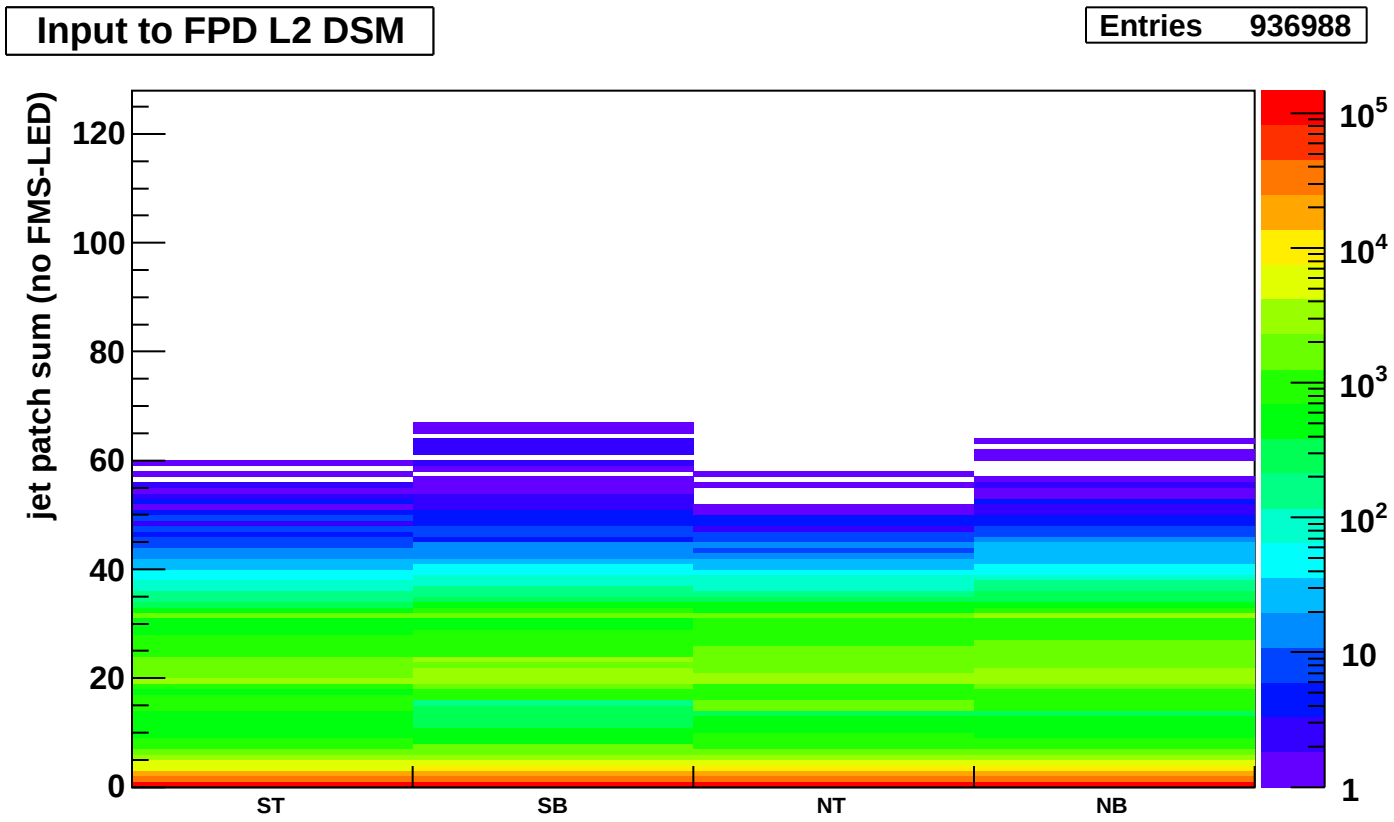

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

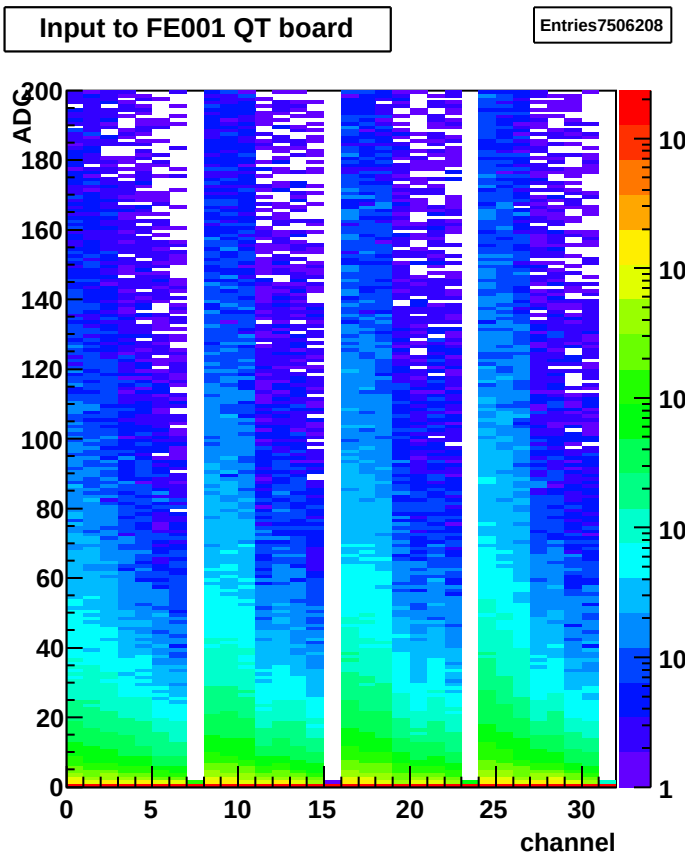

Input to FPD L2 DSM
Entries 703707
HT bits
53131
29197
22038
12570
5008
5014
168868
200364
207517
SMALL
LARGE-S
LARGE-N

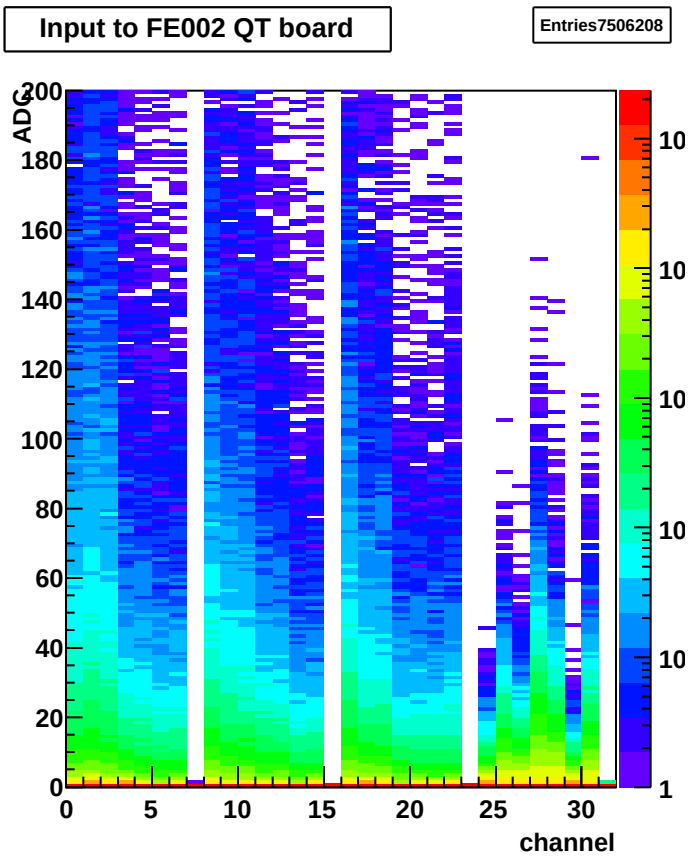

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

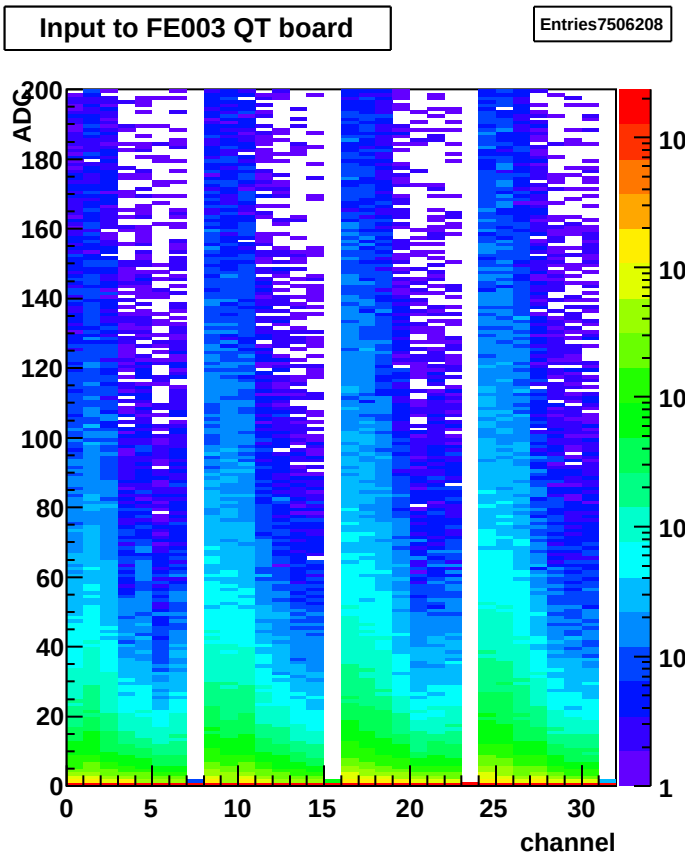

Input to FPD L3 DSM
Entries 3753104
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

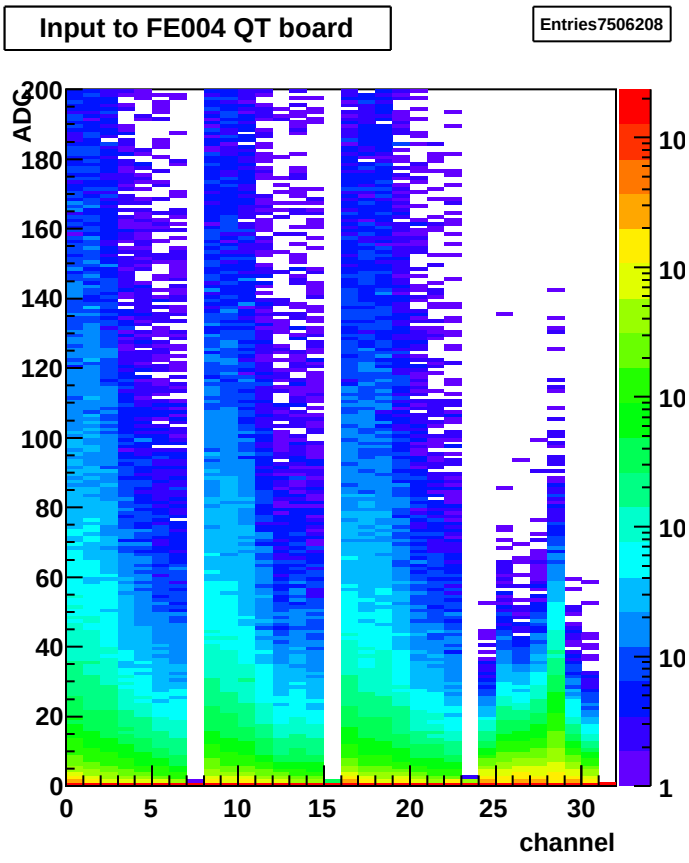

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

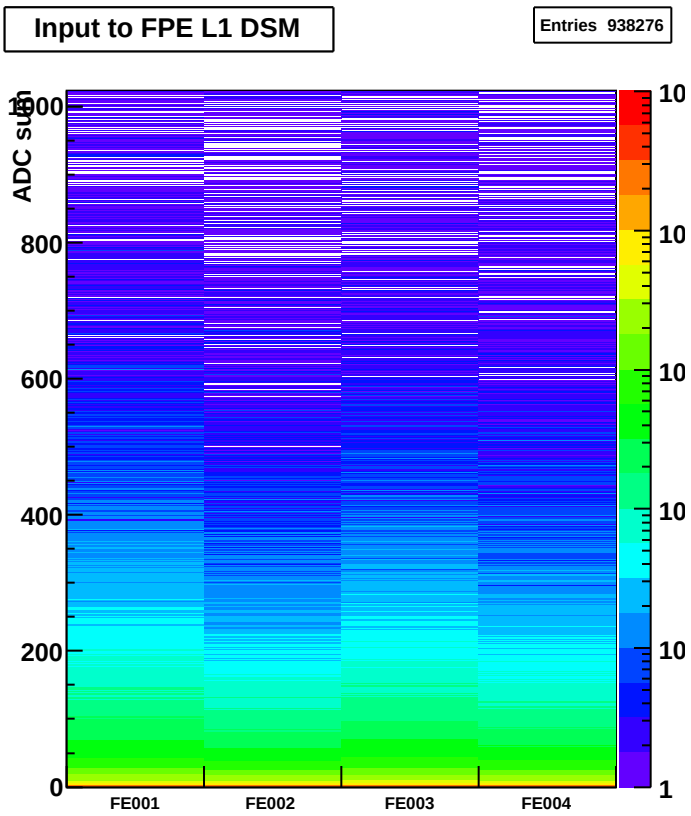

Input to FPD L2 DSM
Entries 936988
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

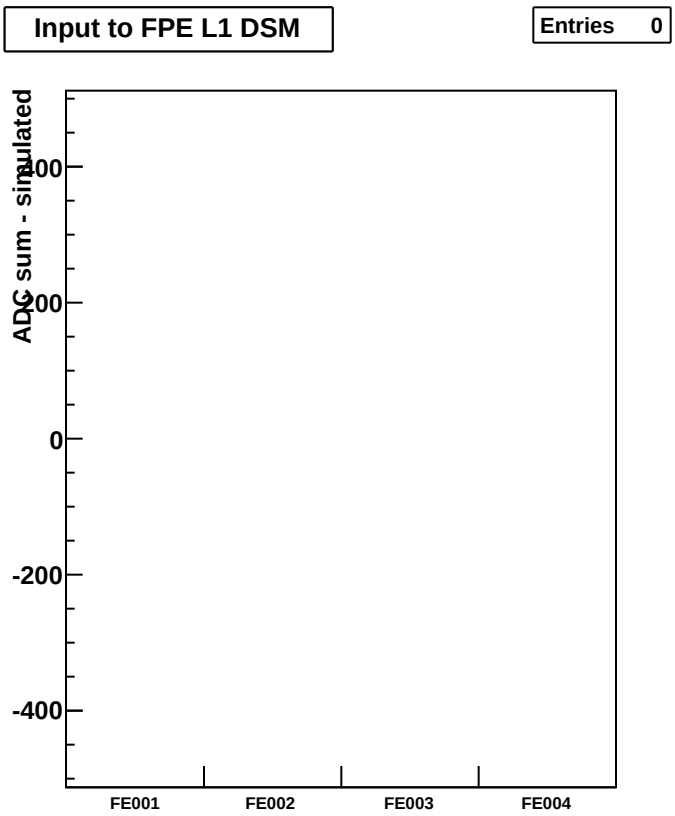

Input to FE001 QT board
Entries7506208
ADC
channel

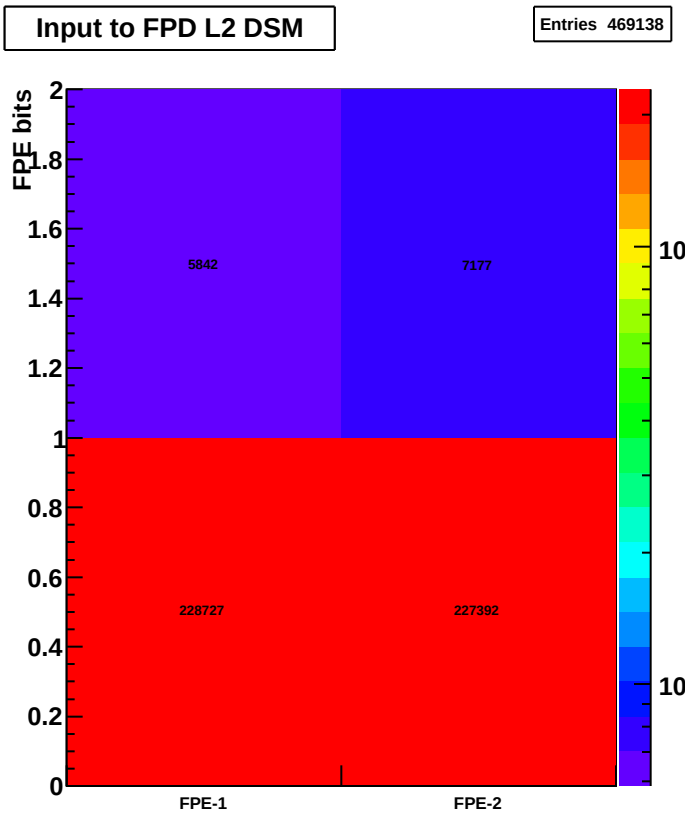

Input to FE002 QT board
Entries7506208
ADC
channel

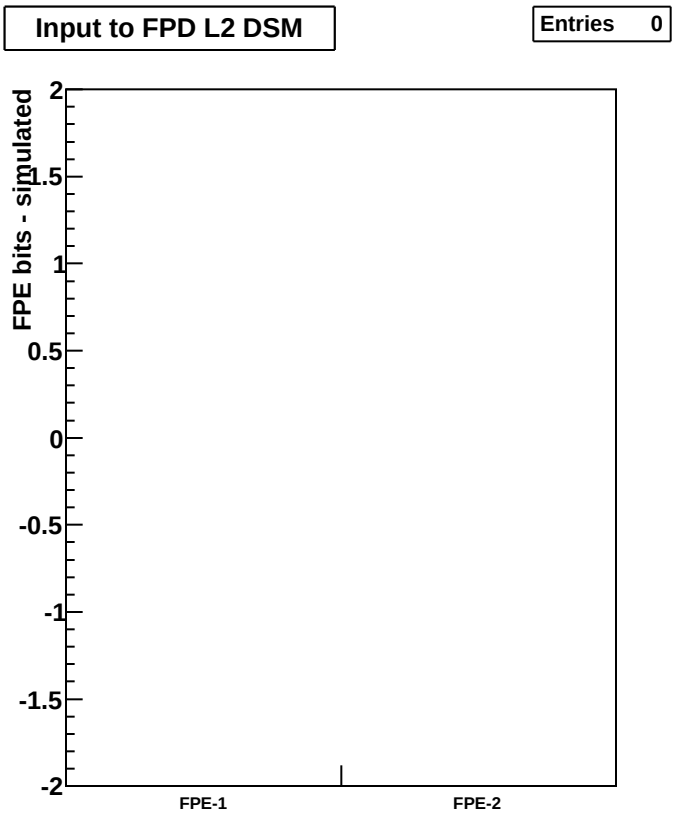

Input to FE003 QT board
Entries7506208
ADC
channel


Input to FE004 QT board
Entries7506208
ADC
channel


Input to FPE L1 DSM
Entries 938276
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 469138
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
5842
7177
228727
227392
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2