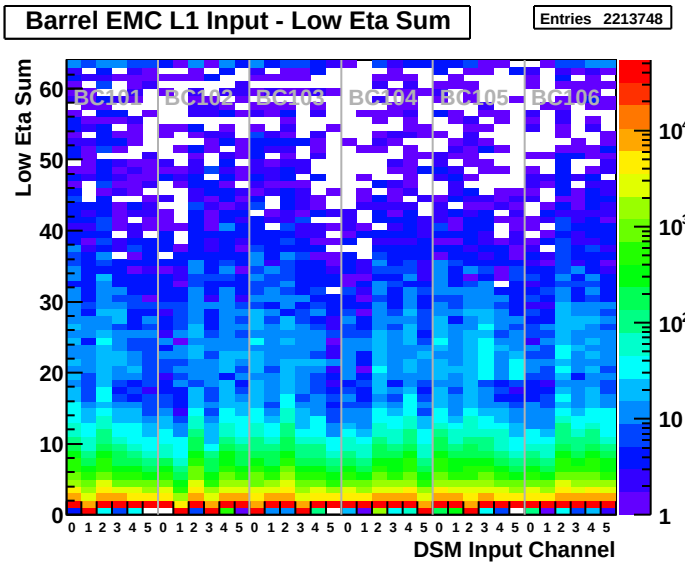
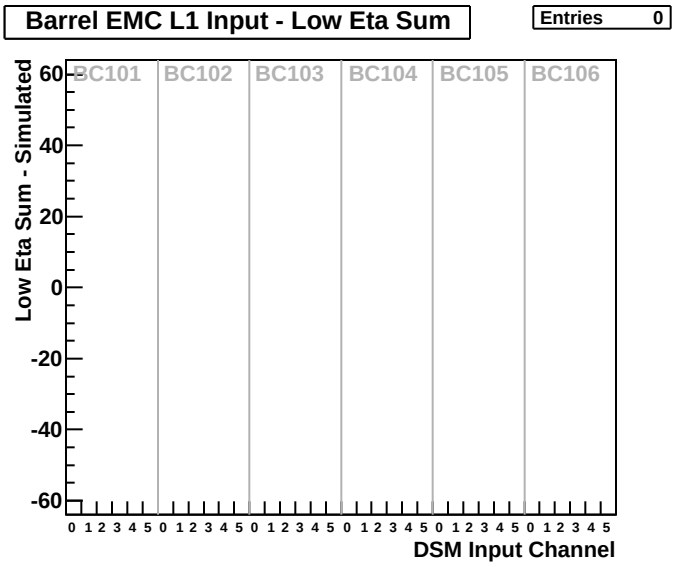
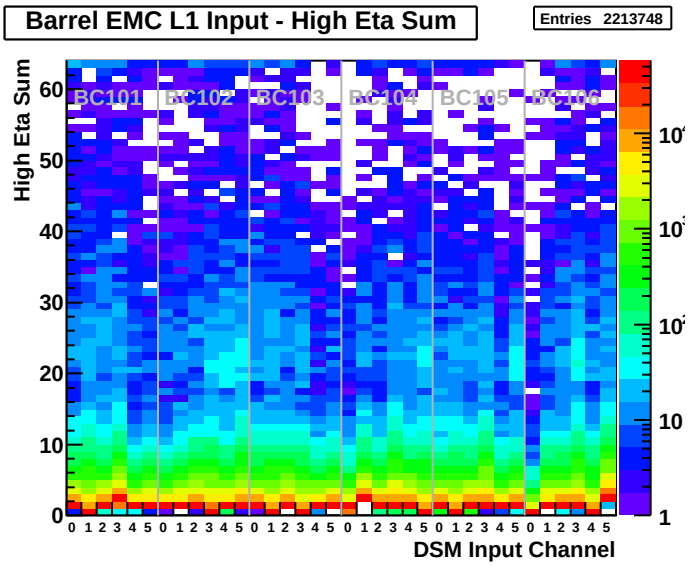
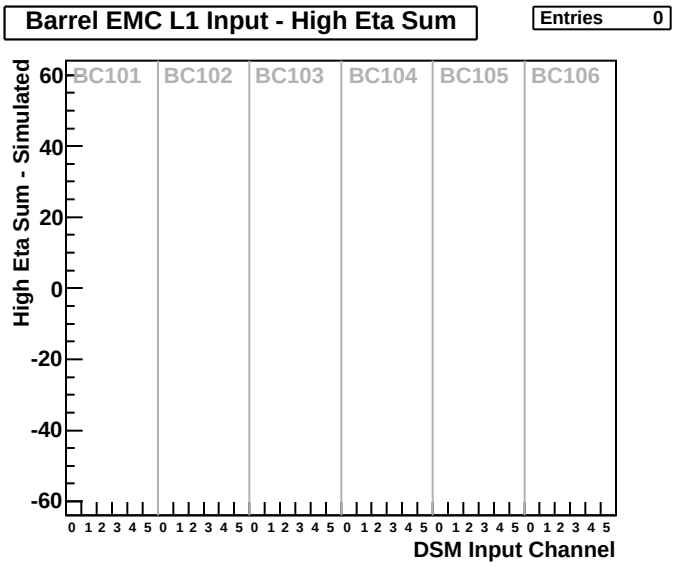
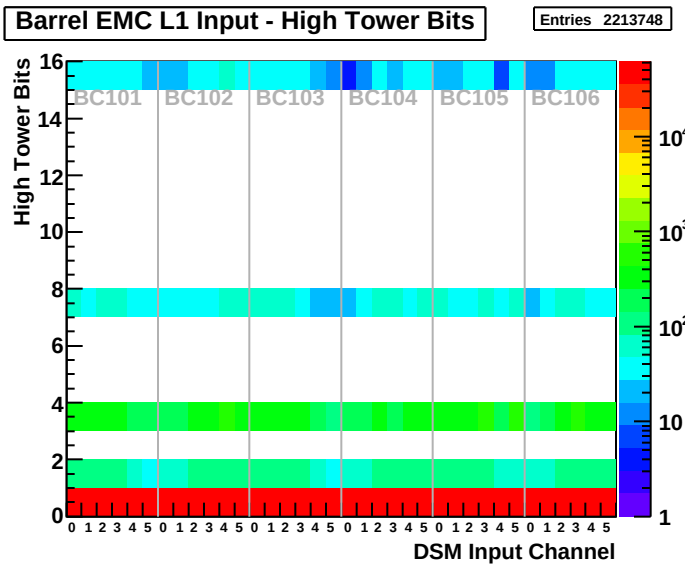
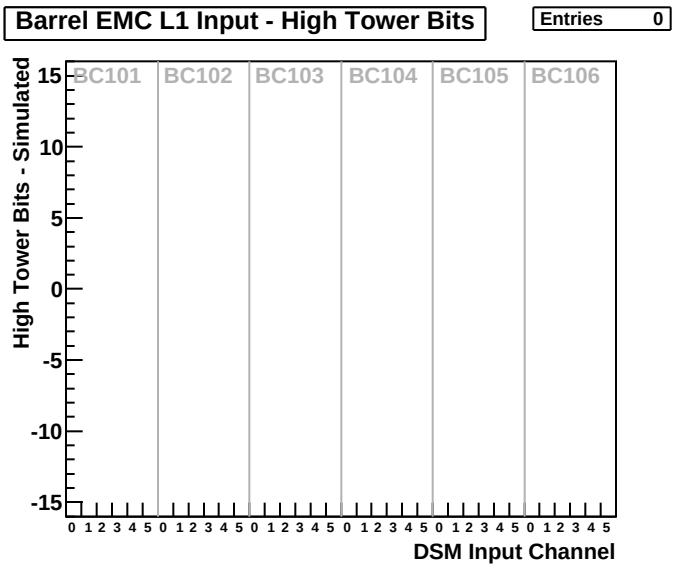

Barrel EMC L1 Input - Low Eta Sum
Entries 2213748
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

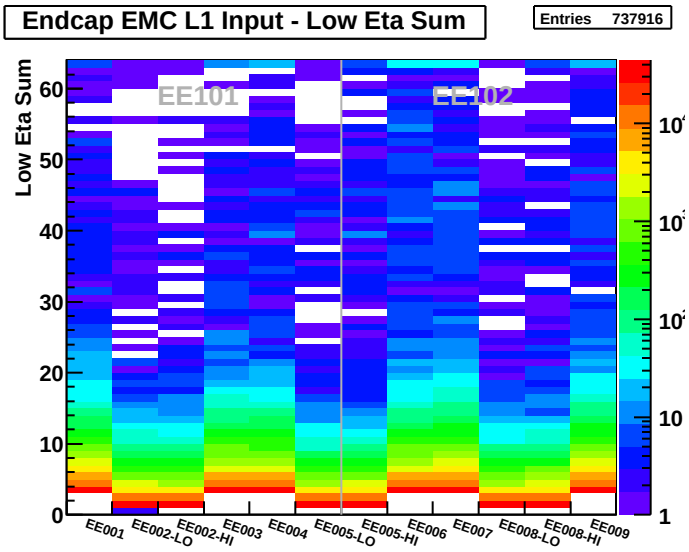

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

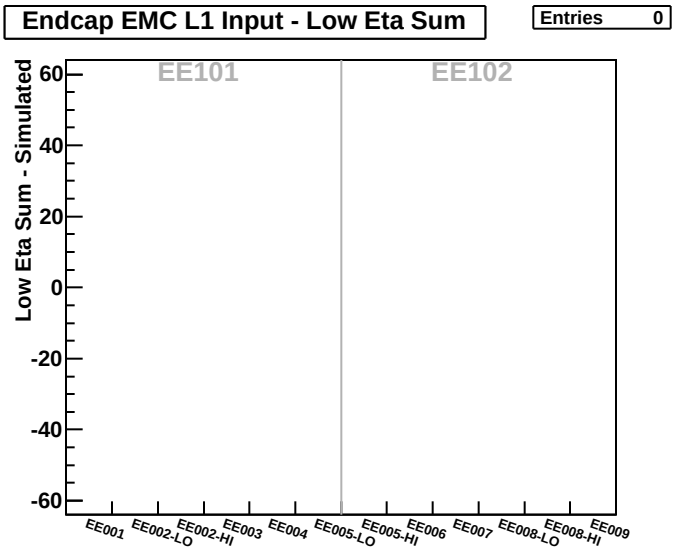

Barrel EMC L1 Input - High Eta Sum
Entries 2213748
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

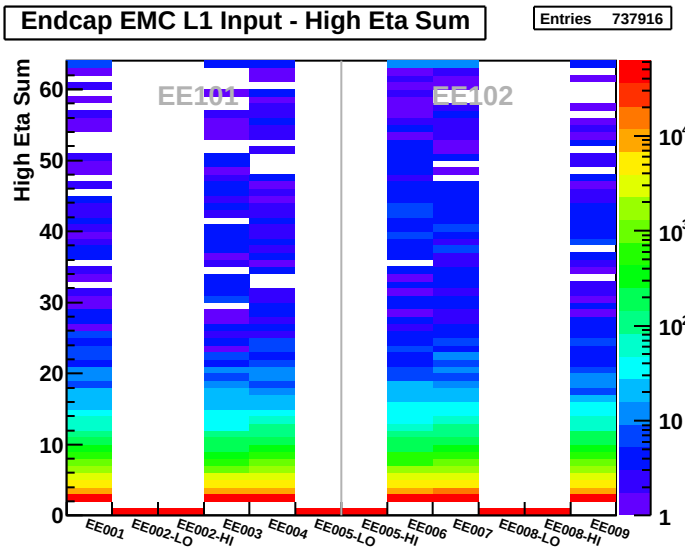

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

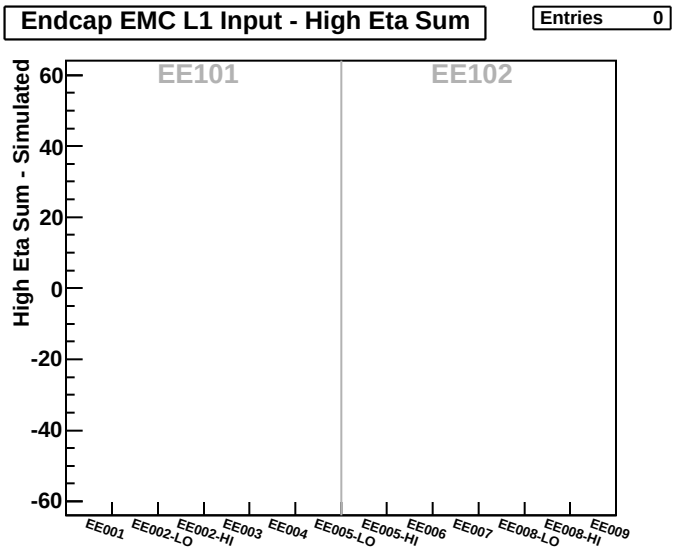

Barrel EMC L1 Input - High Tower Bits
Entries 2213748
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

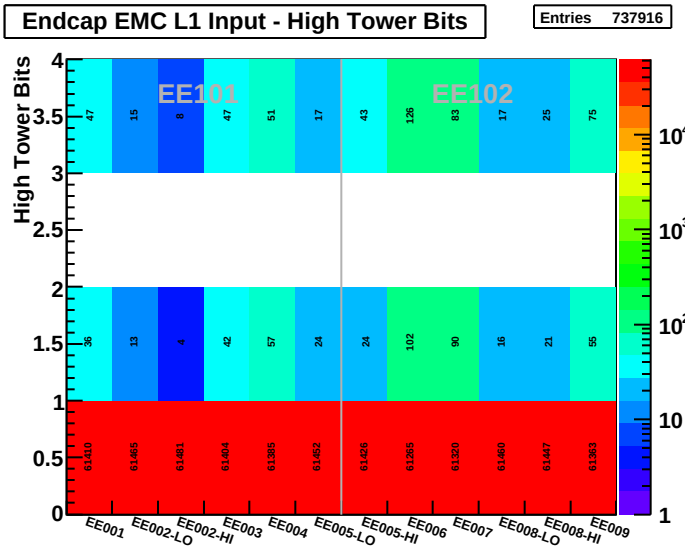

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

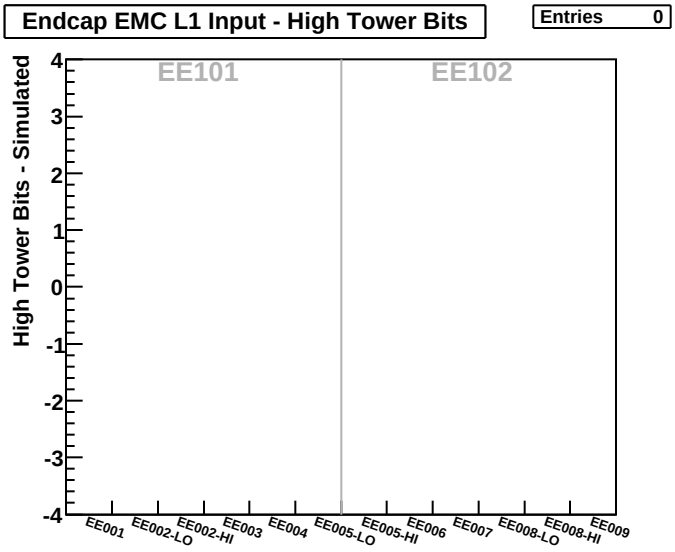

Endcap EMC L1 Input - Low Eta Sum
Entries 737916
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

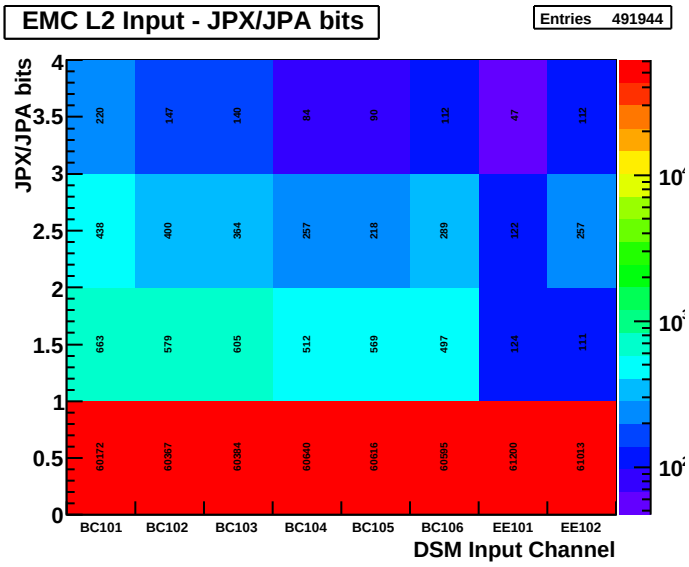

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

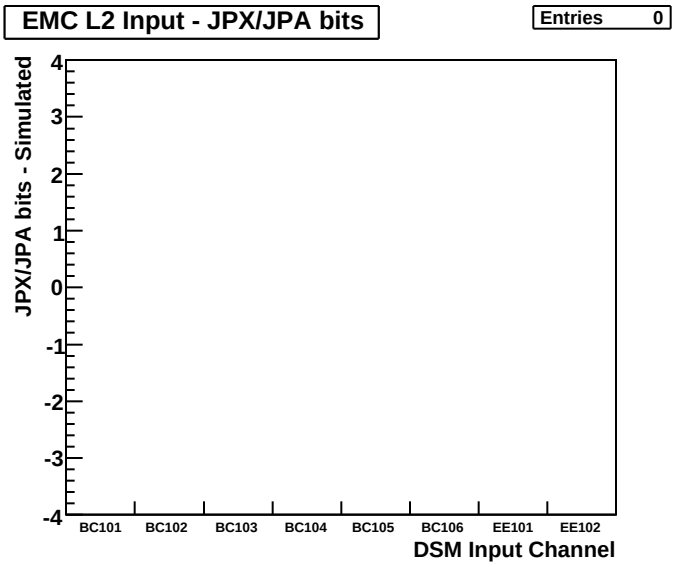

Endcap EMC L1 Input - High Eta Sum
Entries 737916
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

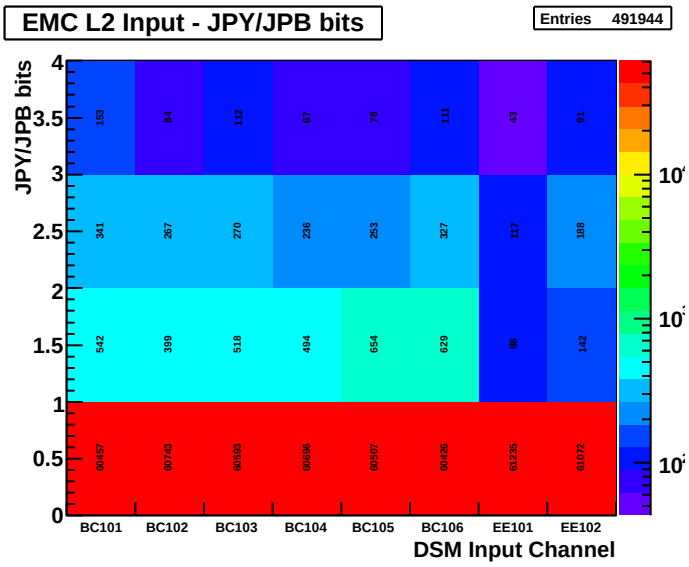

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

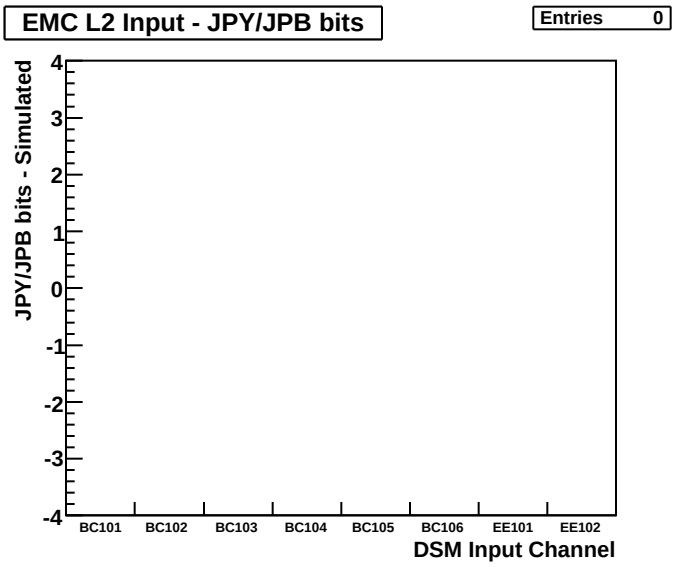

Endcap EMC L1 Input - High Tower Bits
Entries 737916
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

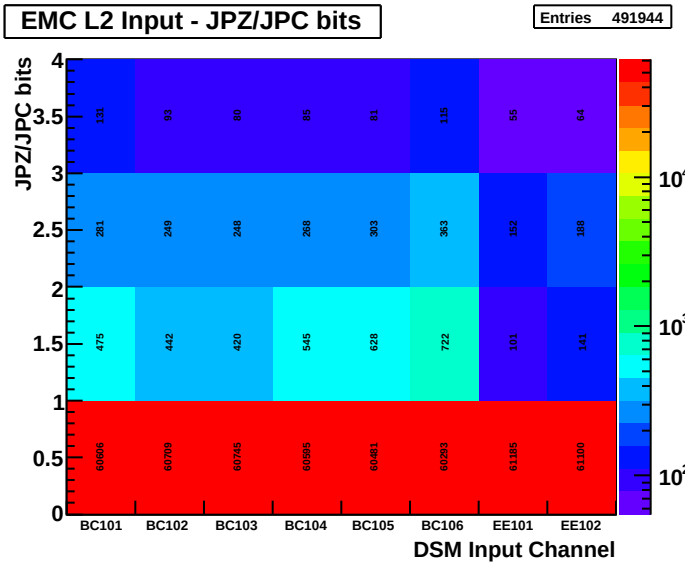

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

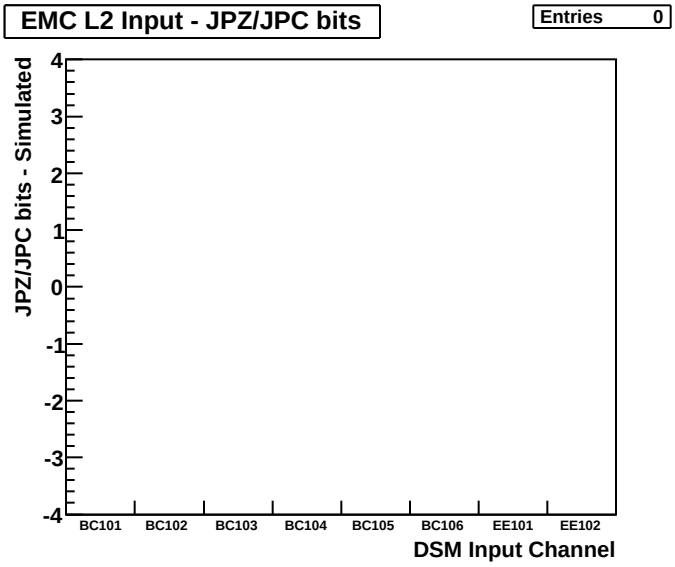

EMC L2 Input - JPX/JPA bits
Entries 491944
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
220 147 140 84 90 112 47 112
438 400 364 257 218 289 122 257
663 579 605 512 569 497 124 111
60172 60367 60384 60640 60616 60595 61200 61013

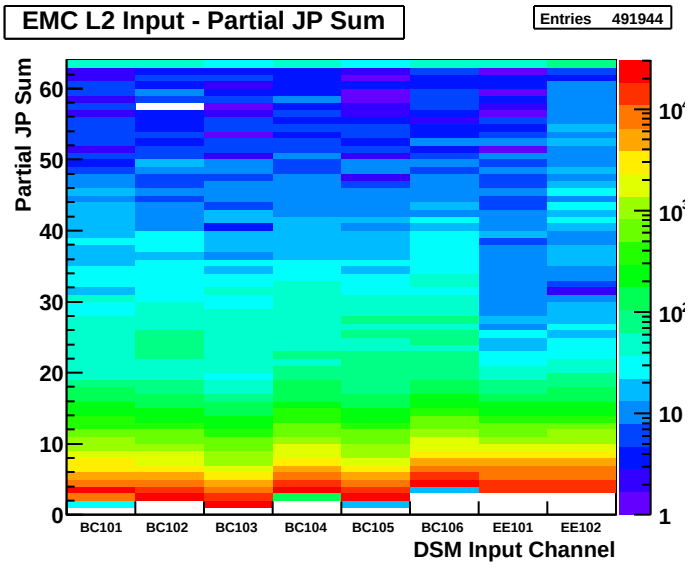

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

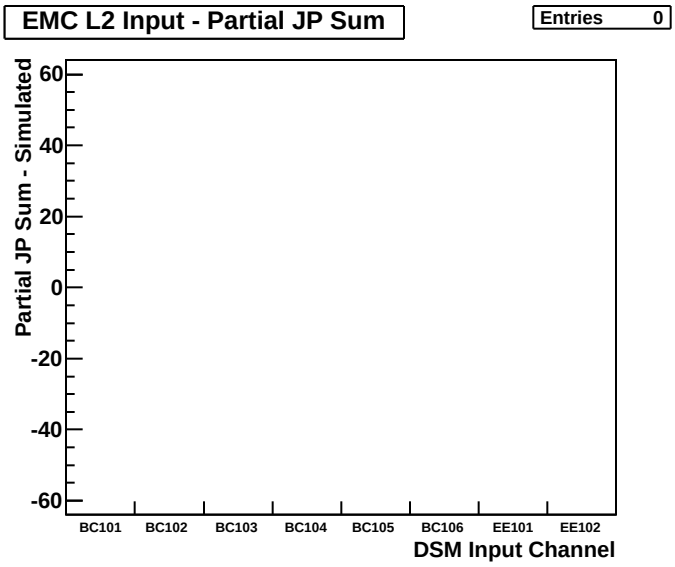

EMC L2 Input - JPY/JPB bits
Entries 491944
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
153 84 112 67 79 111 43 91
341 267 270 236 253 327 117 188
542 399 518 494 654 629 98 142
60457 60743 60593 60696 60507 60426 61235 61072

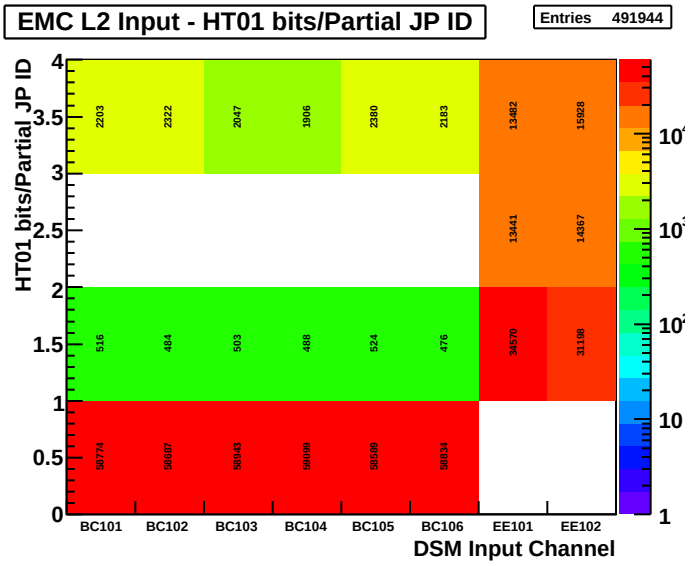

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

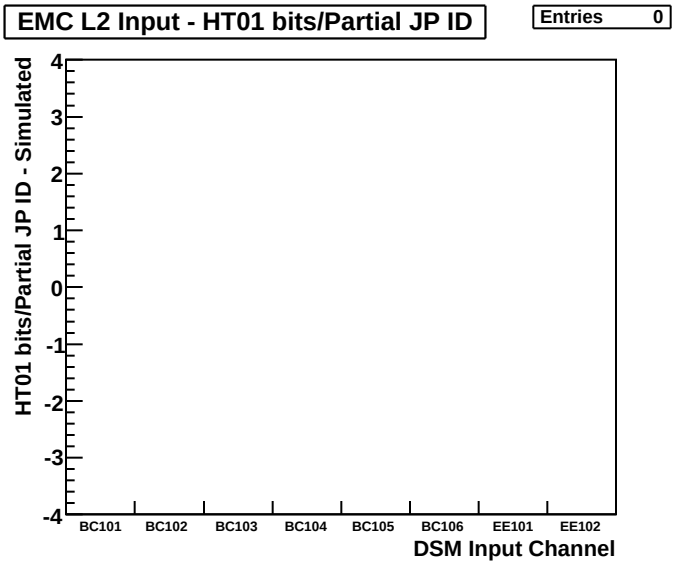

EMC L2 Input - JPZ/JPC bits
Entries 491944
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
131 93 80 85 81 115 55 64
281 249 248 268 303 363 152 188
475 442 420 545 628 722 101 141
60606 60709 60745 60595 60481 60293 61185 61100

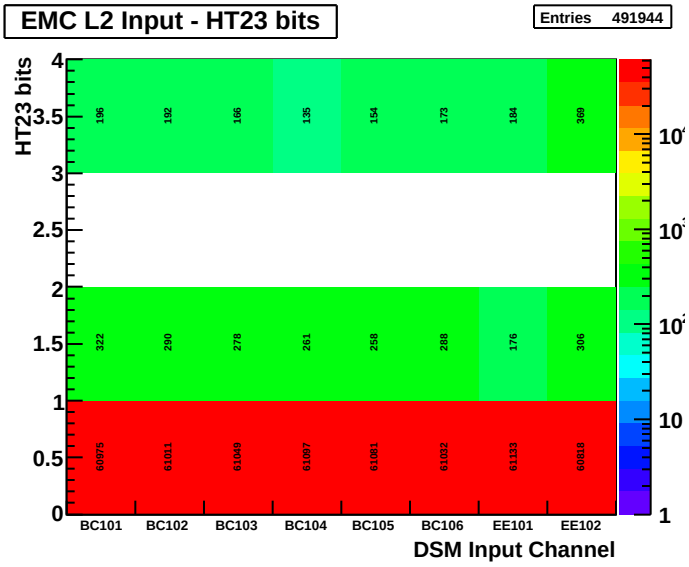

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

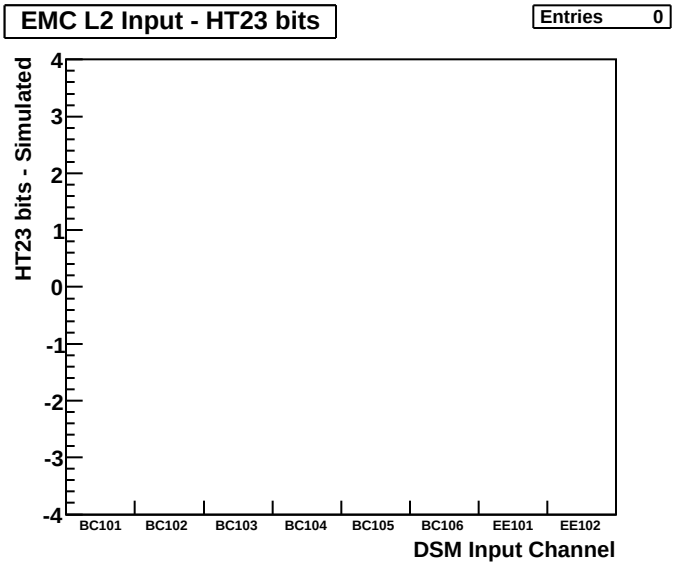

EMC L2 Input - Partial JP Sum
Entries 491944
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

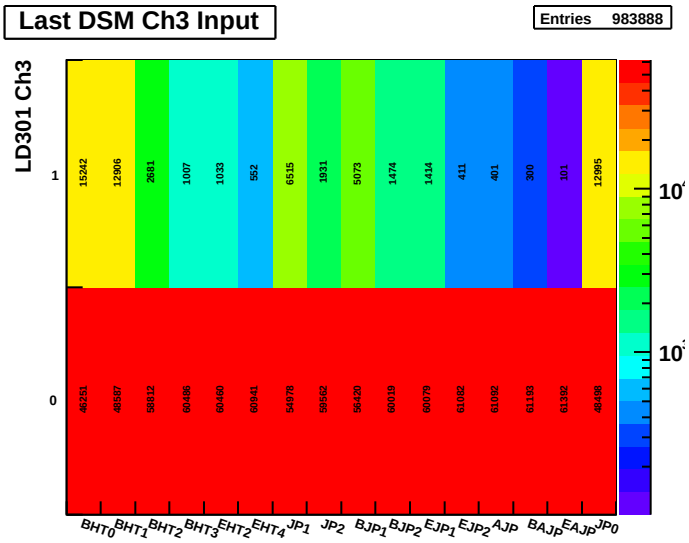

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

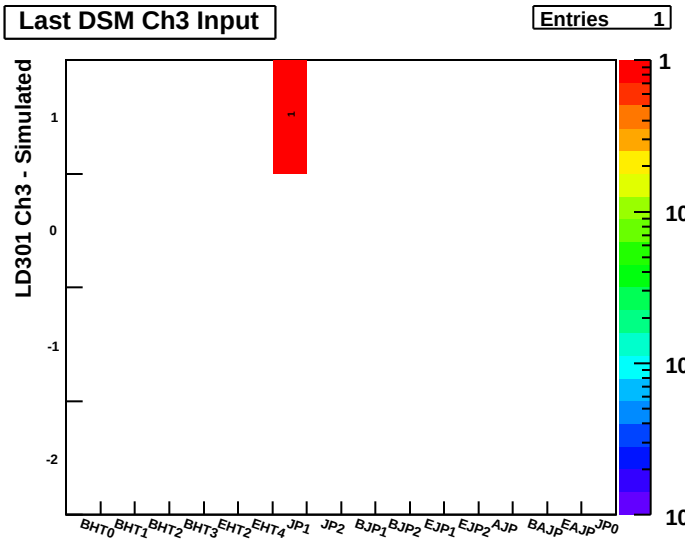

EMC L2 Input - HT01 bits/Partial JP ID
Entries 491944
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

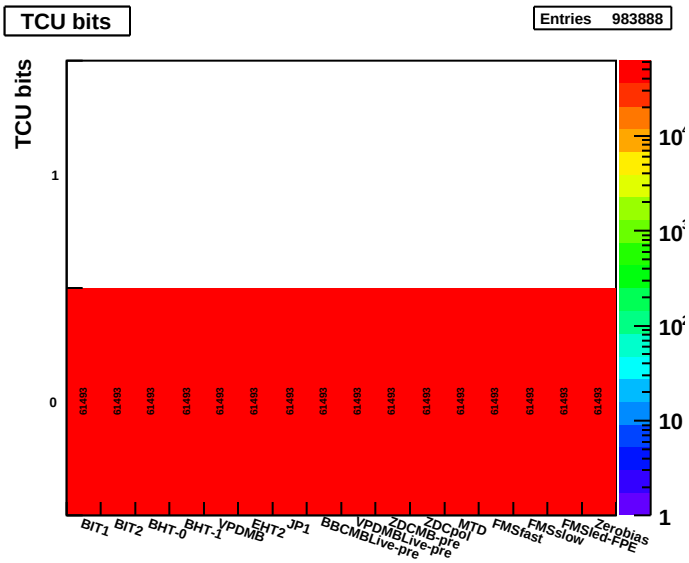

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

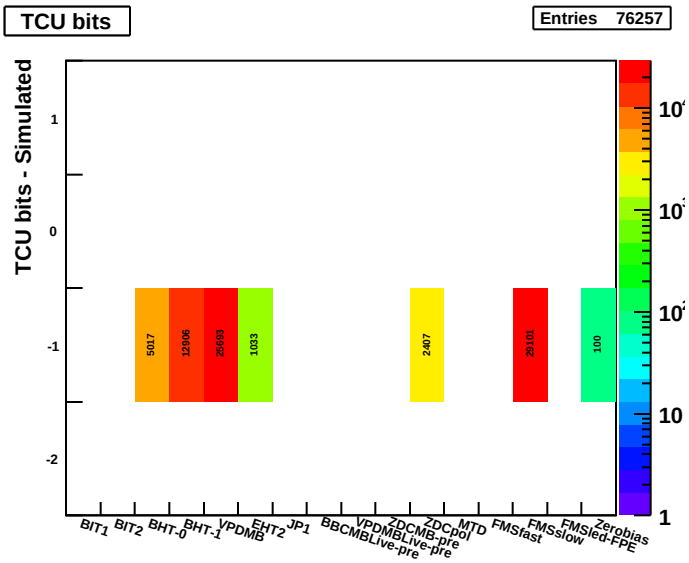

EMC L2 Input - HT23 bits
Entries 491944
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

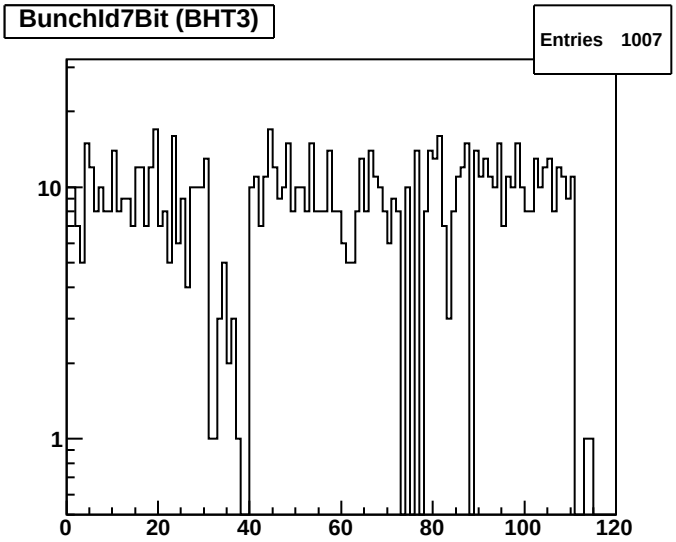

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

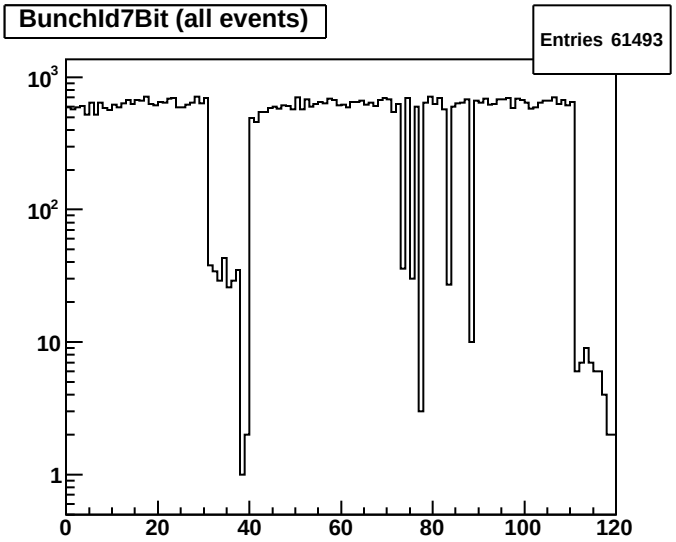

Last DSM Ch3 Input
Entries 983888
LD301 Ch3
15242 12906 2681 1007 1033 552 6515 1931 5073 1474 1414 411 401 300 101 12995
46261 49587 58812 60486 60460 60941 54978 59562 56420 60019 60079 61082 61092 61183 61392 49498
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

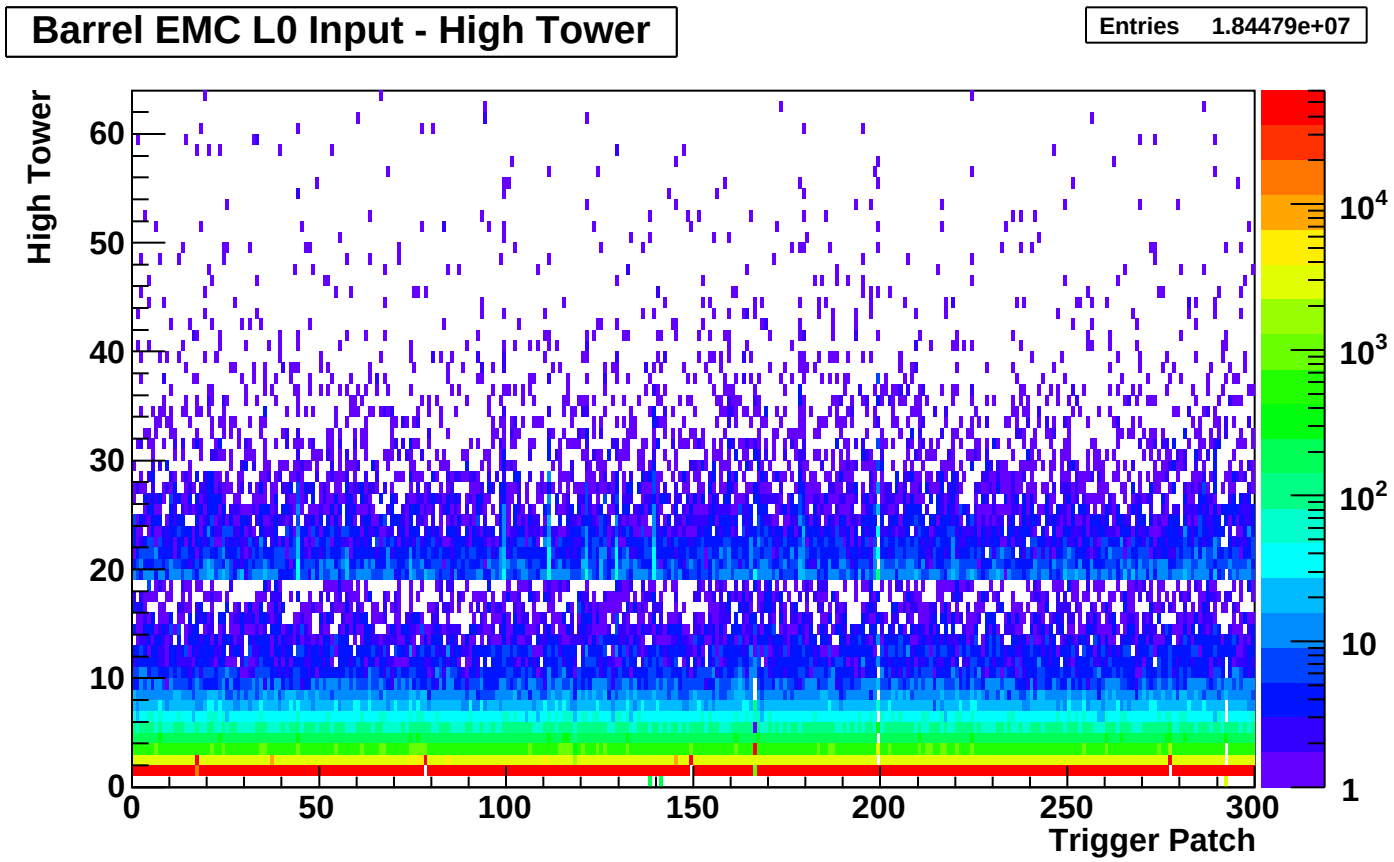

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

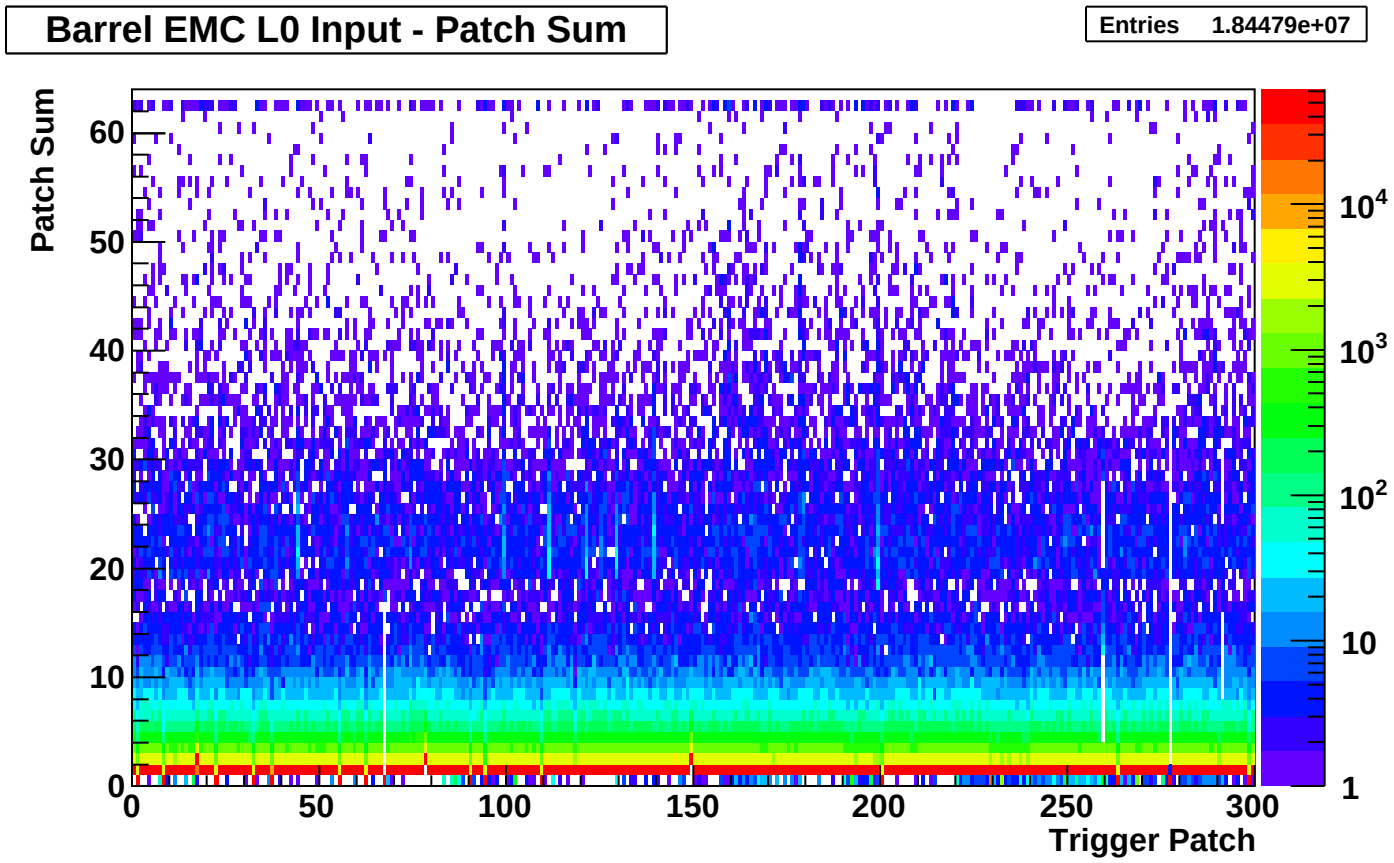

TCU bits
Entries 983888
TCU bits
61493
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

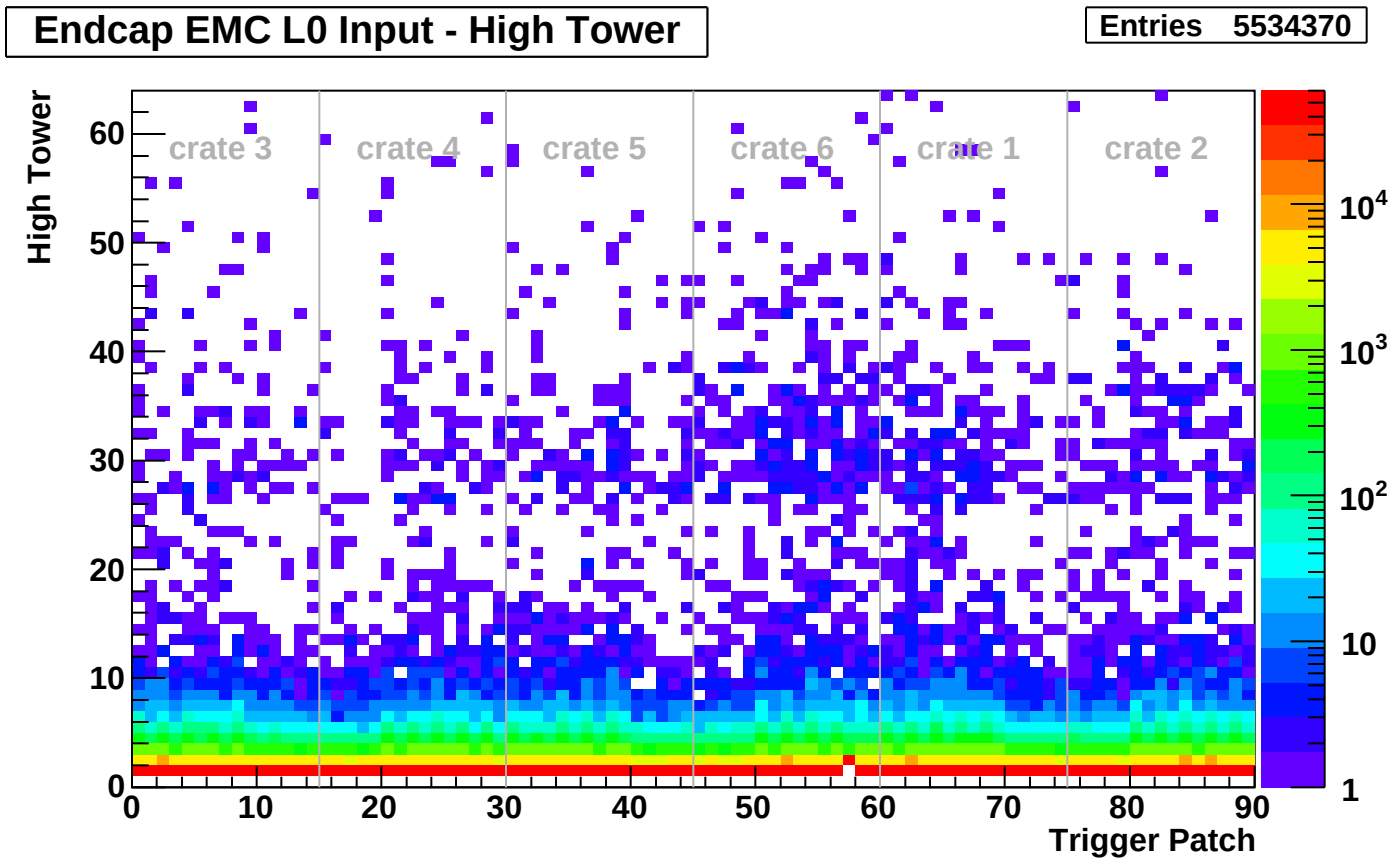

TCU bits
Entries 76257
TCU bits - Simulated
5017 12906 25693 1033 2407 29101 100
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

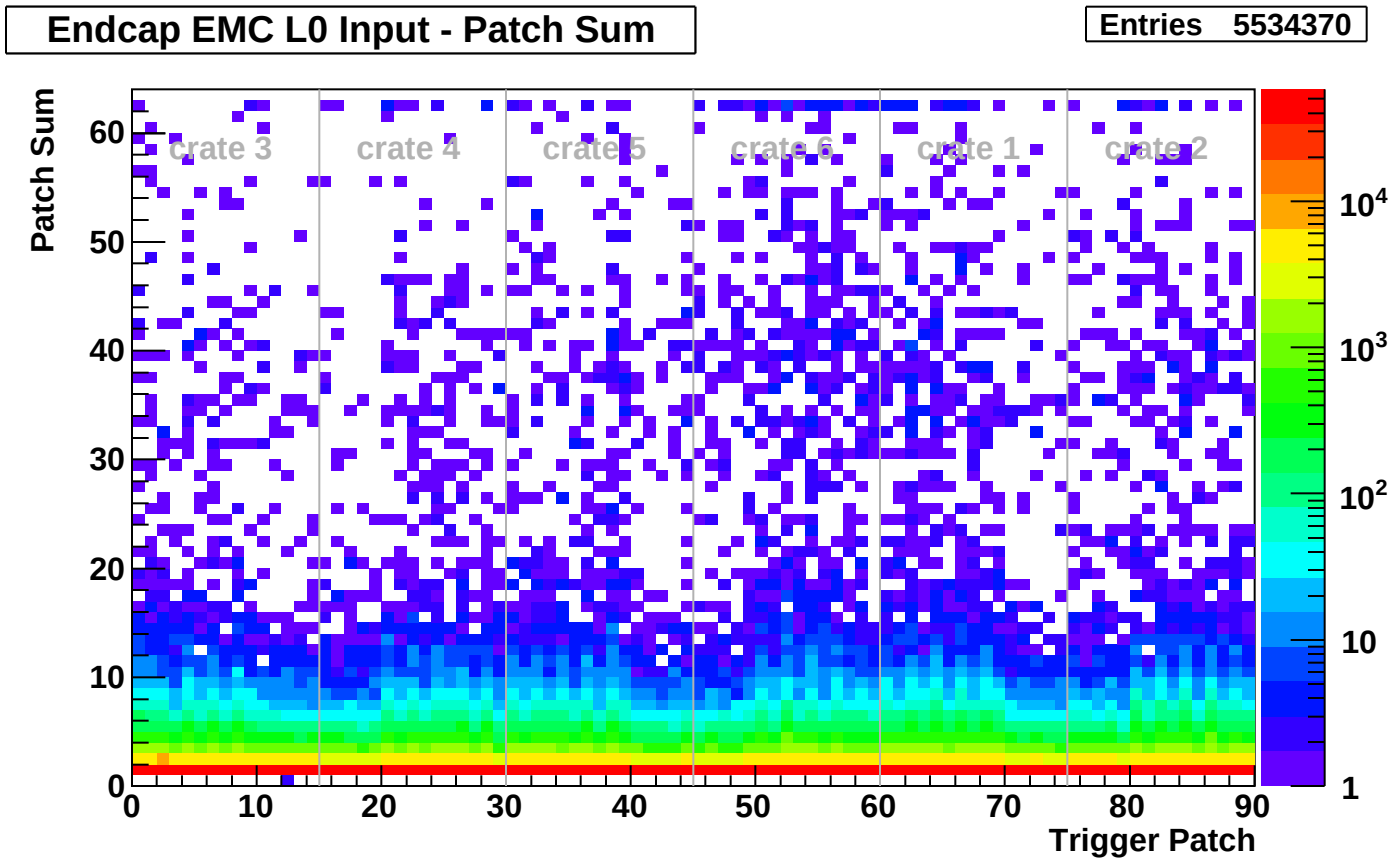

BunchId7Bit (BHT3)
Entries 1007

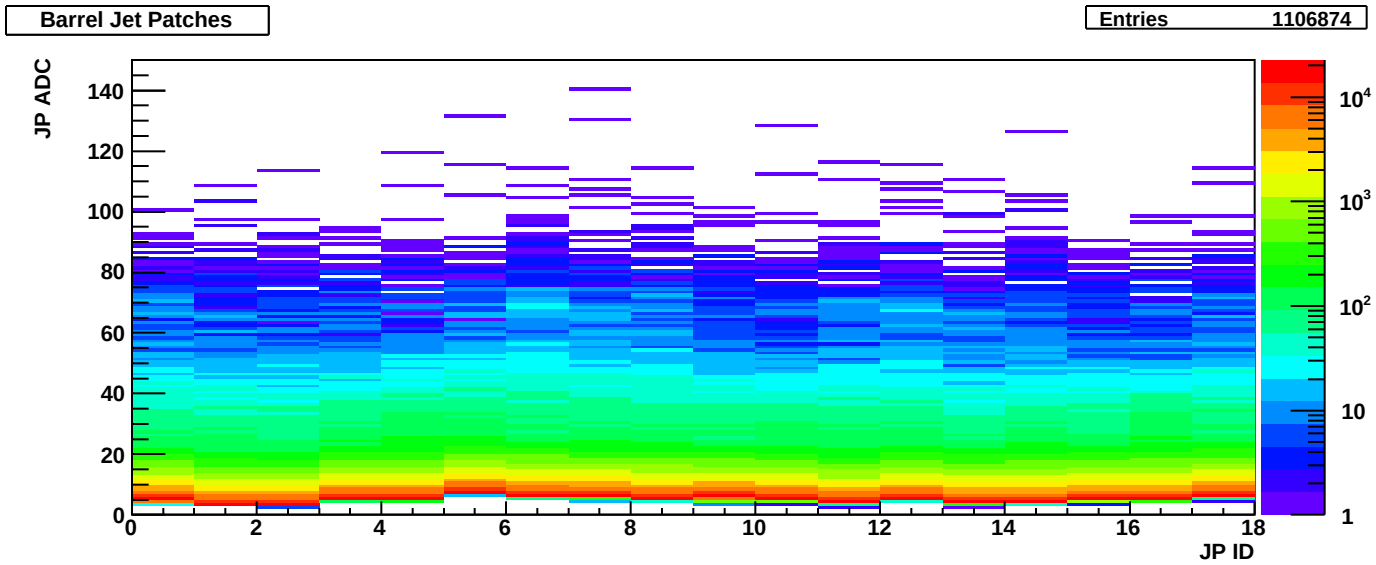

BunchId7Bit (all events)
Entries 61493

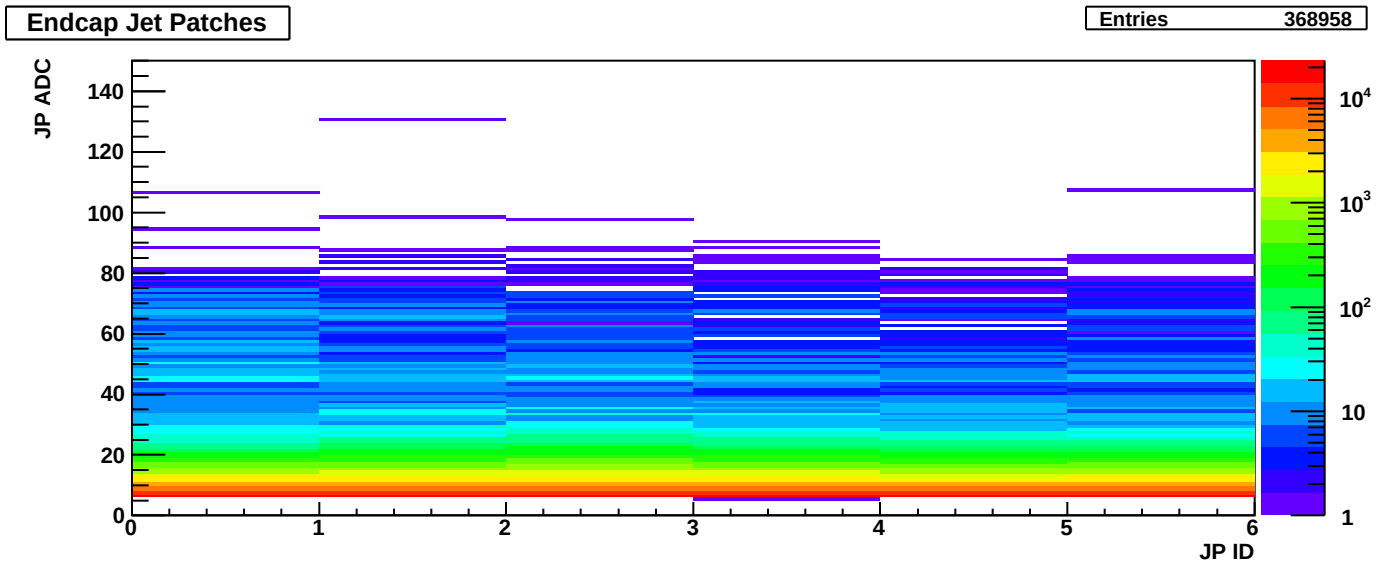

Barrel EMC L0 Input - High Tower
Entries 1.84479e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

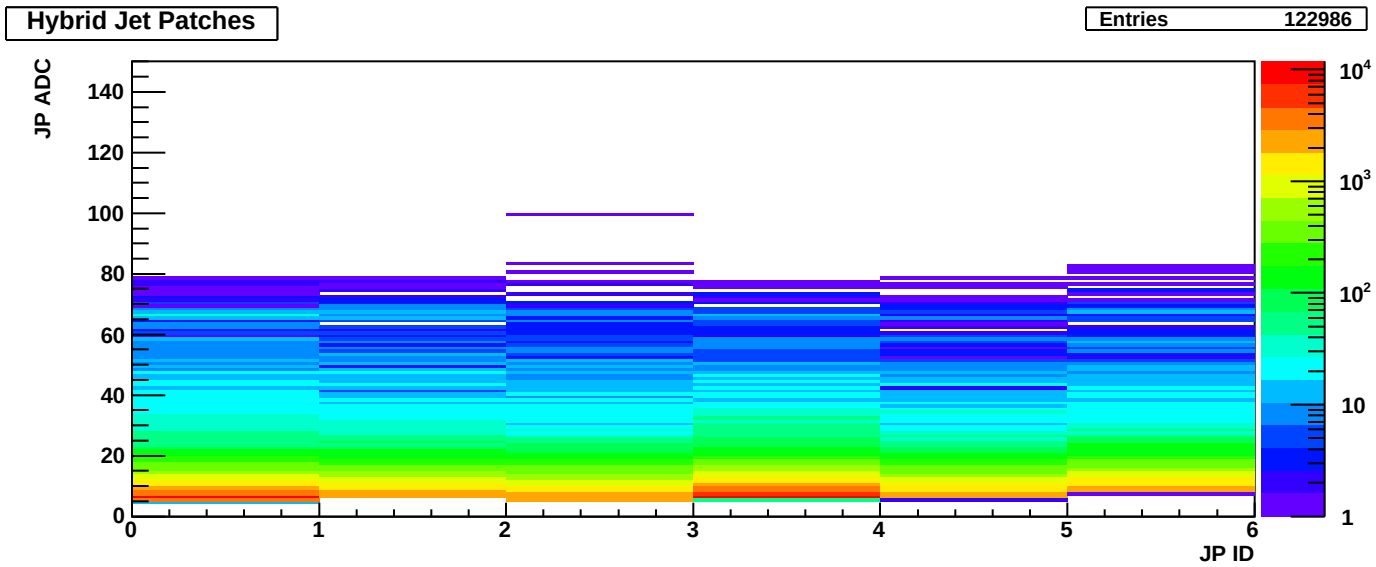

Barrel EMC L0 Input - Patch Sum
Entries 1.84479e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

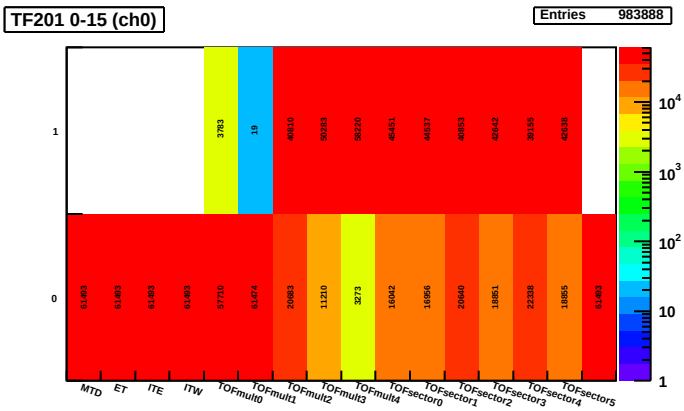

Endcap EMC L0 Input - High Tower
Entries 5534370
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

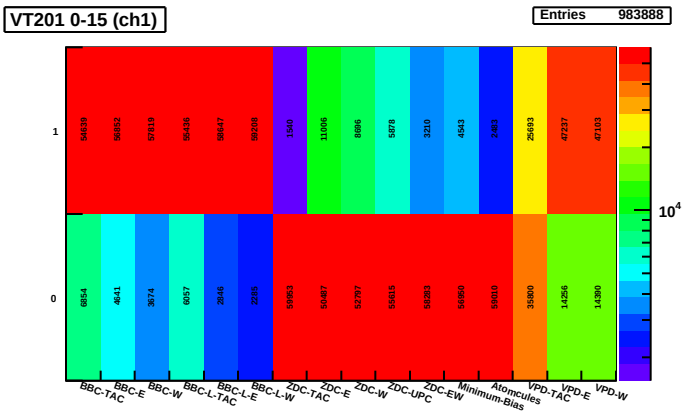

Endcap EMC L0 Input - Patch Sum
Entries 5534370
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

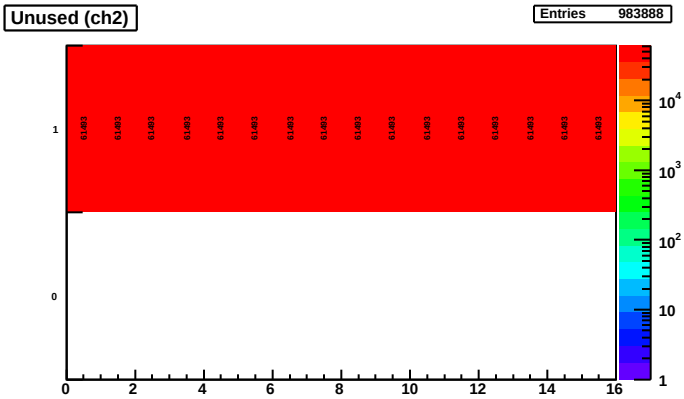

Barrel Jet Patches
Entries 1106874
JP ADC
JP ID

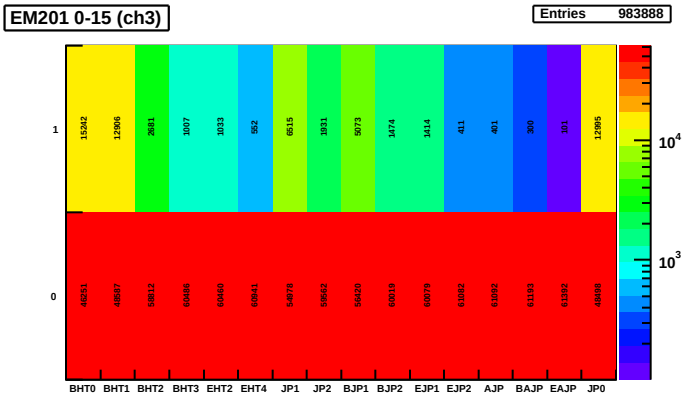

Endcap Jet Patches
Entries 368958
JP ADC
JP ID

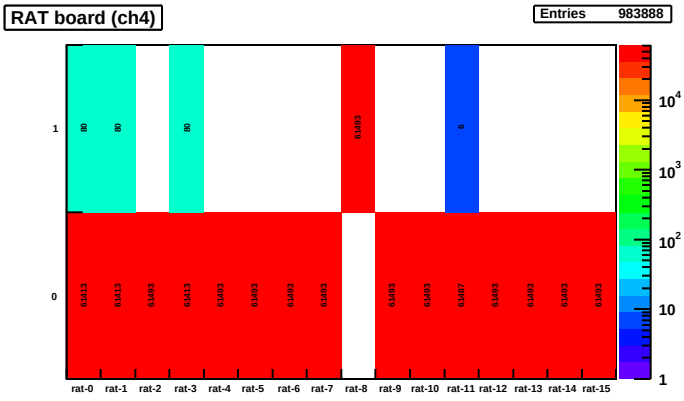

Hybrid Jet Patches
Entries 122986
JP ADC
JP ID

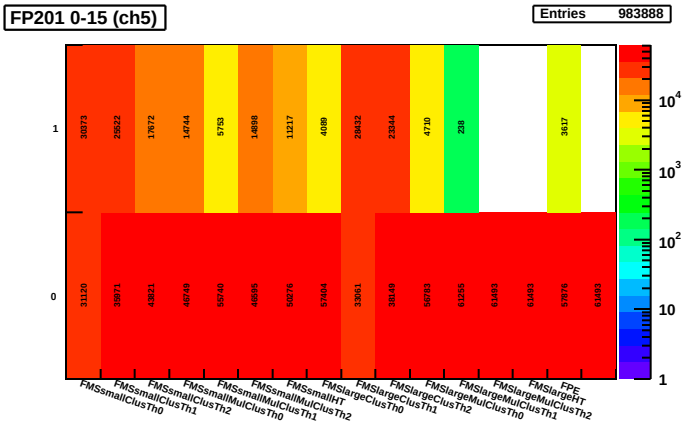

TF201 0-15 (ch0)
Entries 983888
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

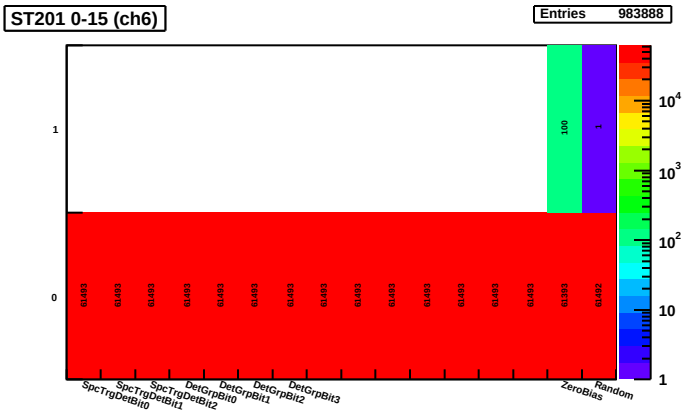

VT201 0-15 (ch1)
Entries 983888
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

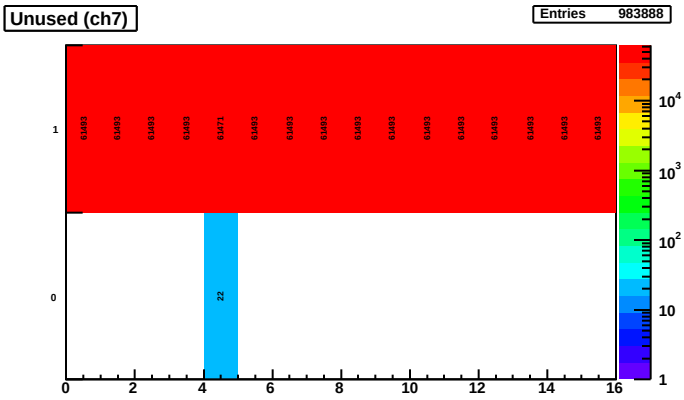

Unused (ch2)
Entries 983888

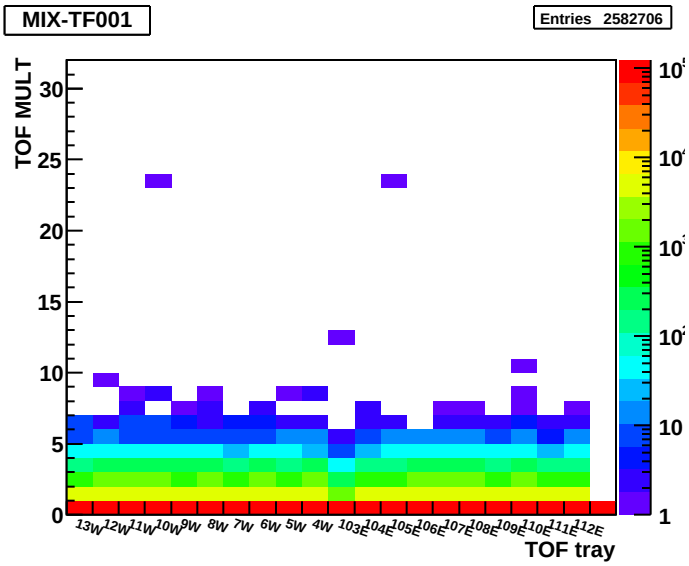

EM201 0-15 (ch3)
Entries 983888
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

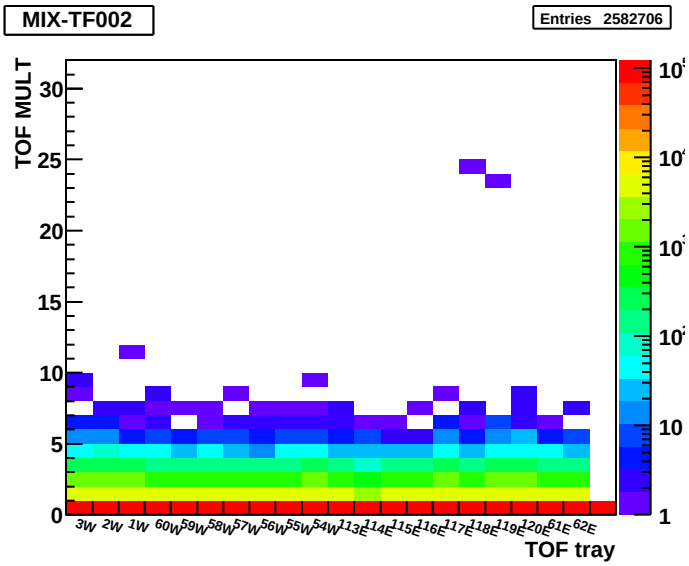

RAT board (ch4)
Entries 983888
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

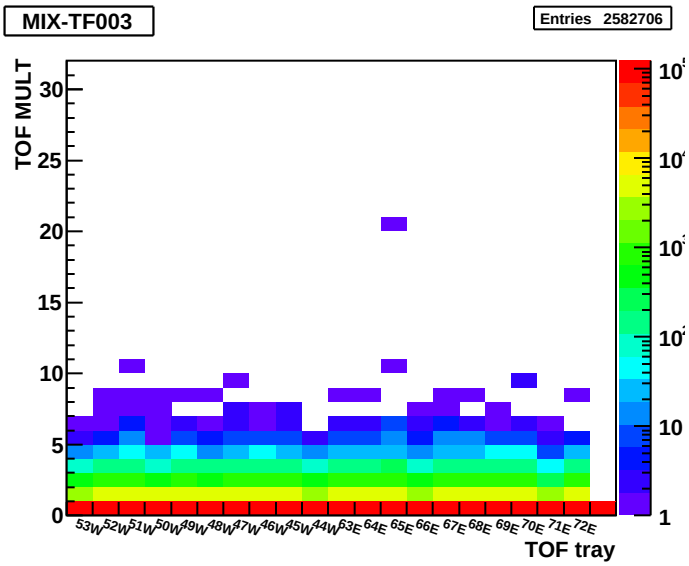

FP201 0-15 (ch5)
Entries 983888
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

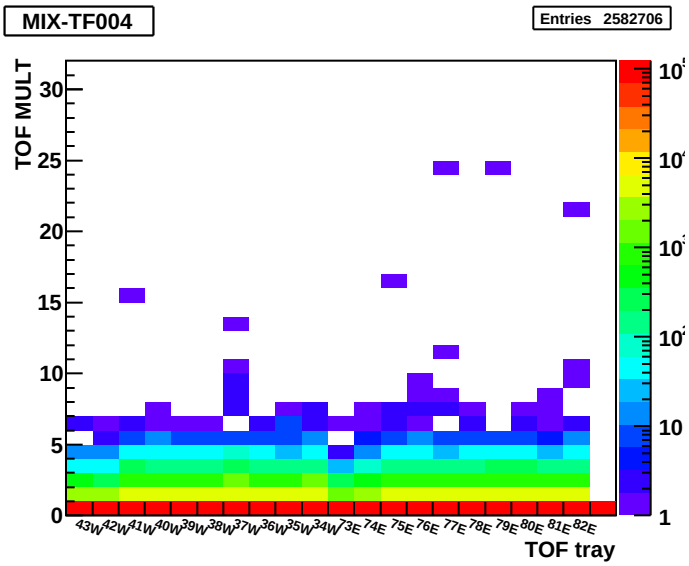

ST201 0-15 (ch6)
Entries 983888
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

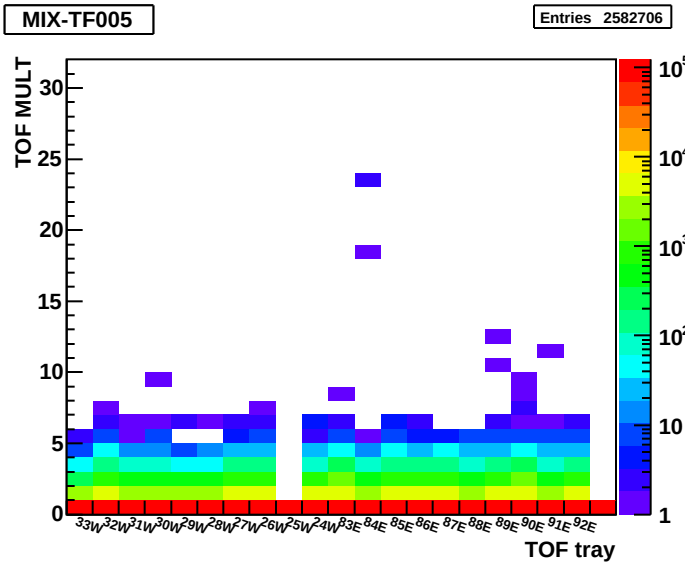

Unused (ch7)
Entries 983888

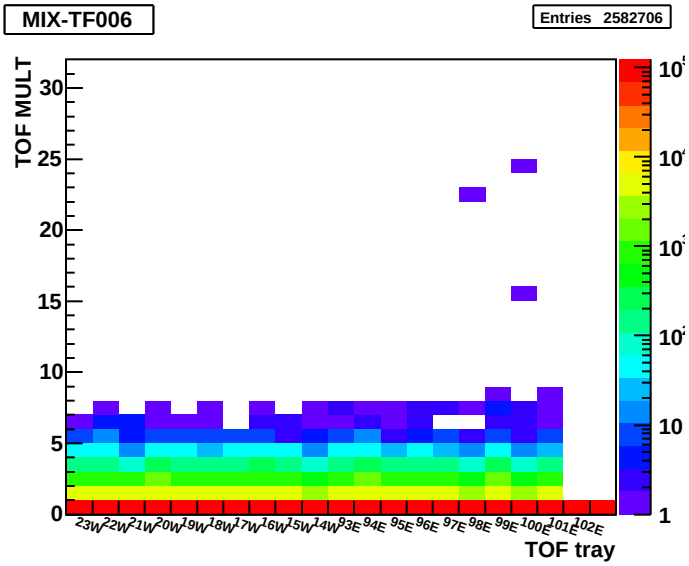

MIX-TF001
Entries 2582706
TOF MULT
TOF tray

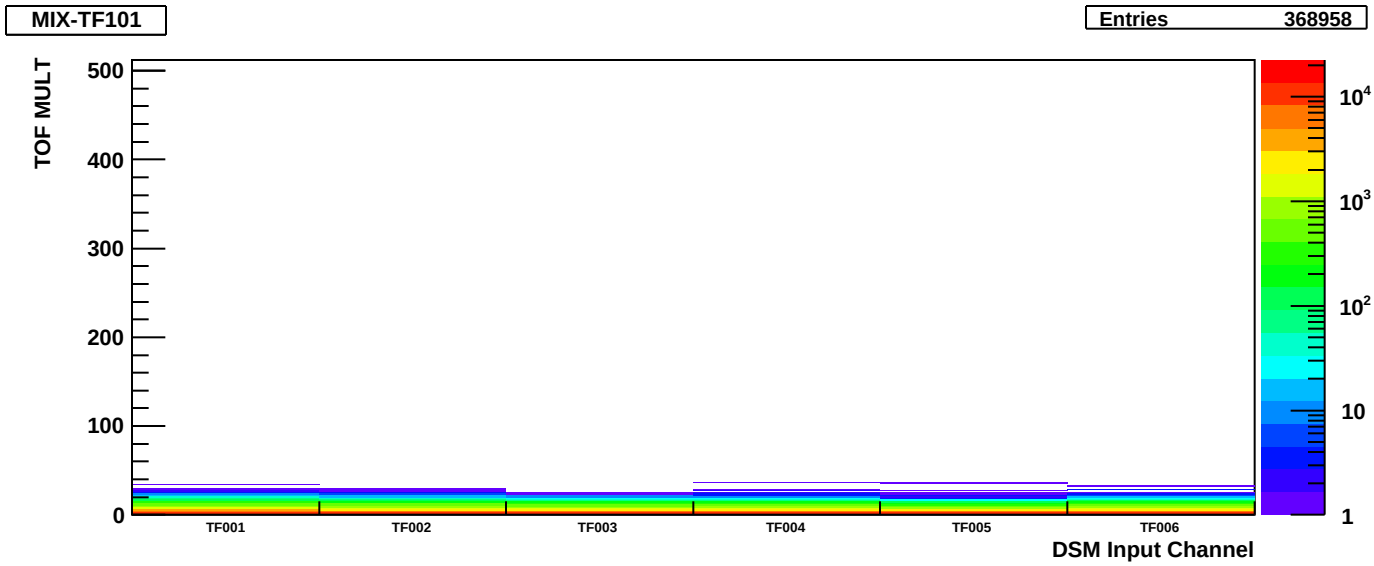

MIX-TF002
Entries 2582706
TOF MULT
TOF tray

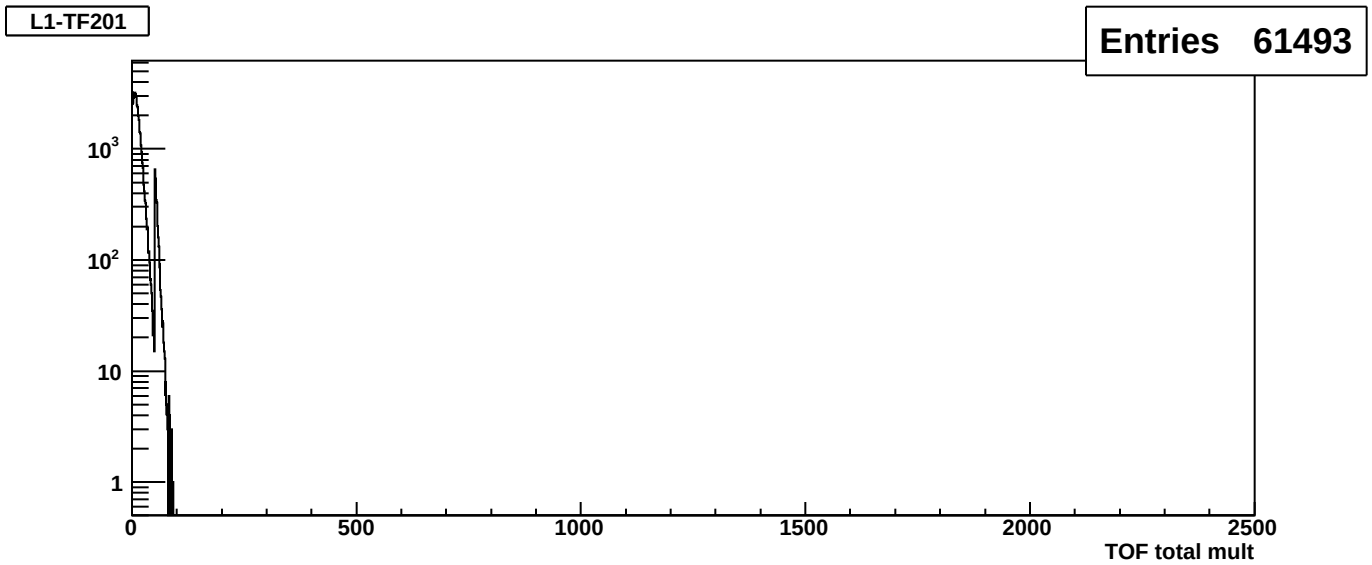

MIX-TF003
Entries 2582706
TOF MULT
TOF tray

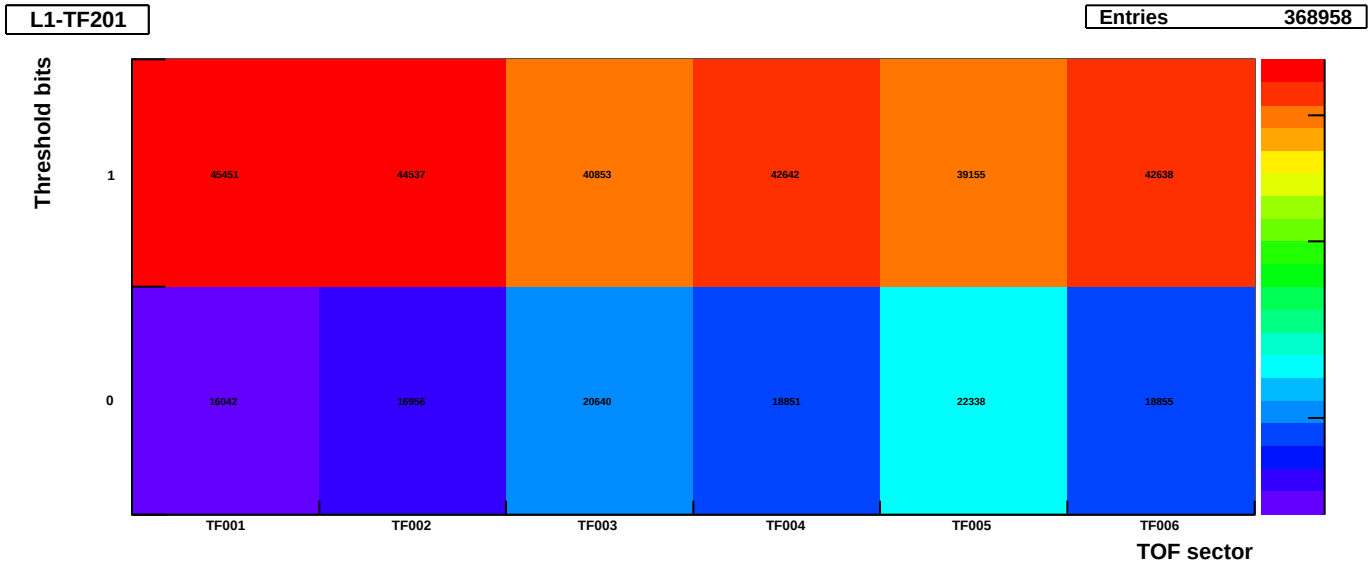

MIX-TF004
Entries 2582706
TOF MULT
TOF tray

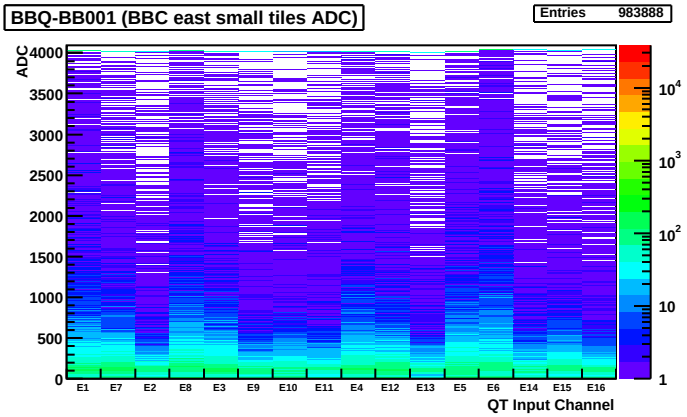

MIX-TF005
Entries 2582706
TOF MULT
TOF tray

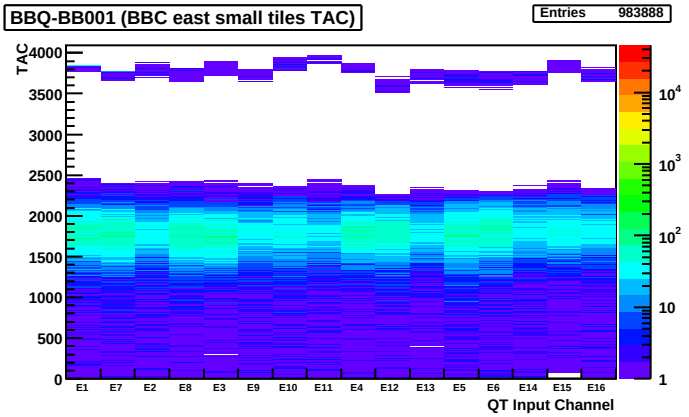

MIX-TF006
Entries 2582706
TOF MULT
TOF tray

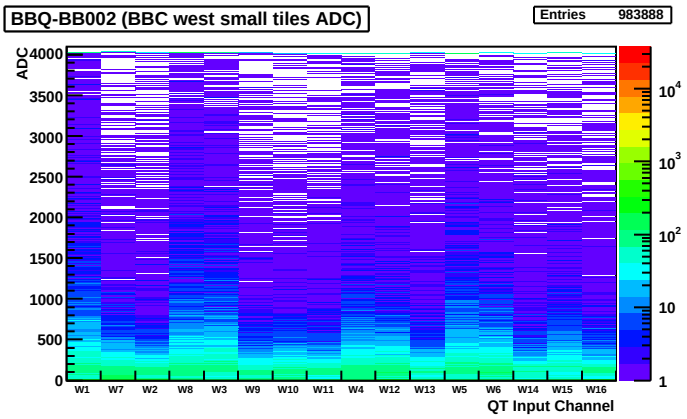

MIX-TF101
Entries 368958
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

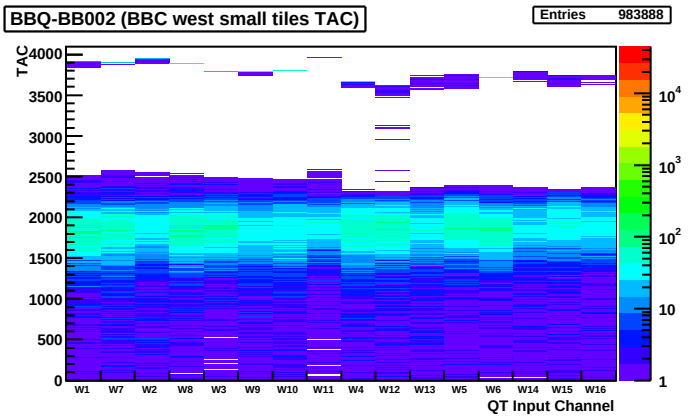

L1-TF201
Entries 61493
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

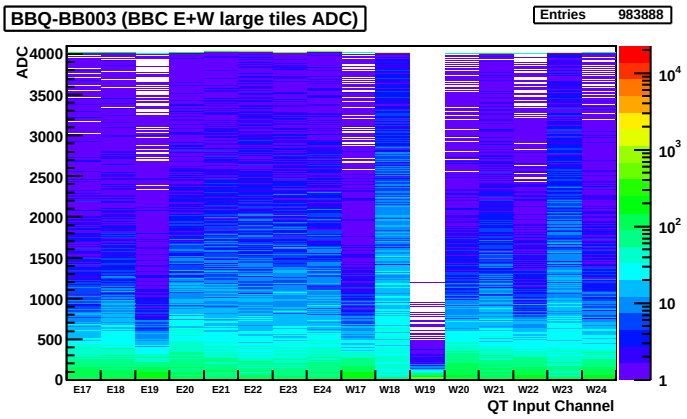

L1-TF201
Entries 368958
Threshold bits
1
0
45451
44537
40853
42642
39155
42638
16042
16956
20640
18851
22338
18855
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

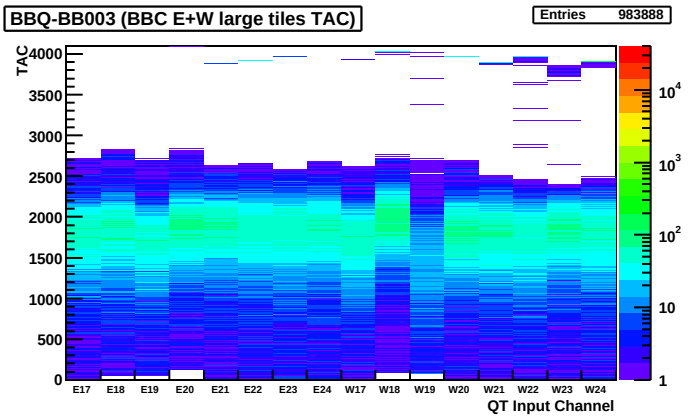

BBQ-BB001 (BBC east small tiles ADC)
Entries 983888
ADC
QT Input Channel

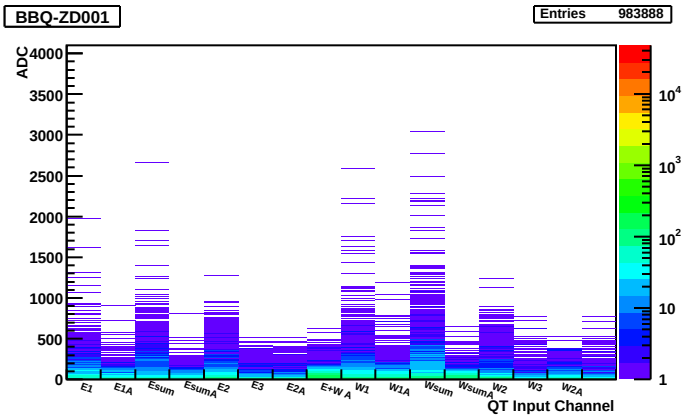

BBQ-BB001 (BBC east small tiles TAC)
Entries 983888
TAC
QT Input Channel

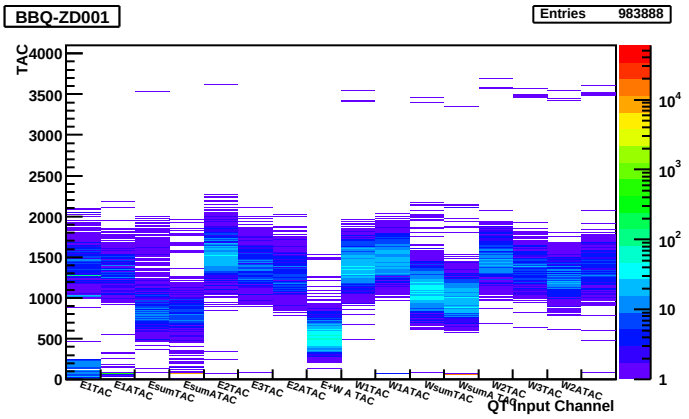

BBQ-BB002 (BBC west small tiles ADC)
Entries 983888
ADC
QT Input Channel

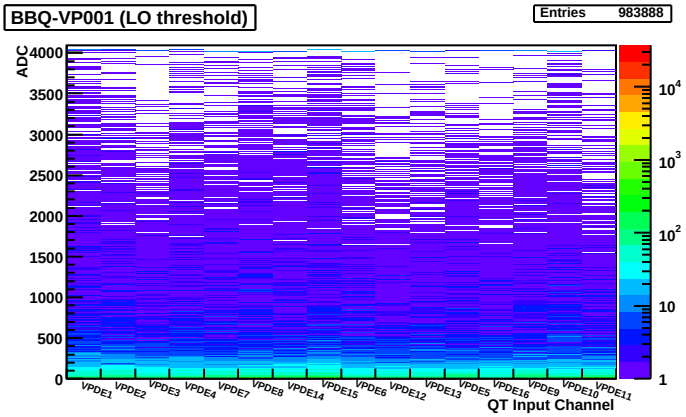

BBQ-BB002 (BBC west small tiles TAC)
Entries 983888
TAC
QT Input Channel

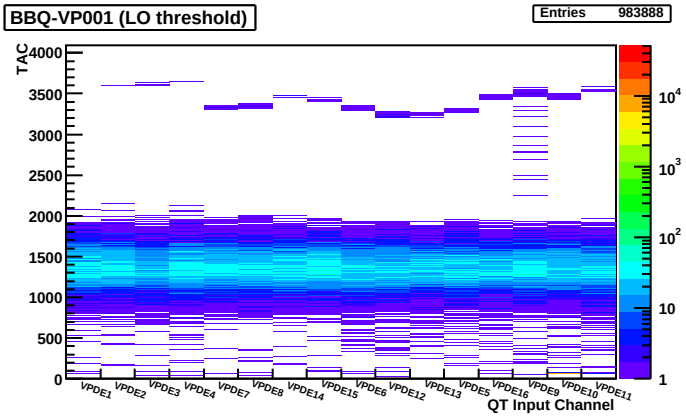

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 983888
ADC
QT Input Channel

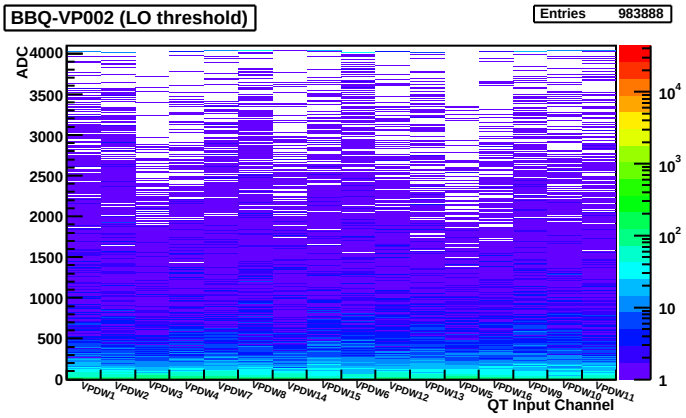

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 983888
TAC
QT Input Channel

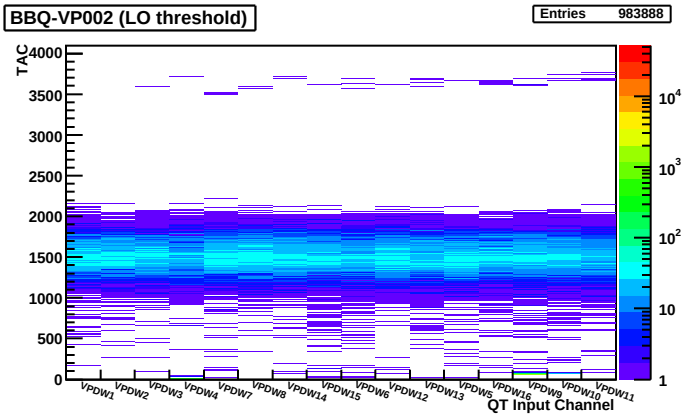

BBQ-ZD001
Entries 983888
ADC
QT Input Channel

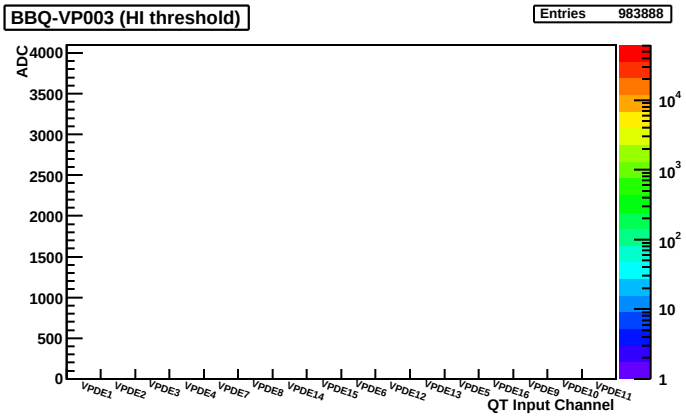

BBQ-ZD001
Entries 983888
TAC
QT Input Channel

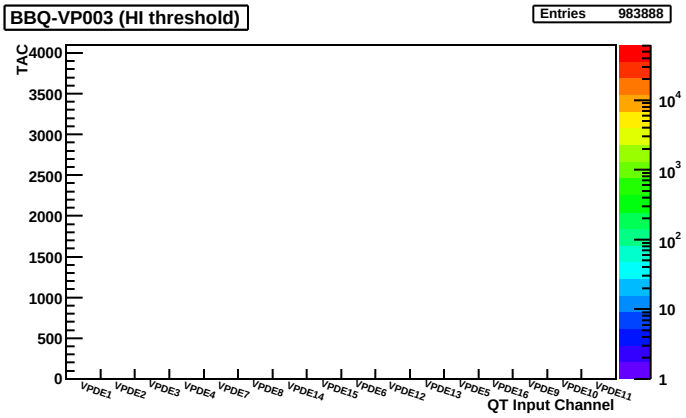

BBQ-VP001 (LO threshold)
Entries 983888
ADC
QT Input Channel

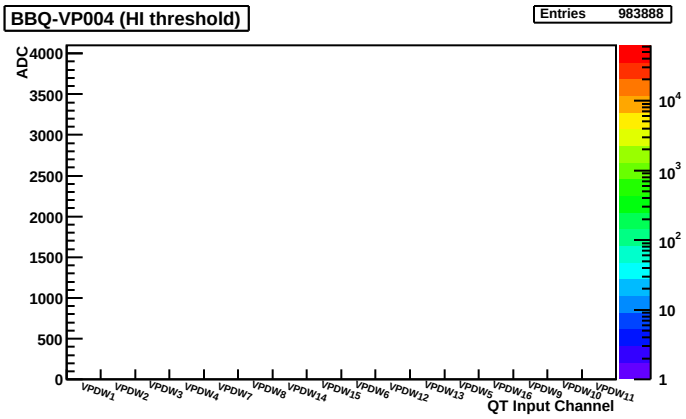

BBQ-VP001 (LO threshold)
Entries 983888
TAC
QT Input Channel

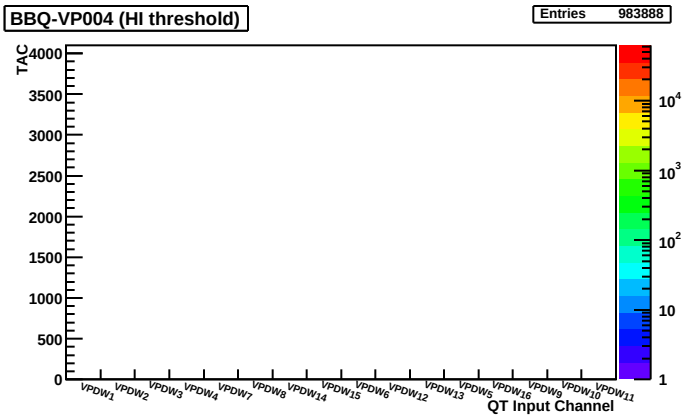

BBQ-VP002 (LO threshold)
Entries 983888
ADC
QT Input Channel

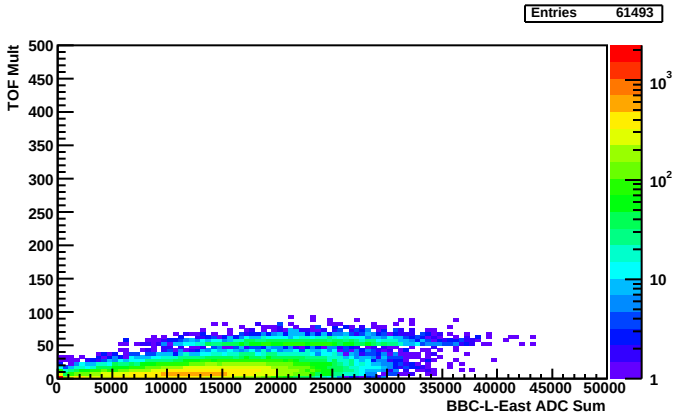

BBQ-VP002 (LO threshold)
Entries 983888
TAC
QT Input Channel

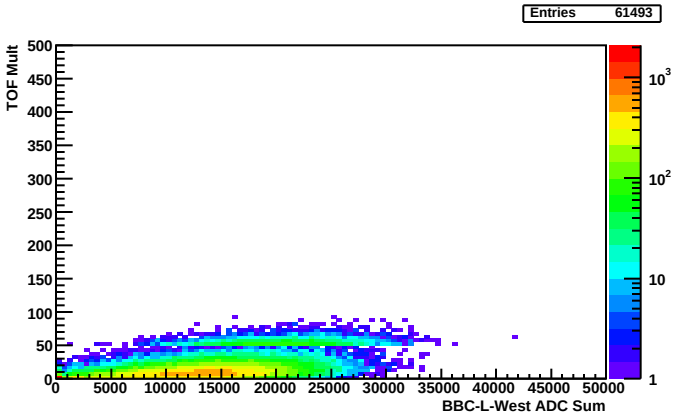

BBQ-VP003 (HI threshold)
Entries 983888
ADC
QT Input Channel

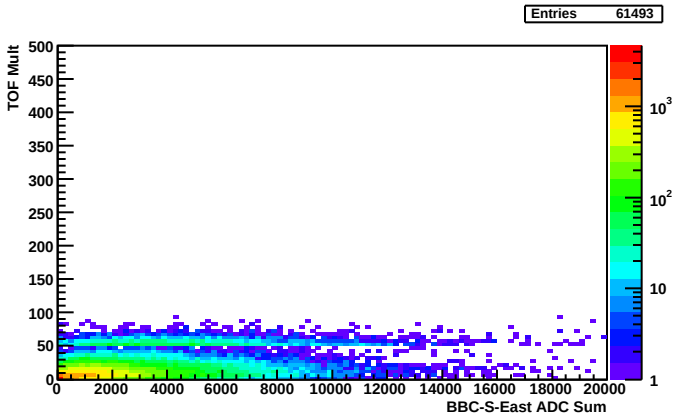

BBQ-VP003 (HI threshold)
Entries 983888
TAC
QT Input Channel

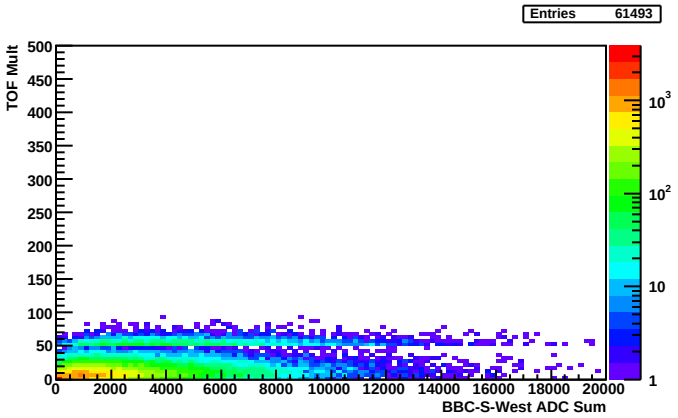

BBQ-VP004 (HI threshold)
Entries 983888
ADC
QT Input Channel

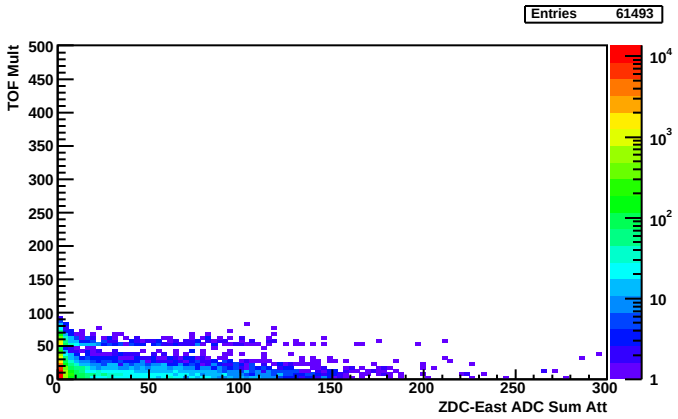

BBQ-VP004 (HI threshold)
Entries 983888
TAC
QT Input Channel

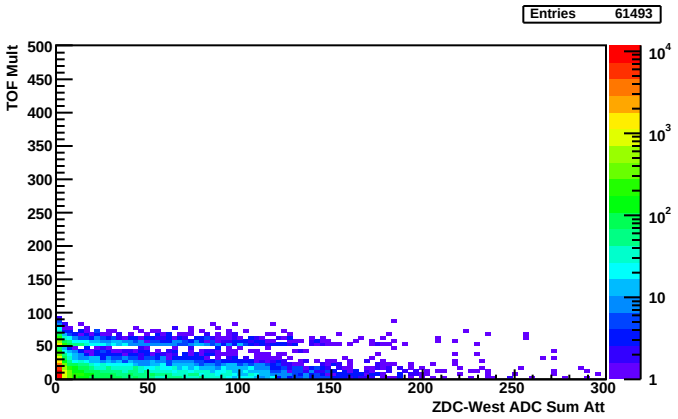

Entries 61493
TOF Mult
BBC-L-East ADC Sum

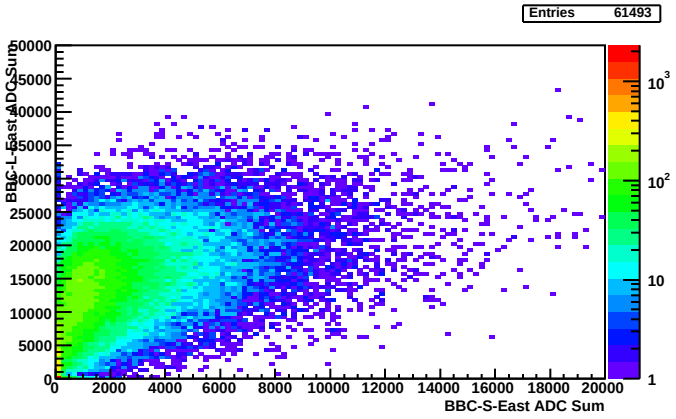

Entries 61493
TOF Mult
BBC-L-West ADC Sum

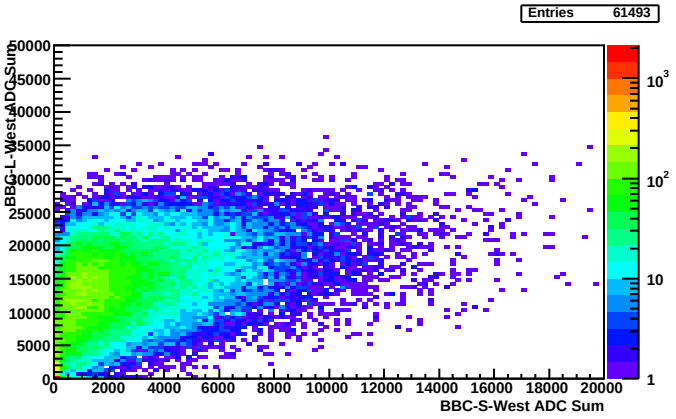

Entries 61493
TOF Mult
BBC-S-East ADC Sum

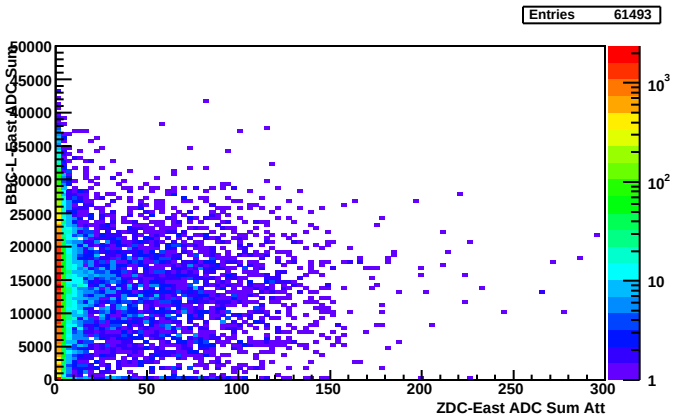

Entries 61493
TOF Mult
BBC-S-West ADC Sum

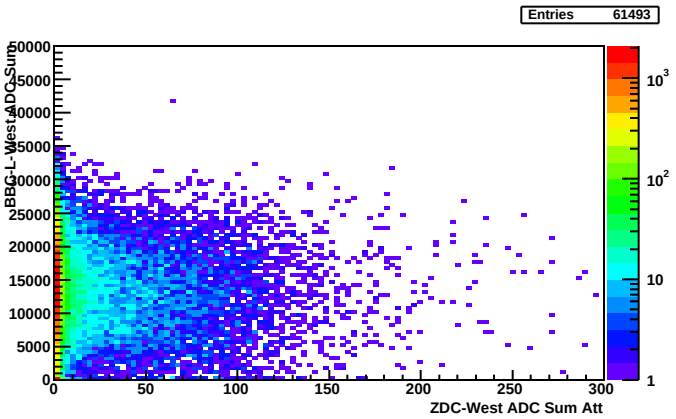

Entries 61493
TOF Mult
ZDC-East ADC Sum Att

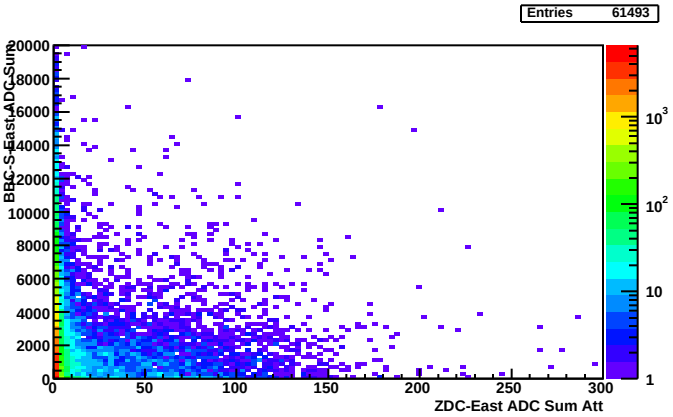

Entries 61493
TOF Mult
ZDC-West ADC Sum Att

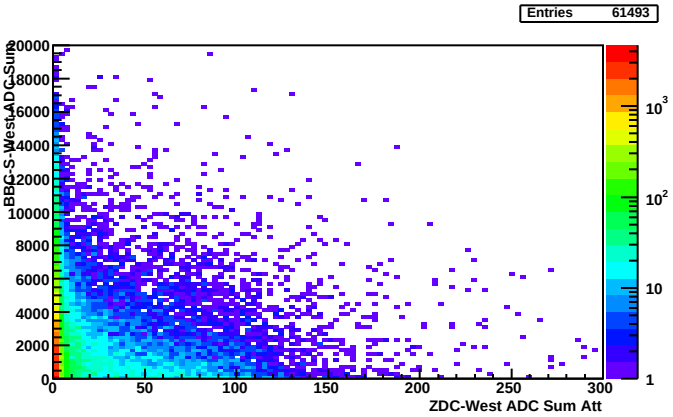

Entries 61493
BBC-L-East ADC Sum
BBC-S-East ADC Sum

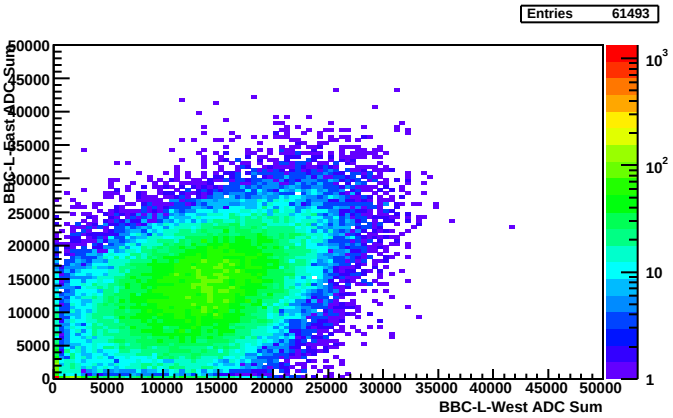

Entries 61493
BBC-L-West ADC Sum
BBC-S-West ADC Sum

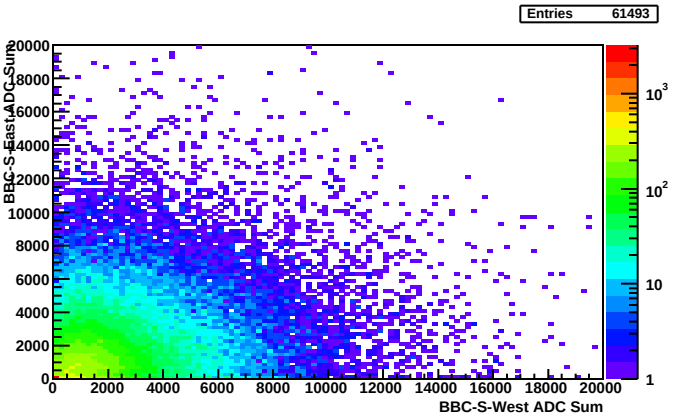

Entries 61493
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

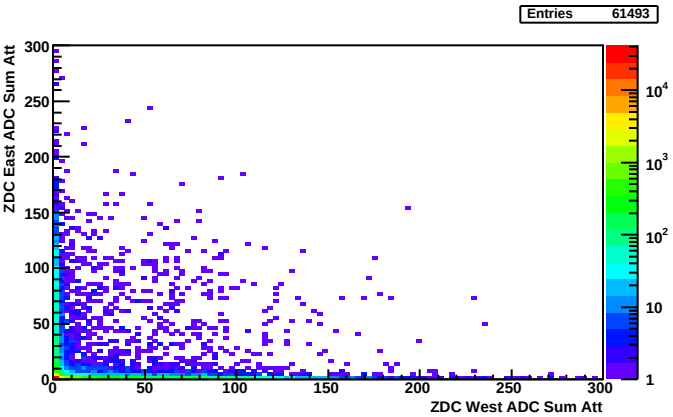

Entries 61493
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

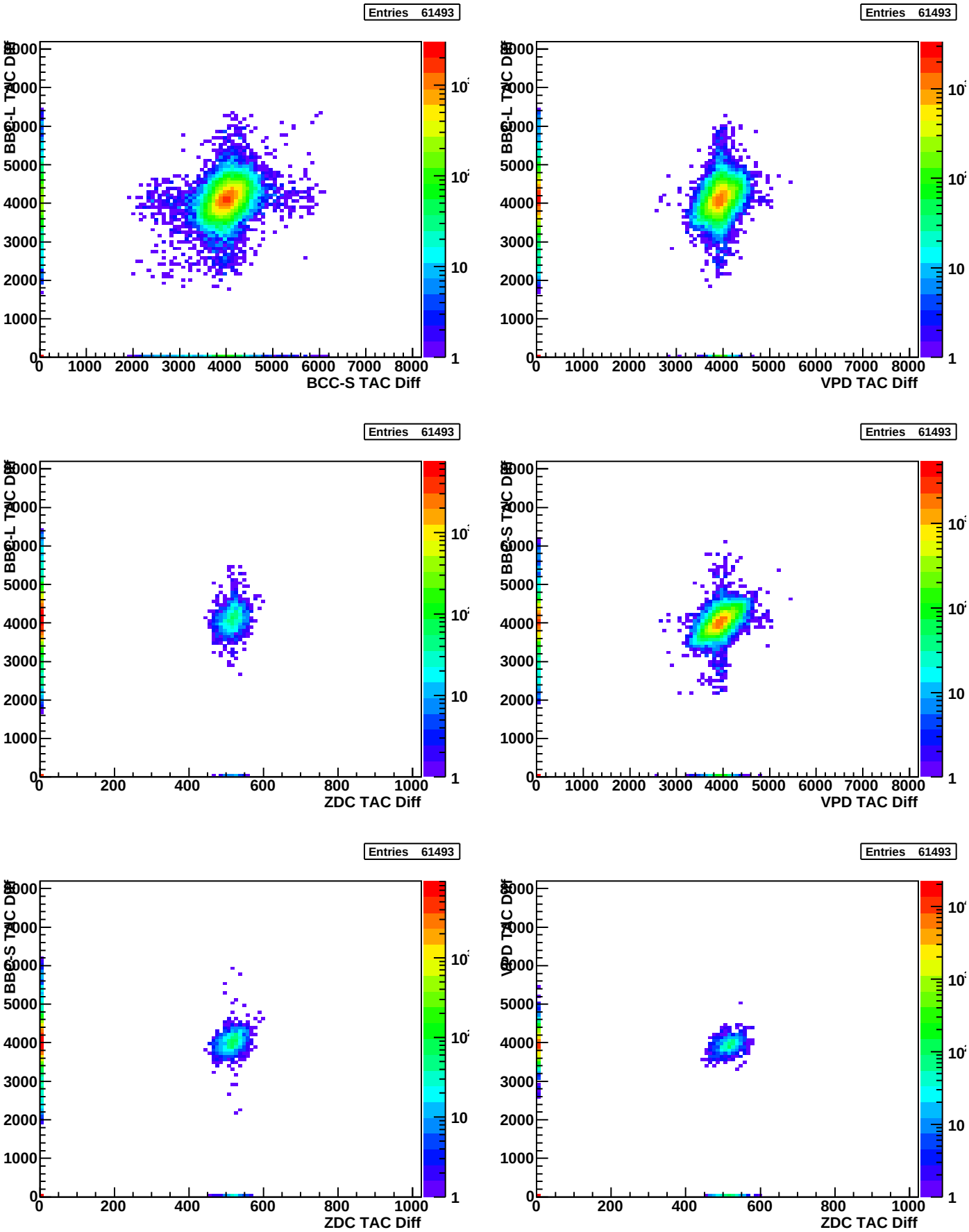

Entries 61493
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

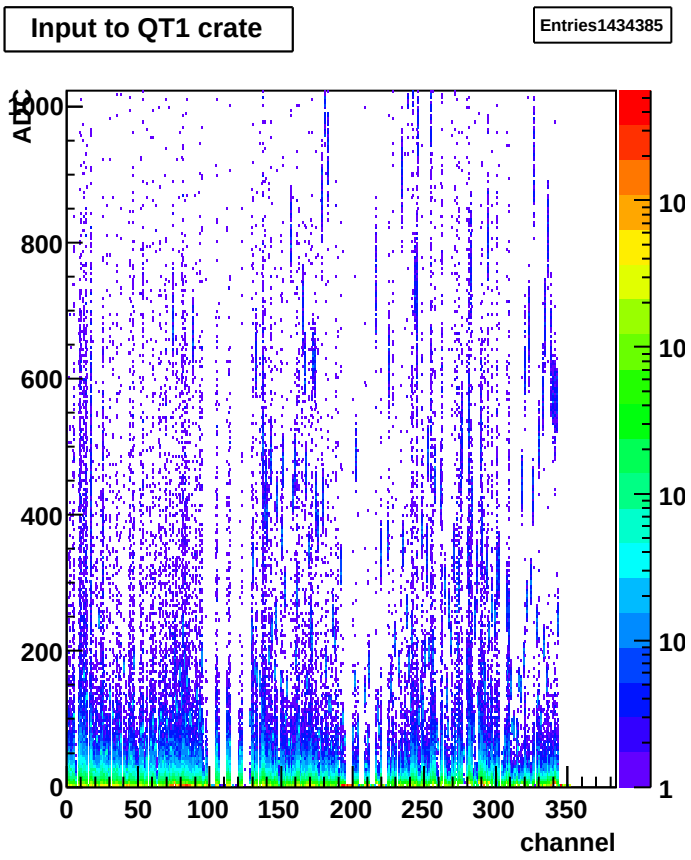

Entries 61493
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

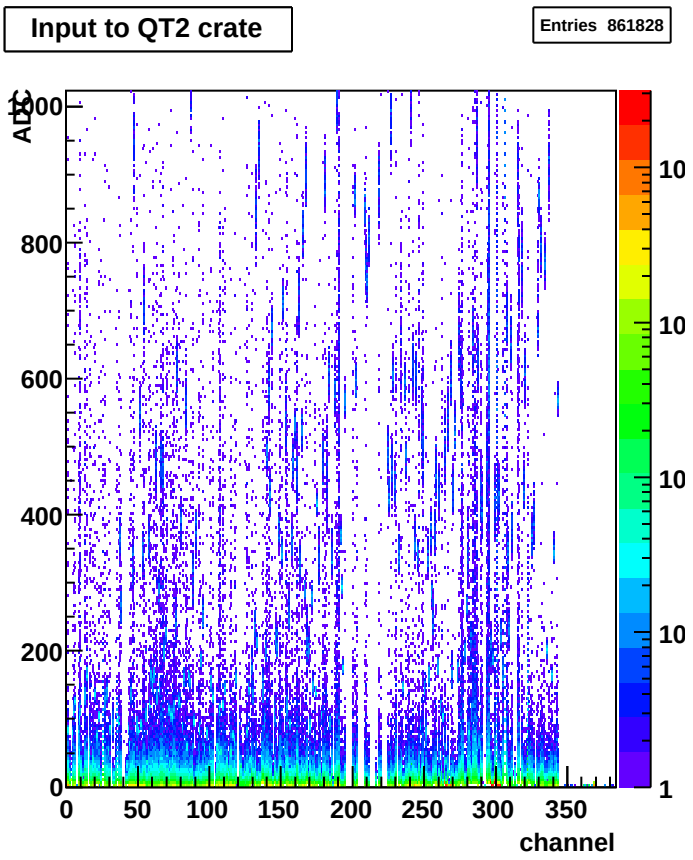

Entries 61493
BBC-L-East ADC Sum
BBC-L-West ADC Sum

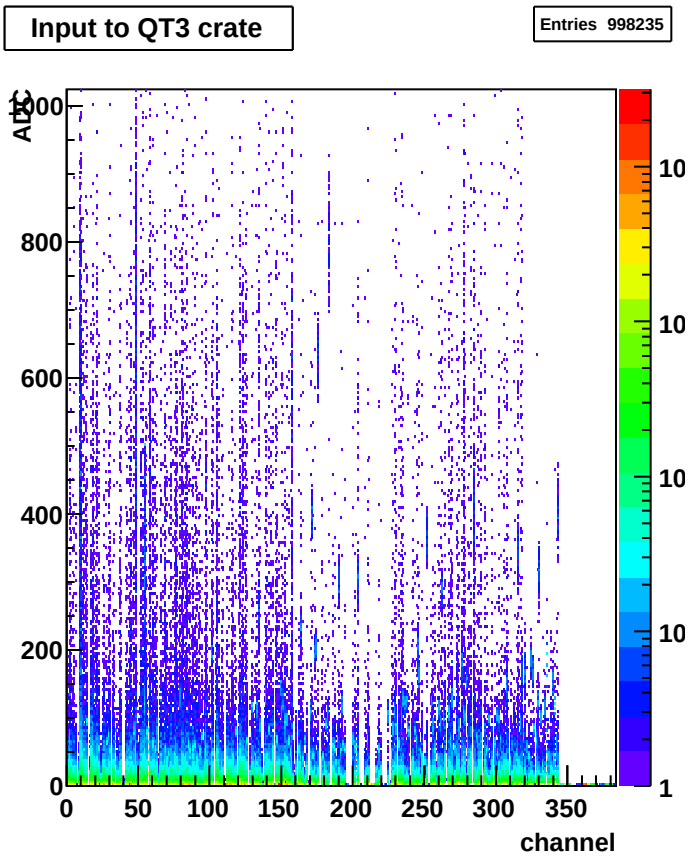

Entries 61493
BBC-S-East ADC Sum
BBC-S-West ADC Sum

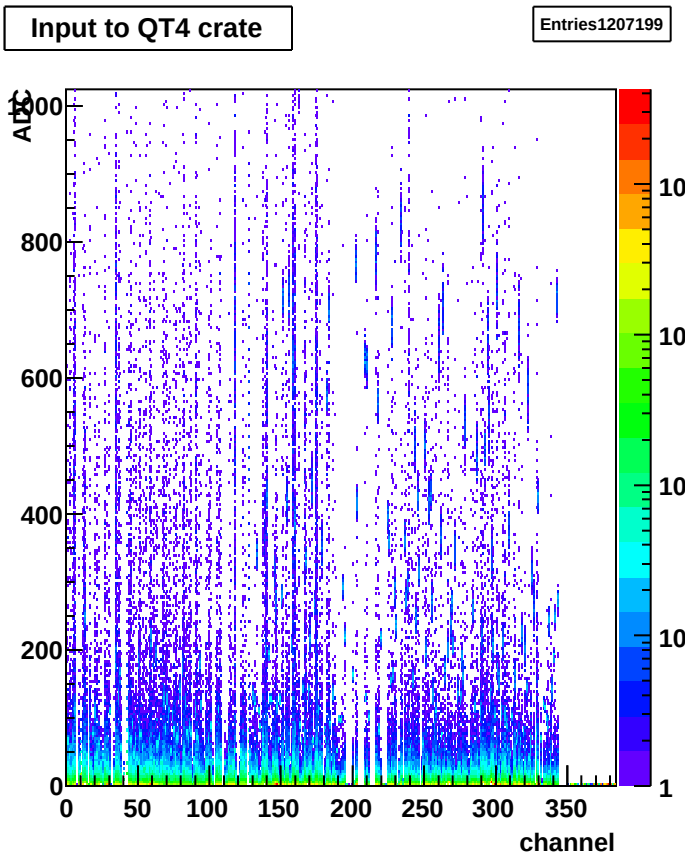

Entries 61493
ZDC East ADC Sum Att
ZDC West ADC Sum Att

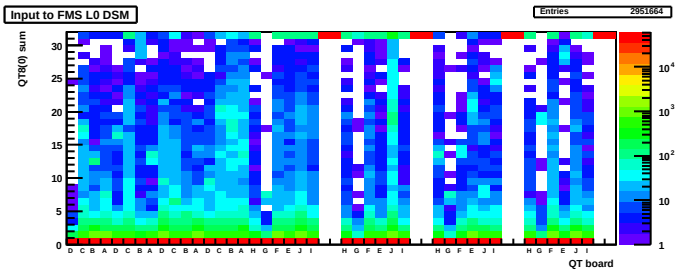

Entries 61493
BBC-L TAC Diff
BCC-S TAC Diff
Entries 61493
BBC-L TAC Diff
VPD TAC Diff
Entries 61493
BBC-L TAC Diff
ZDC TAC Diff
Entries 61493
BBC-S TAC Diff
VPD TAC Diff
Entries 61493
BBC-S TAC Diff
ZDC TAC Diff
Entries 61493
VPD TAC Diff
ZDC TAC Diff

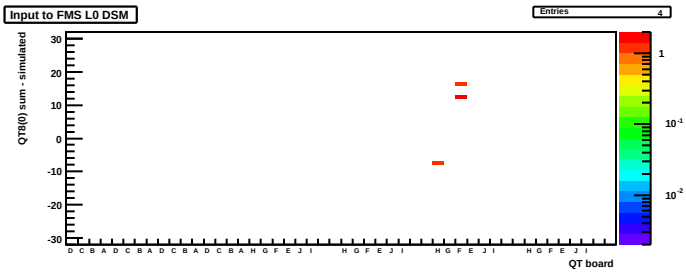

Input to QT1 crate
Entries1434385
ADC
channel

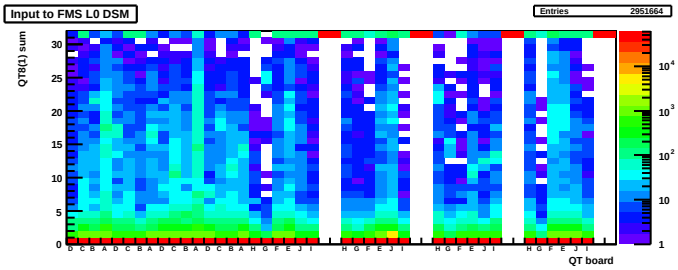

Input to QT2 crate
Entries 861828
ADC
channel

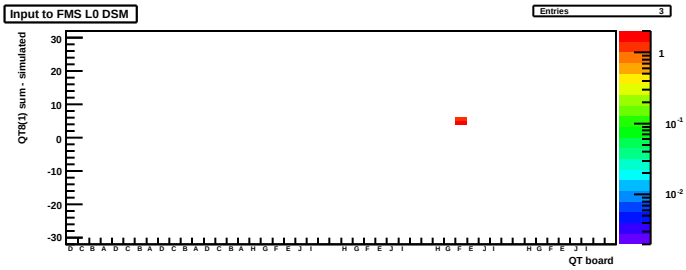

Input to QT3 crate
Entries 998235
ADC
channel

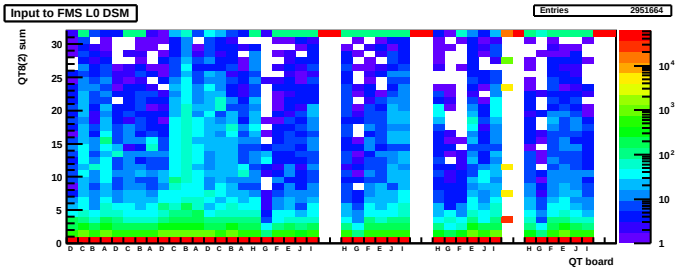

Input to QT4 crate
Entries1207199
ADC
channel

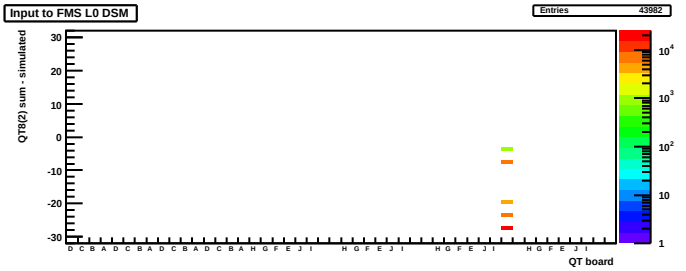

Input to FMS L0 DSM
Entries 2951664
QT8(0) sum
QT board

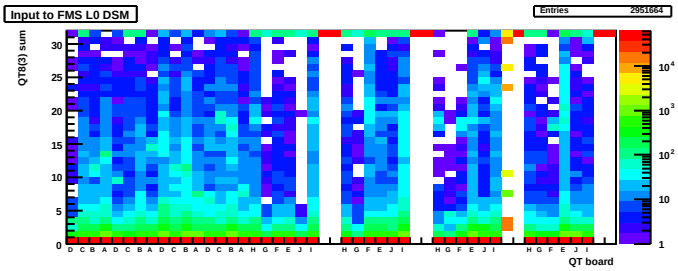

Input to FMS L0 DSM
Entries 4
QT8(0) sum - simulated
QT board

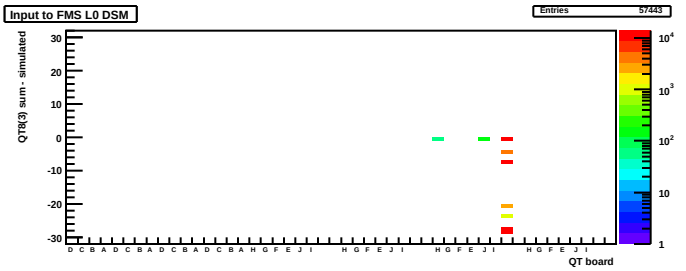

Input to FMS L0 DSM
Entries 2951664
QT8(1) sum
QT board

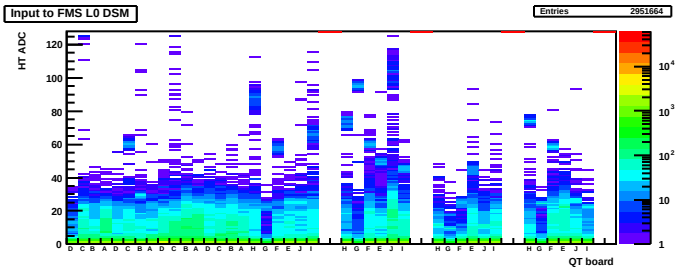

Input to FMS L0 DSM
Entries 3
QT8(1) sum - simulated
QT board

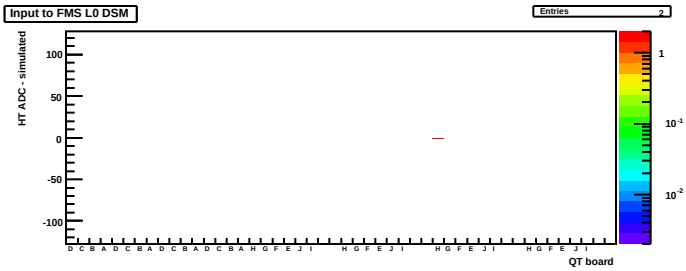

Input to FMS L0 DSM
Entries 2951664
QT8(2) sum
QT board

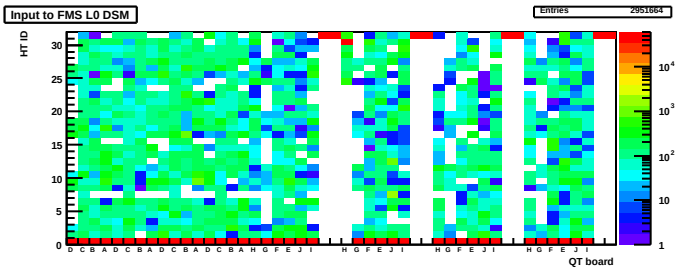

Input to FMS L0 DSM
Entries 43982
QT8(2) sum - simulated
QT board

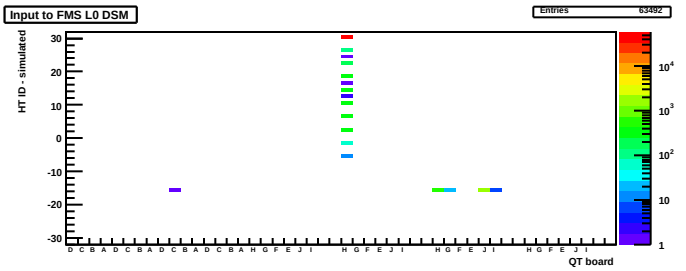

Input to FMS L0 DSM
Entries 2951664
QT8(3) sum
QT board

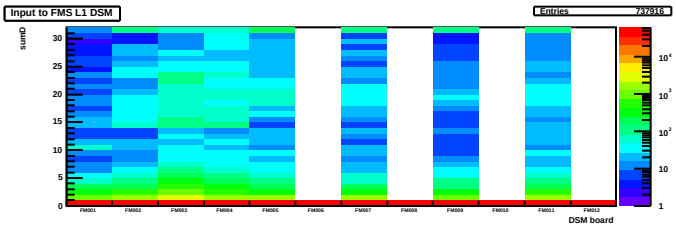

Input to FMS L0 DSM
Entries 57443
QT8(3) sum - simulated
QT board

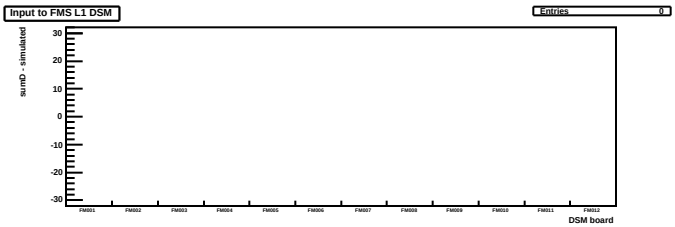

Input to FMS L0 DSM
Entries 2951664
HT ADC
QT board

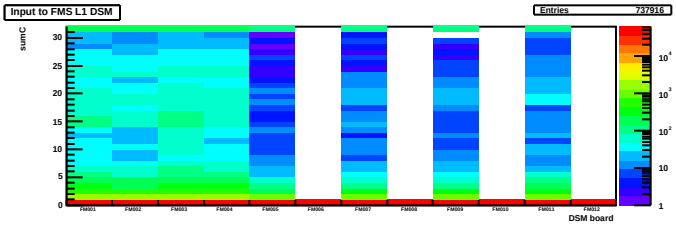

Input to FMS L0 DSM
Entries 2
HT ADC - simulated
QT board

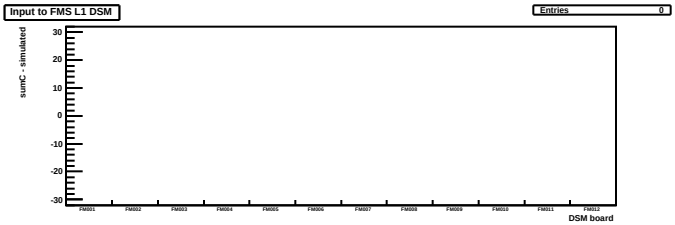

Input to FMS L0 DSM
Entries 2951664
HT ID
QT board

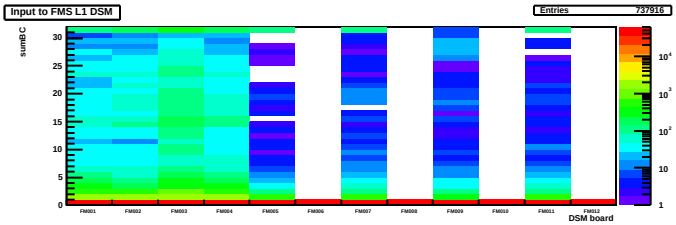

Input to FMS L0 DSM
Entries 63492
HT ID - simulated
QT board

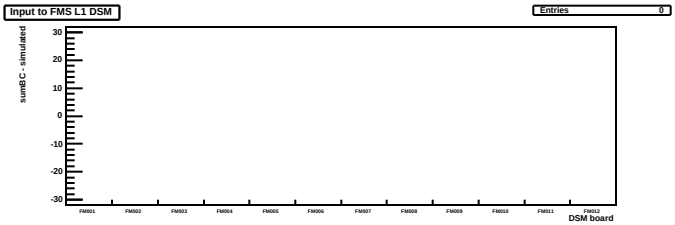

Input to FMS L1 DSM
Entries 737916
sumD
DSM board

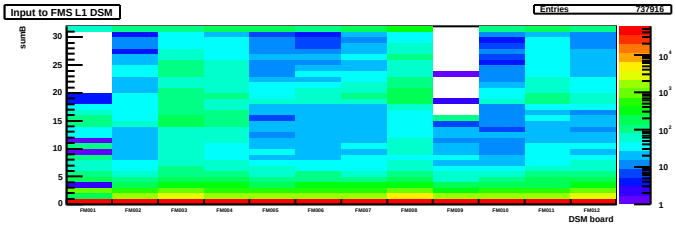

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

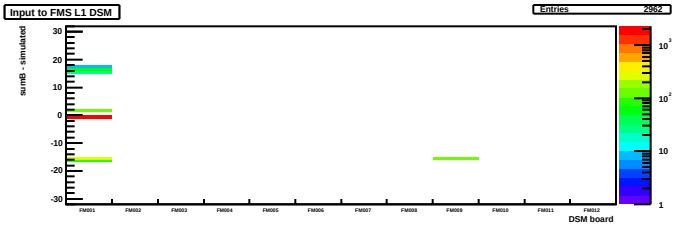

Input to FMS L1 DSM
Entries 737916
sumC
DSM board

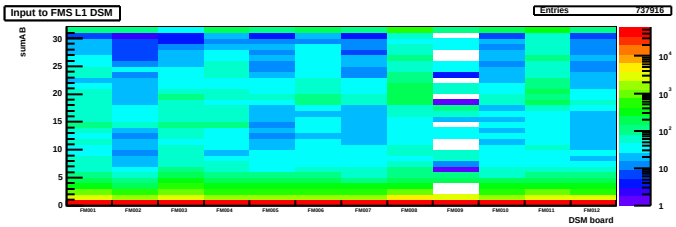

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

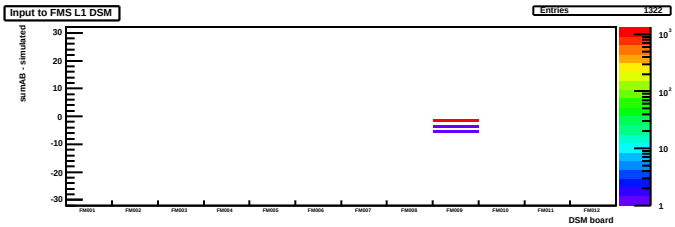

Input to FMS L1 DSM
Entries 737916
sumBC
DSM board

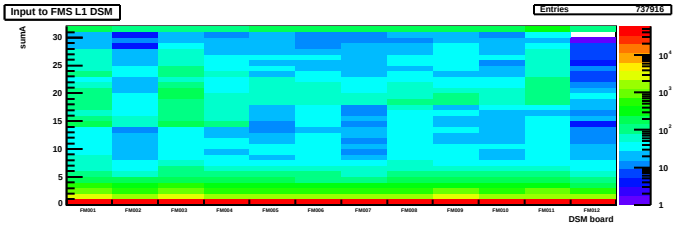

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

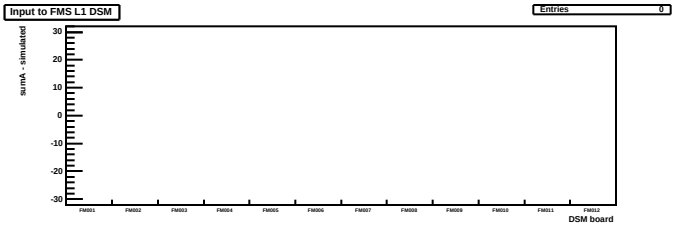

Input to FMS L1 DSM
Entries 737916
sumB
DSM board

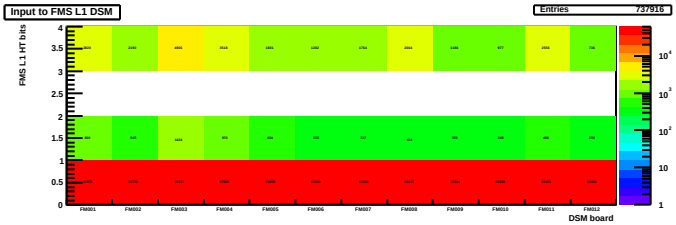

Input to FMS L1 DSM
Entries 2962
sumB - simulated
DSM board

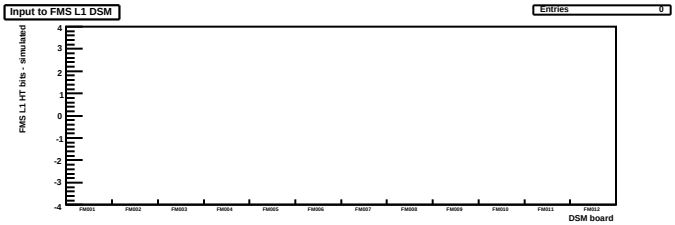

Input to FMS L1 DSM
Entries 737916
sumAB
DSM board

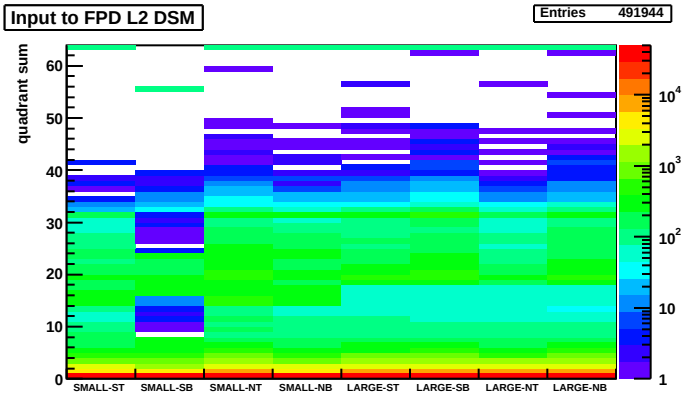

Input to FMS L1 DSM
Entries 1322
sumAB - simulated
DSM board

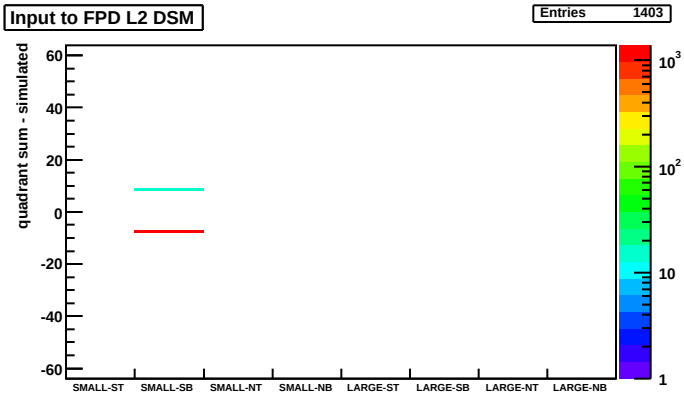

Input to FMS L1 DSM
Entries 737916
sumA
DSM board

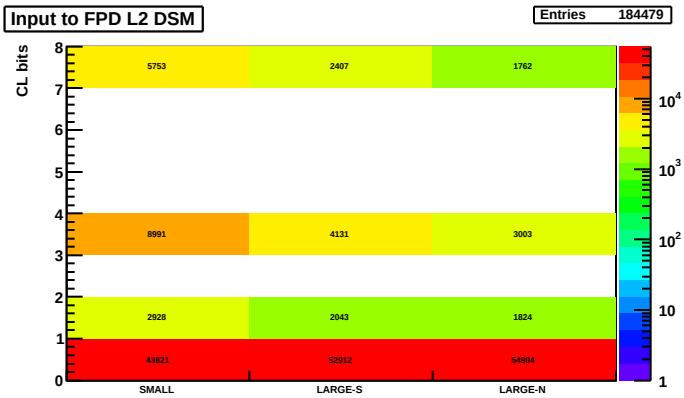

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

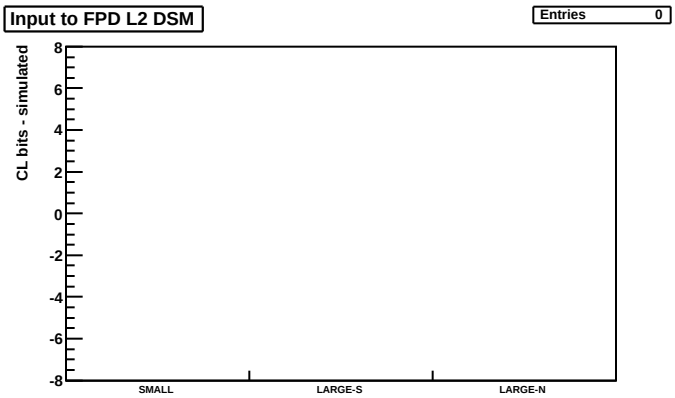

Input to FMS L1 DSM
Entries 737916
FMS L1 HT bits
DSM board

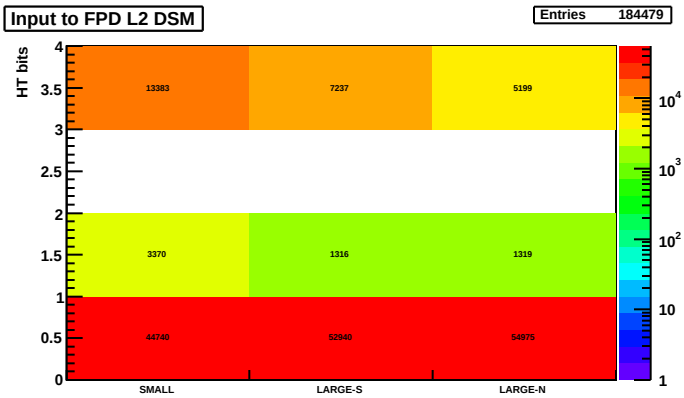

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

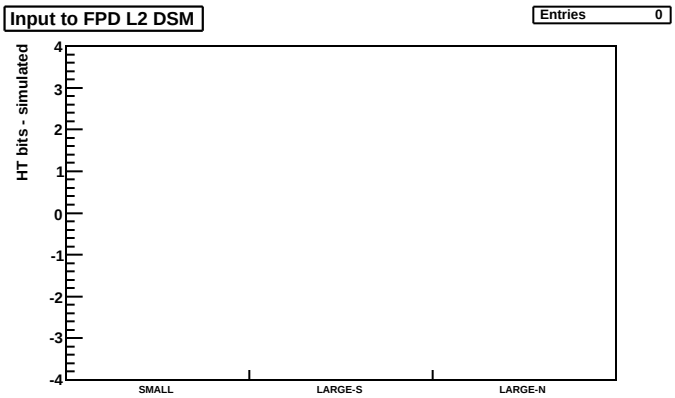

Input to FPD L2 DSM
Entries 491944
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

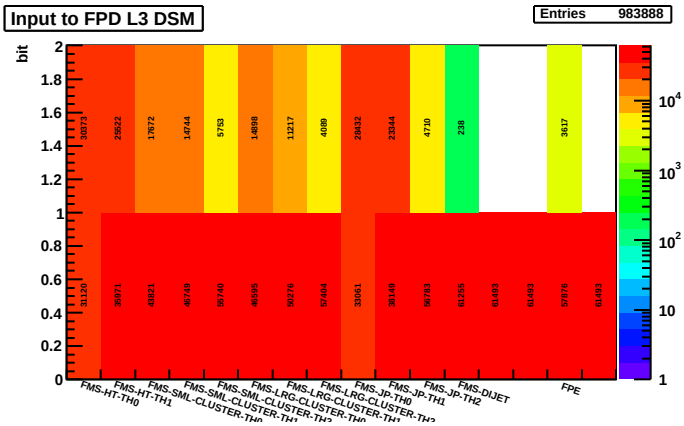

Input to FPD L2 DSM
Entries 1403
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

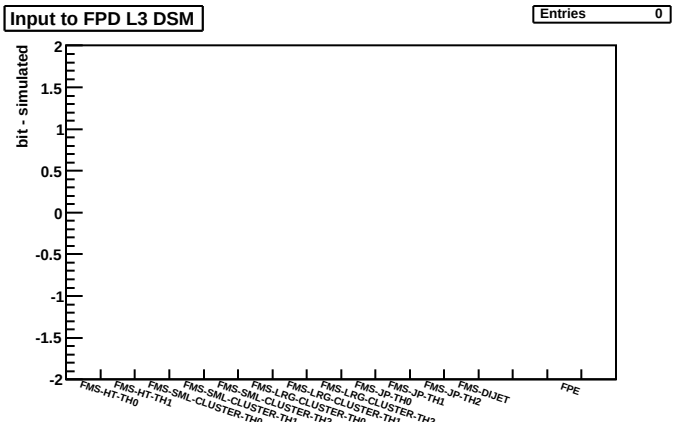

Input to FPD L2 DSM
Entries 184479
CL bits
5753 2407 1762
8991 4131 3003
2928 2043 1824
43821 52912 54904
SMALL LARGE-S LARGE-N

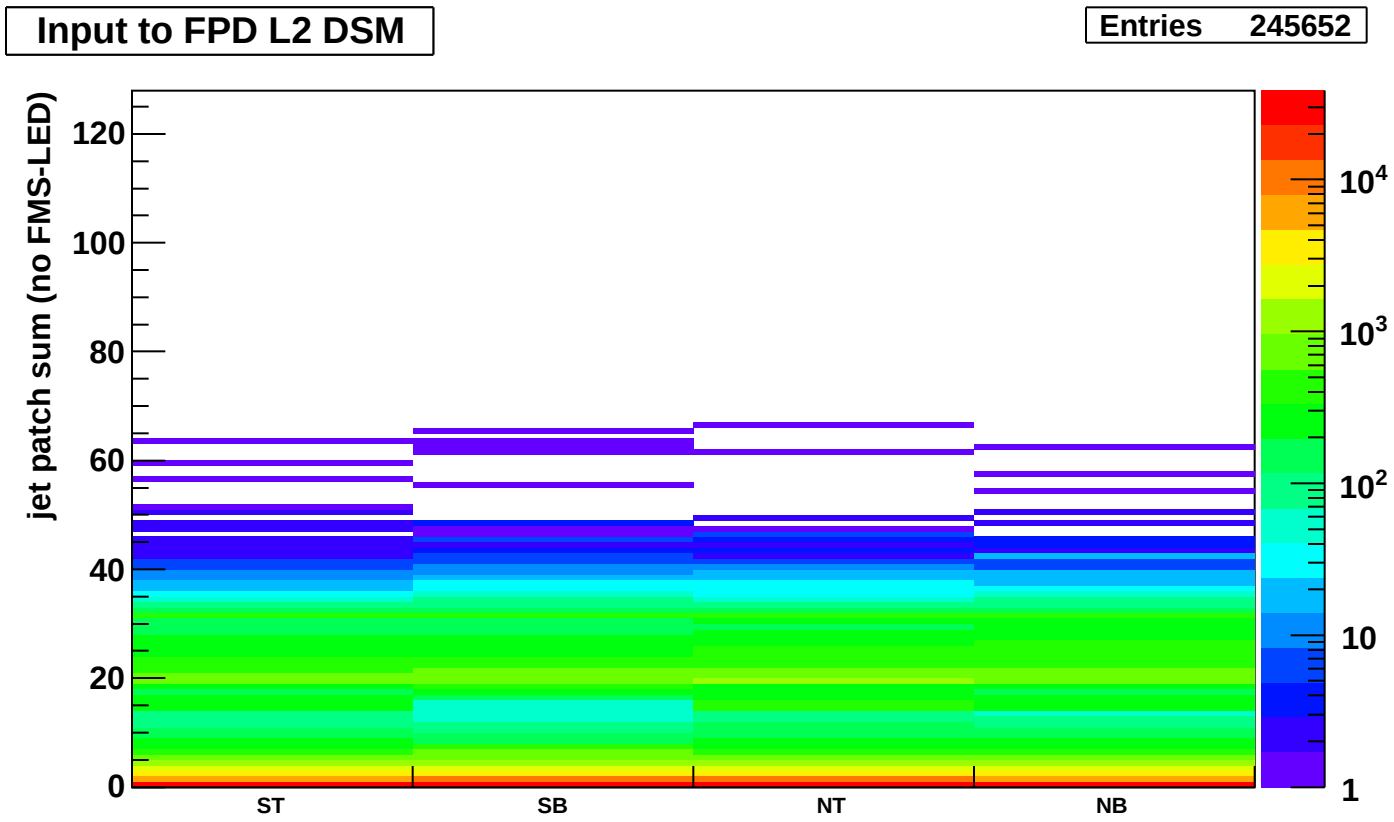

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL LARGE-S LARGE-N

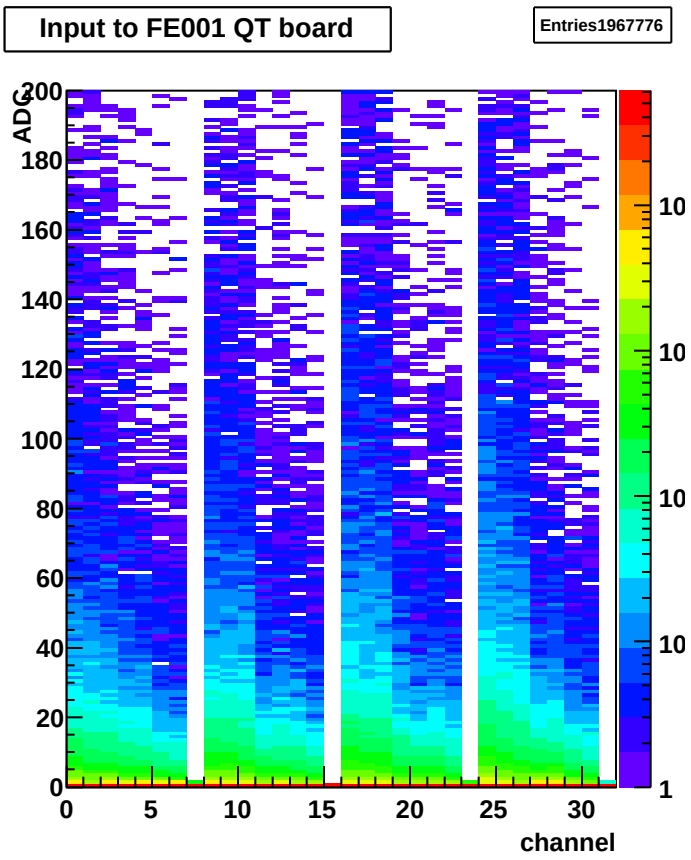

Input to FPD L2 DSM
Entries 184479
HT bits
13383 7237 5199
3370 1316 1319
44740 52940 54975
SMALL LARGE-S LARGE-N

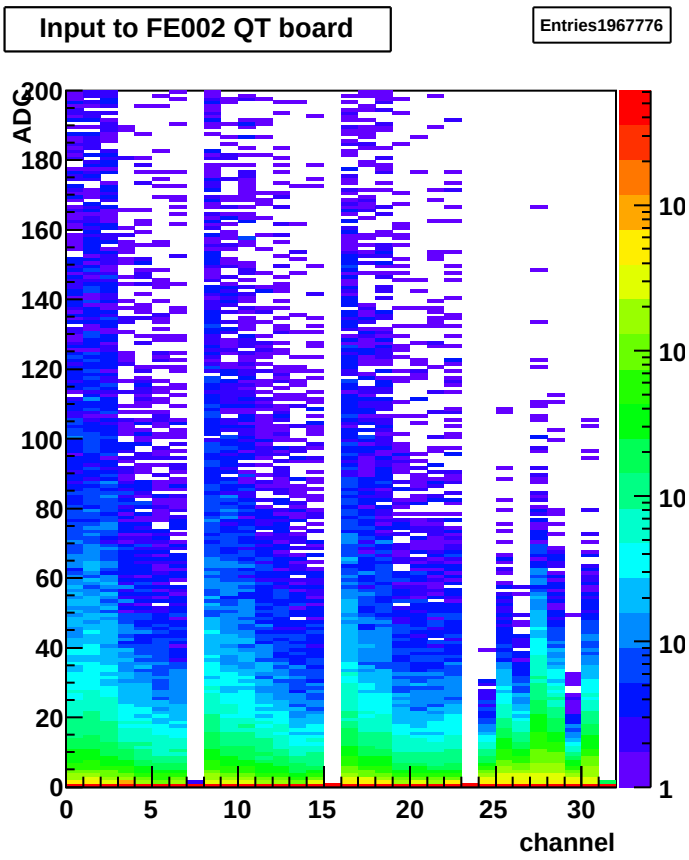

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

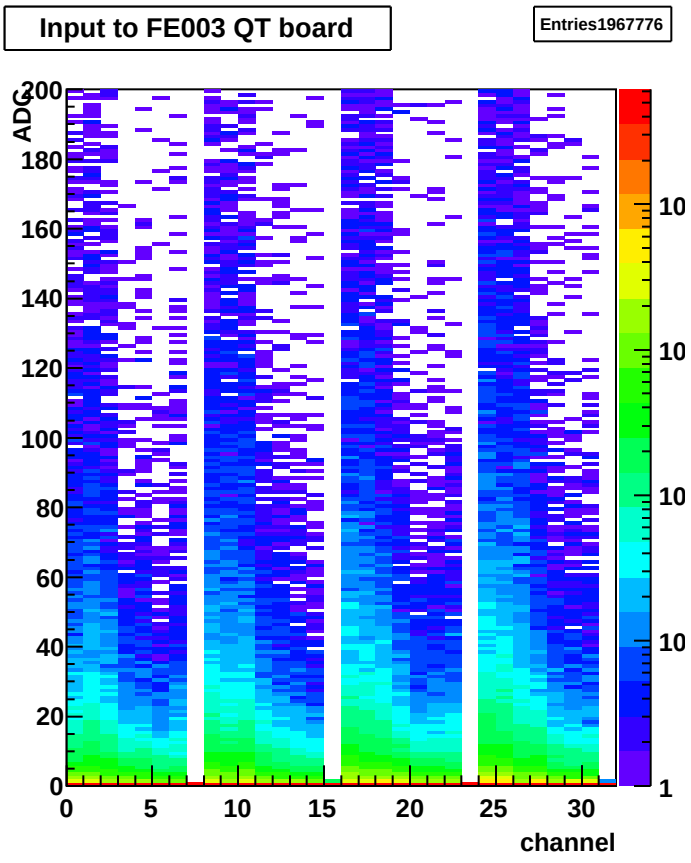

Input to FPD L3 DSM
Entries 983888
bit
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

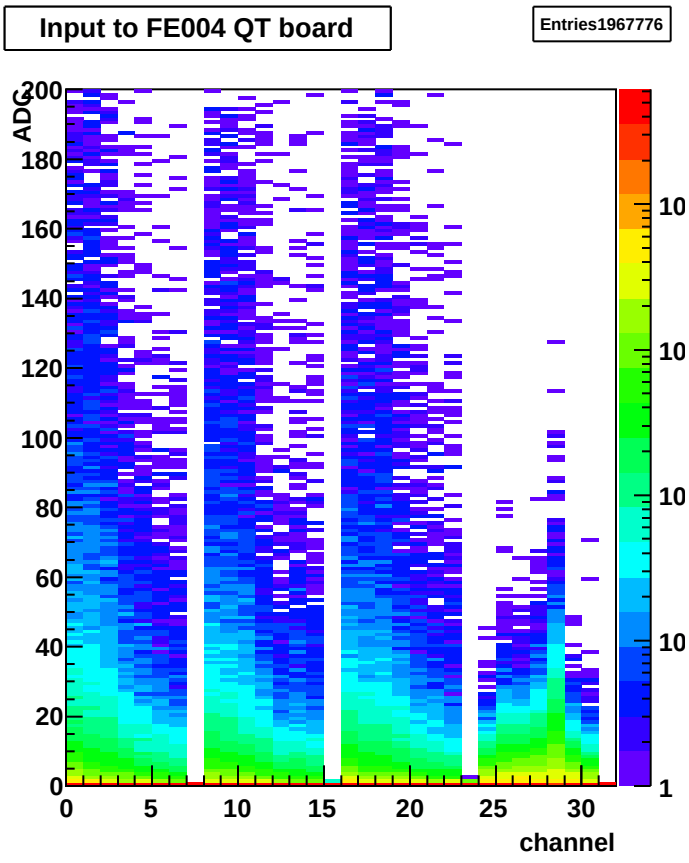

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

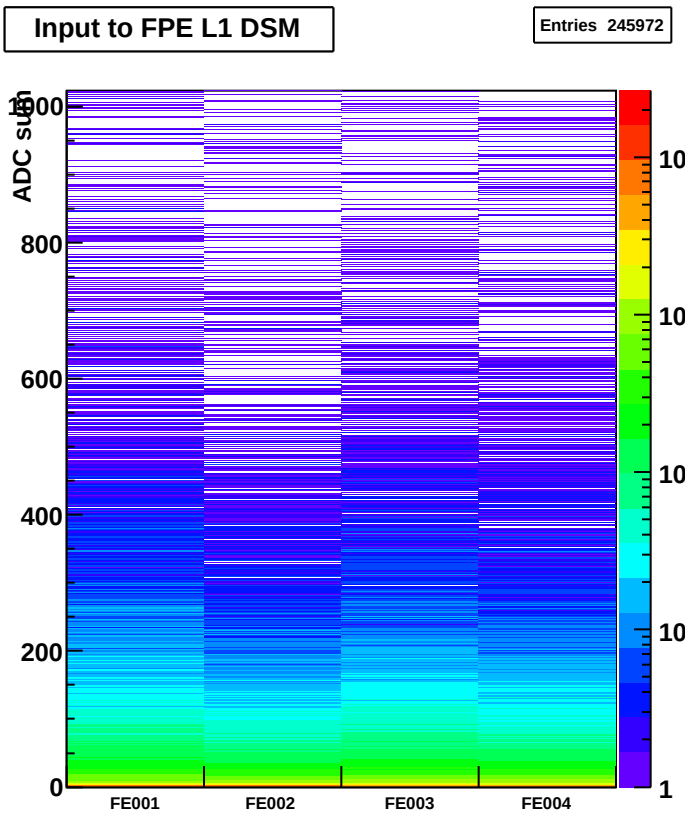

Input to FPD L2 DSM
Entries 245652
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

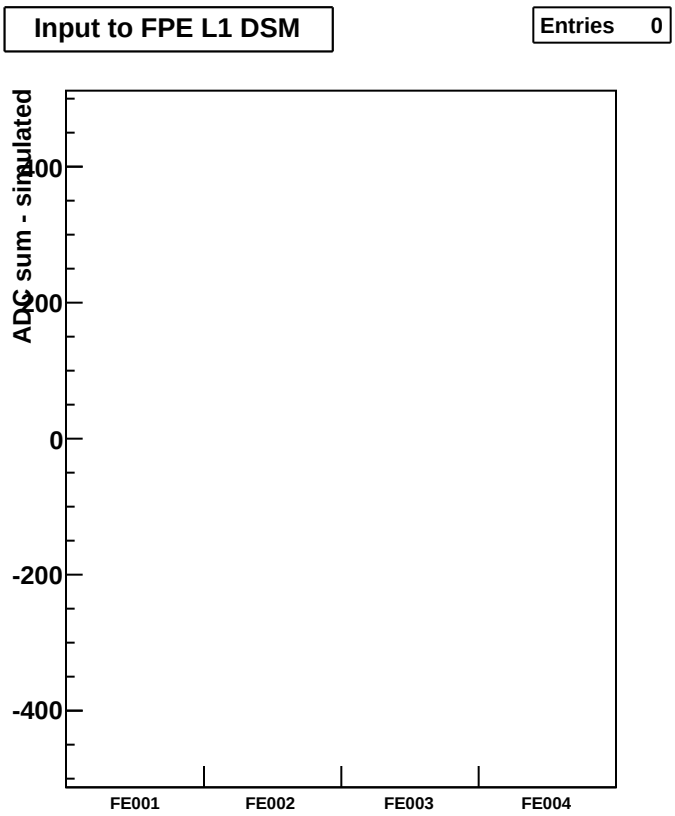

Input to FE001 QT board
Entries1967776
ADC
channel

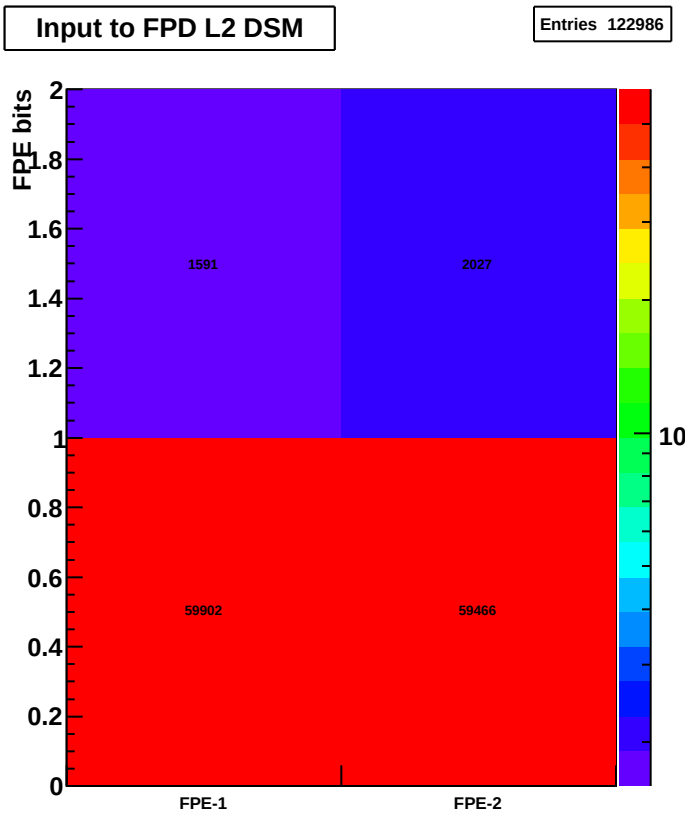

Input to FE002 QT board
Entries1967776
ADC
channel

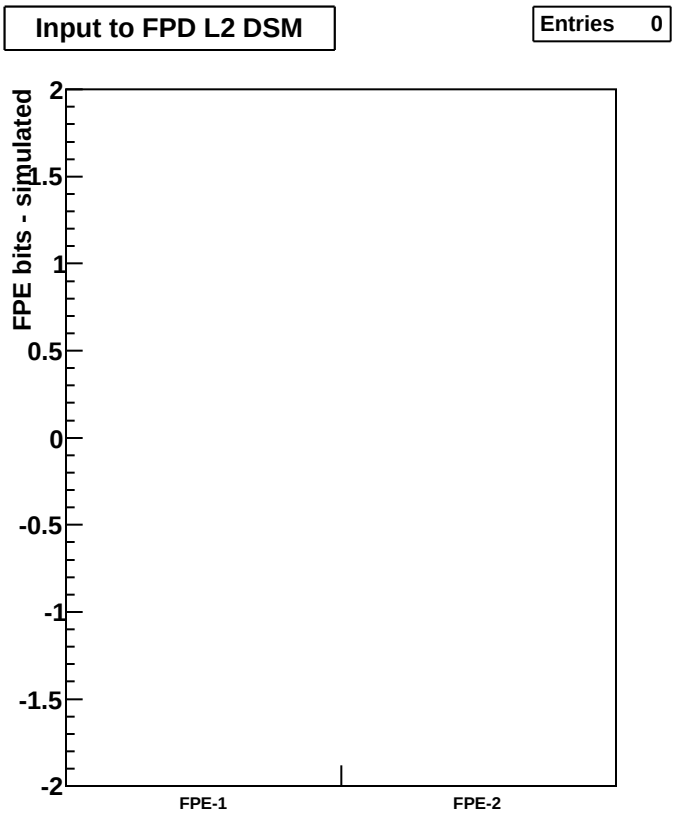

Input to FE003 QT board
Entries1967776
ADC
channel


Input to FE004 QT board
Entries1967776
ADC
channel


Input to FPE L1 DSM
Entries 245972
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 122986
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
1591
2027
59902
59466
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2