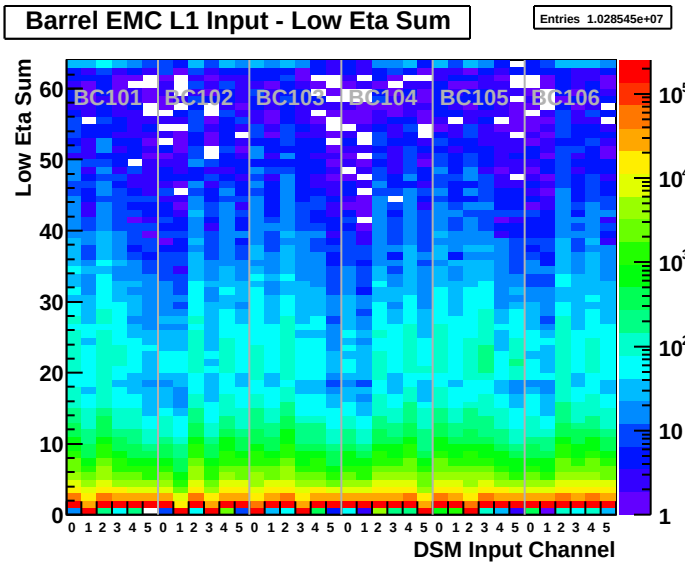
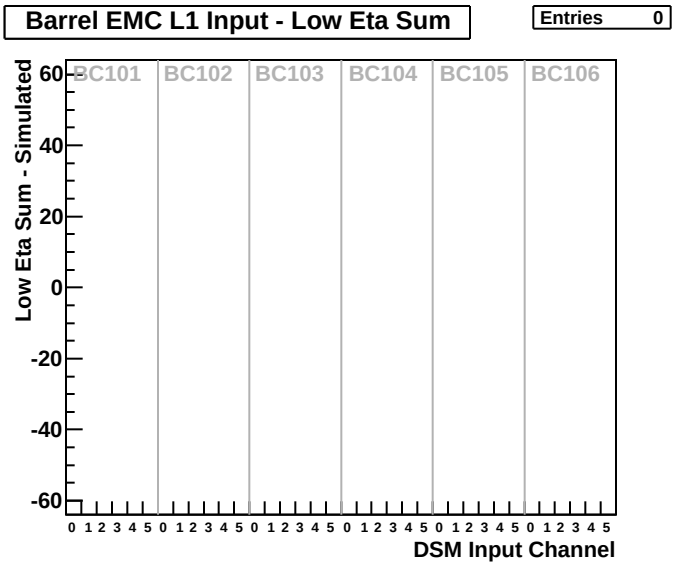
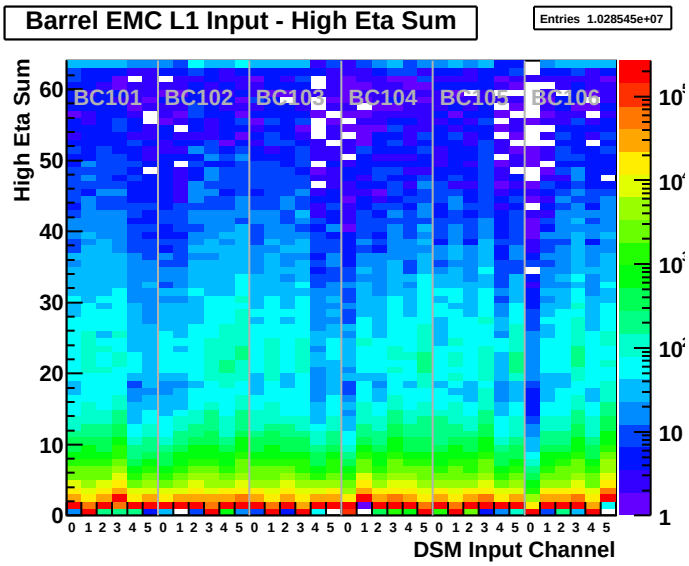
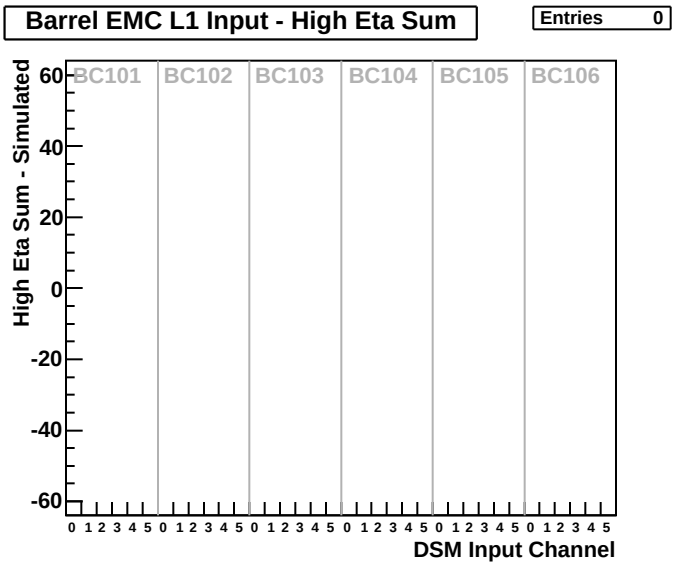
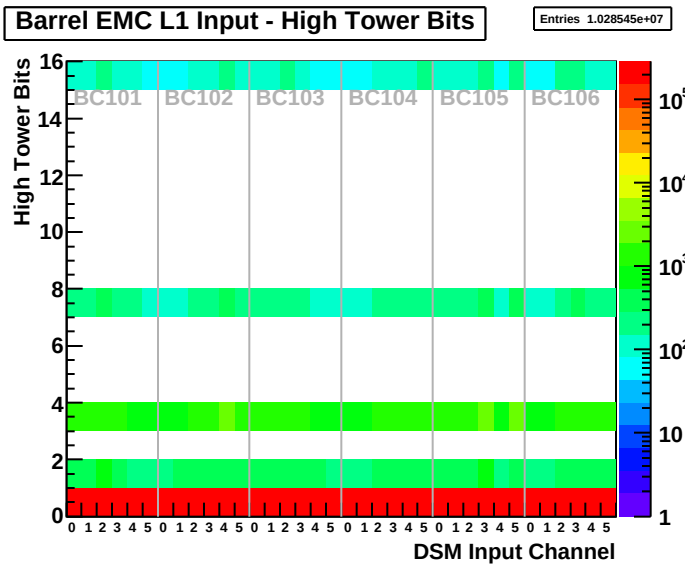
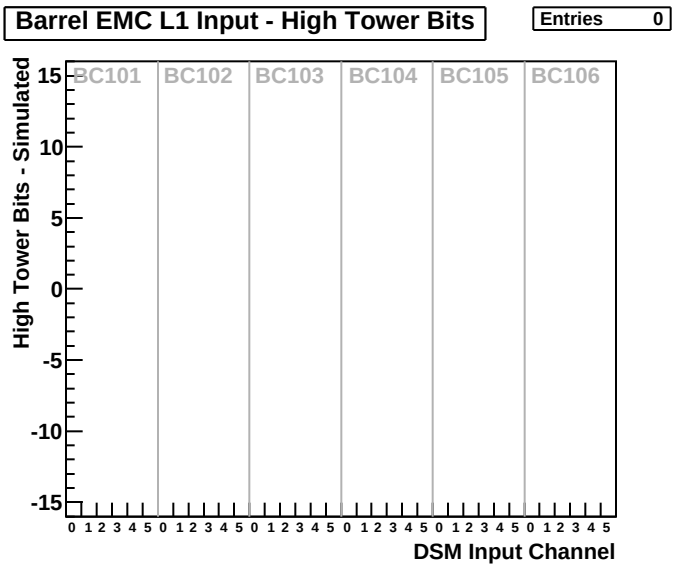

Barrel EMC L1 Input - Low Eta Sum
Entries 1.028545e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

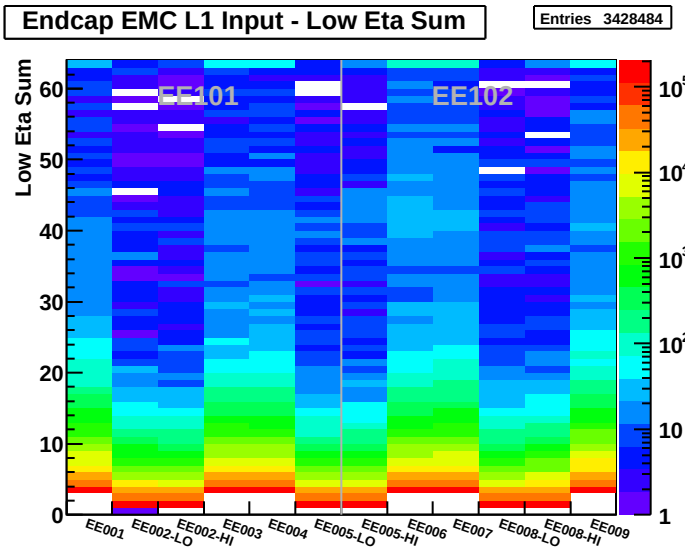

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

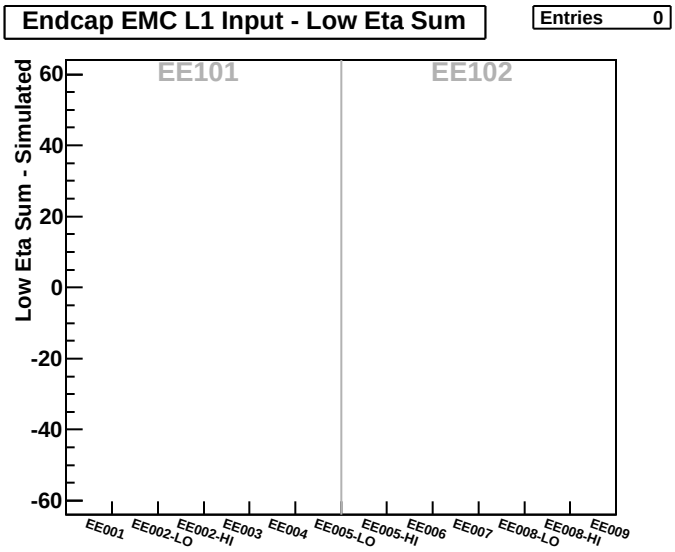

Barrel EMC L1 Input - High Eta Sum
Entries 1.028545e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

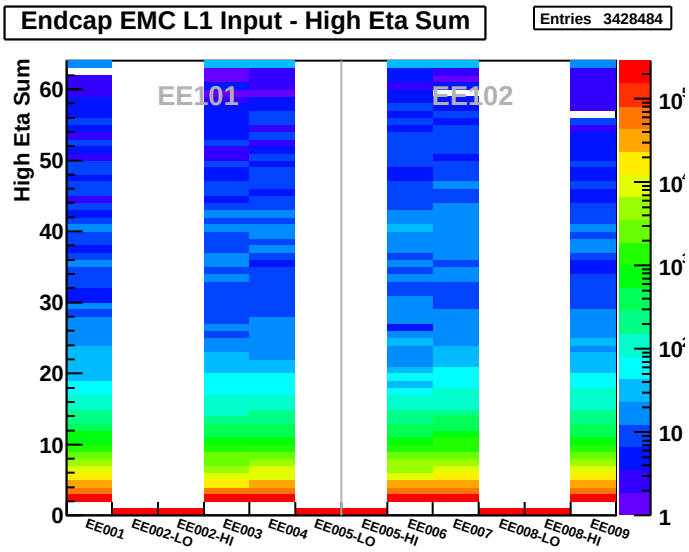

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

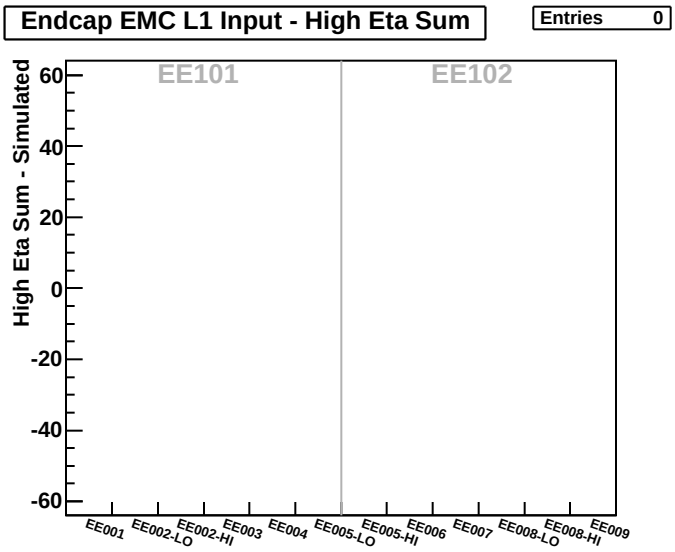

Barrel EMC L1 Input - High Tower Bits
Entries 1.028545e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

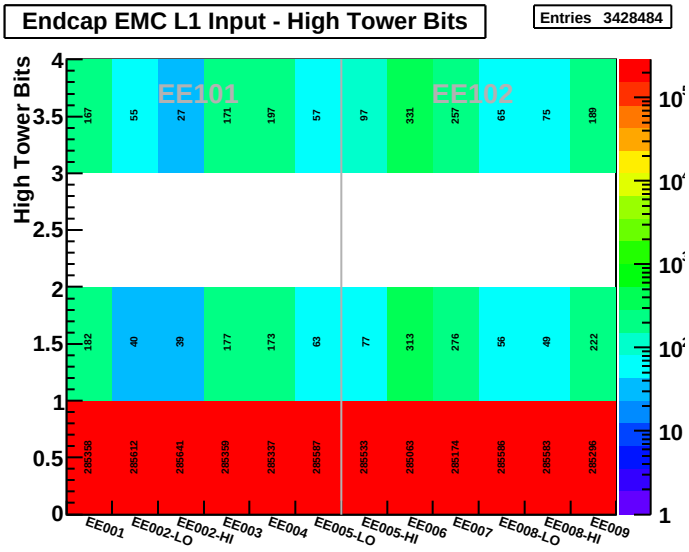

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

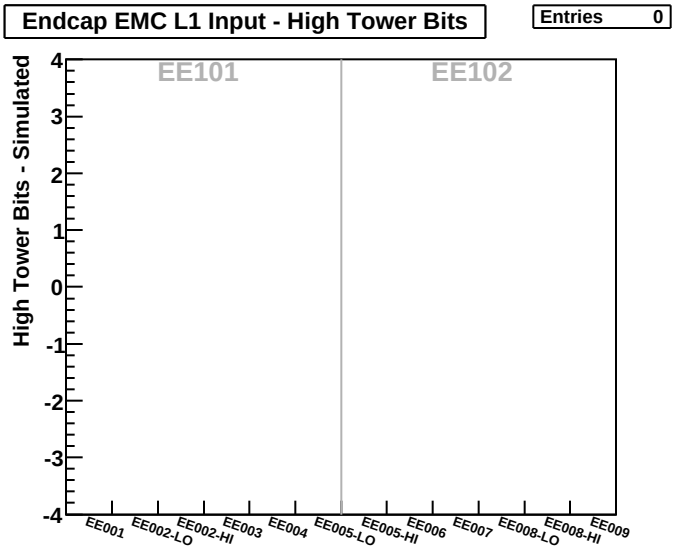

Endcap EMC L1 Input - Low Eta Sum
Entries 3428484
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

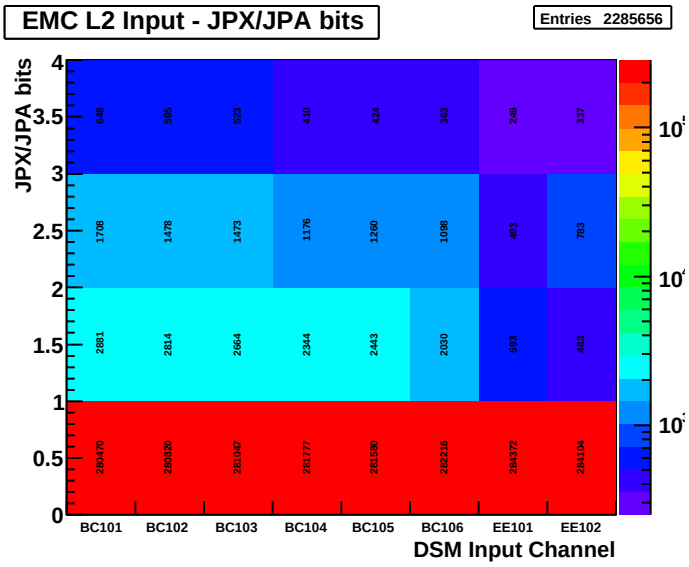

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

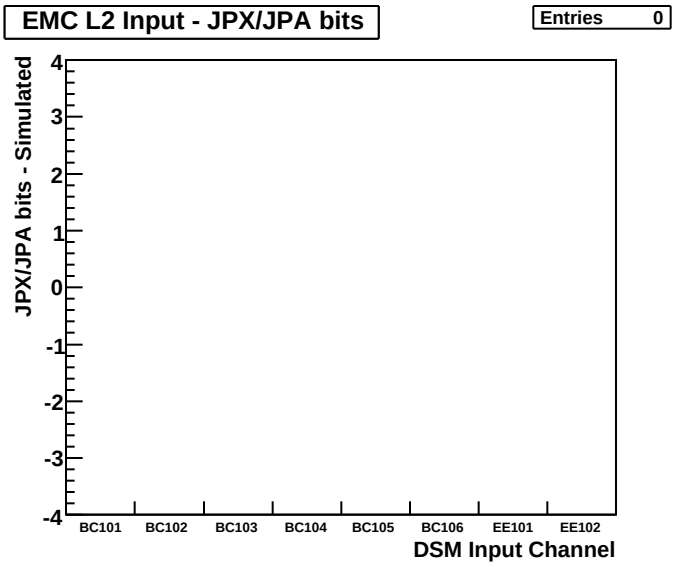

Endcap EMC L1 Input - High Eta Sum
Entries 3428484
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

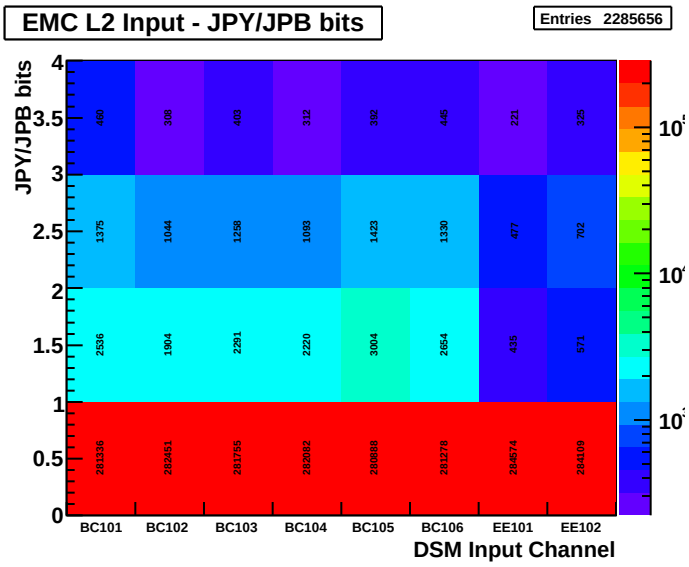

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

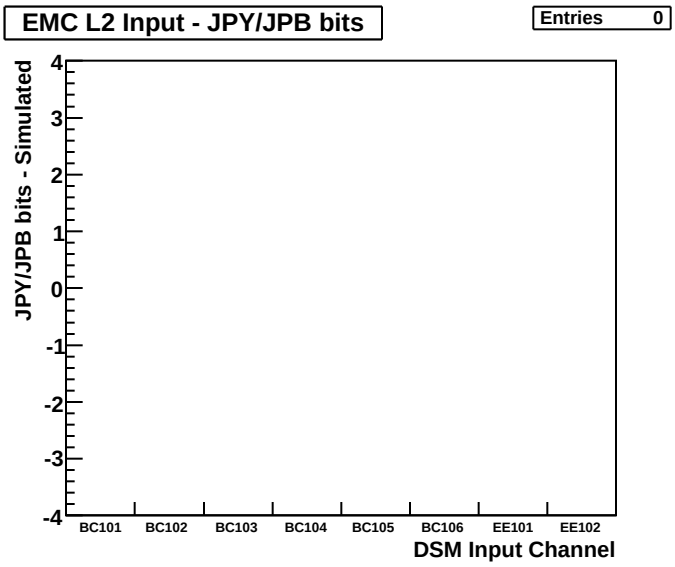

Endcap EMC L1 Input - High Tower Bits
Entries 3428484
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

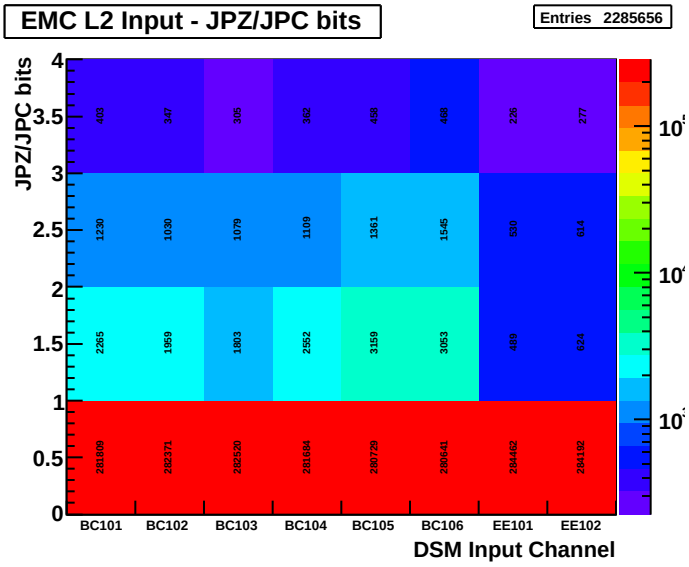

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

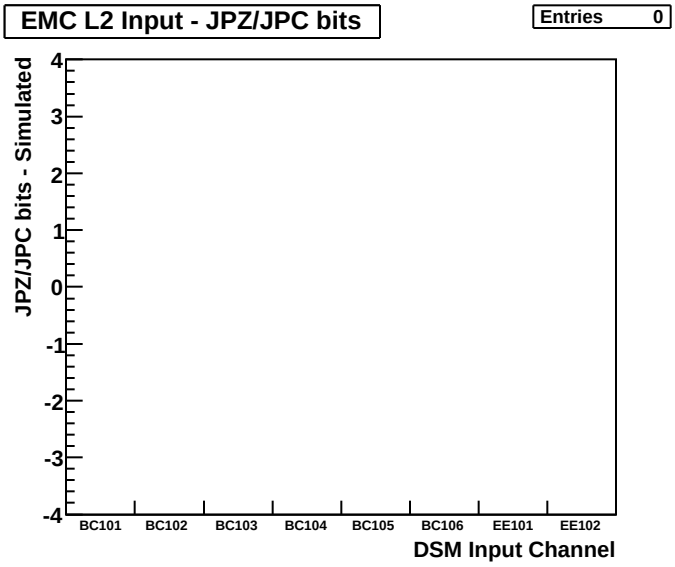

EMC L2 Input - JPX/JPA bits
Entries 2285656
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
648 595 523 410 424 363 249 337
1708 1478 1473 1176 1260 1098 493 783
2881 2814 2664 2344 2443 2030 593 483
280470 280820 281047 281777 281580 282216 284372 284104

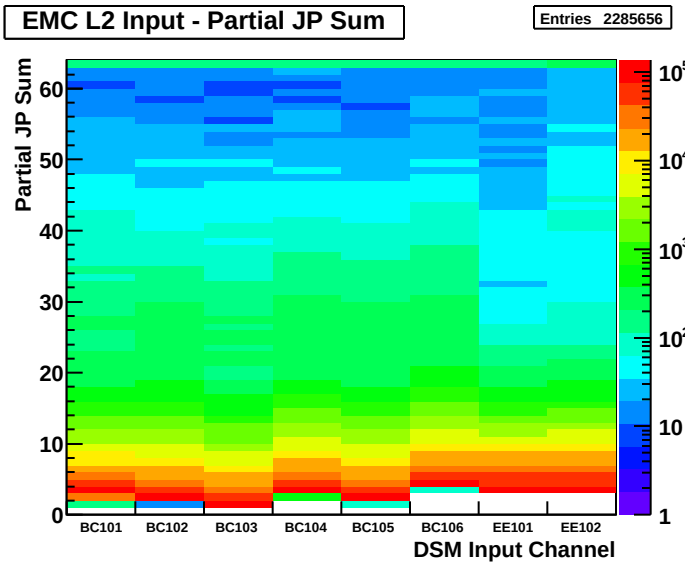

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

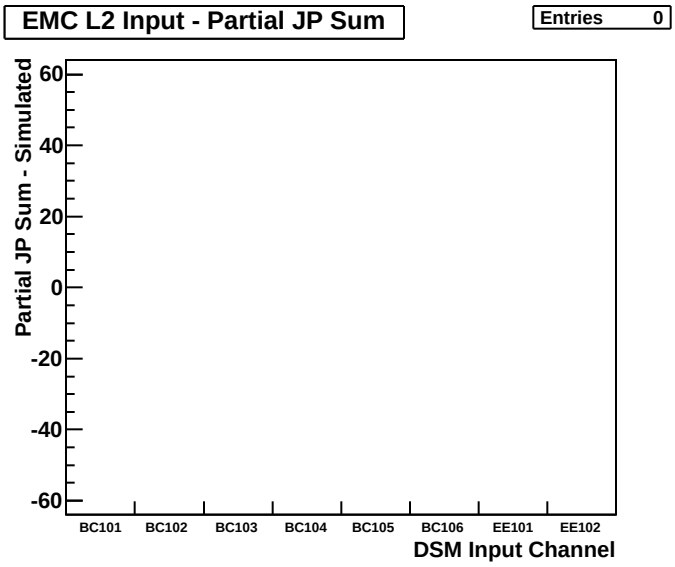

EMC L2 Input - JPY/JPB bits
Entries 2285656
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
469 308 403 312 392 445 221 325
1375 1044 1258 1093 1423 1330 477 702
2536 1904 2291 2220 3004 2654 435 571
281336 282451 281755 282082 280888 281278 284574 284109

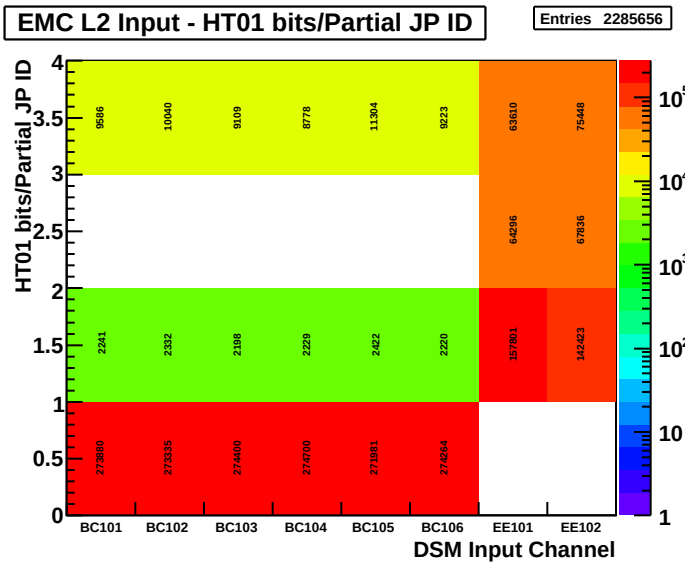

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

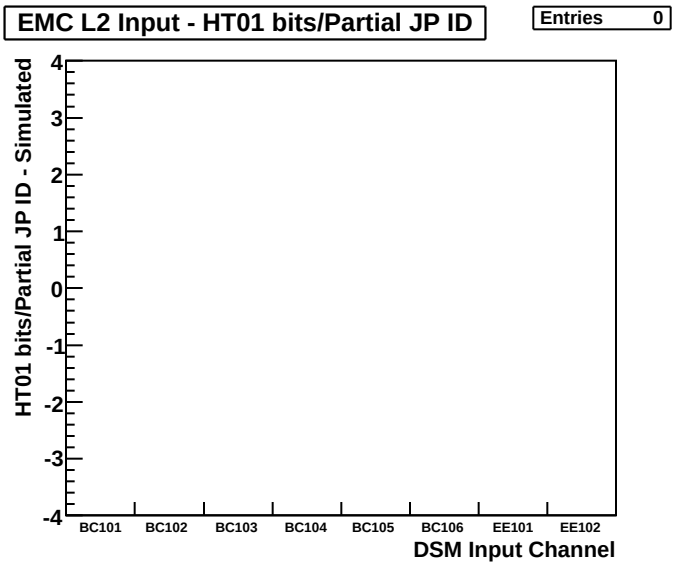

EMC L2 Input - JPZ/JPC bits
Entries 2285656
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
403 347 305 362 458 468 226 277
1230 1030 1079 1109 1361 1545 530 614
2265 1969 1803 2562 3159 3063 499 624
281609 282371 282520 281684 280729 280641 284462 284102

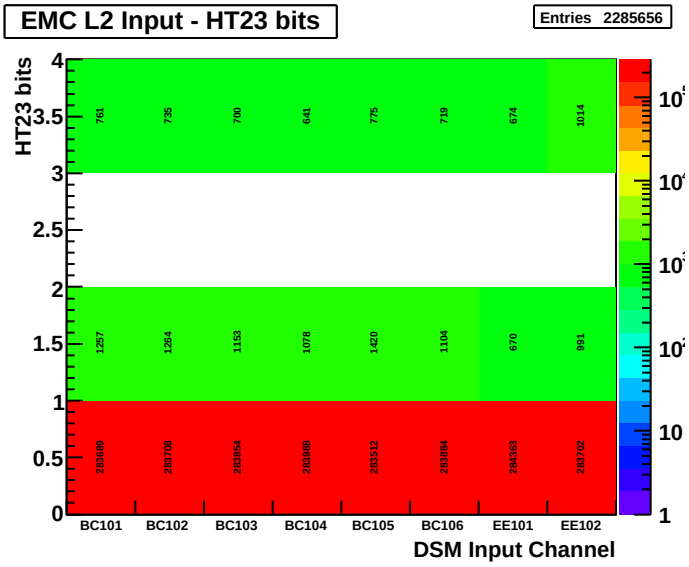

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

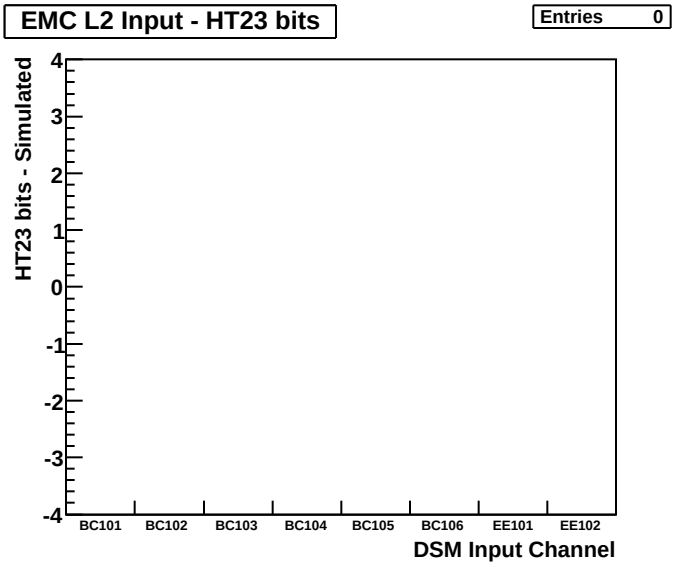

EMC L2 Input - Partial JP Sum
Entries 2285656
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

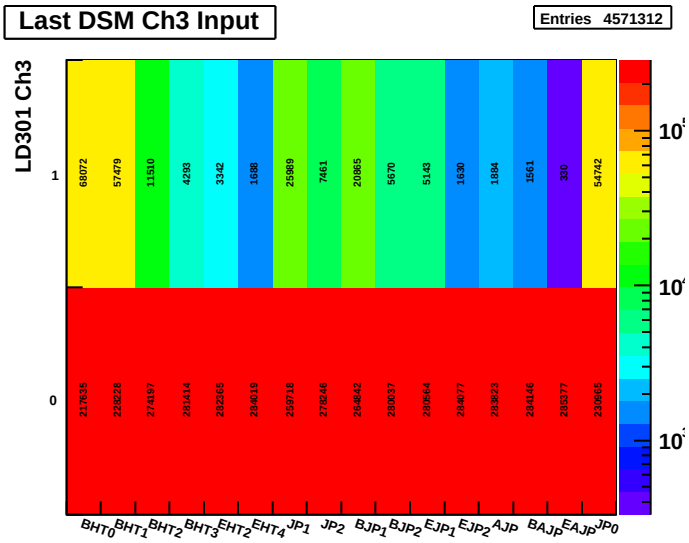

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

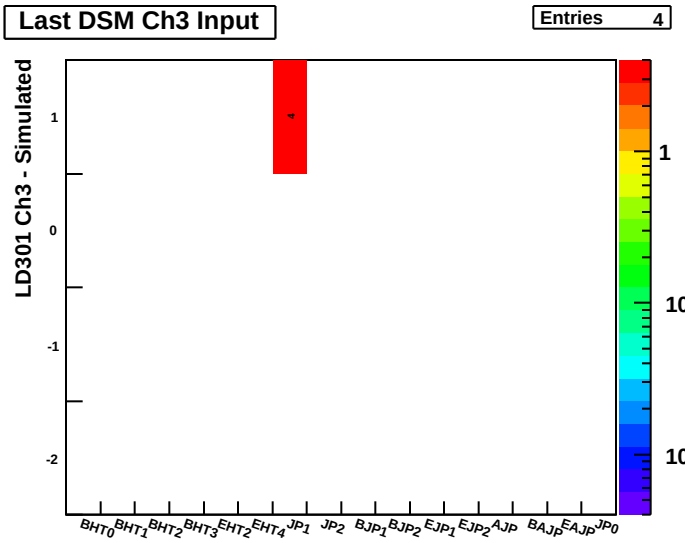

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2285656
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

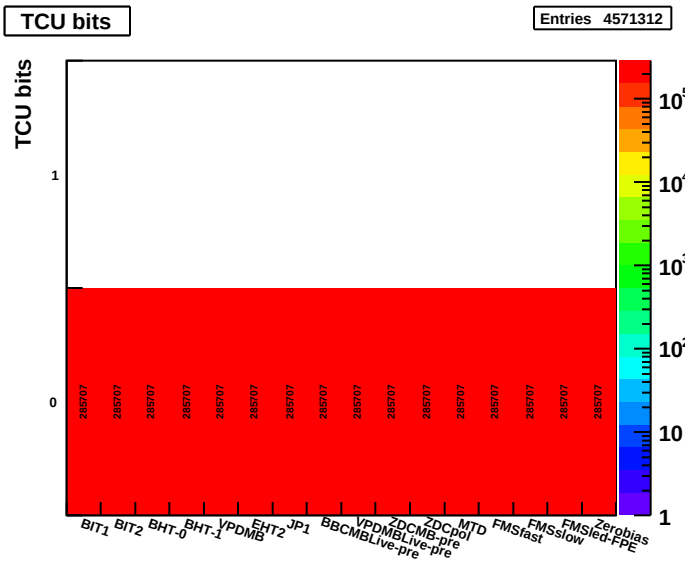

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

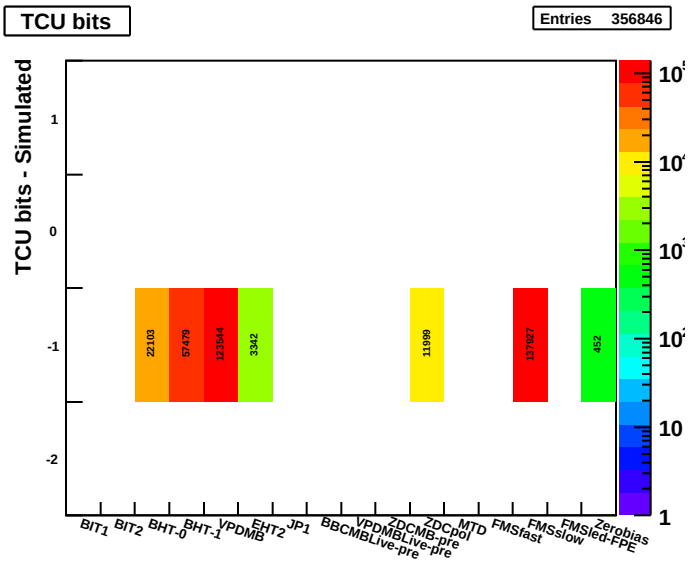

EMC L2 Input - HT23 bits
Entries 2285656
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

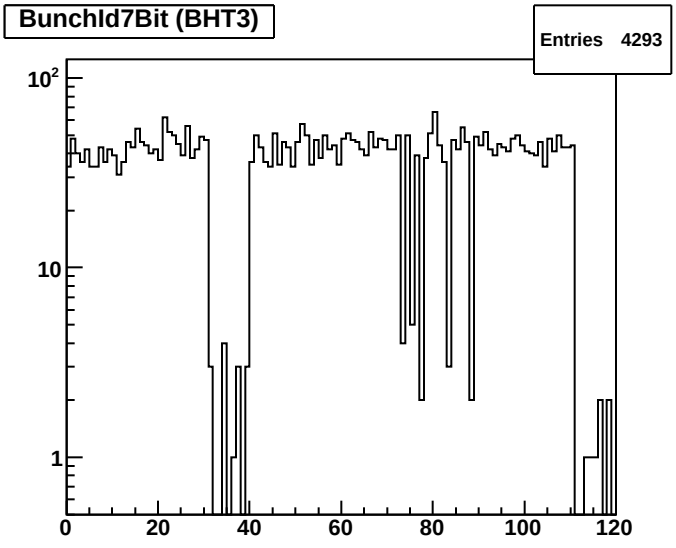

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

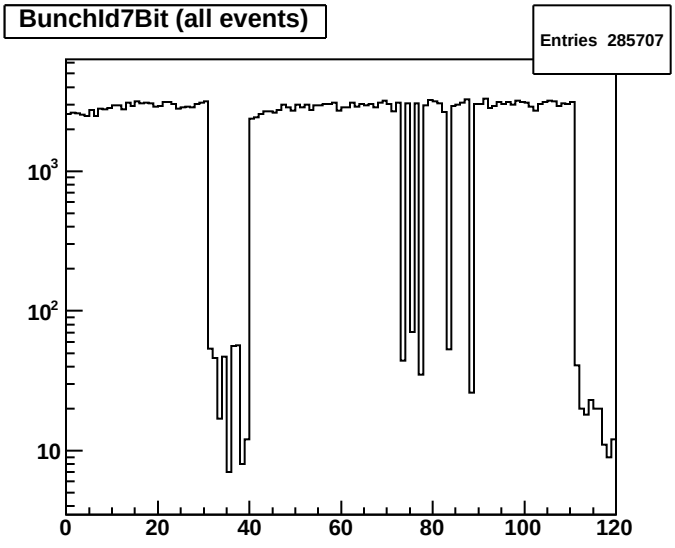

Last DSM Ch3 Input
Entries 4571312
LD301 Ch3
68072 57479 11510 4293 3342 1688 25989 7461 20865 5670 5143 1630 1884 1561 330 54742
217635 228228 274197 281414 282365 284019 259718 278246 264842 280037 280564 284077 283823 284146 285377 230965
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

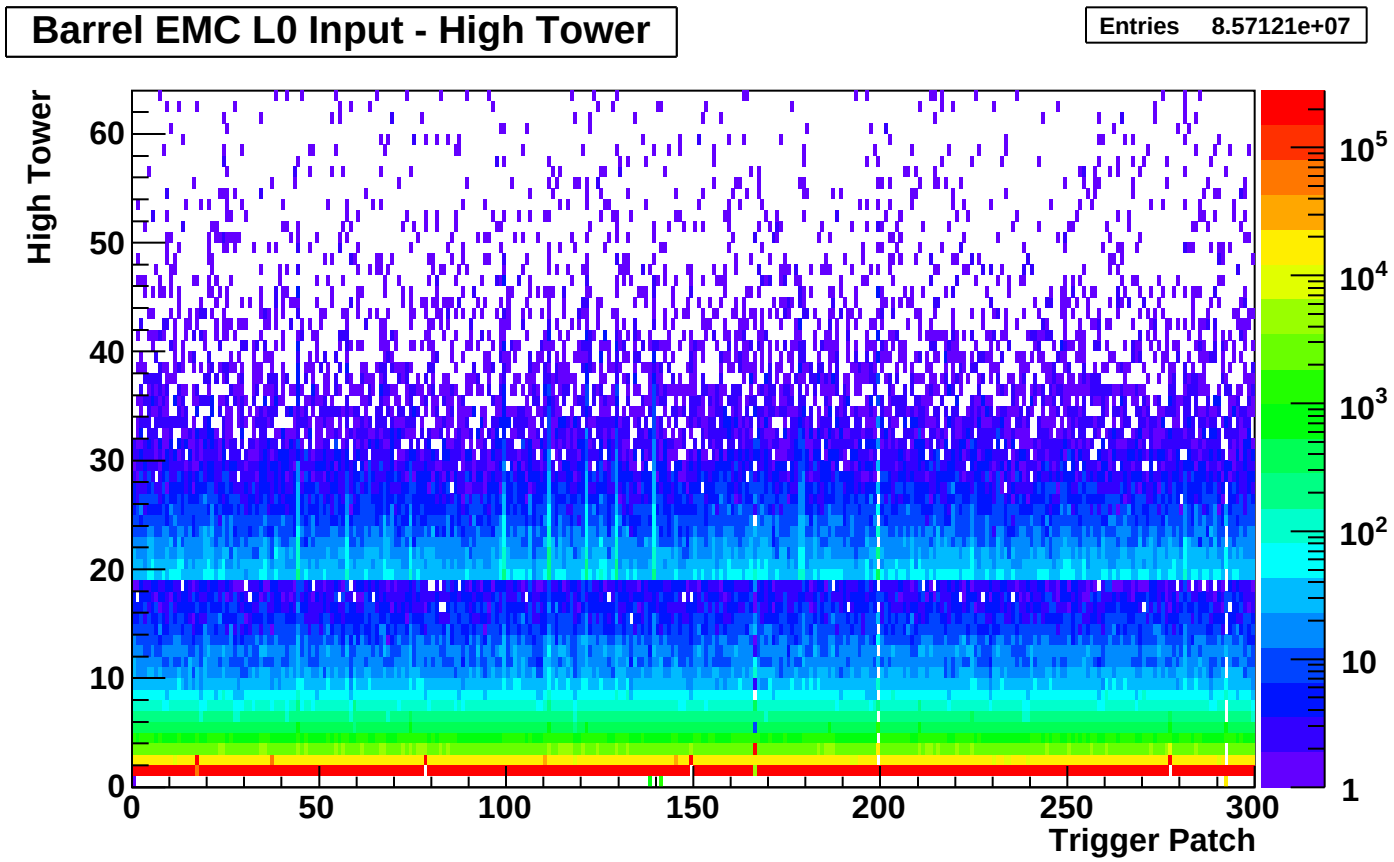

Last DSM Ch3 Input
Entries 4
LD301 Ch3 - Simulated
4
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

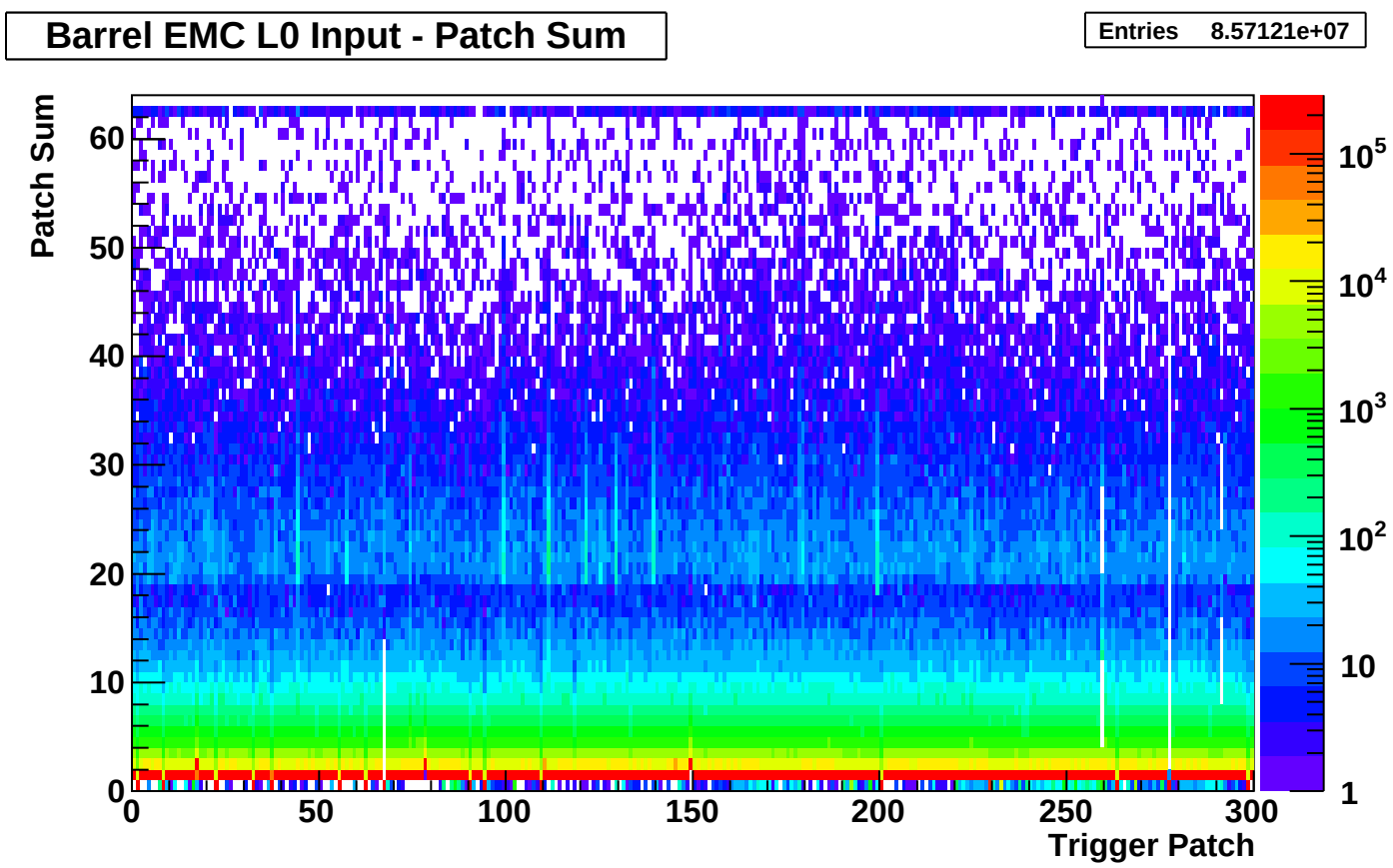

TCU bits
Entries 4571312
TCU bits
285707
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

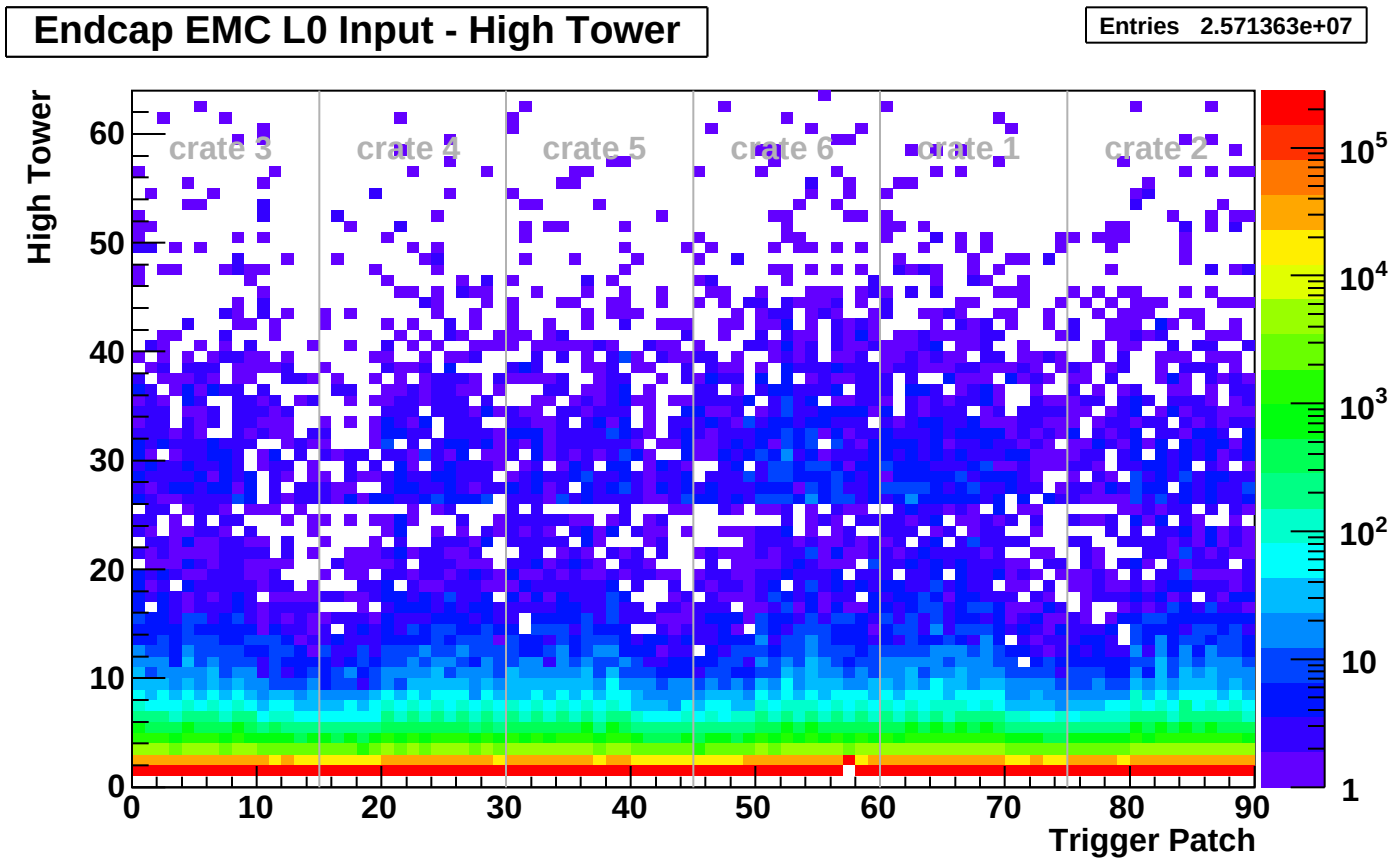

TCU bits
Entries 356846
TCU bits - Simulated
22103 5479 123544 3342 11999 137927 452
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

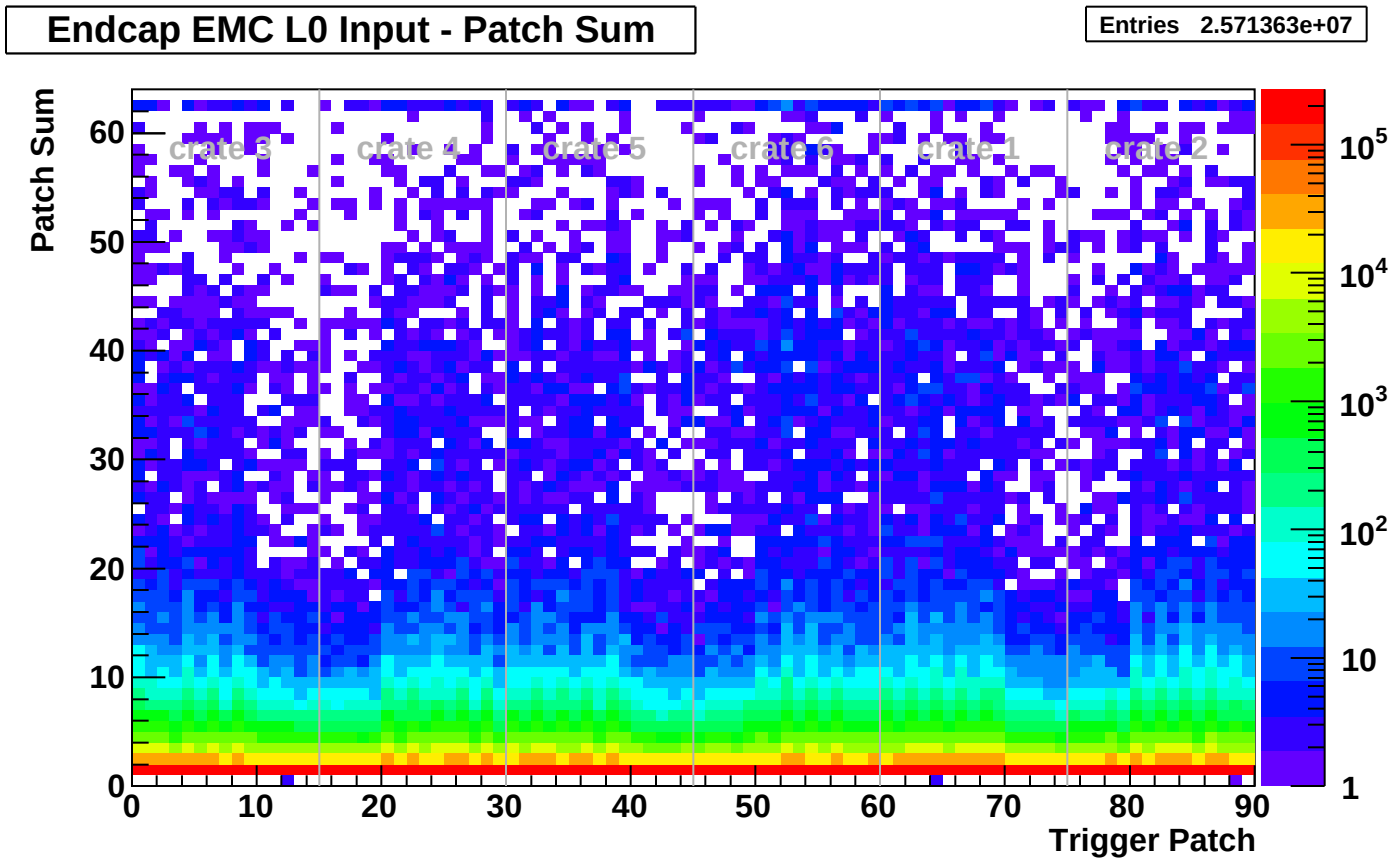

BunchId7Bit (BHT3)
Entries 4293

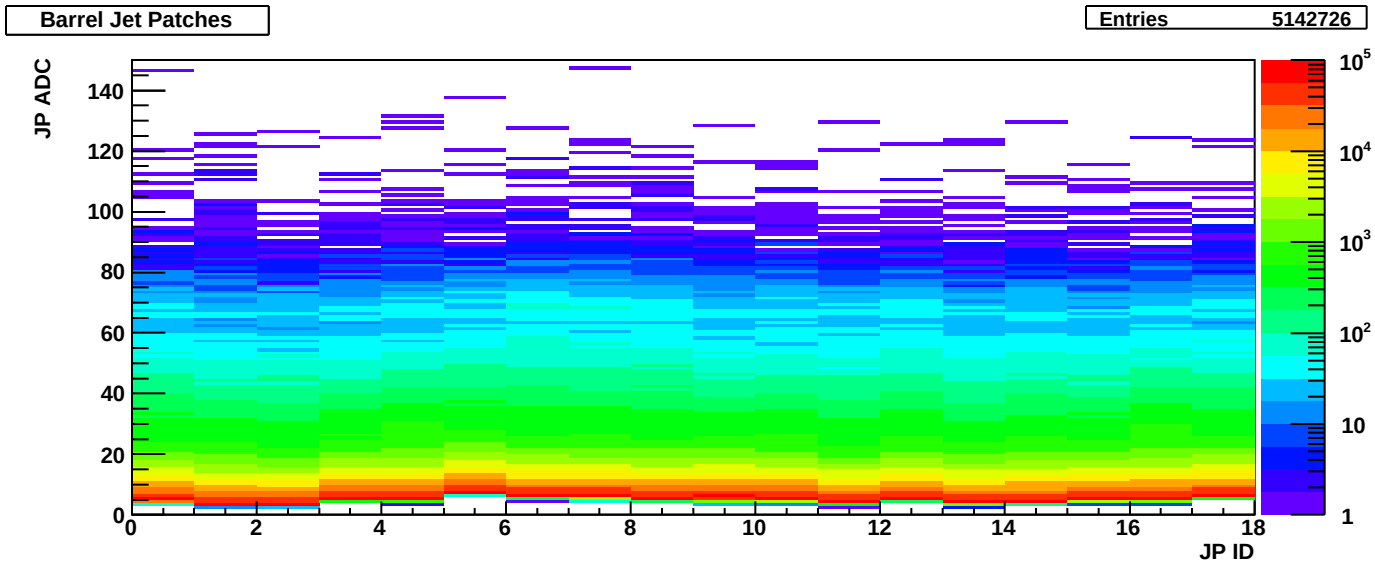

BunchId7Bit (all events)
Entries 285707

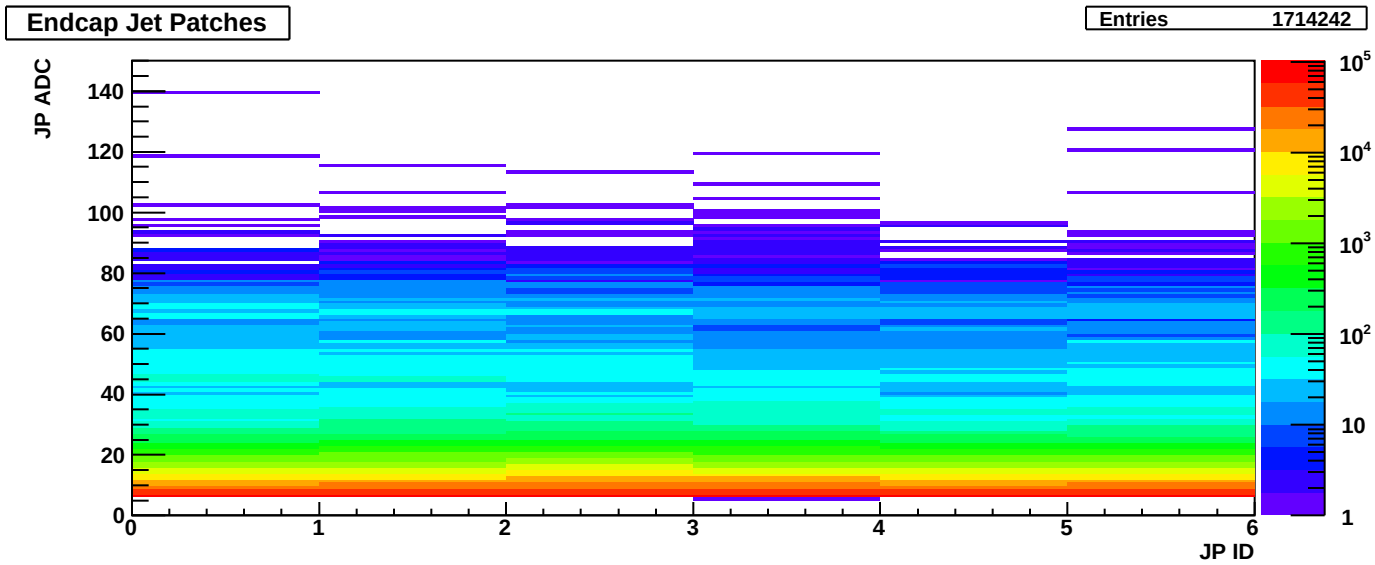

Barrel EMC L0 Input - High Tower
Entries 8.57121e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

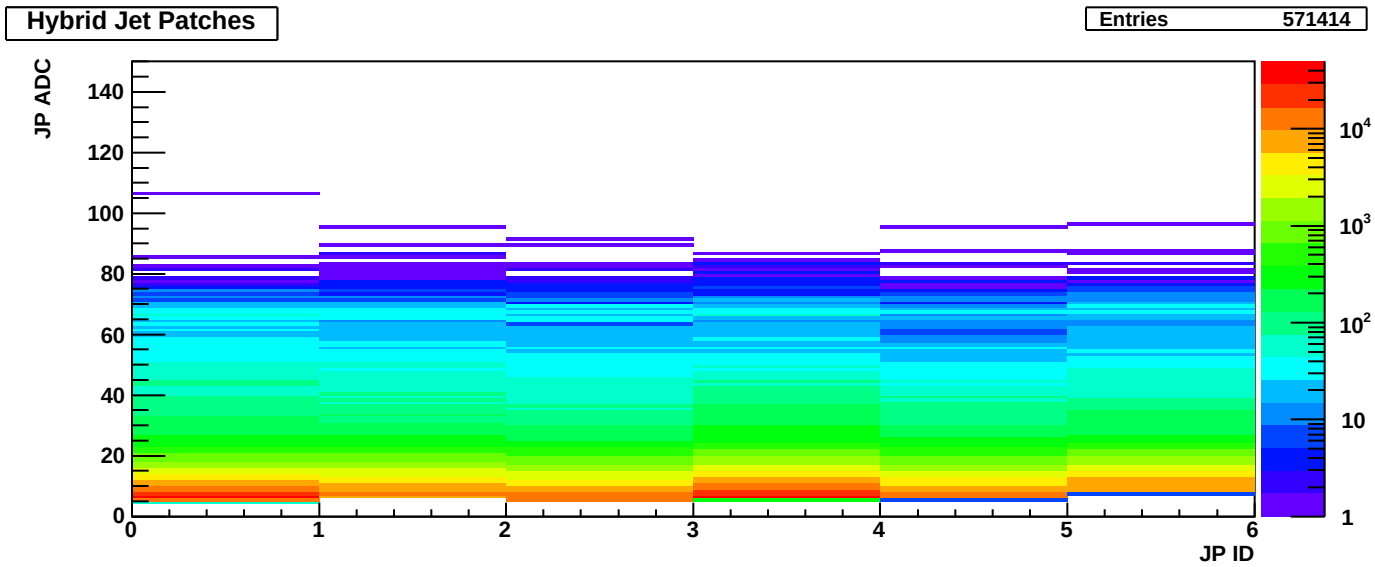

Barrel EMC L0 Input - Patch Sum
Entries 8.57121e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5


Endcap EMC L0 Input - High Tower
Entries 2.571363e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

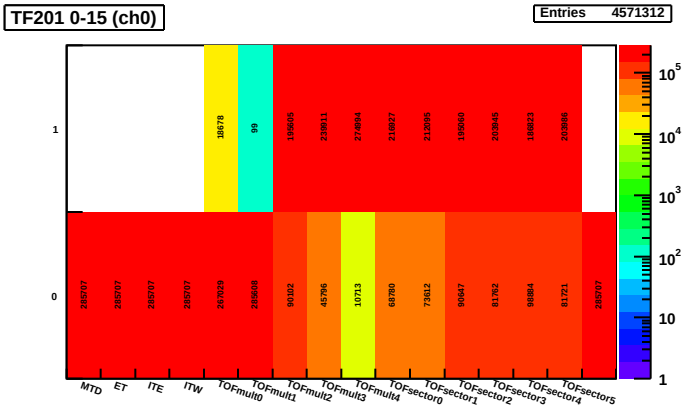

Endcap EMC L0 Input - Patch Sum
Entries 2.571363e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2


Barrel Jet Patches
Entries 5142726
JP ADC
JP ID

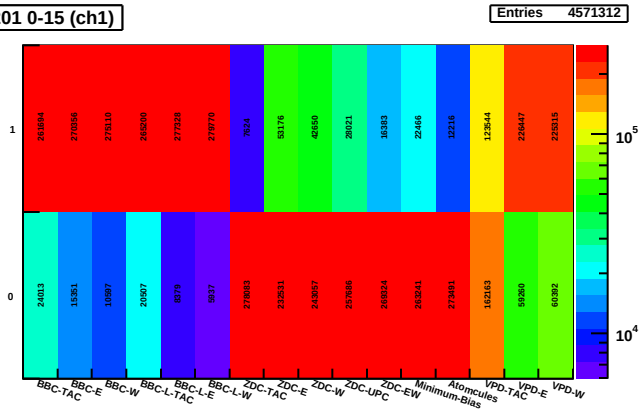

Endcap Jet Patches
Entries 1714242
JP ADC
JP ID


Hybrid Jet Patches
Entries 571414
JP ADC
JP ID

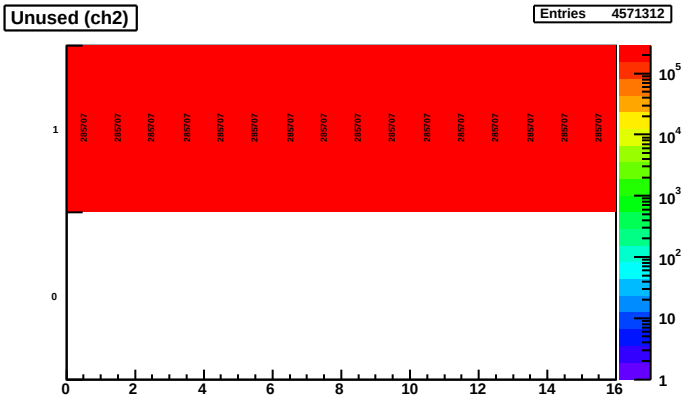
TF201 0-15 (ch0)



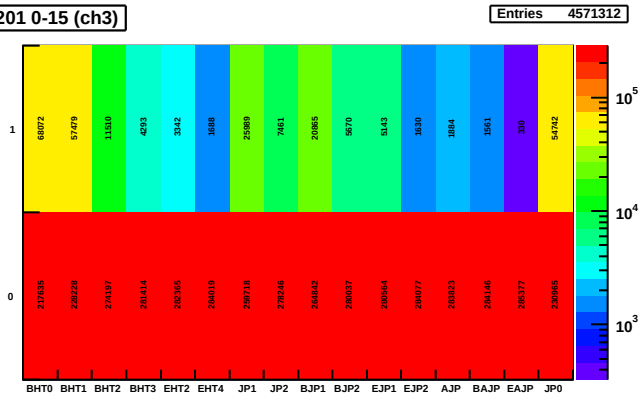
VT201 0-15 (ch1)



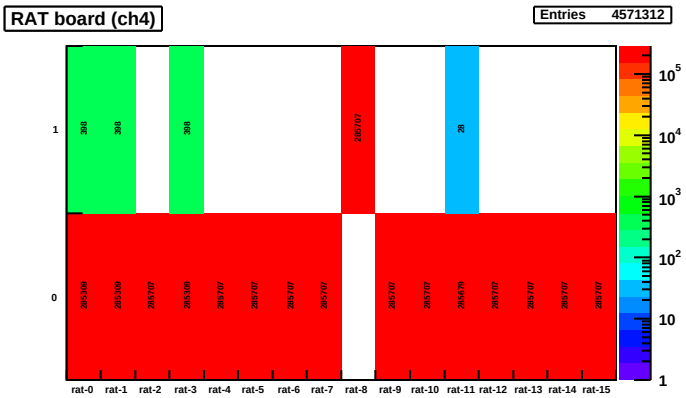
Unused (ch2)



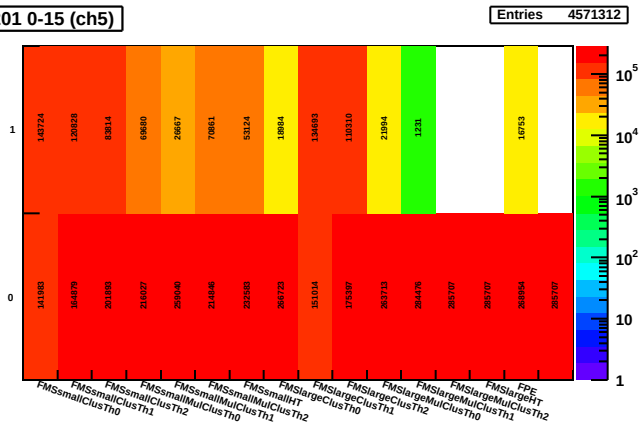
EM201 0-15 (ch3)



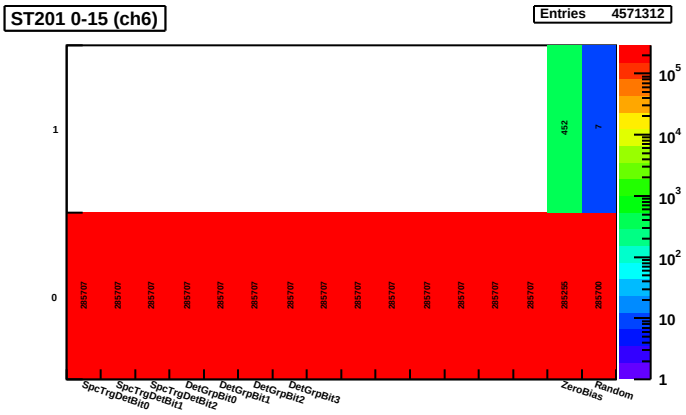
RAT board (ch4)



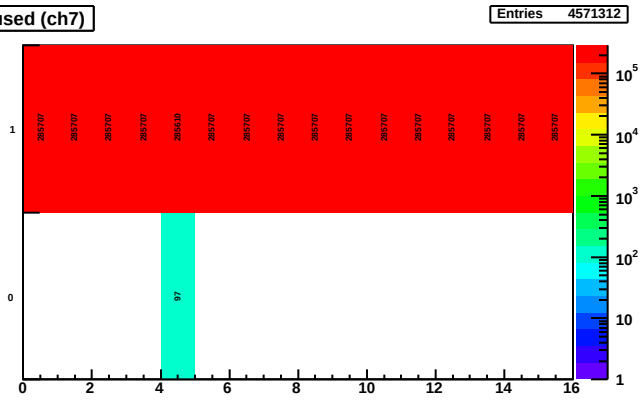
FP201 0-15 (ch5)

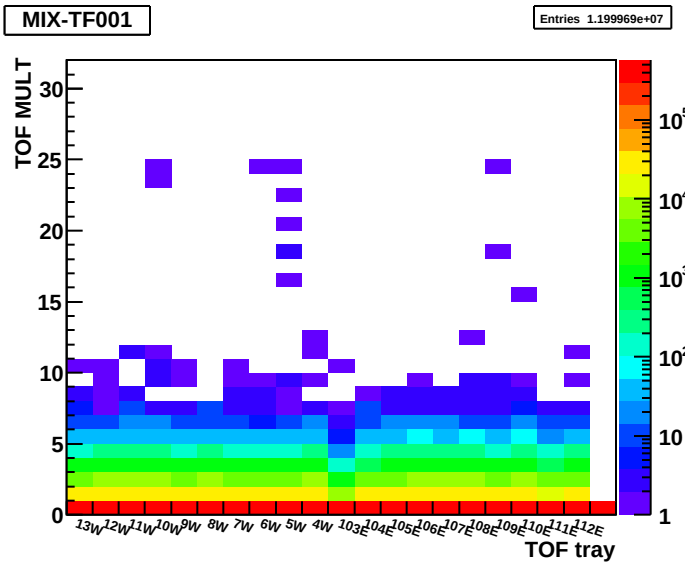


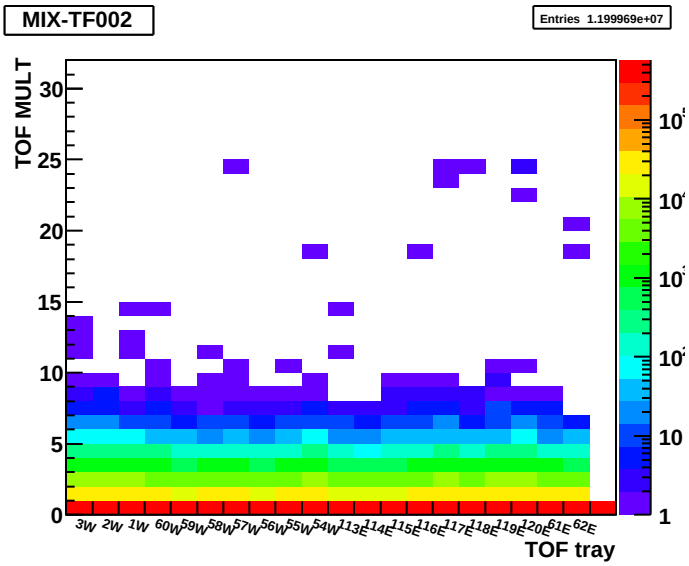
ST201 0-15 (ch6)

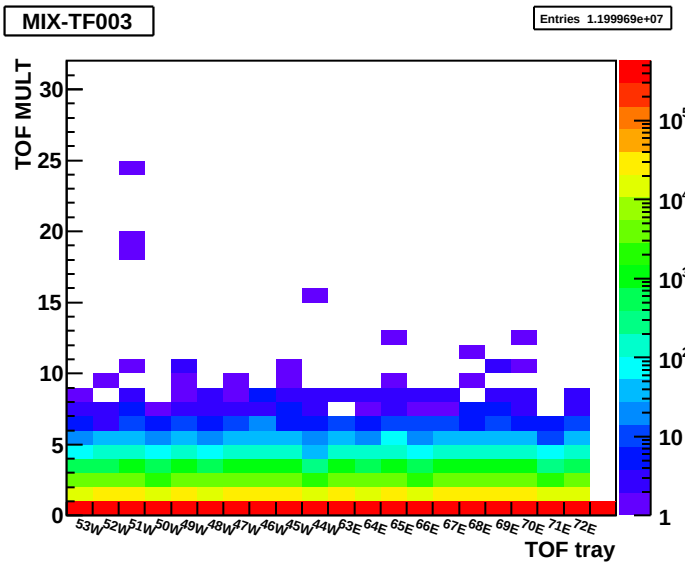


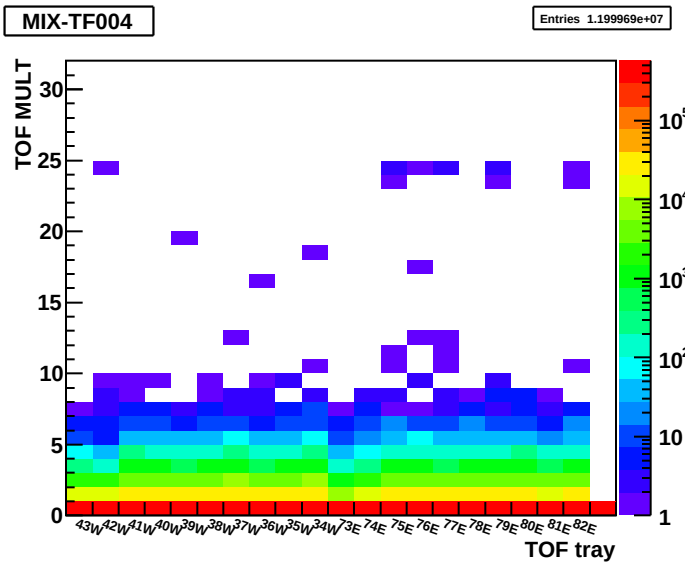
Unused (ch7)

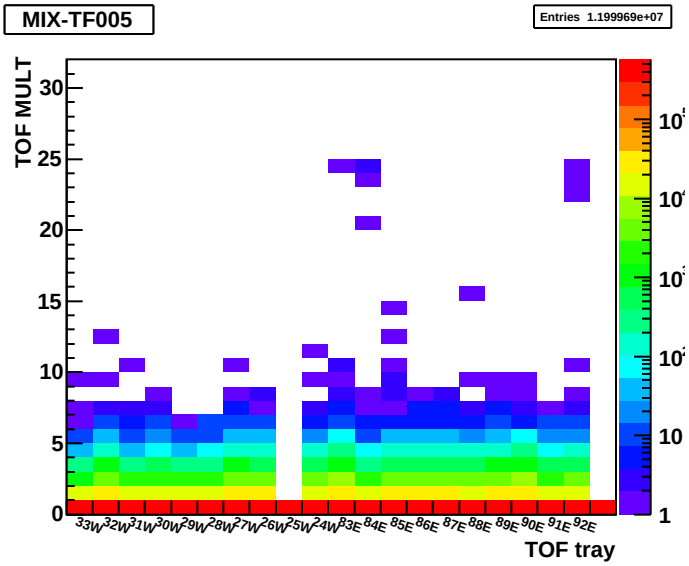


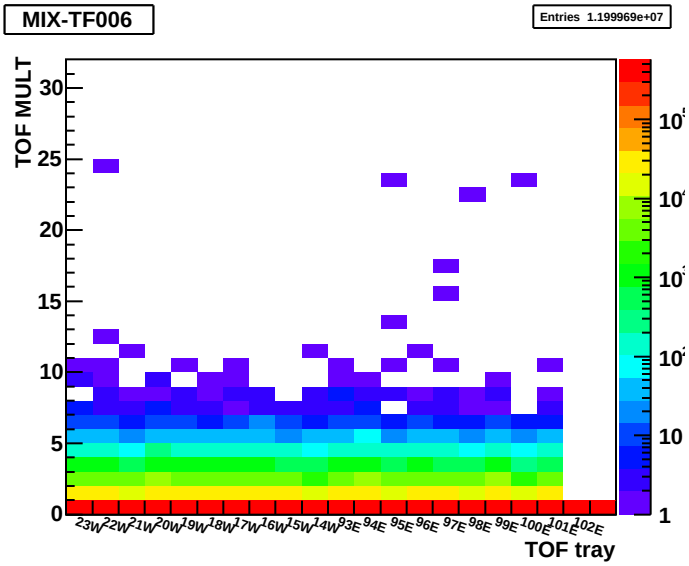

MIX-TF001
Entries 1.199969e+07
TOF MULT
TOF tray

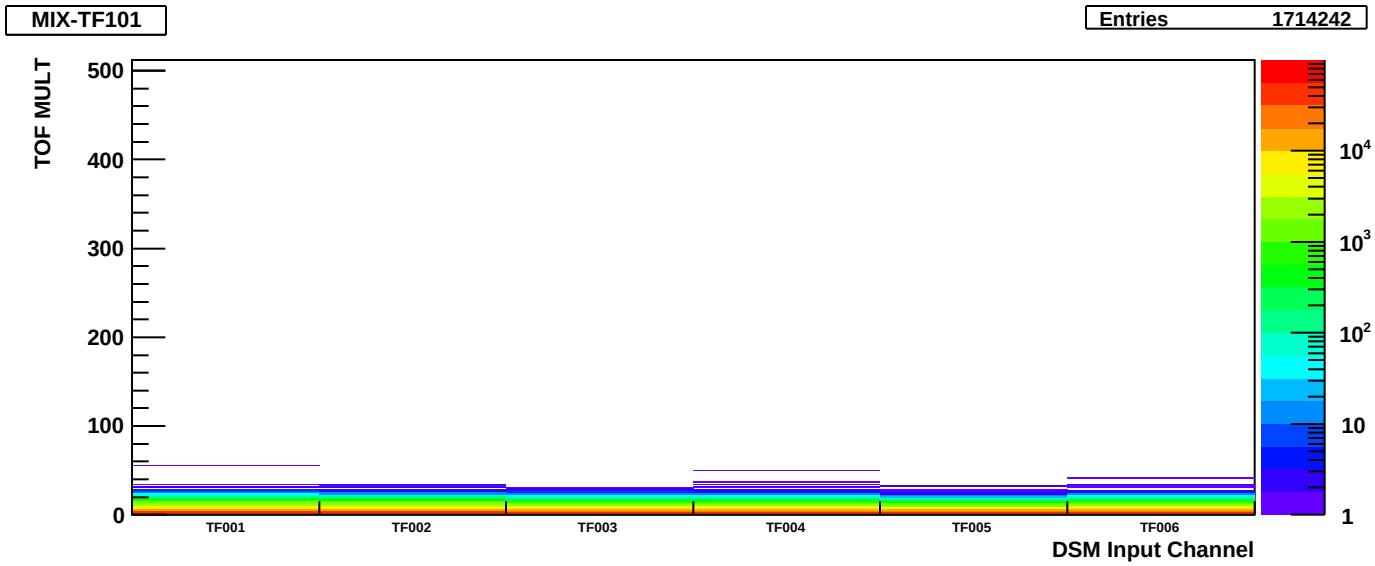

MIX-TF002
Entries 1.199969e+07
TOF MULT
TOF tray

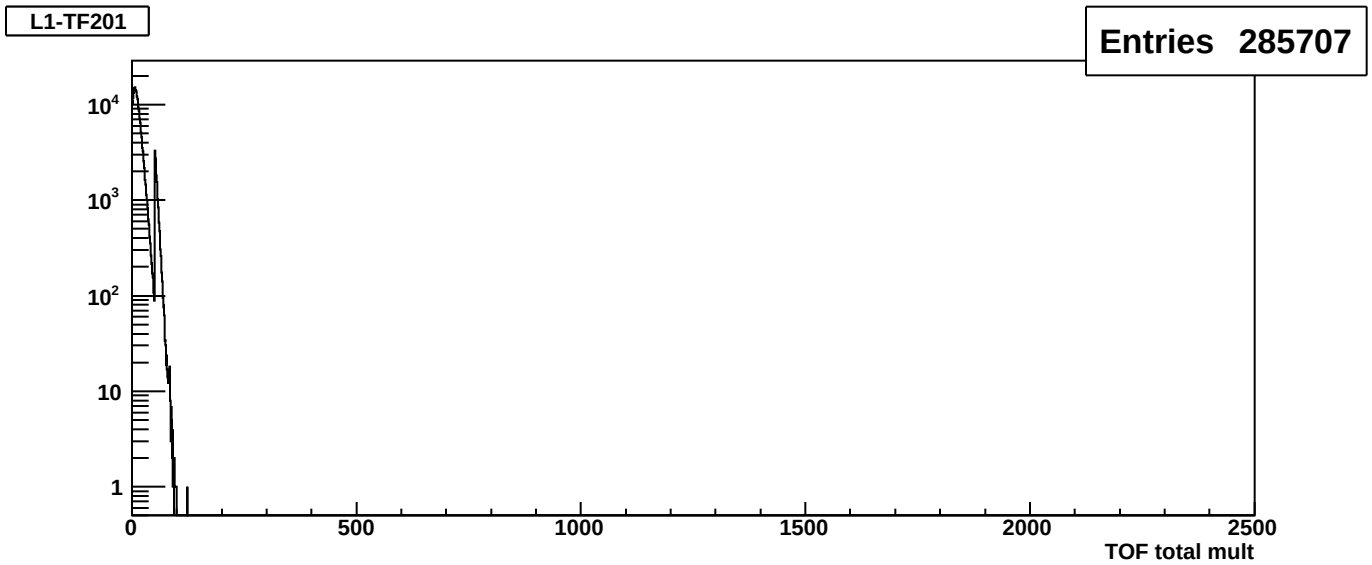

MIX-TF003
Entries 1.199969e+07
TOF MULT
TOF tray

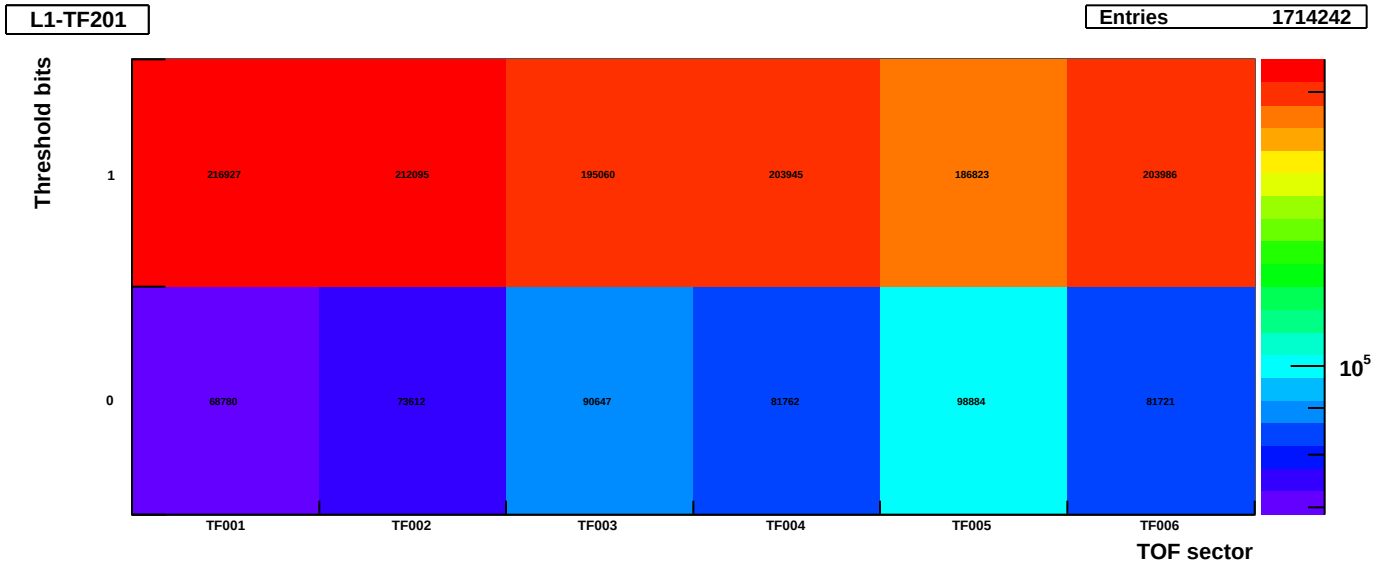

MIX-TF004
Entries 1.199969e+07
TOF MULT
TOF tray

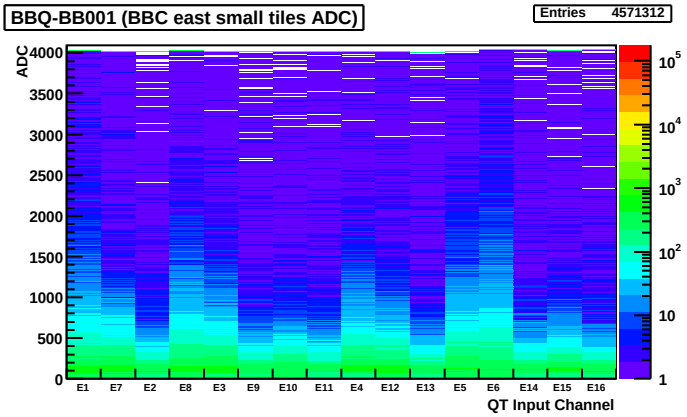

MIX-TF005
Entries 1.199969e+07
TOF MULT
TOF tray

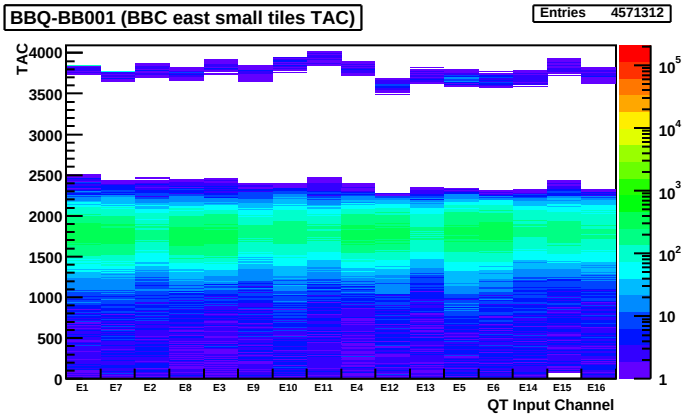

MIX-TF006
Entries 1.199969e+07
TOF MULT
TOF tray

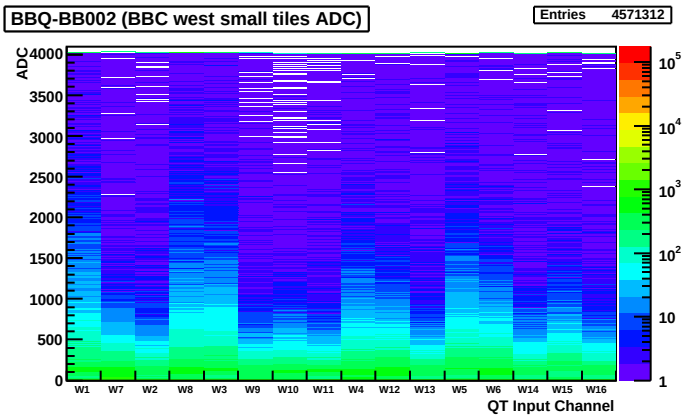

MIX-TF101
Entries 1714242
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

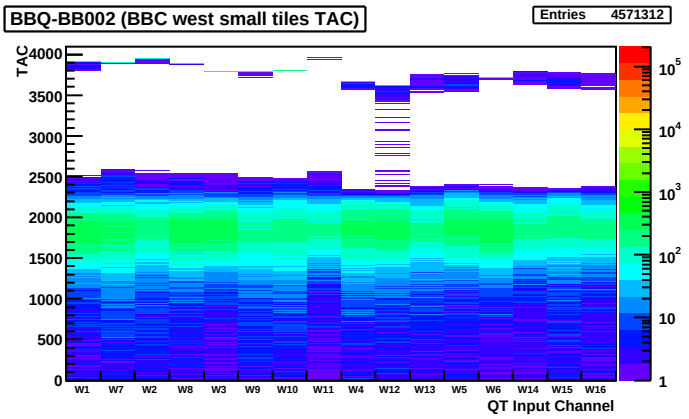

L1-TF201
Entries 285707
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

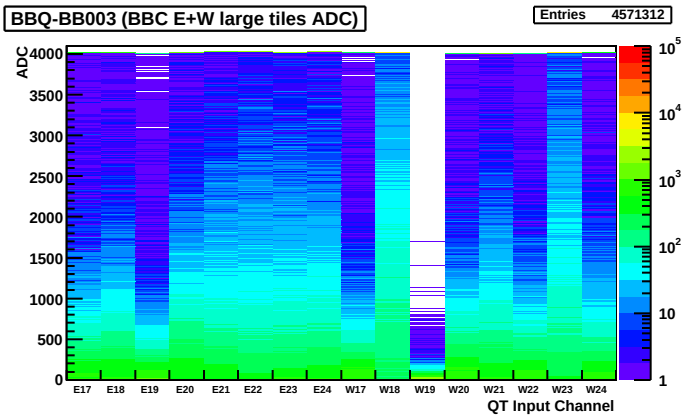

L1-TF201
Entries 1714242
Threshold bits
1
0
216927
212095
195060
203945
186823
203986
68780
73612
90647
81762
98884
81721
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

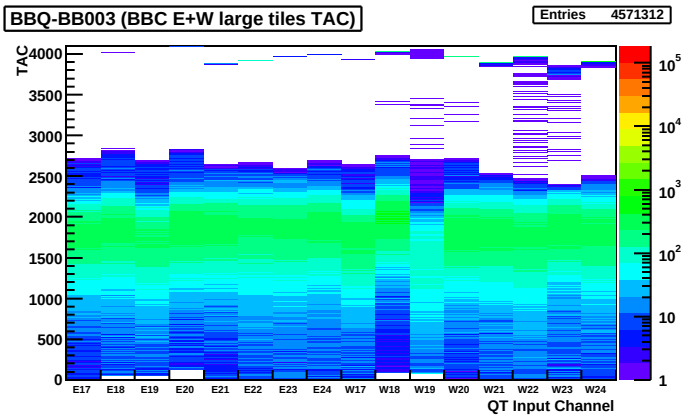

BBQ-BB001 (BBC east small tiles ADC)
Entries 4571312
ADC
QT Input Channel

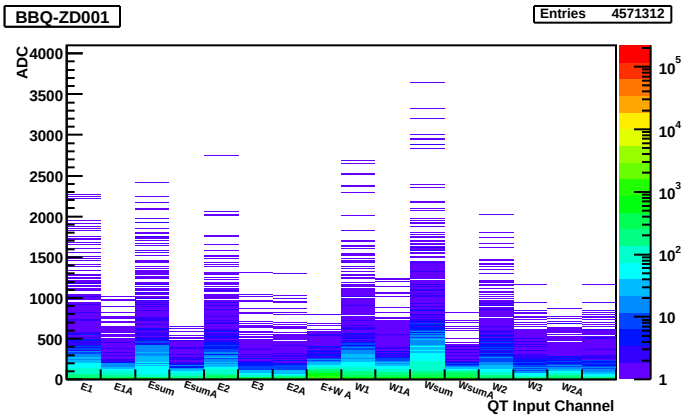

BBQ-BB001 (BBC east small tiles TAC)
Entries 4571312
TAC
QT Input Channel

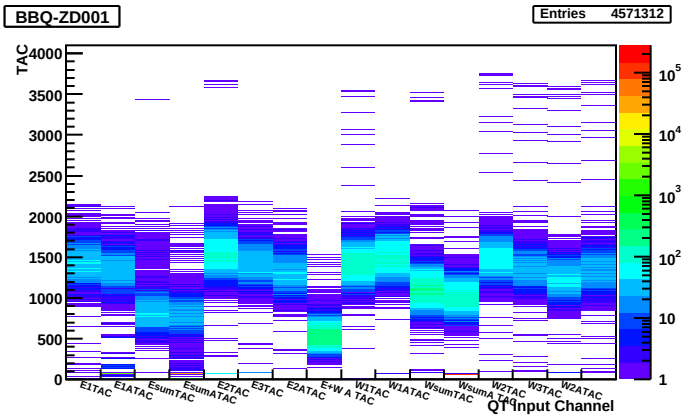

BBQ-BB002 (BBC west small tiles ADC)
Entries 4571312
ADC
QT Input Channel

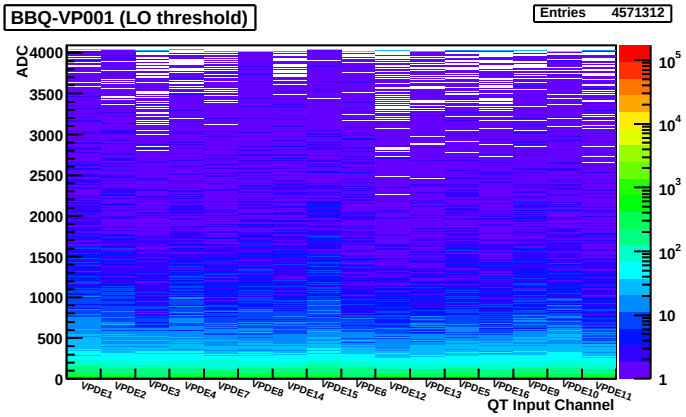

BBQ-BB002 (BBC west small tiles TAC)
Entries 4571312
TAC
QT Input Channel

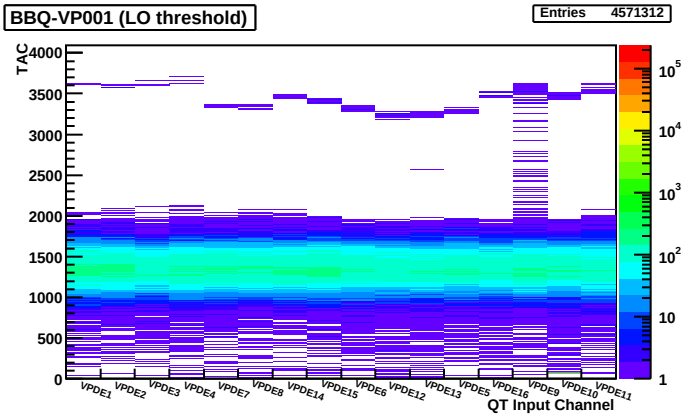

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4571312
ADC
QT Input Channel

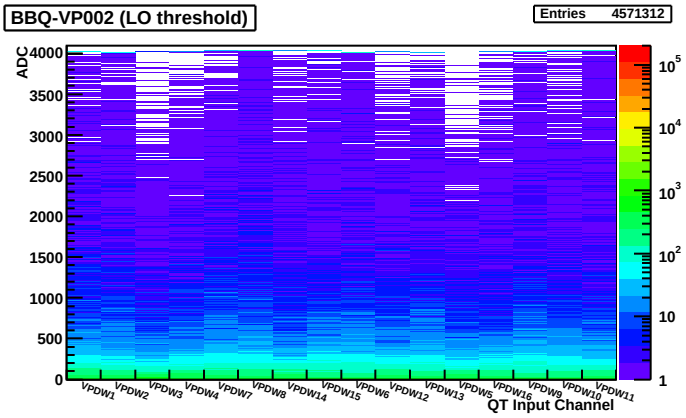

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4571312
TAC
QT Input Channel

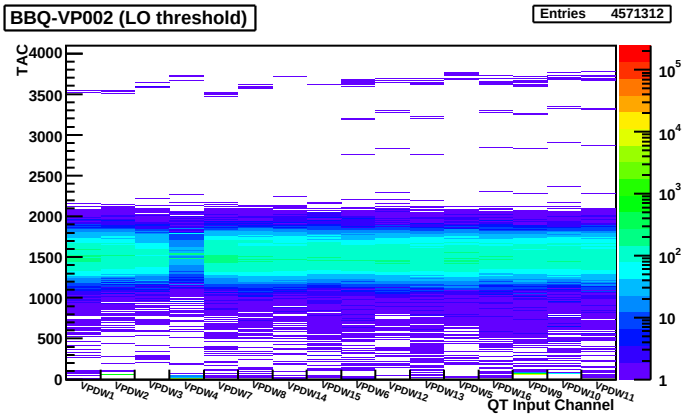

BBQ-ZD001
Entries 4571312
ADC
QT Input Channel

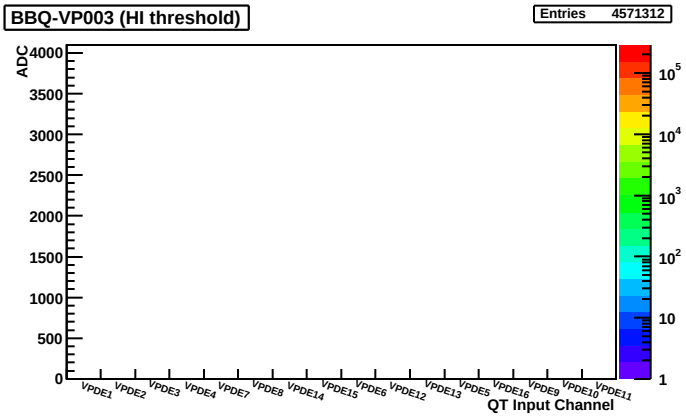

BBQ-ZD001
Entries 4571312
TAC
QT Input Channel

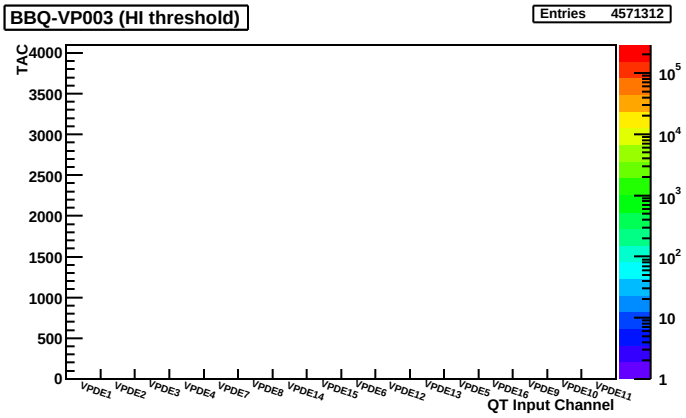

BBQ-VP001 (LO threshold)
Entries 4571312
ADC
QT Input Channel

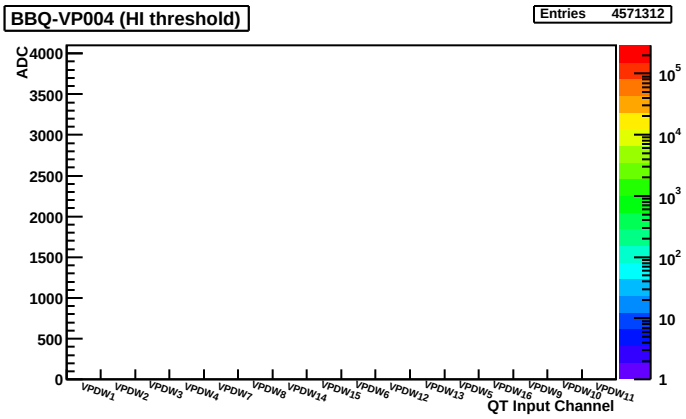

BBQ-VP001 (LO threshold)
Entries 4571312
TAC
QT Input Channel

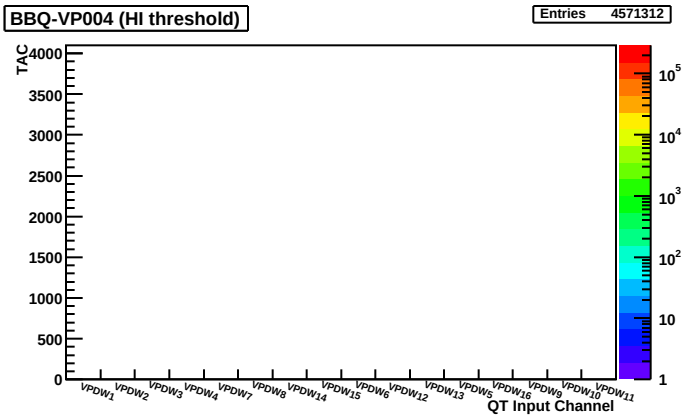

BBQ-VP002 (LO threshold)
Entries 4571312
ADC
QT Input Channel

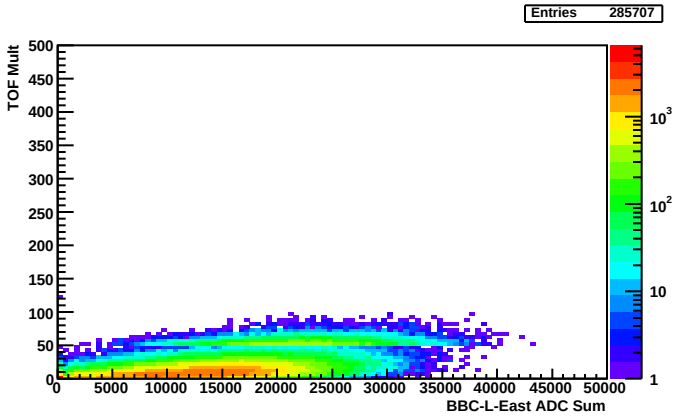

BBQ-VP002 (LO threshold)
Entries 4571312
TAC
QT Input Channel

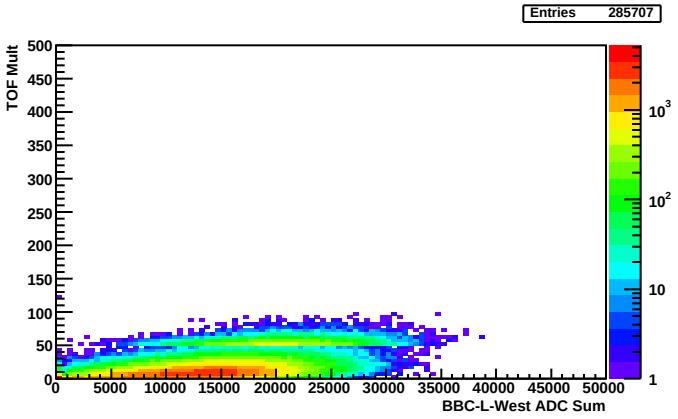

BBQ-VP003 (HI threshold)
Entries 4571312
ADC
QT Input Channel

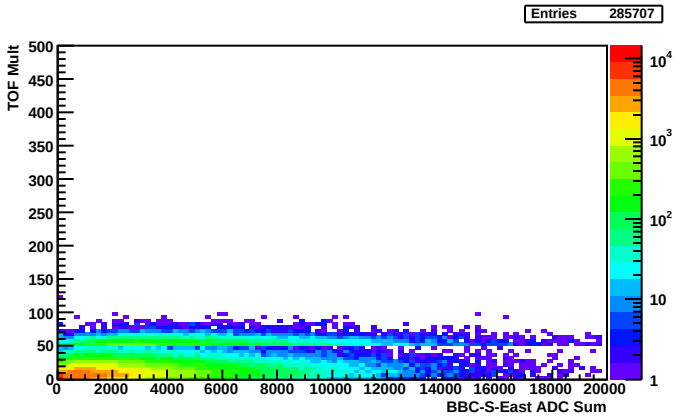

BBQ-VP003 (HI threshold)
Entries 4571312
TAC
QT Input Channel

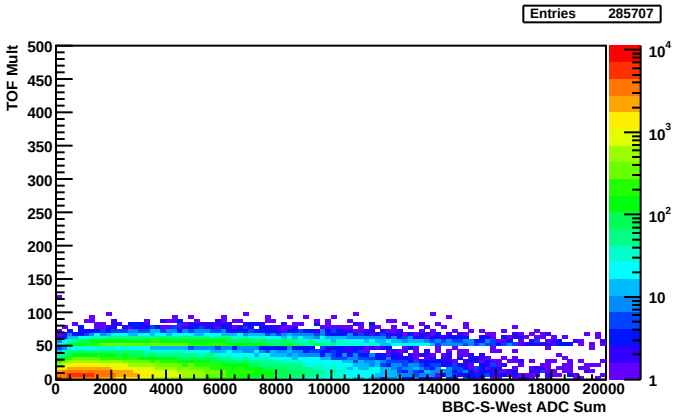

BBQ-VP004 (HI threshold)
Entries 4571312
ADC
QT Input Channel

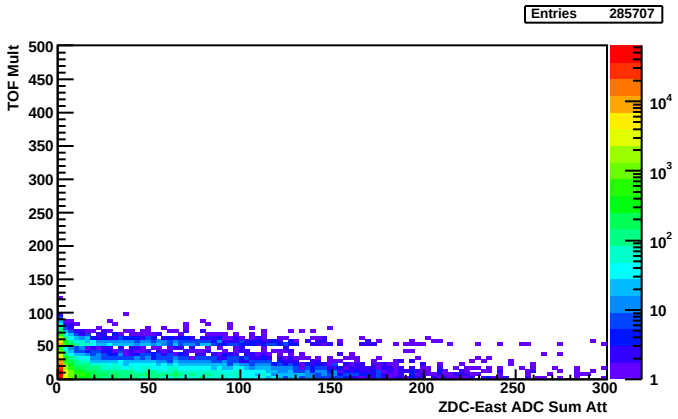

BBQ-VP004 (HI threshold)
Entries 4571312
TAC
QT Input Channel

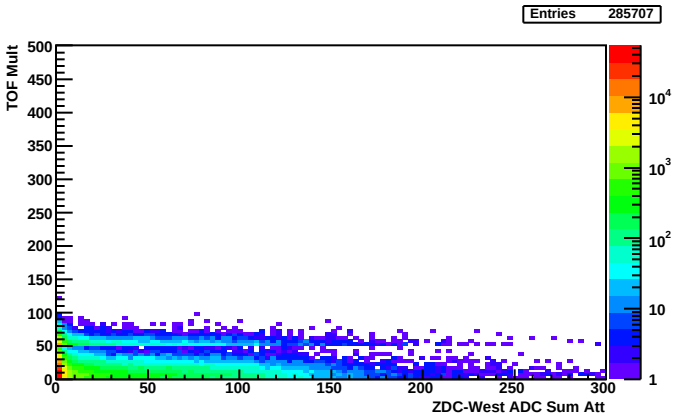

Entries 285707
TOF Mult
BBC-L-East ADC Sum

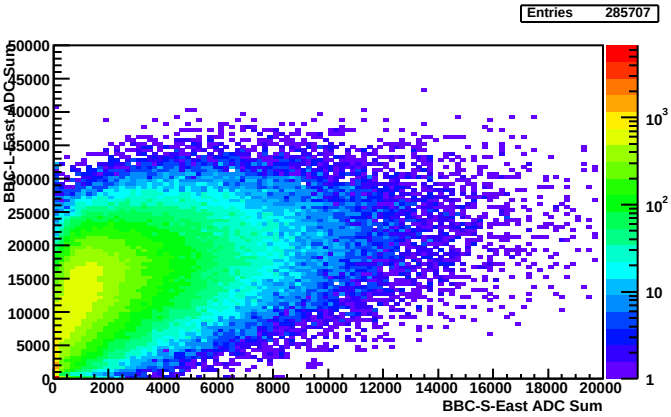

Entries 285707
TOF Mult
BBC-L-West ADC Sum

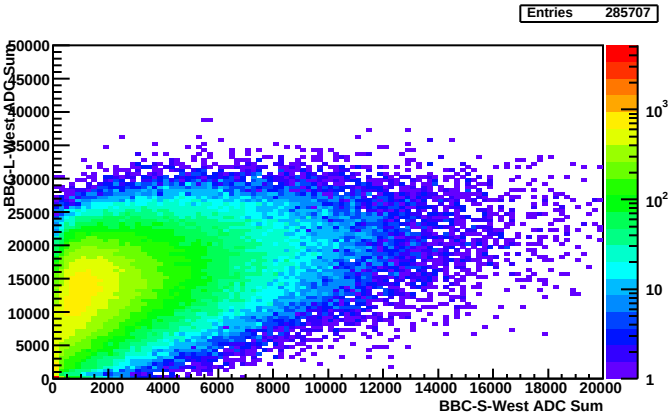

Entries 285707
TOF Mult
BBC-S-East ADC Sum

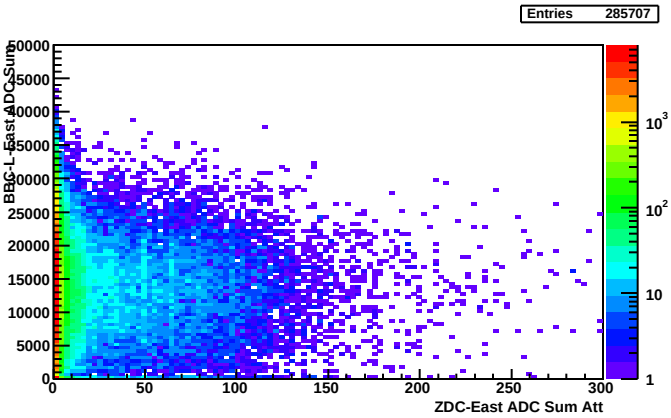

Entries 285707
TOF Mult
BBC-S-West ADC Sum

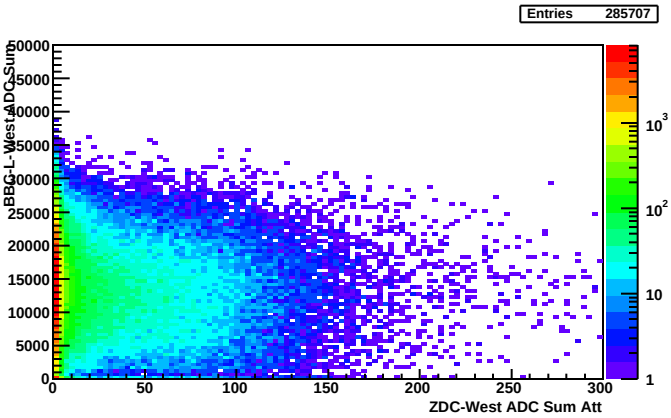

Entries 285707
TOF Mult
ZDC-East ADC Sum Att

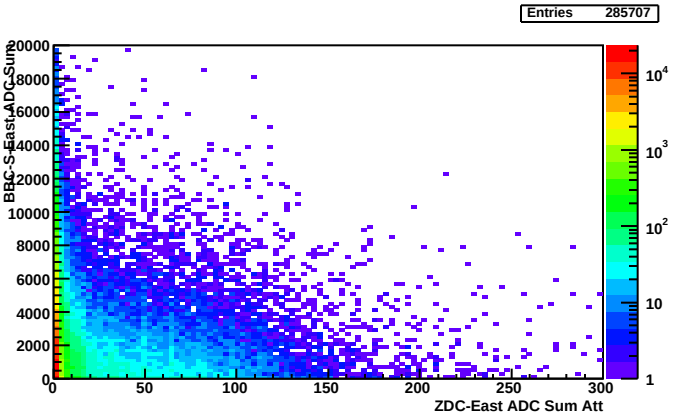

Entries 285707
TOF Mult
ZDC-West ADC Sum Att

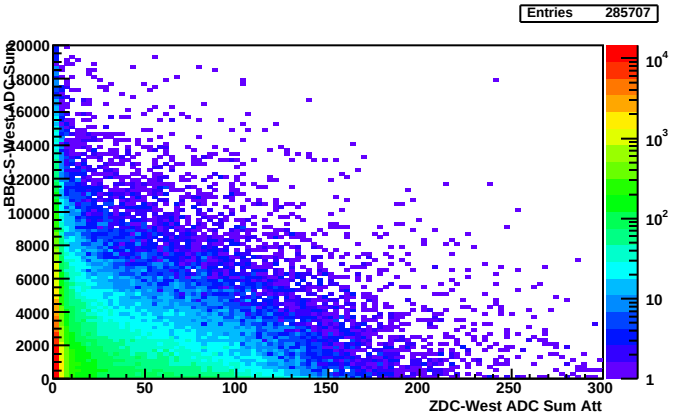

Entries 285707
BBC-L-East ADC Sum
BBC-S-East ADC Sum

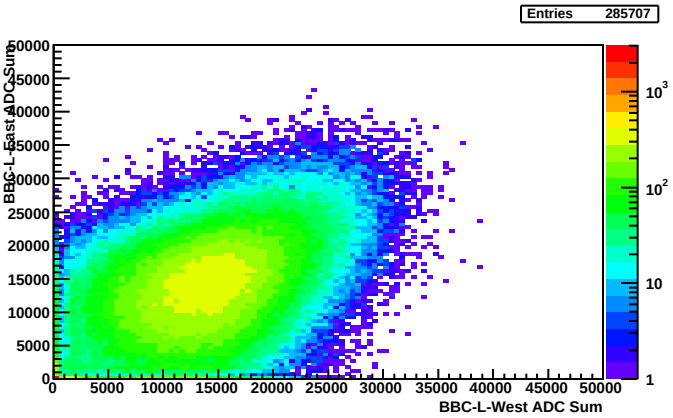

Entries 285707
BBC-L-West ADC Sum
BBC-S-West ADC Sum

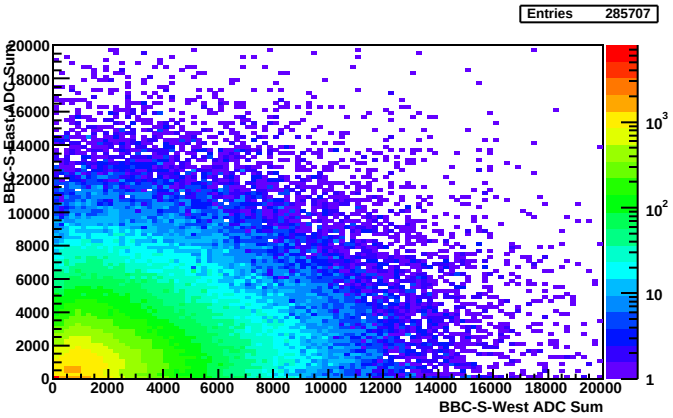

Entries 285707
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

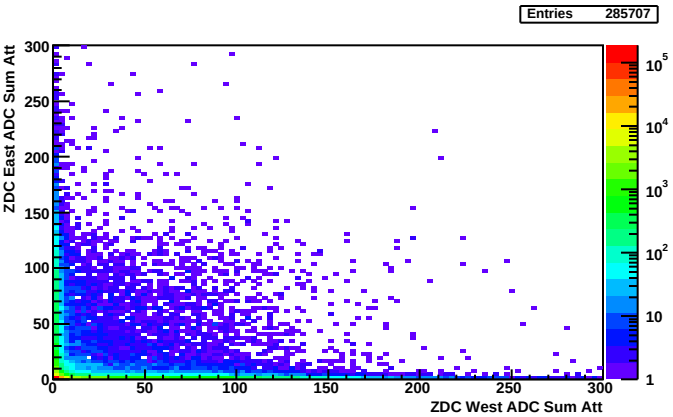

Entries 285707
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

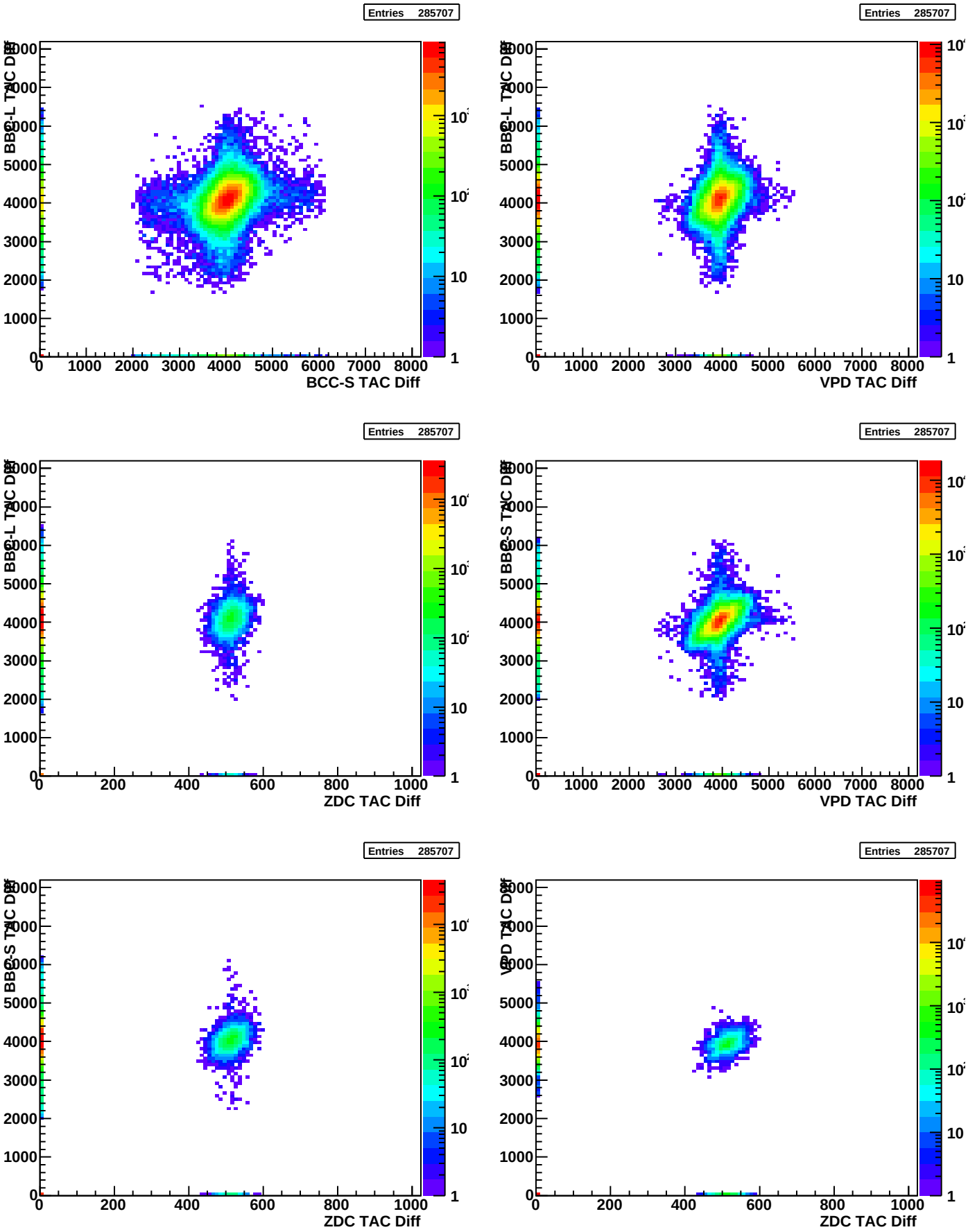

Entries 285707
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

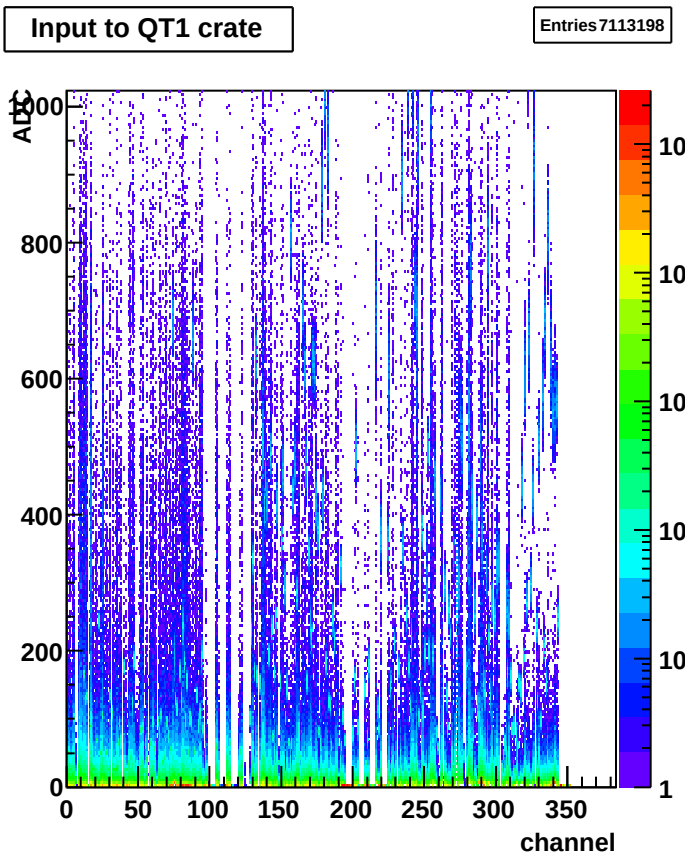

Entries 285707
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

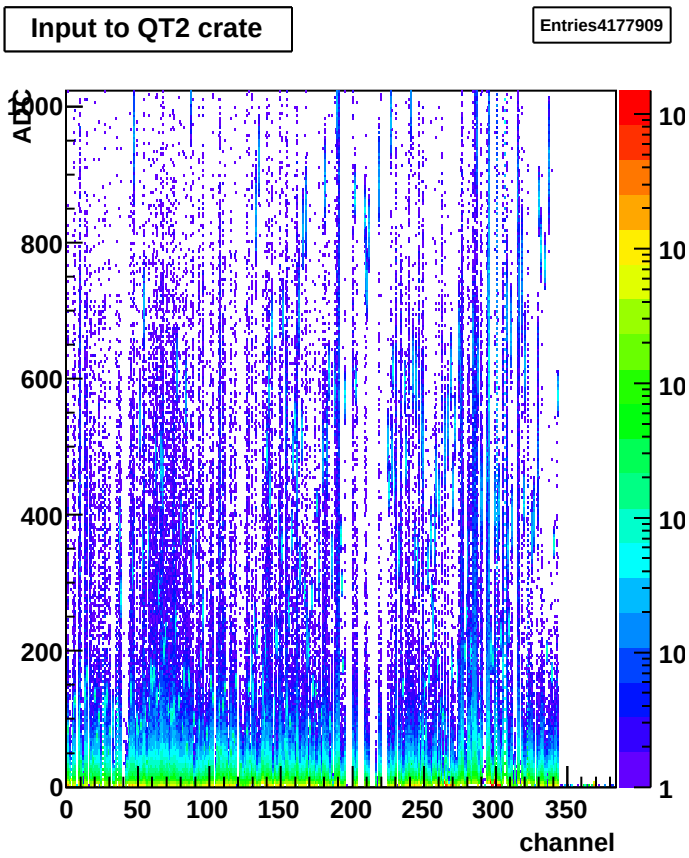

Entries 285707
BBC-L-East ADC Sum
BBC-L-West ADC Sum

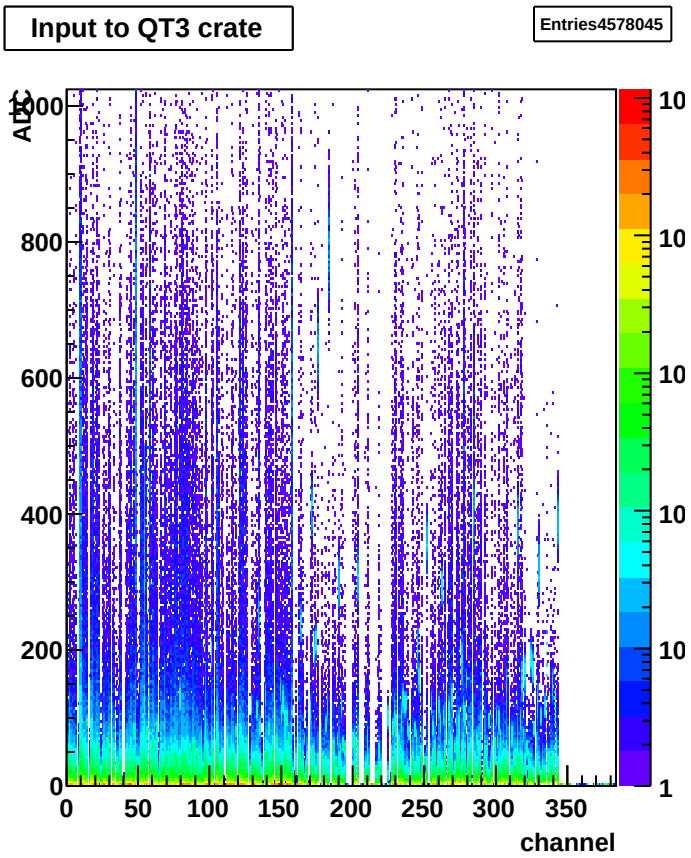

Entries 285707
BBC-S-East ADC Sum
BBC-S-West ADC Sum

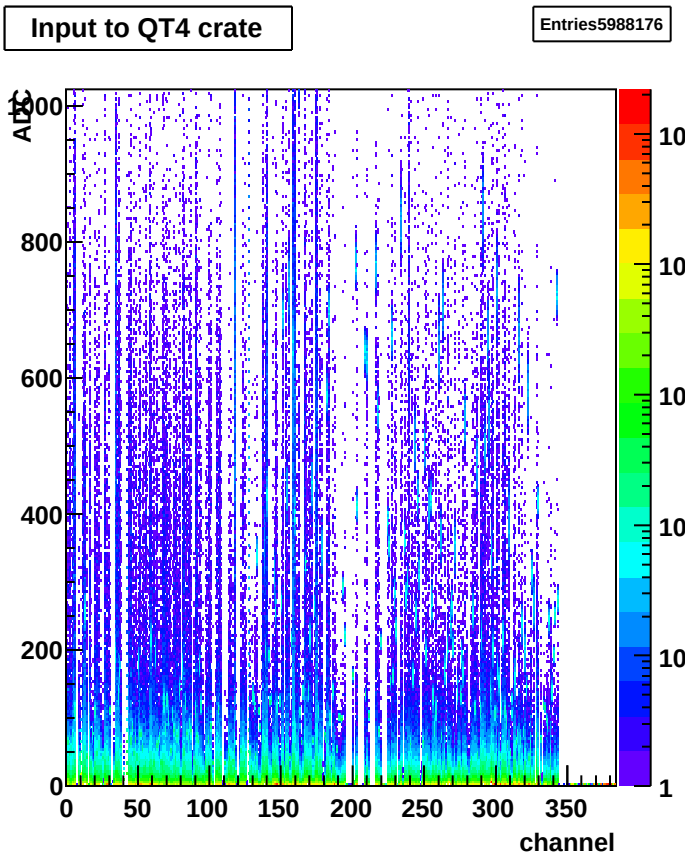

Entries 285707
ZDC East ADC Sum Att
ZDC West ADC Sum Att

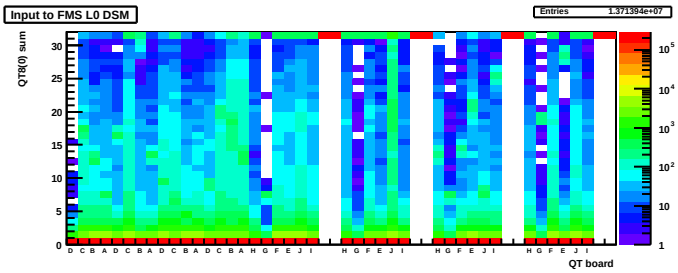

Entries 285707
BBC-L TAC Diff
BCC-S TAC Diff
Entries 285707
BBC-L TAC Diff
VPD TAC Diff
Entries 285707
BBC-L TAC Diff
ZDC TAC Diff
Entries 285707
BBC-S TAC Diff
VPD TAC Diff
Entries 285707
BBC-S TAC Diff
ZDC TAC Diff
Entries 285707
VPD TAC Diff
ZDC TAC Diff

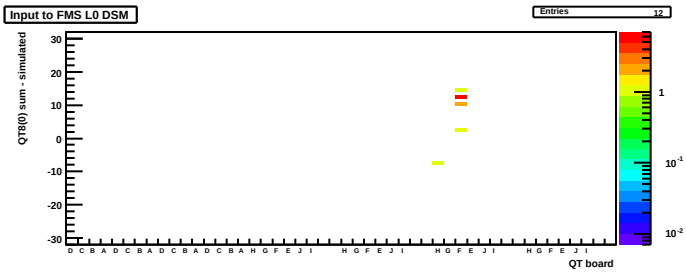

Input to QT1 crate
Entries7113198
ADC
channel

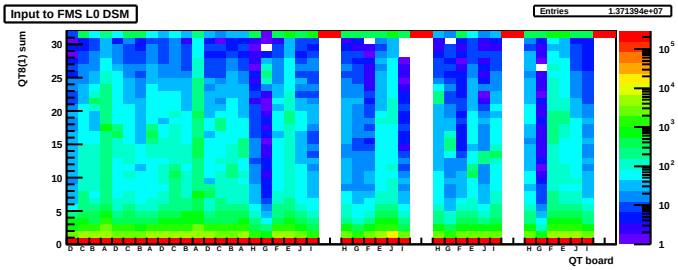

Input to QT2 crate
Entries4177909
ADC
channel

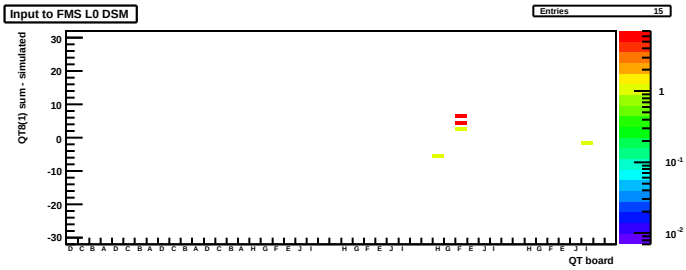

Input to QT3 crate
Entries4578045
ADC
channel

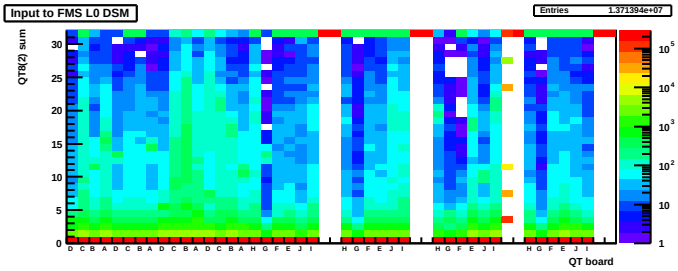

Input to QT4 crate
Entries5988176
ADC
channel

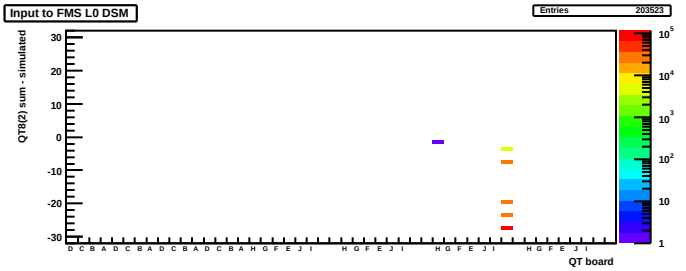

Input to FMS L0 DSM
Entries 1.371394e+07
QT8(0) sum
QT board

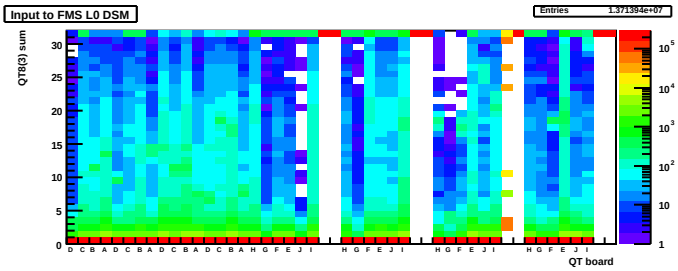

Input to FMS L0 DSM
Entries 12
QT8(0) sum - simulated
QT board

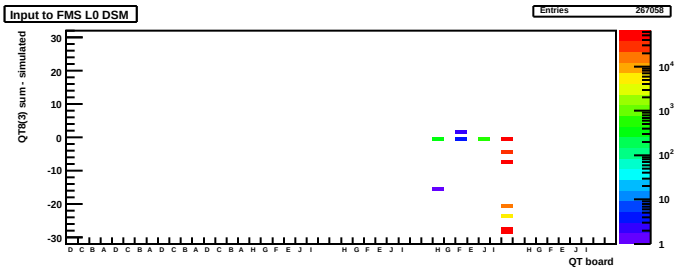

Input to FMS L0 DSM
Entries 1.371394e+07
QT8(1) sum
QT board

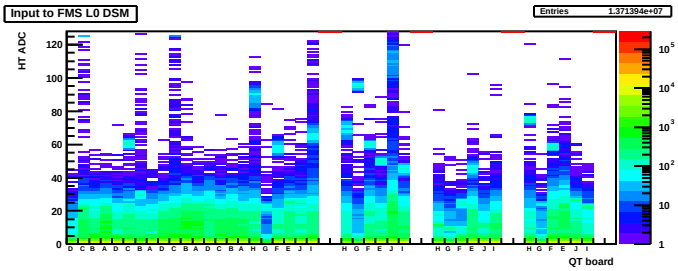

Input to FMS L0 DSM
Entries 15
QT8(1) sum - simulated
QT board

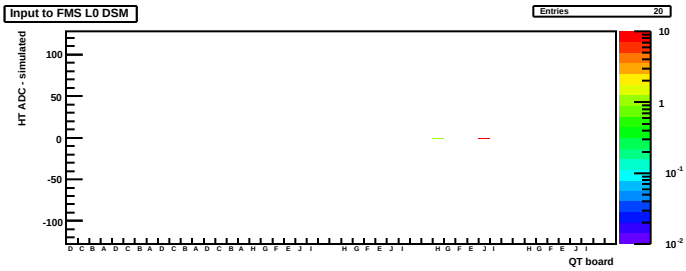

Input to FMS L0 DSM
Entries 1.371394e+07
QT8(2) sum
QT board

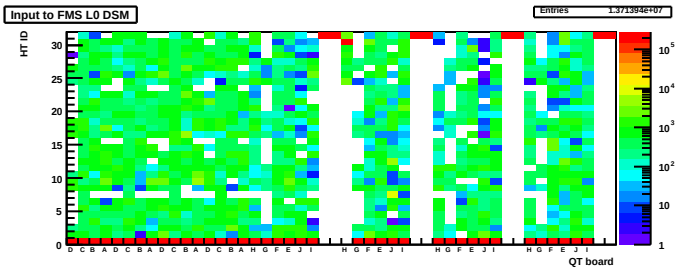

Input to FMS L0 DSM
Entries 203523
QT8(2) sum - simulated
QT board

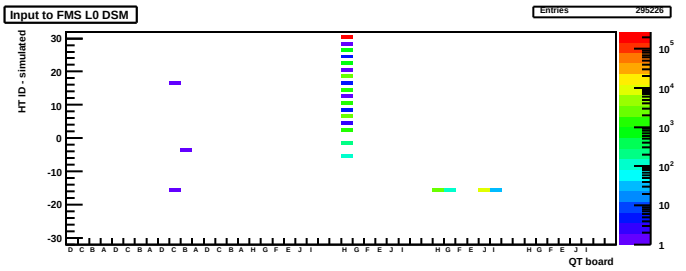

Input to FMS L0 DSM
Entries 1.371394e+07
QT8(3) sum
QT board

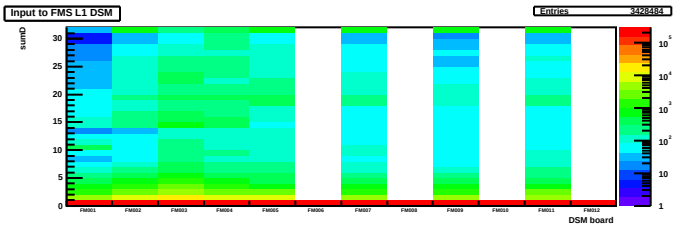

Input to FMS L0 DSM
Entries 287058
QT8(3) sum - simulated
QT board

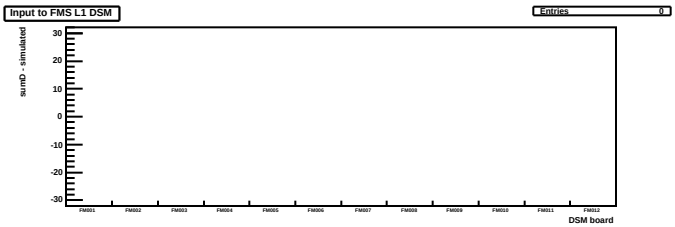

Input to FMS L0 DSM
Entries 1.371394e+07
HT ADC
QT board

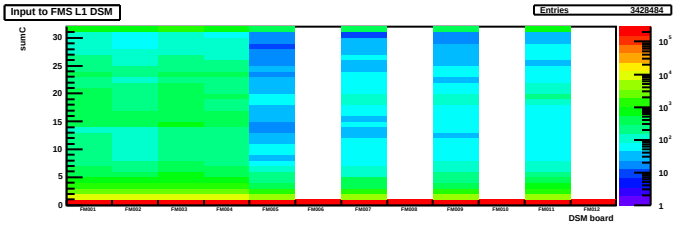

Input to FMS L0 DSM
Entries 20
HT ADC - simulated
QT board

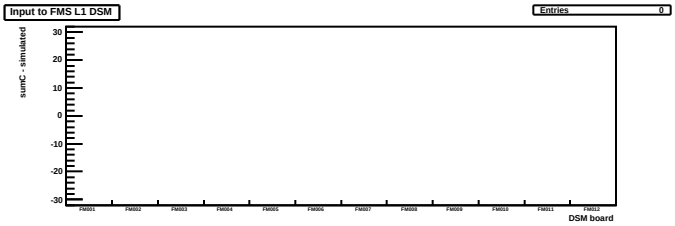

Input to FMS L0 DSM
Entries 1.371394e+07
HT ID
QT board

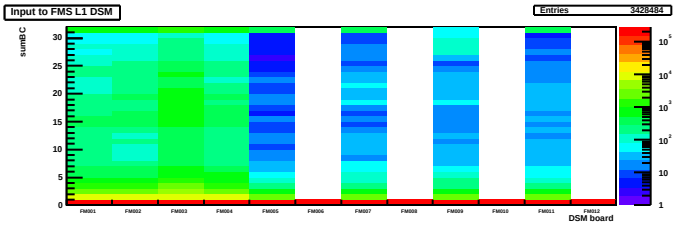

Input to FMS L0 DSM
Entries 295226
HT ID - simulated
QT board

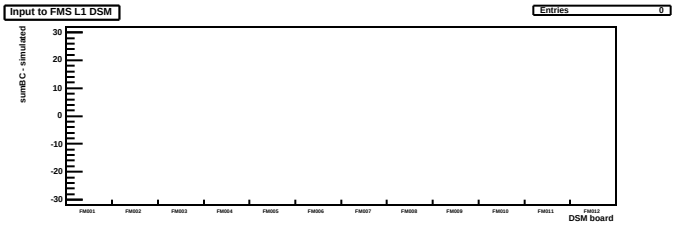

Input to FMS L1 DSM
Entries 3428484
sumD
DSM board

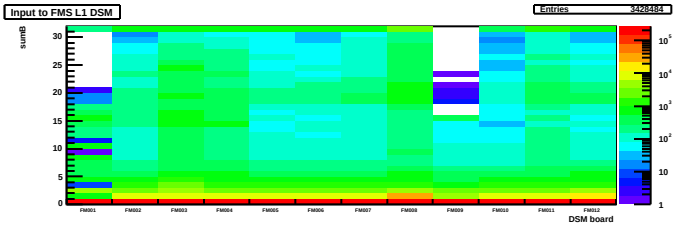

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

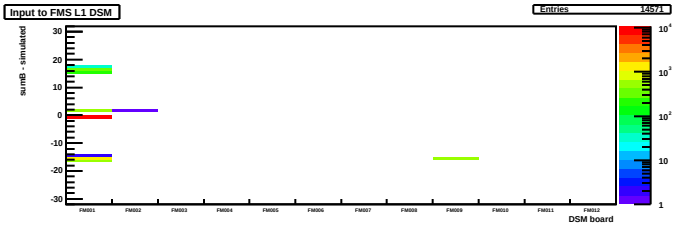

Input to FMS L1 DSM
Entries 3428484
sumC
DSM board

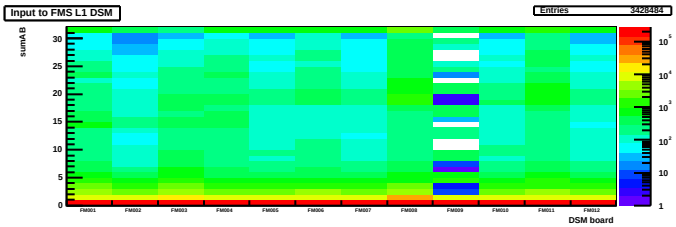

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

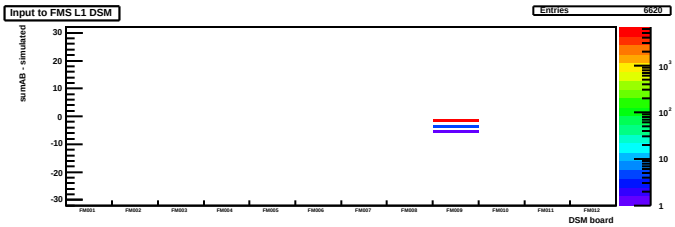

Input to FMS L1 DSM
Entries 3428484
sumBC
DSM board

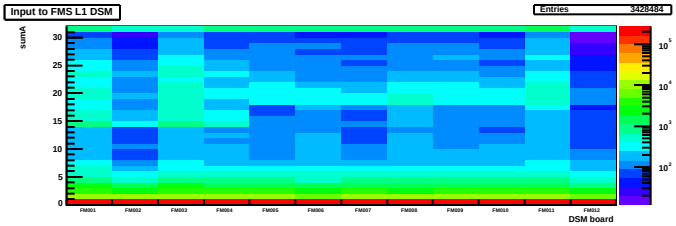

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

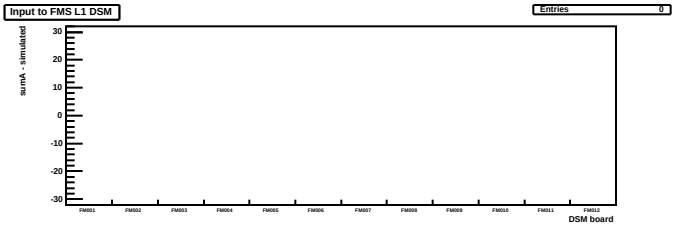

Input to FMS L1 DSM
Entries 3428484
sumB
DSM board

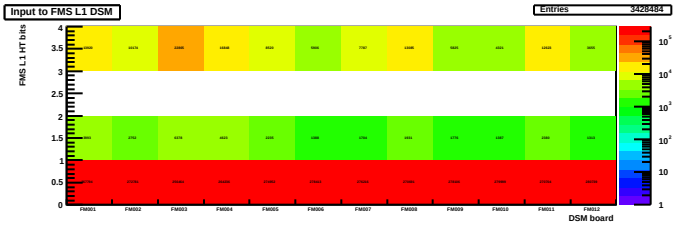

Input to FMS L1 DSM
Entries 14571
sumB - simulated
DSM board

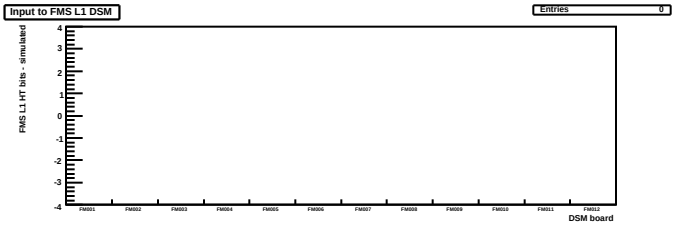

Input to FMS L1 DSM
Entries 3428484
sumAB
DSM board

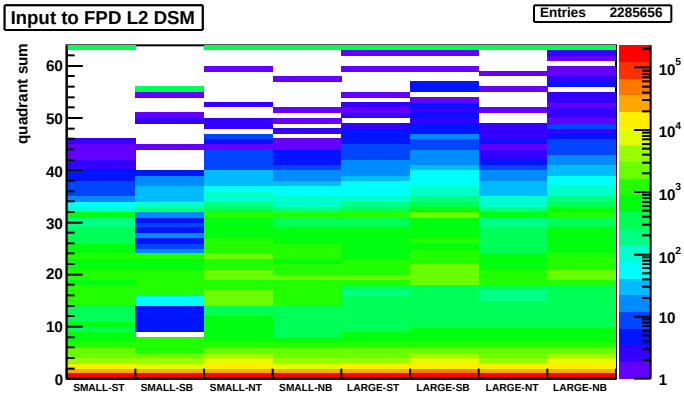

Input to FMS L1 DSM
Entries 6620
sumAB - simulated
DSM board

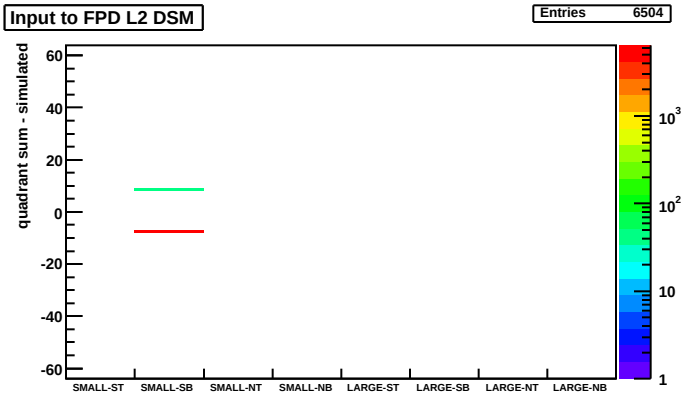

Input to FMS L1 DSM
Entries 3428484
sumA
DSM board

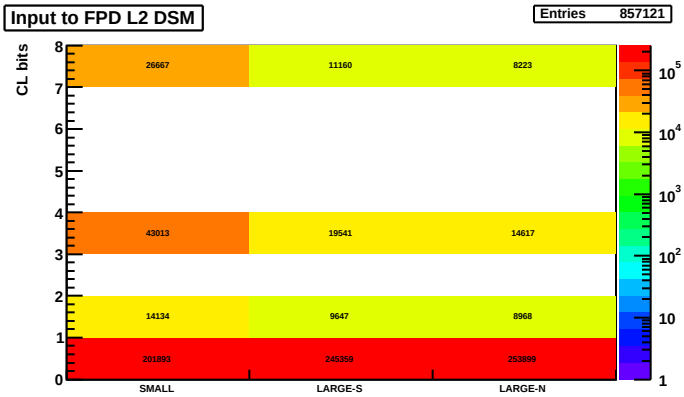

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

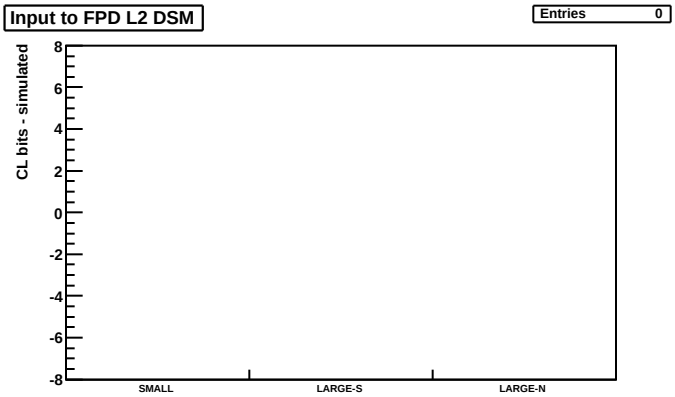

Input to FMS L1 DSM
Entries 3428484
FMS L1 HT bits
DSM board

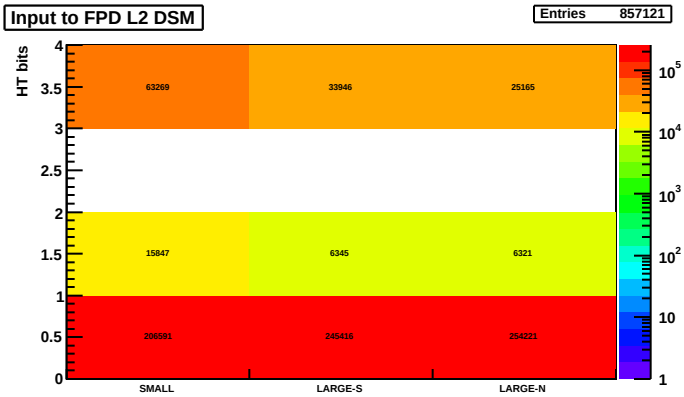

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

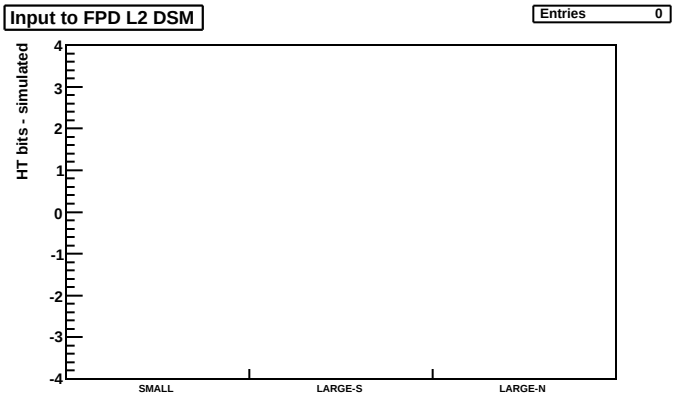

Input to FPD L2 DSM
Entries 2285656
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

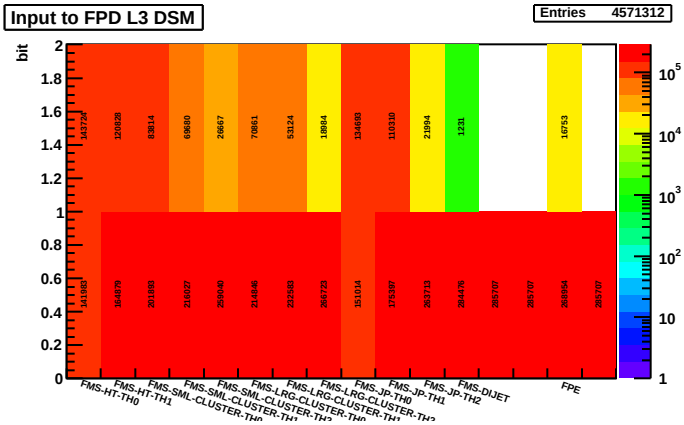

Input to FPD L2 DSM
Entries 6504
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

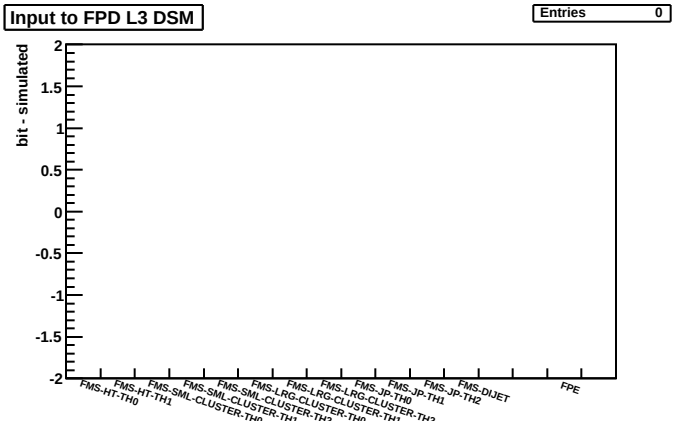

Input to FPD L2 DSM
Entries 857121
CL bits
26667
11160
8223
43013
19541
14617
14134
9647
8968
201893
245359
253899
SMALL
LARGE-S
LARGE-N

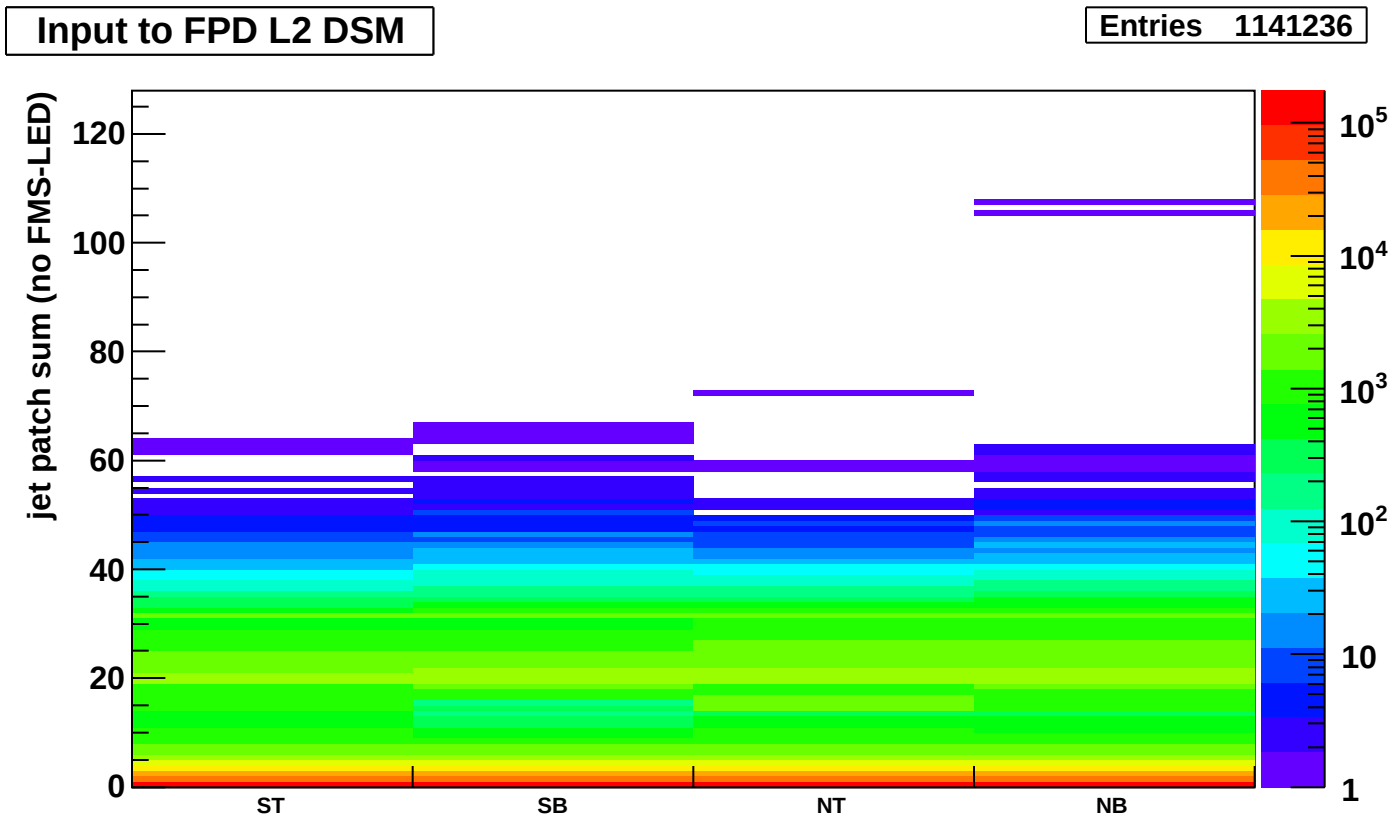

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

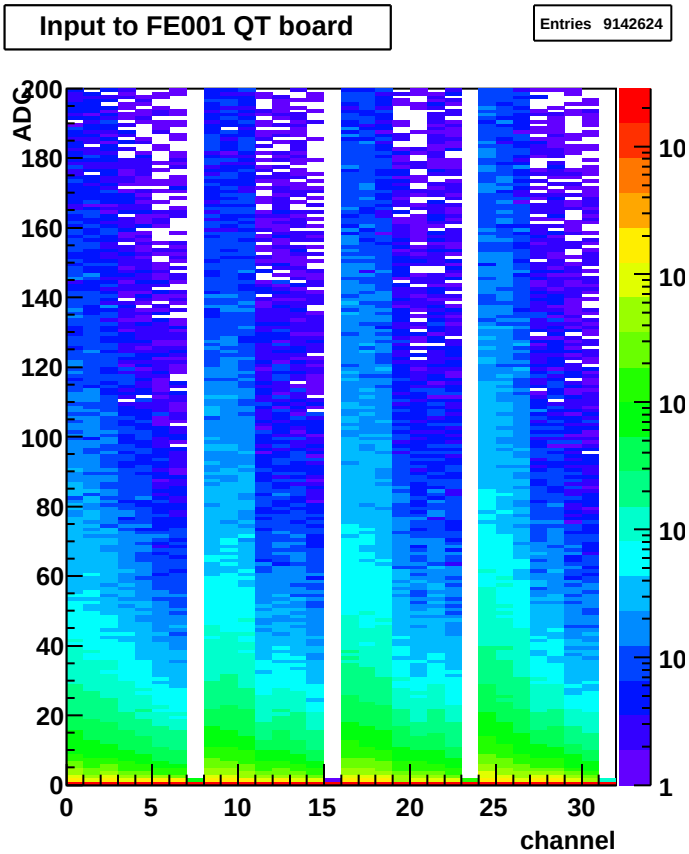

Input to FPD L2 DSM
Entries 857121
HT bits
63269
33946
25165
15847
6345
6321
206591
245416
254221
SMALL
LARGE-S
LARGE-N

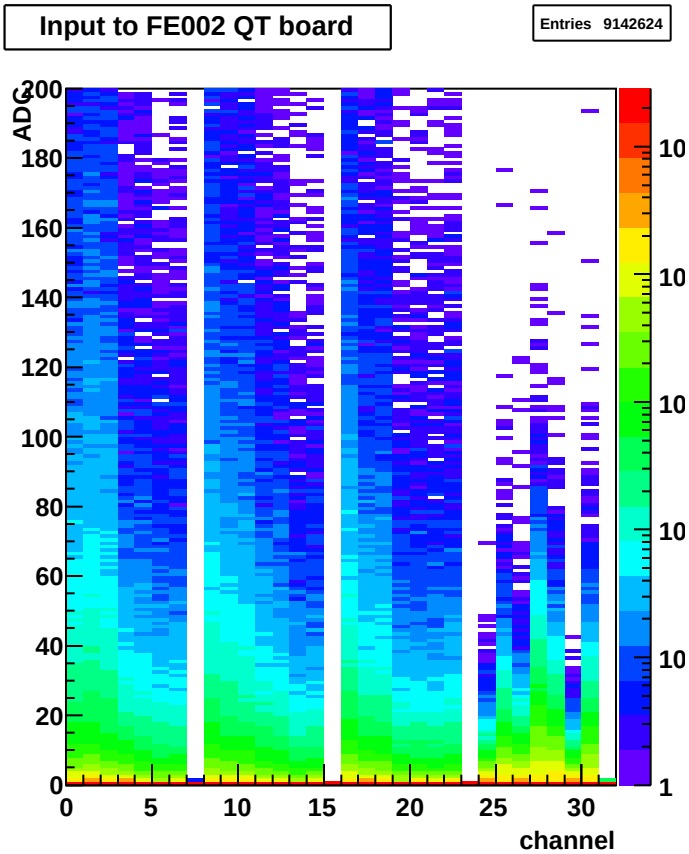

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

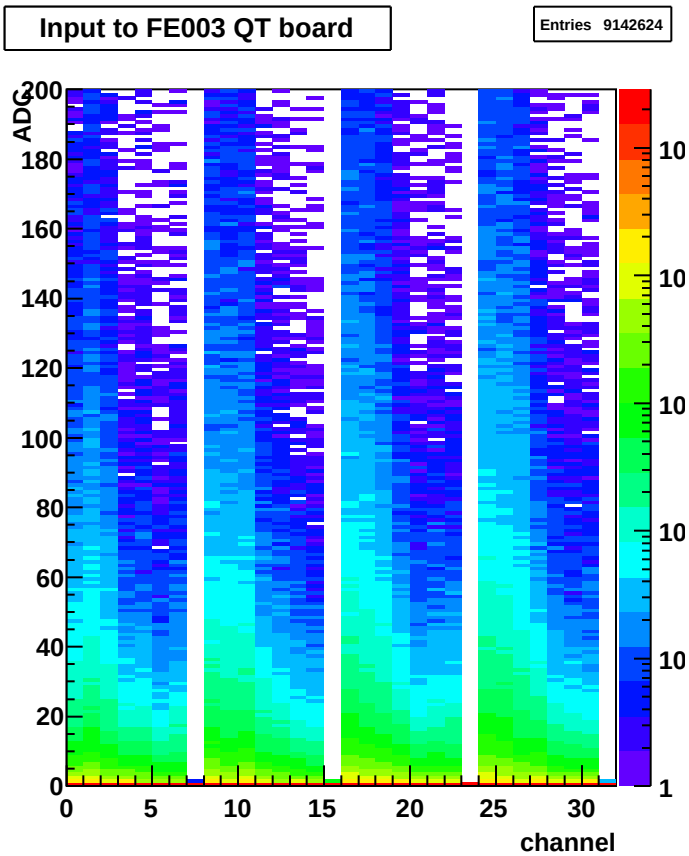

Input to FPD L3 DSM
Entries 4571312
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

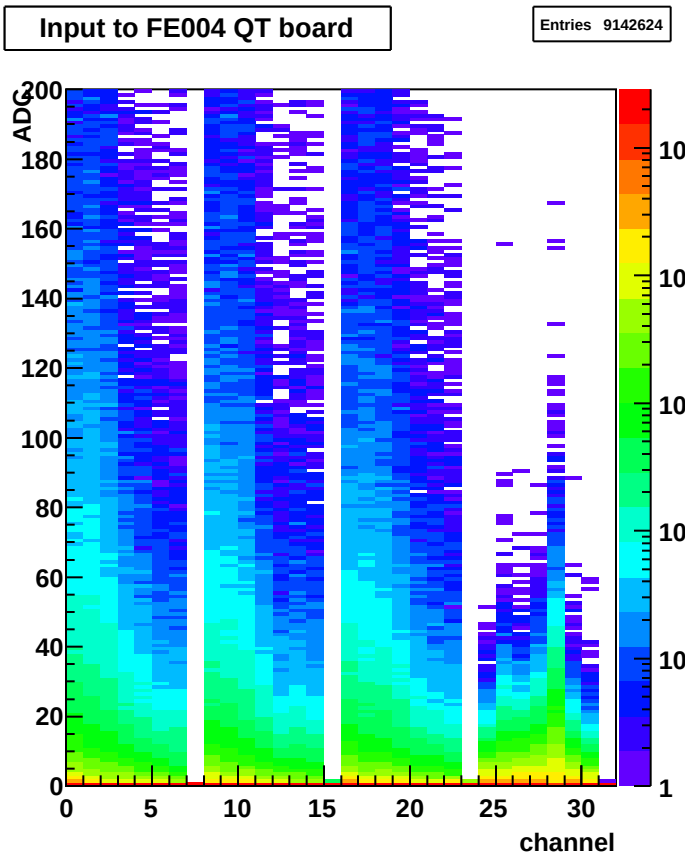

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

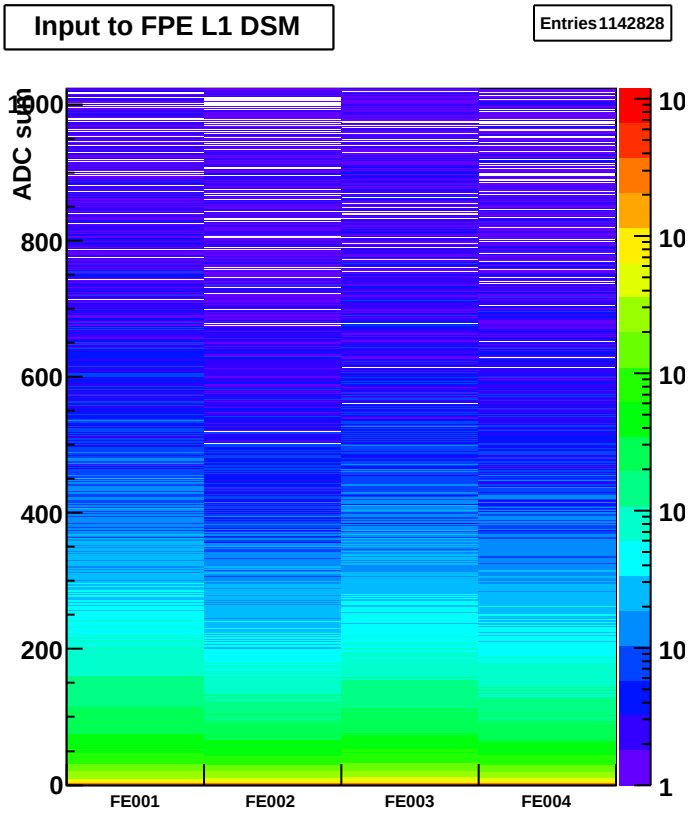

Input to FPD L2 DSM
Entries 1141236
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

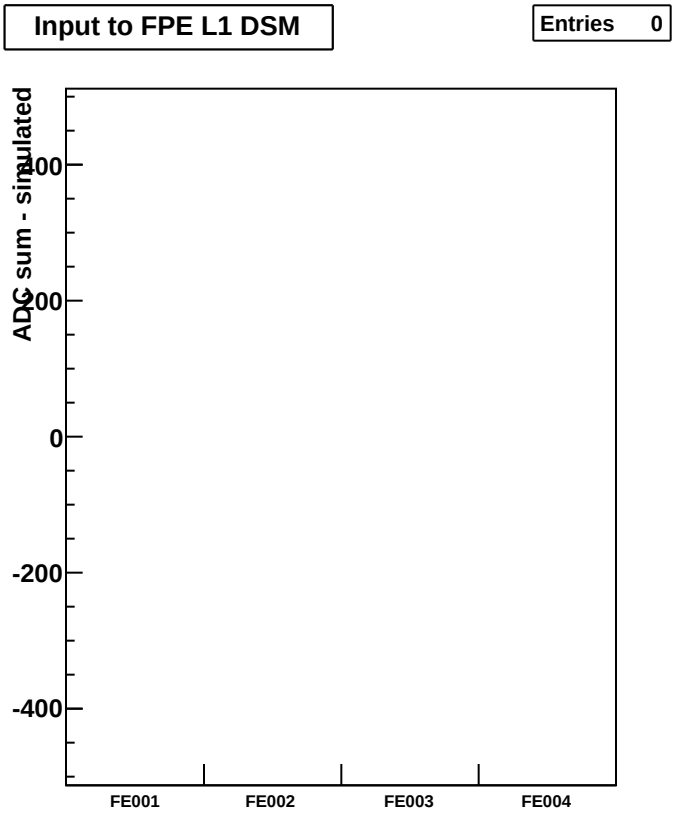

Input to FE001 QT board
Entries 9142624
ADC
channel

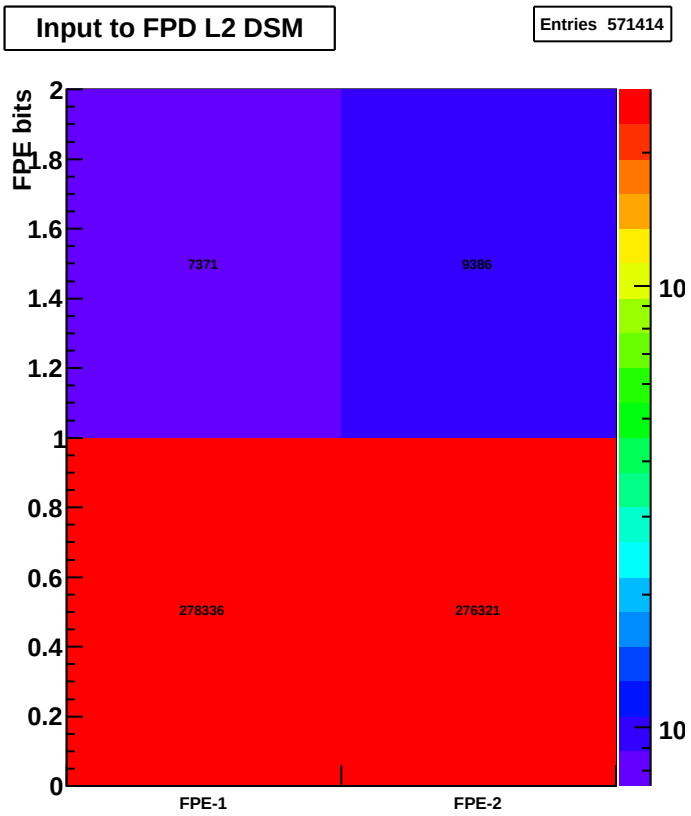

Input to FE002 QT board
Entries 9142624
ADC
channel

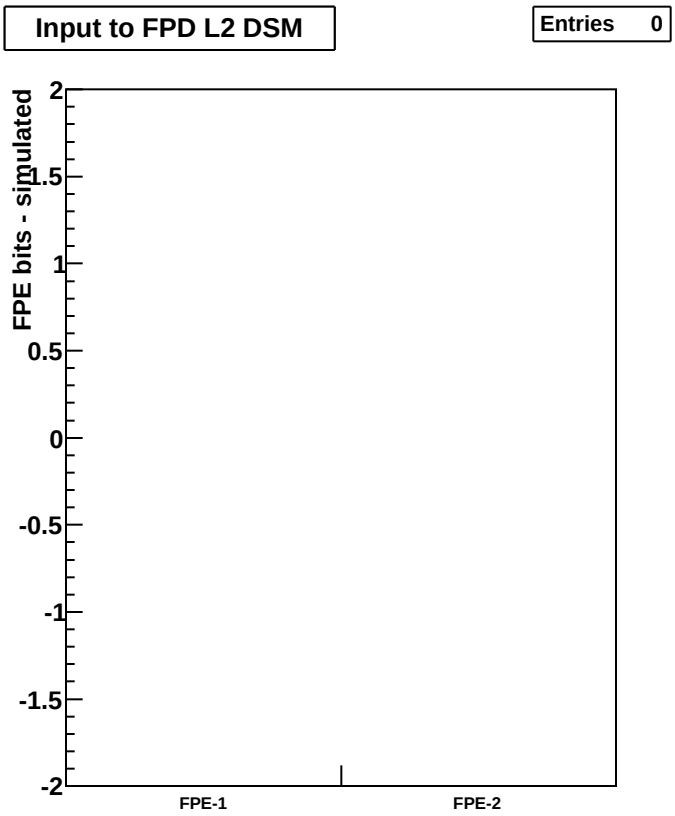

Input to FE003 QT board
Entries 9142624
ADC
channel


Input to FE004 QT board
Entries 9142624
ADC
channel


Input to FPE L1 DSM
Entries 1142828
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 571414
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
7371
9386
278336
276321
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2