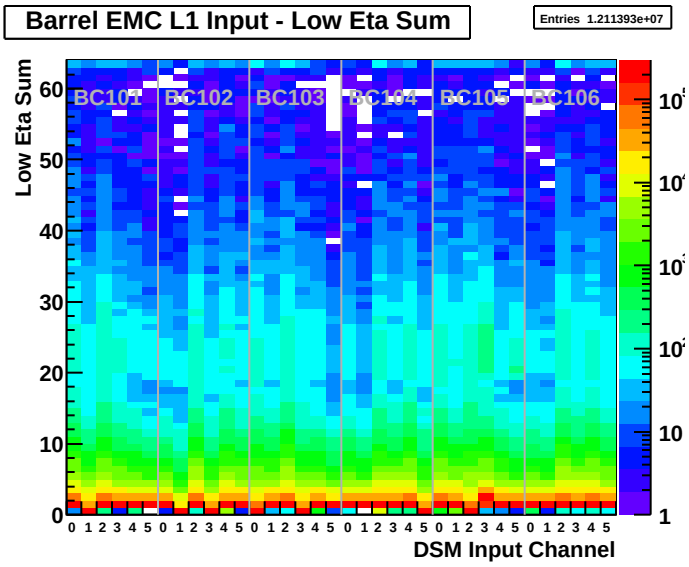
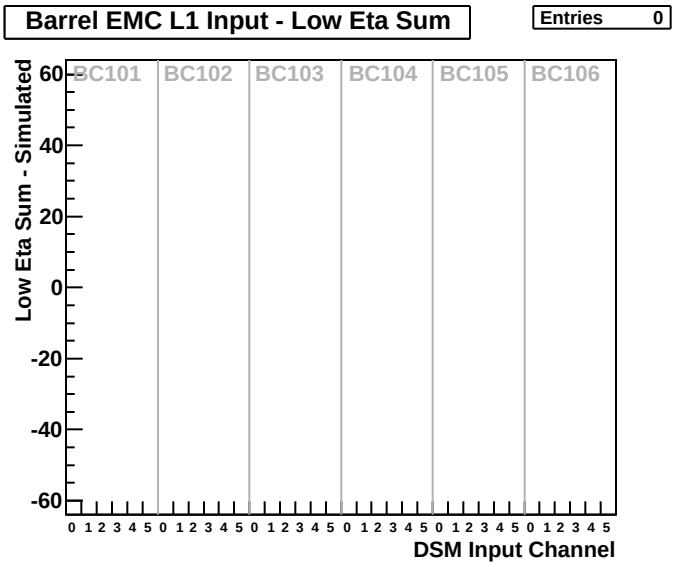
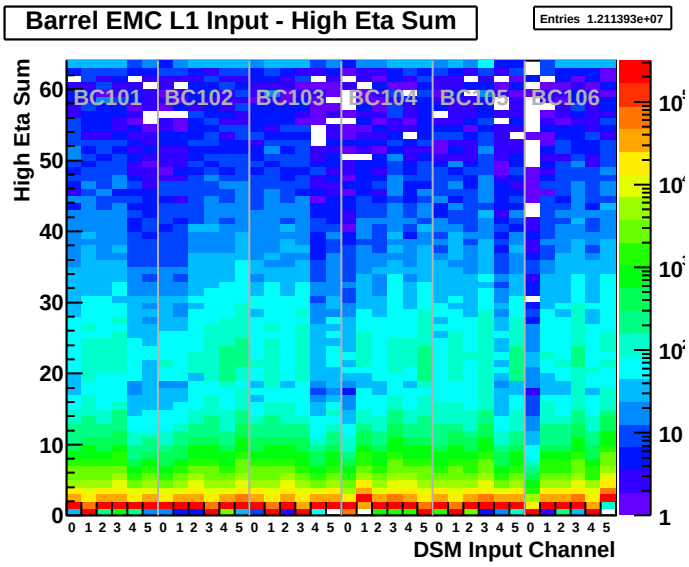
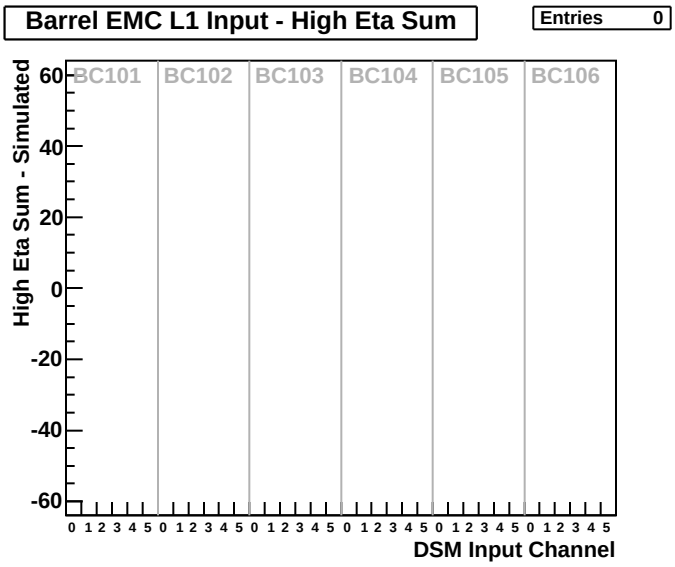
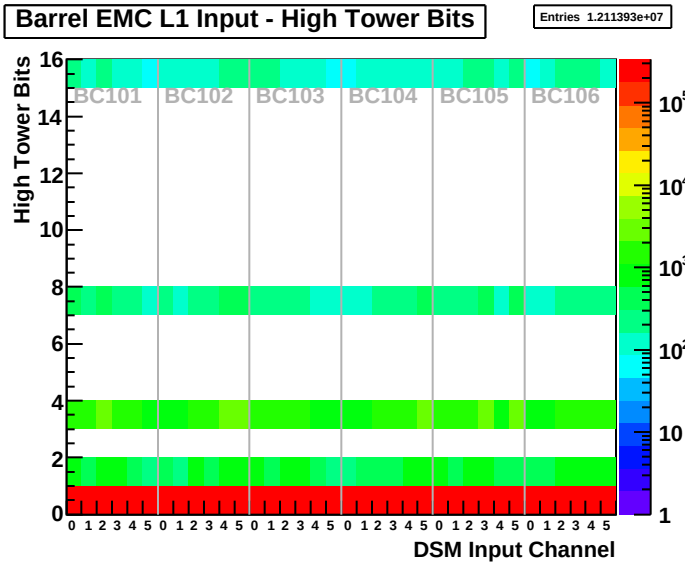
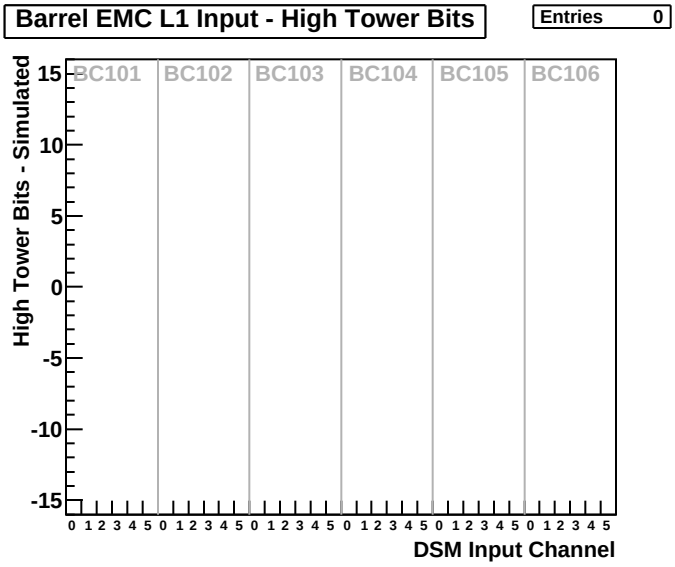

Barrel EMC L1 Input - Low Eta Sum
Entries 1.211393e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

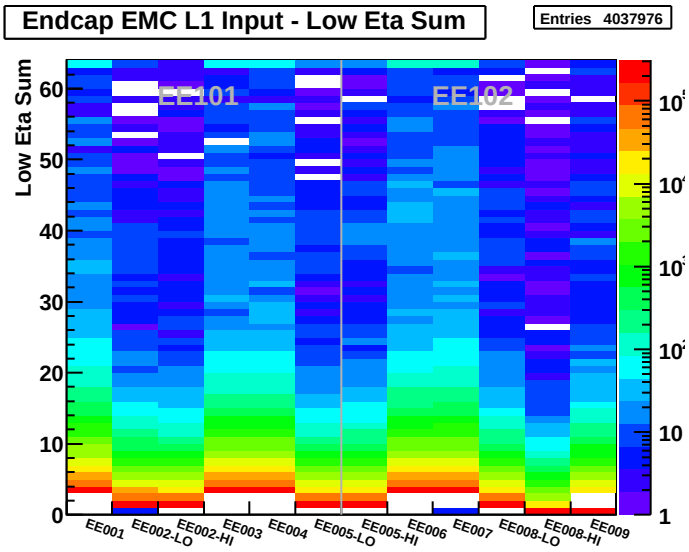

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

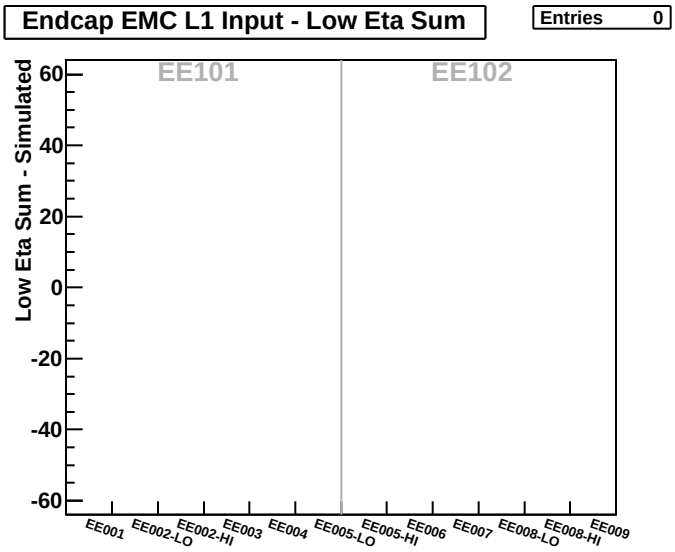

Barrel EMC L1 Input - High Eta Sum
Entries 1.211393e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

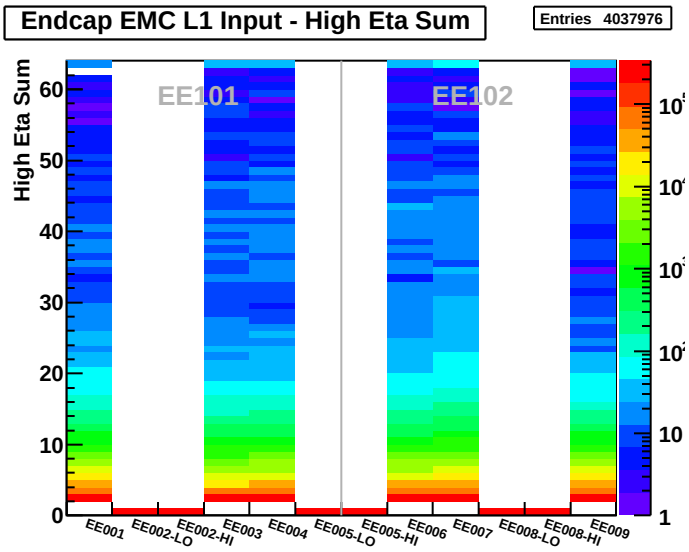

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

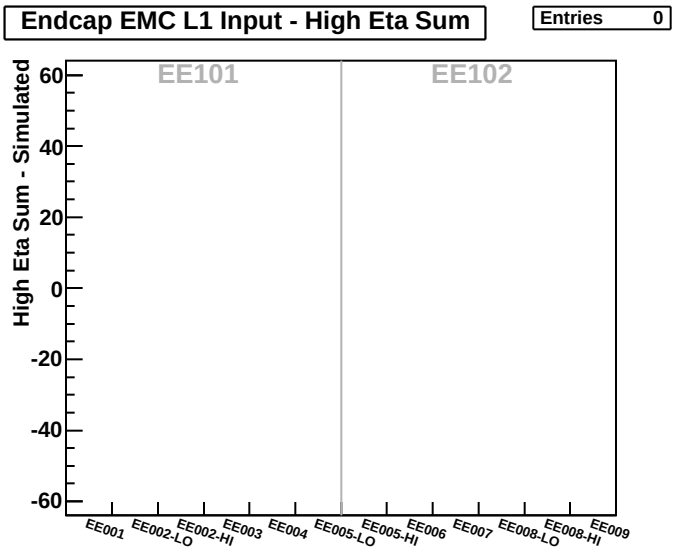

Barrel EMC L1 Input - High Tower Bits
Entries 1.211393e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

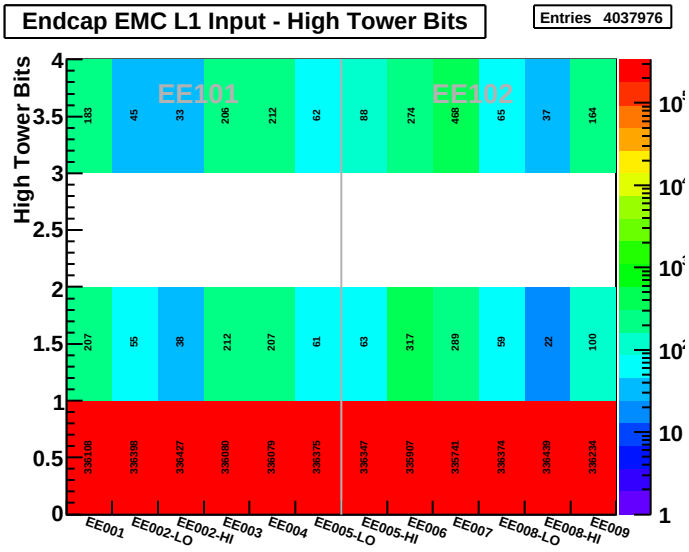

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

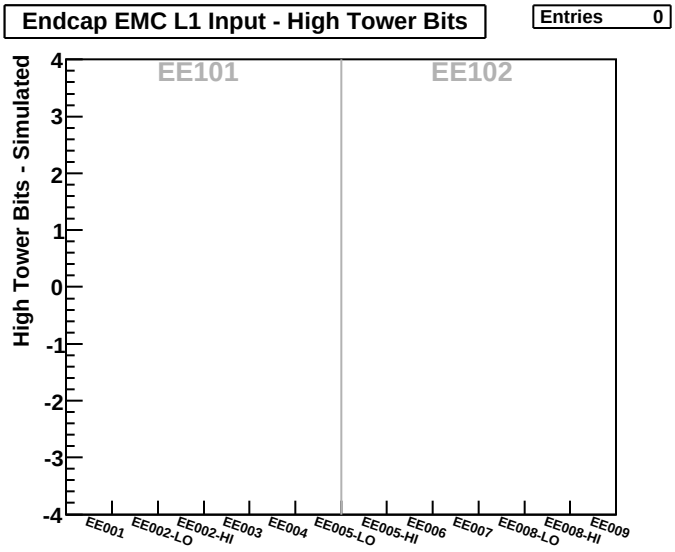

Endcap EMC L1 Input - Low Eta Sum
Entries 4037976
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

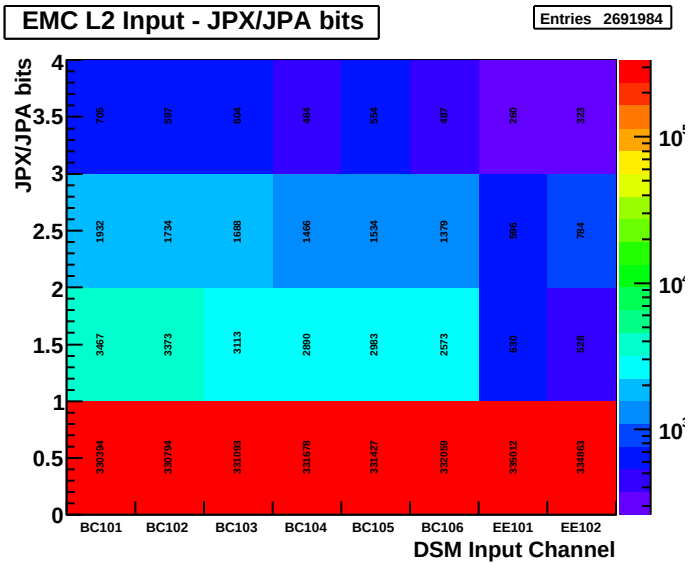

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

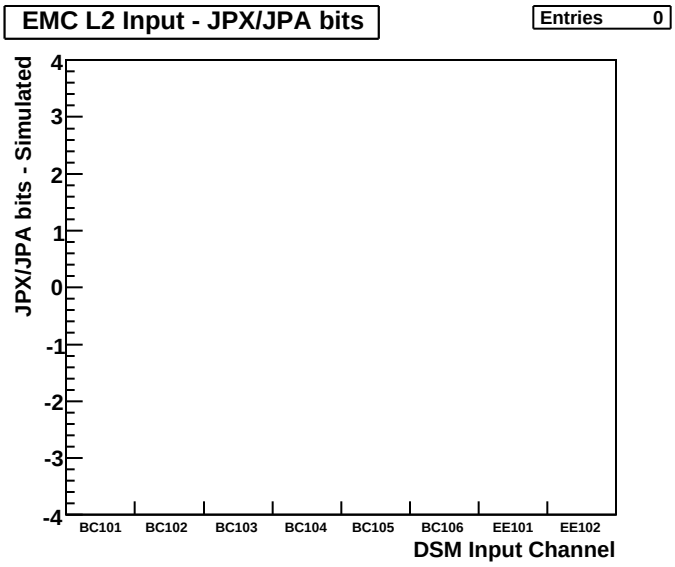

Endcap EMC L1 Input - High Eta Sum
Entries 4037976
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

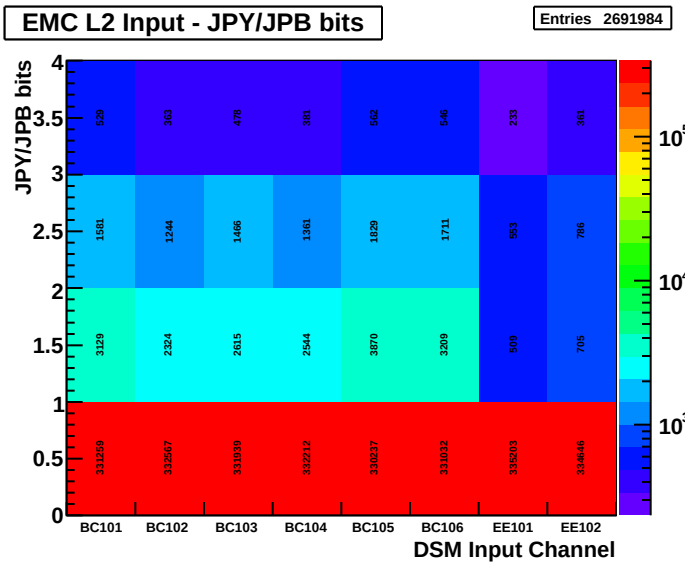

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

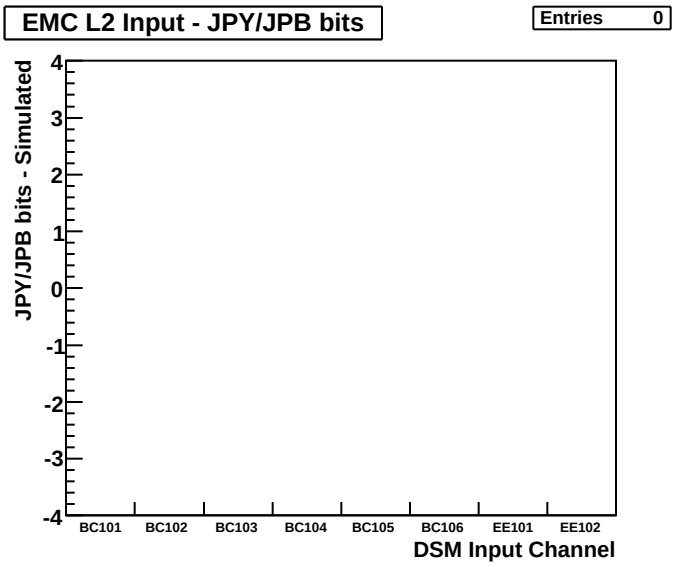

Endcap EMC L1 Input - High Tower Bits
Entries 4037976
High Tower Bits
EE101
EE102
183 45 33 206 212 62 88 274 468 65 37 164
207 55 38 212 207 61 63 317 289 59 22 100
336108 336398 336427 336080 336079 336375 336347 335907 335741 336374 336439 336234
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

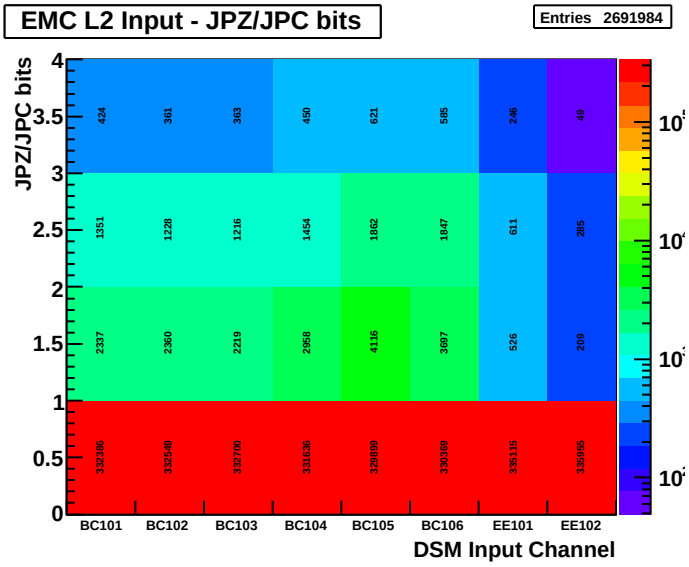

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

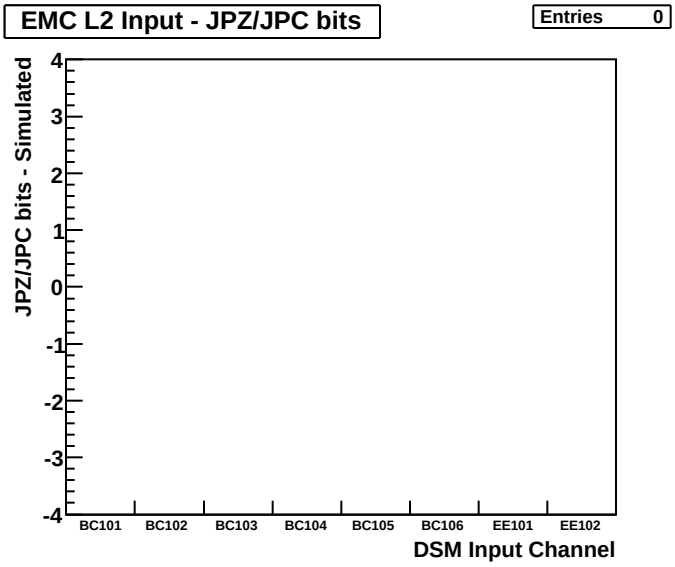

EMC L2 Input - JPX/JPA bits
Entries 2691984
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
705 597 604 464 554 487 260 323
1932 1734 1688 1466 1534 1379 986 784
3467 3373 3113 2890 2983 2573 630 528
330394 330794 331093 331678 331427 332059 335012 334863

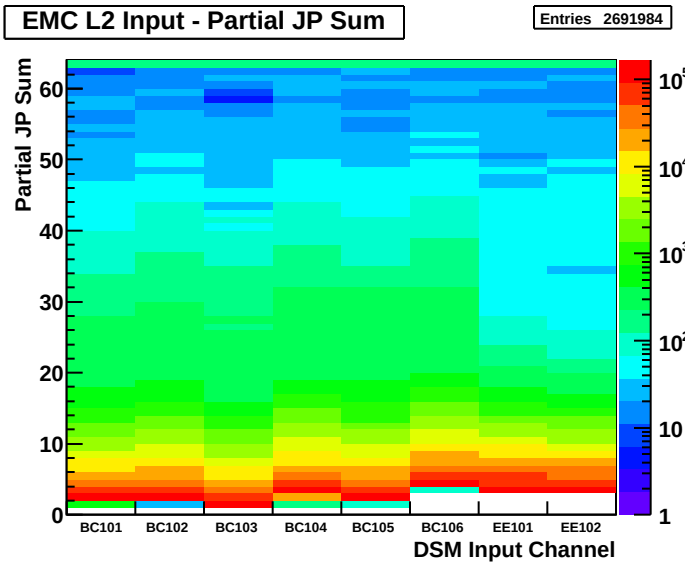

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

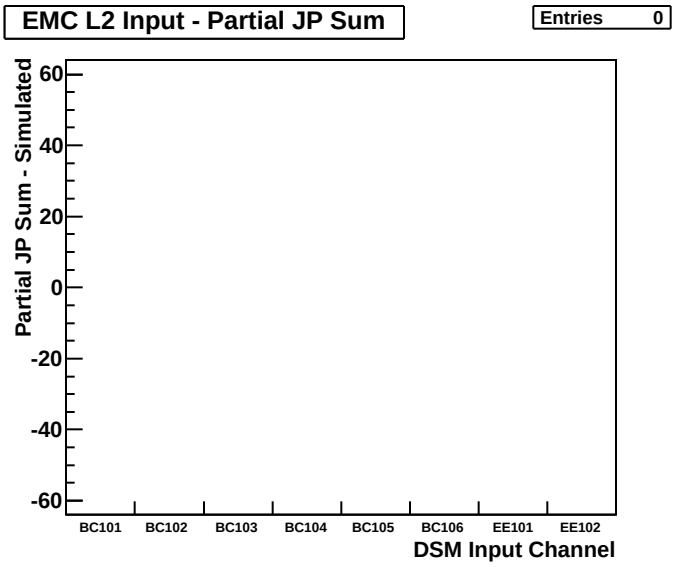

EMC L2 Input - JPY/JPB bits
Entries 2691984
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
529 368 478 381 562 546 233 361
1581 1244 1466 1361 1829 1711 553 786
3129 2324 2615 2544 3970 3209 509 705
331259 332567 331939 332212 330237 331032 335203 334646

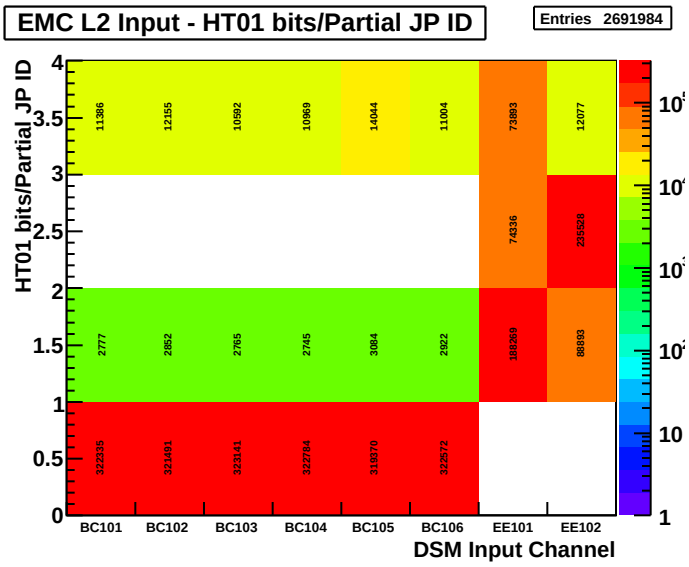

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

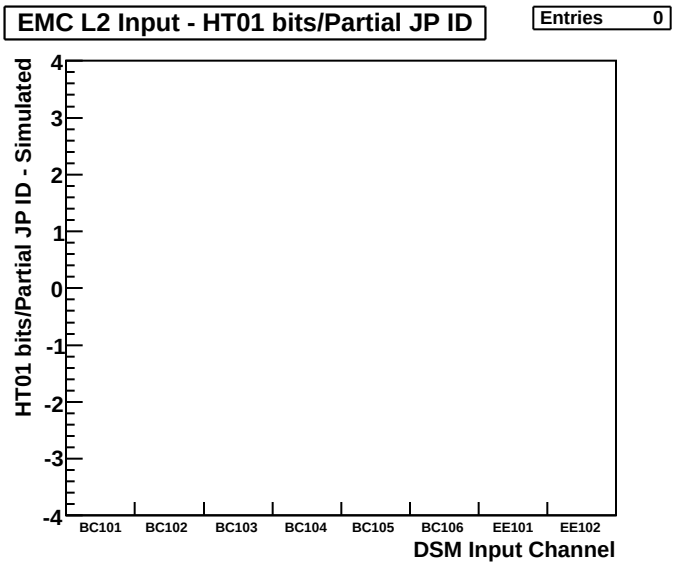

EMC L2 Input - JPZ/JPC bits
Entries 2691984
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
424 361 363 450 621 585 246 49
1351 1228 1216 1454 1862 1847 611 285
2337 2360 2219 2958 4116 3697 526 209
332386 332549 332700 331636 329899 330369 335115 335955

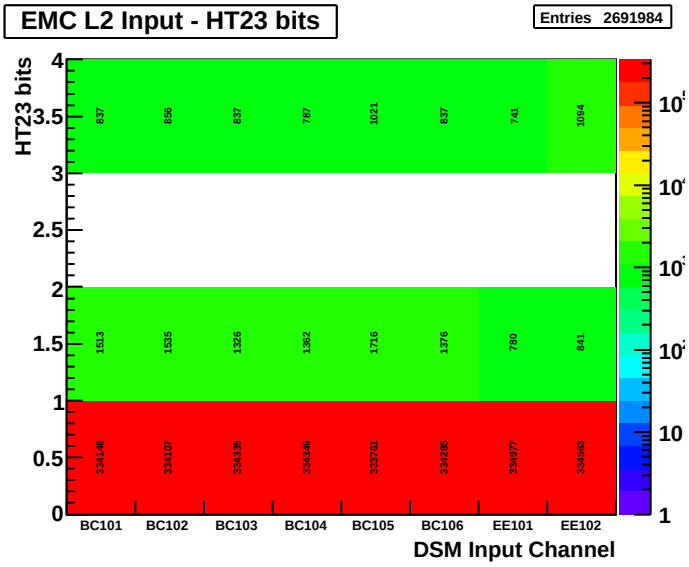

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

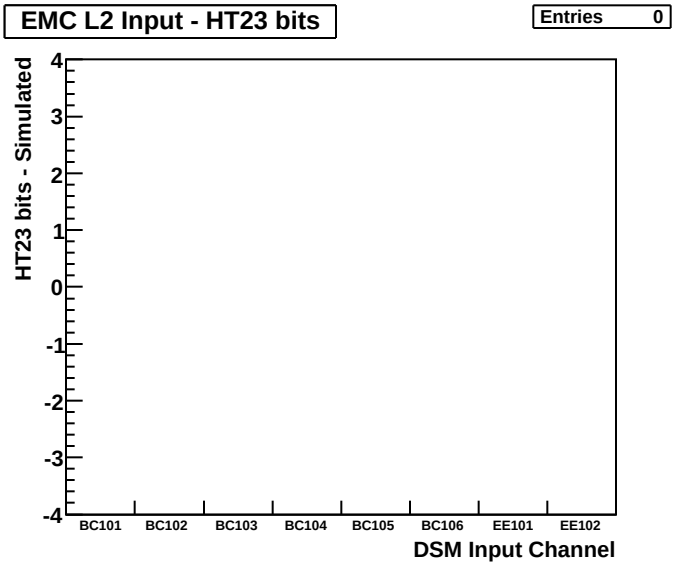

EMC L2 Input - Partial JP Sum
Entries 2691984
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

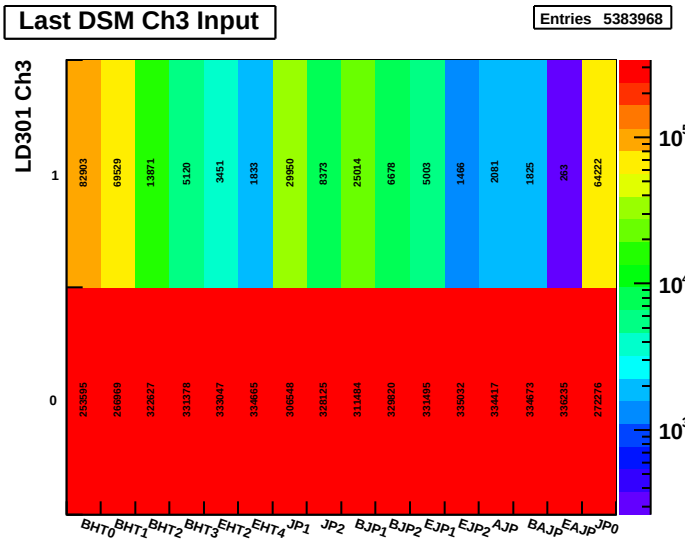

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

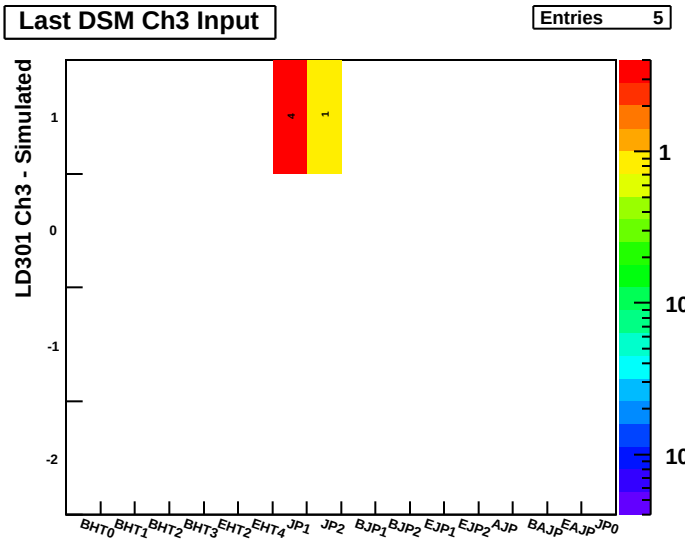

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2691984
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

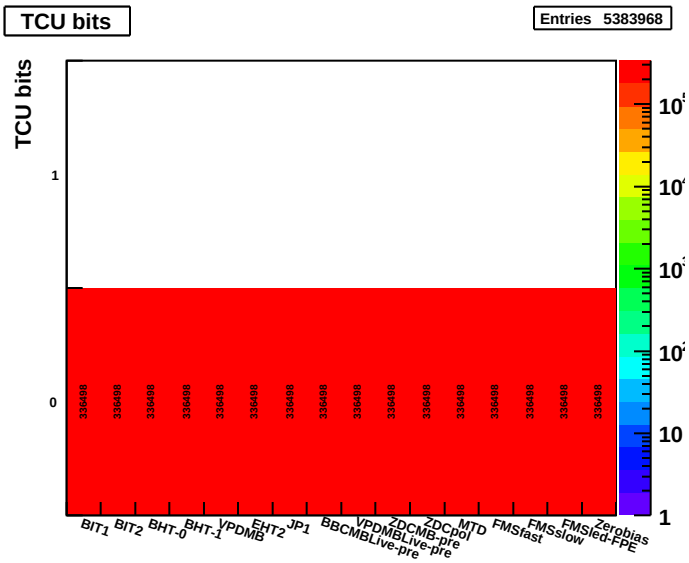

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

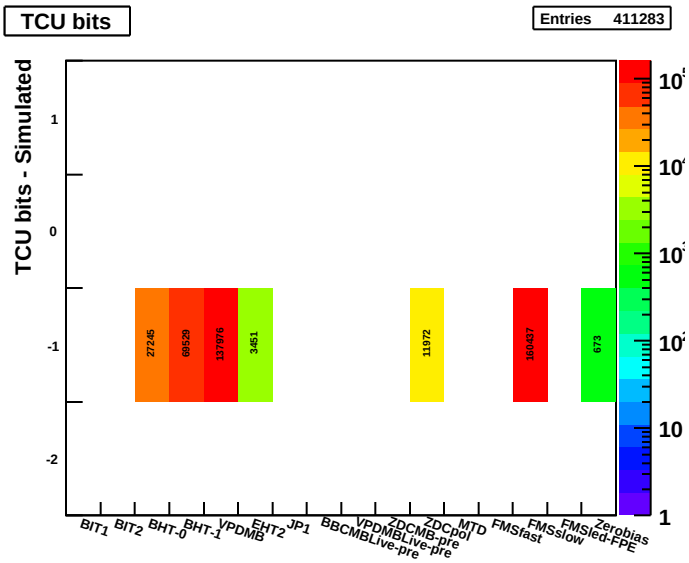

EMC L2 Input - HT23 bits
Entries 2691984
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

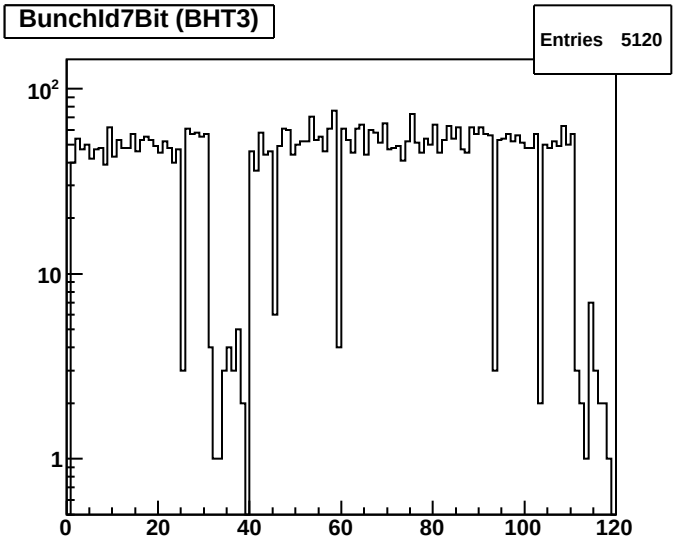

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

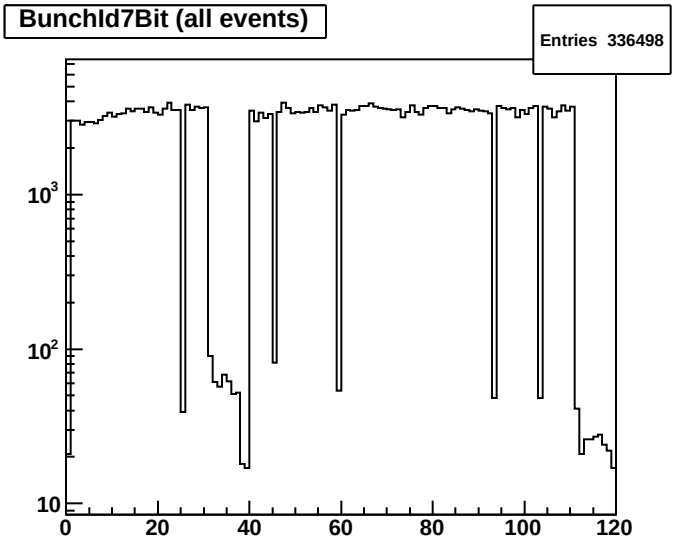

Last DSM Ch3 Input
Entries 5383968
LD301 Ch3
82903 69529 13871 5120 3451 1833 29950 8373 25014 6678 5003 1466 2081 1825 263 64222
253595 266959 322627 331378 333047 334665 306548 328125 311484 328820 331495 335032 334417 334673 336235 272276
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

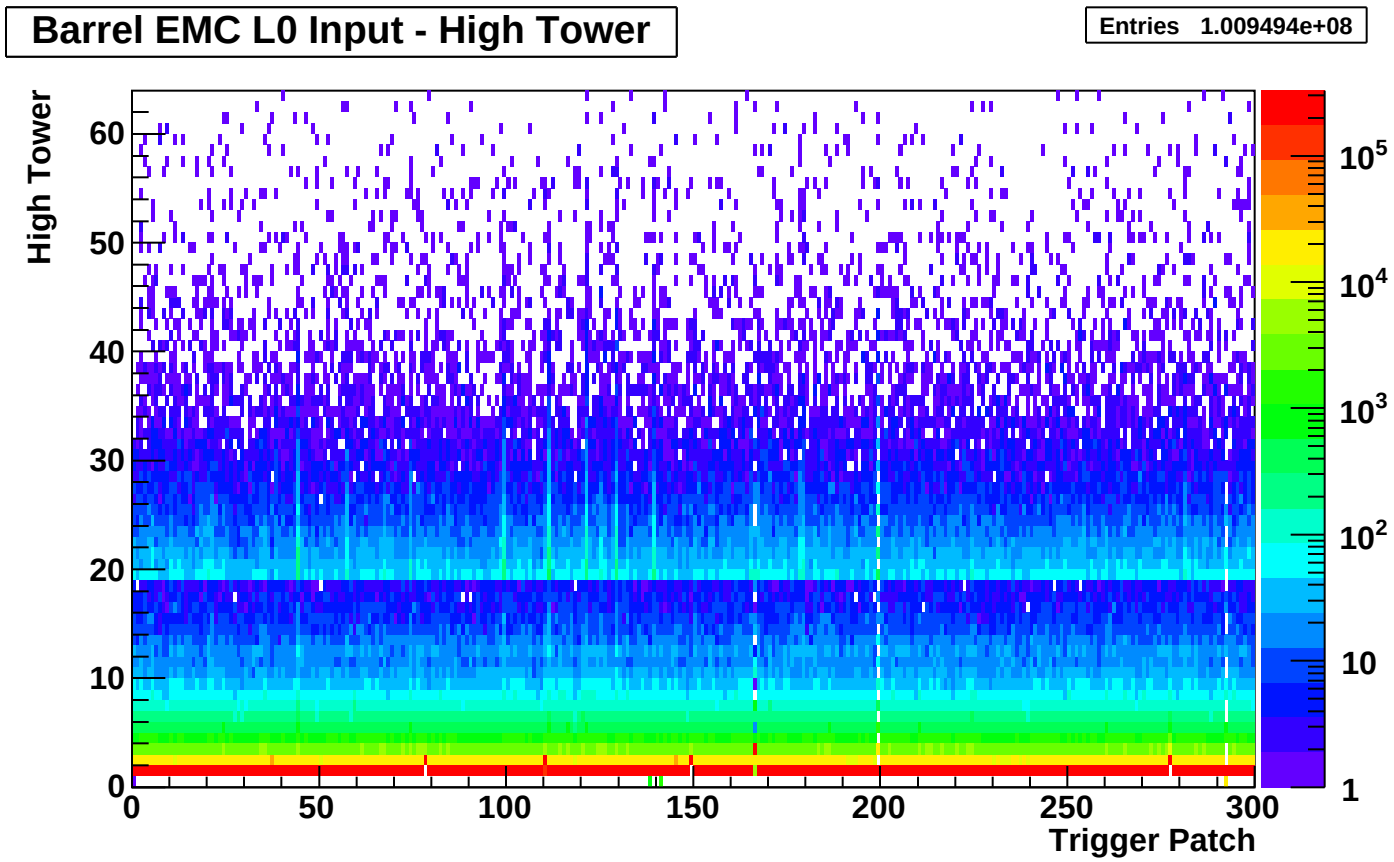

Last DSM Ch3 Input
Entries 5
LD301 Ch3 - Simulated
4 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

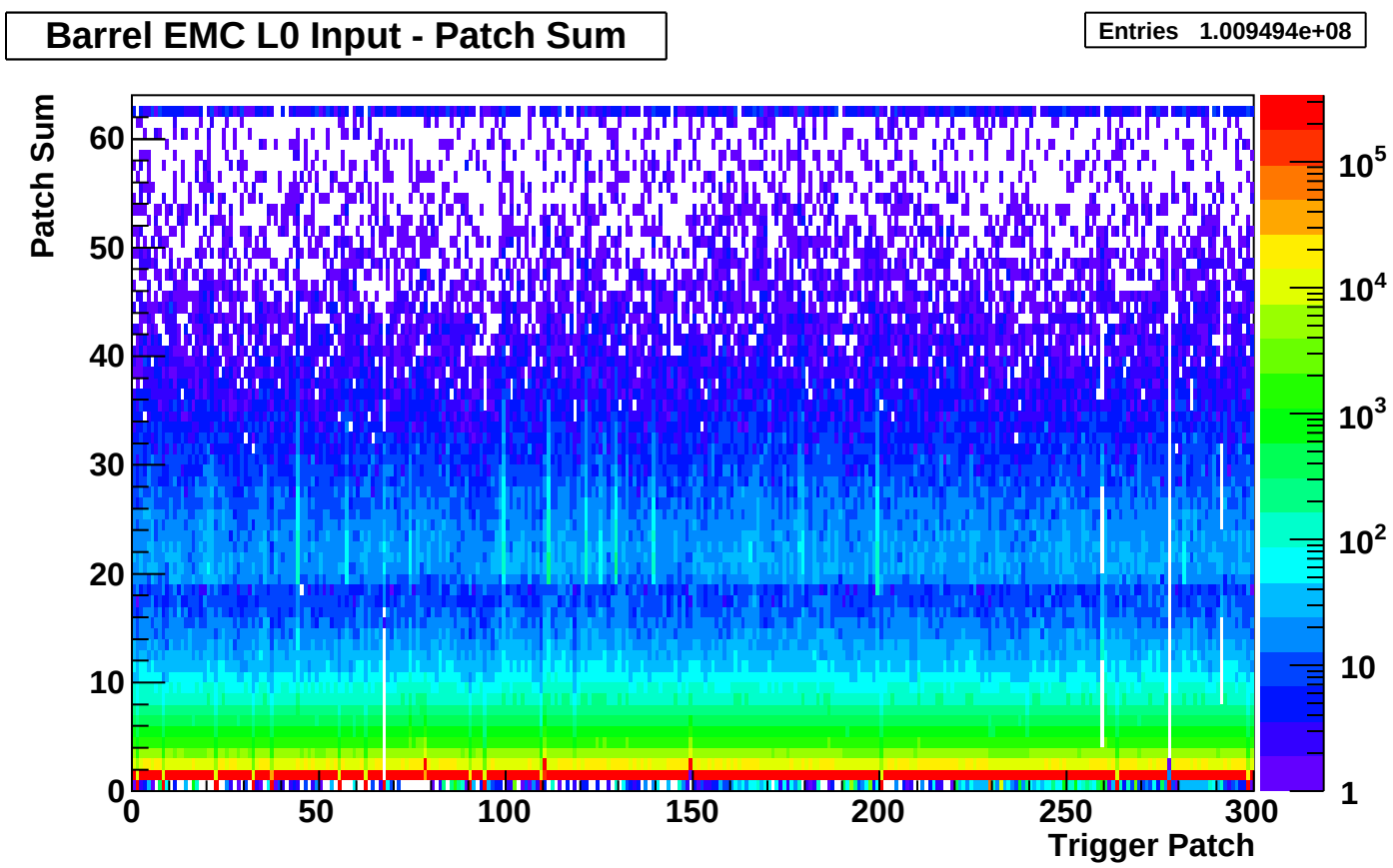

TCU bits
Entries 5383968
TCU bits
336498
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

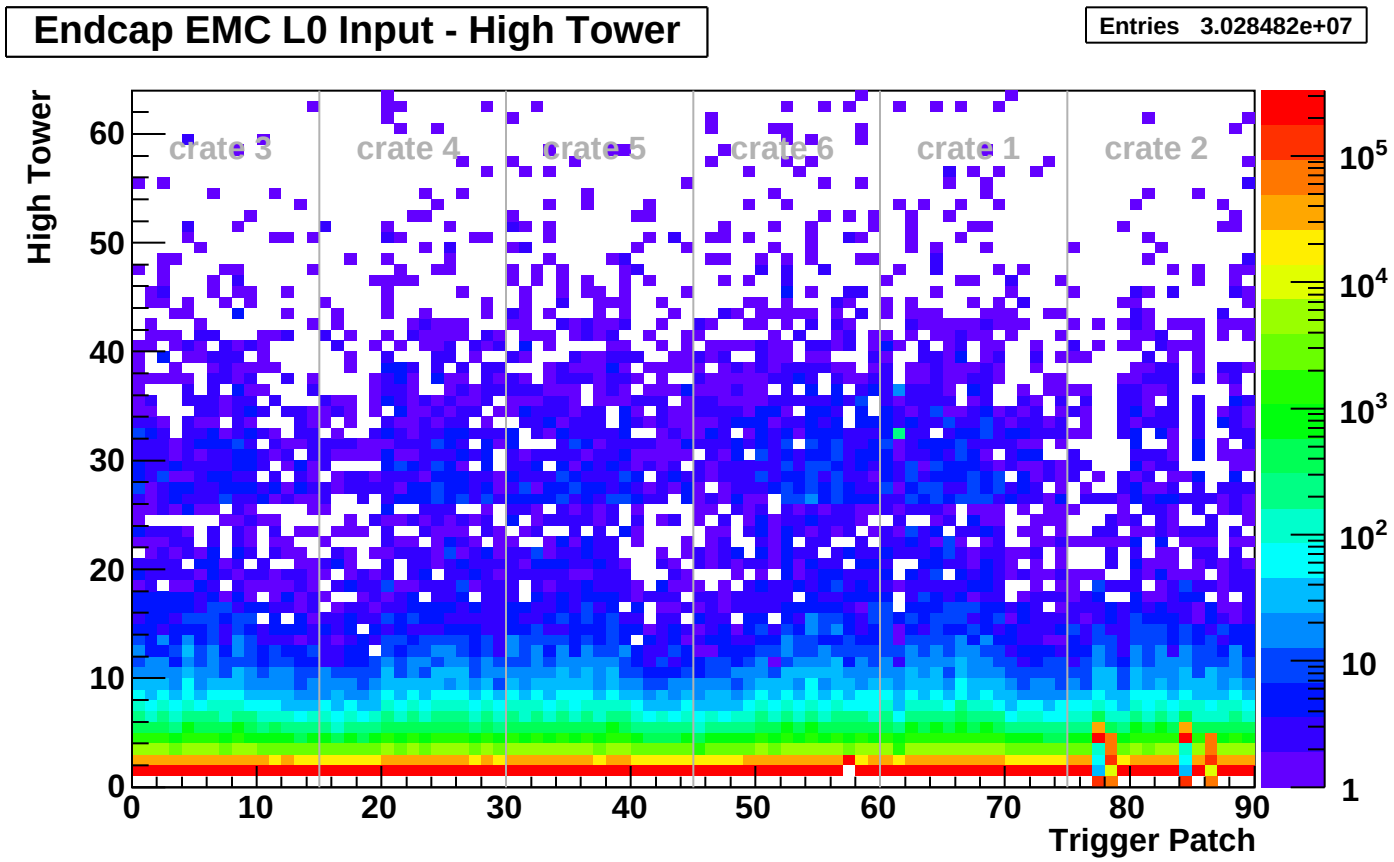

TCU bits
Entries 411283
TCU bits - Simulated
27245 69529 137976 3451 11972 160437 673
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

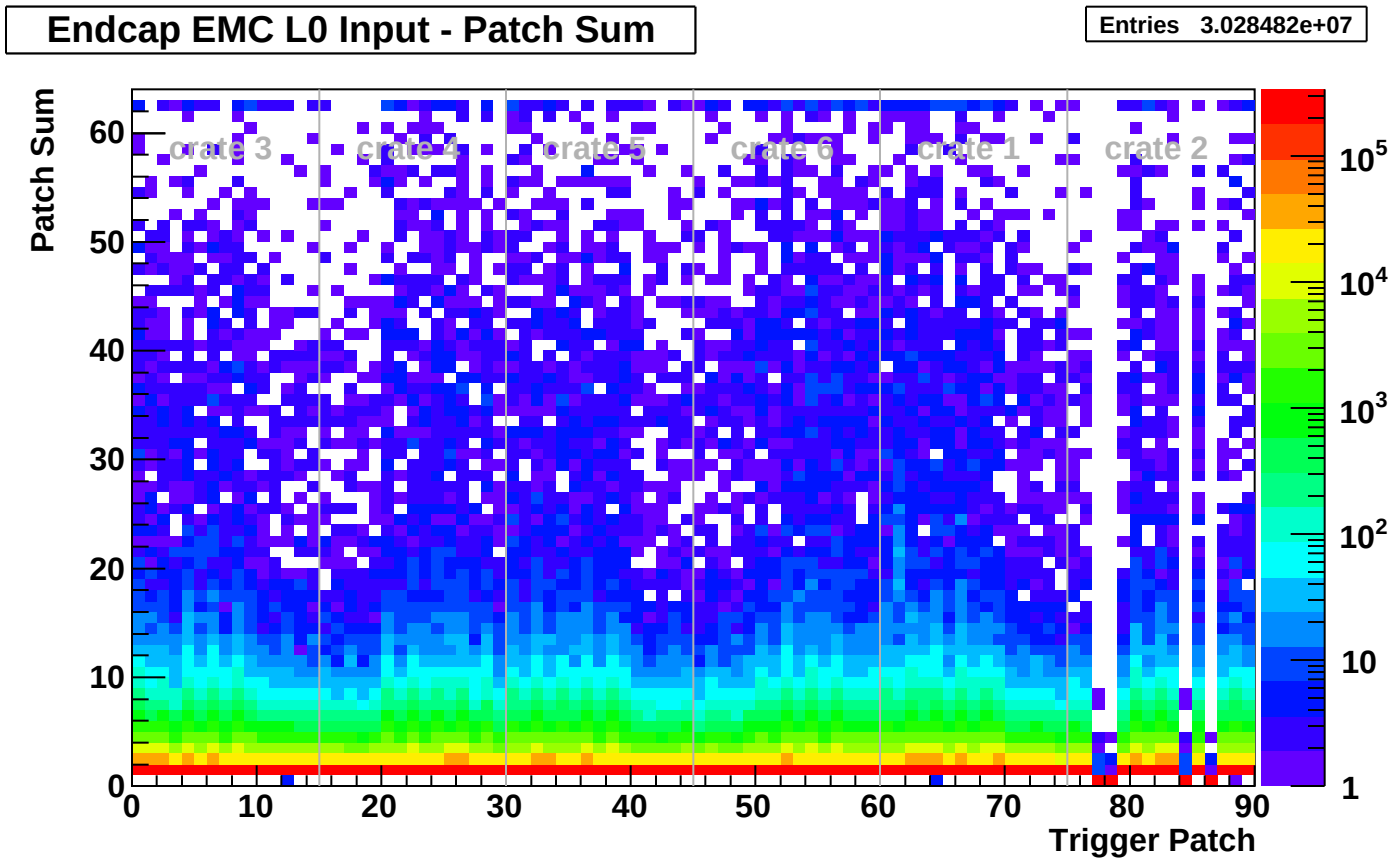

BunchId7Bit (BHT3)
Entries 5120

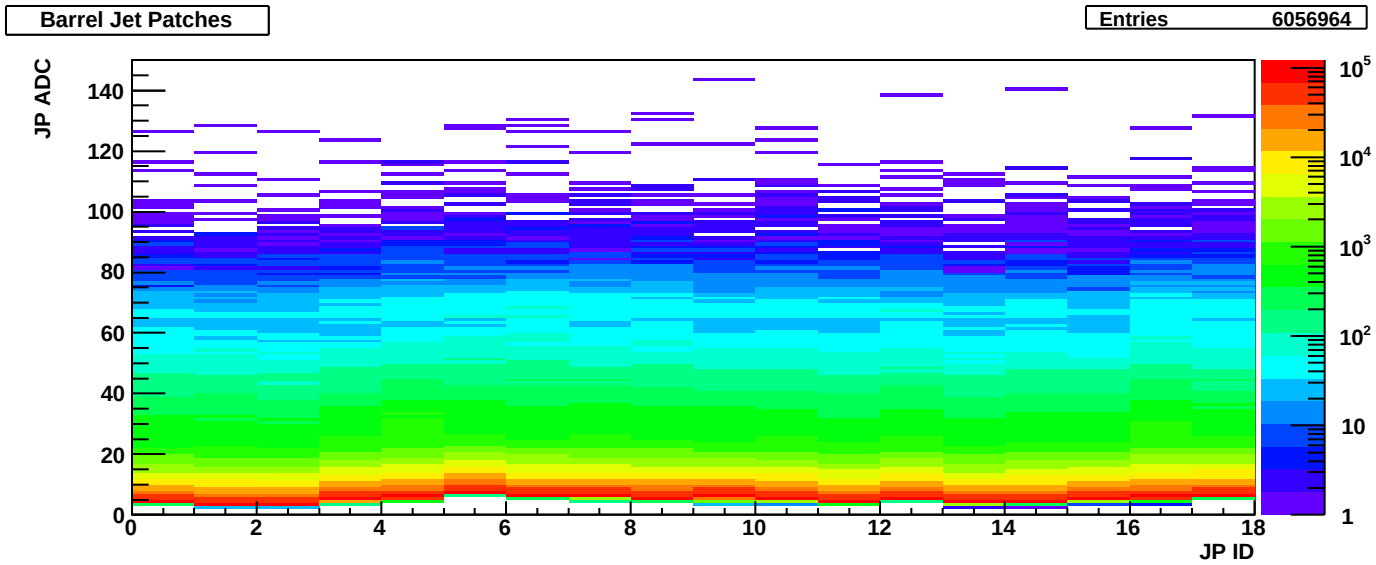

BunchId7Bit (all events)
Entries 336498

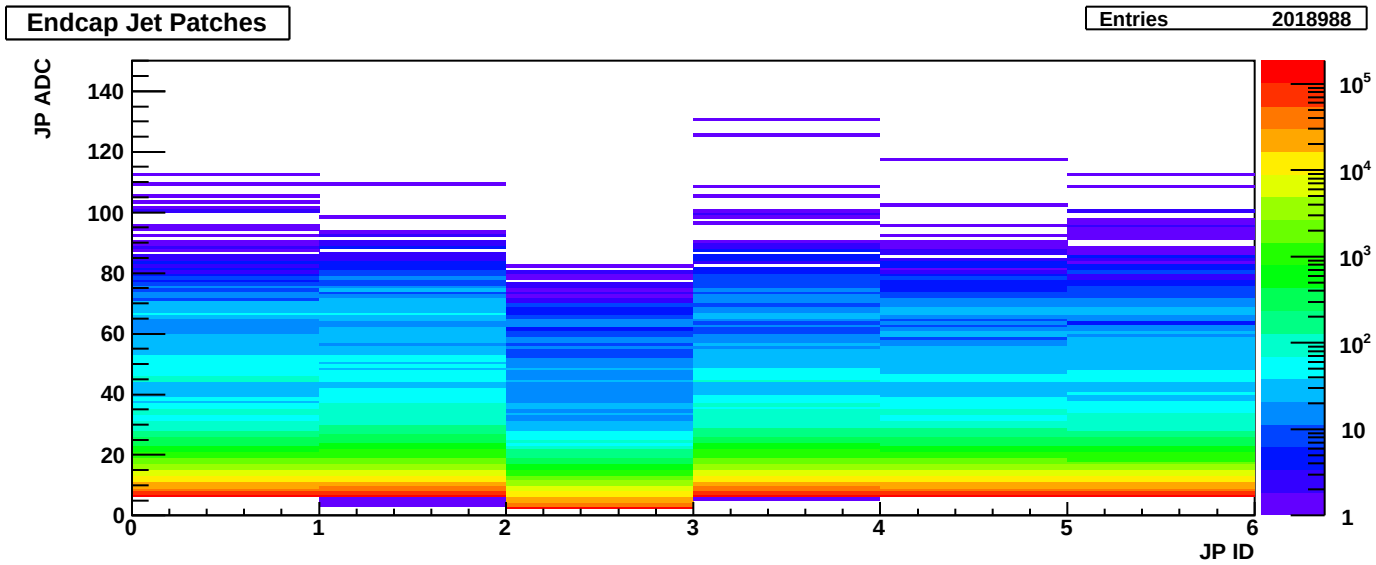

Barrel EMC L0 Input - High Tower
Entries 1.009494e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

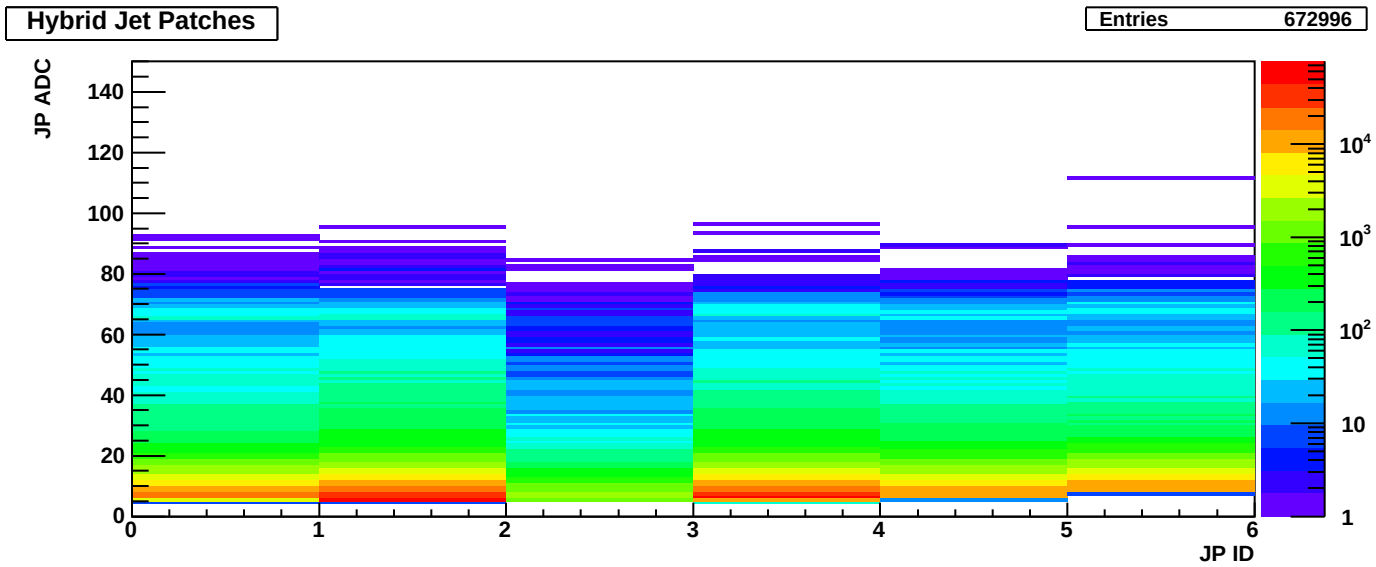

Barrel EMC L0 Input - Patch Sum
Entries 1.009494e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

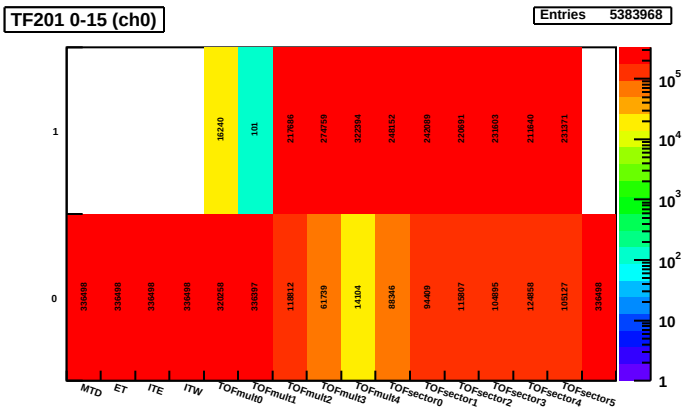

Endcap EMC L0 Input - High Tower
Entries 3.028482e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

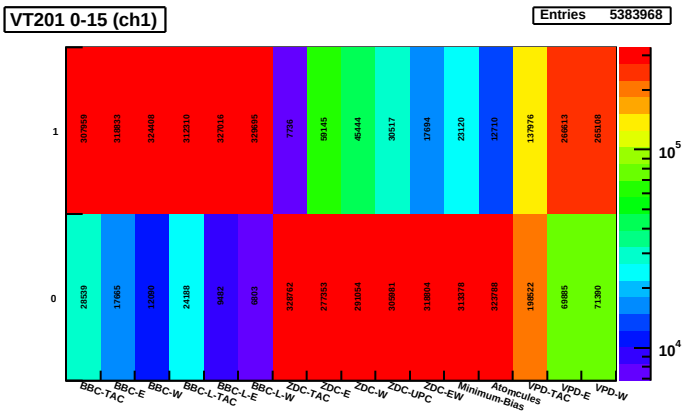

Endcap EMC L0 Input - Patch Sum
Entries 3.028482e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

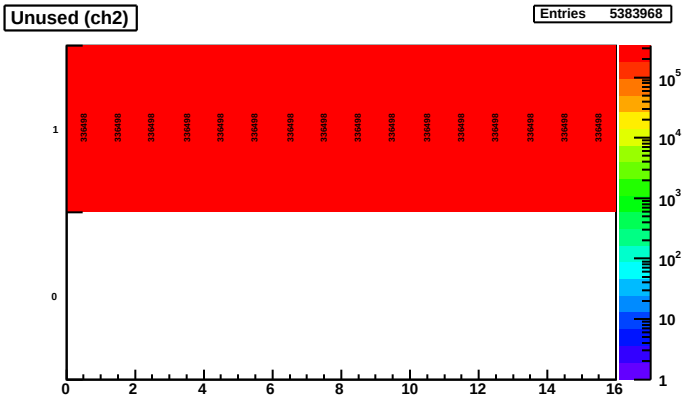

Barrel Jet Patches
Entries 6056964
JP ADC
JP ID

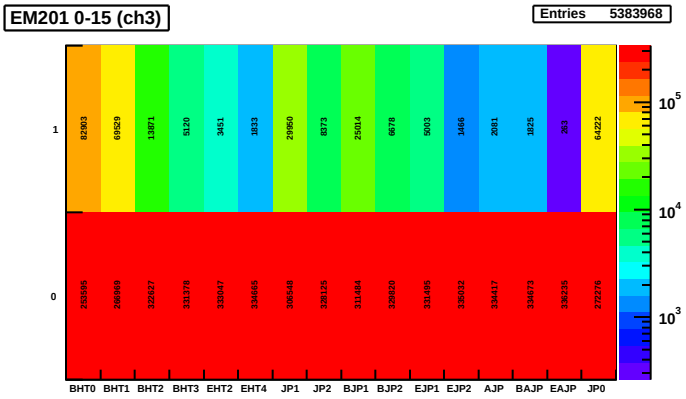

Endcap Jet Patches
Entries 2018988
JP ADC
JP ID

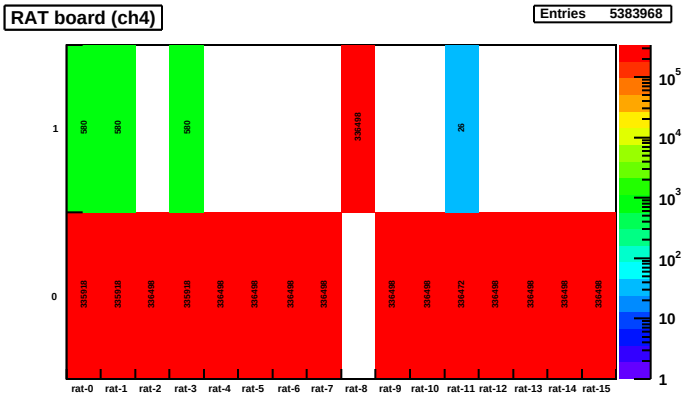

Hybrid Jet Patches
Entries 672996
JP ADC
JP ID

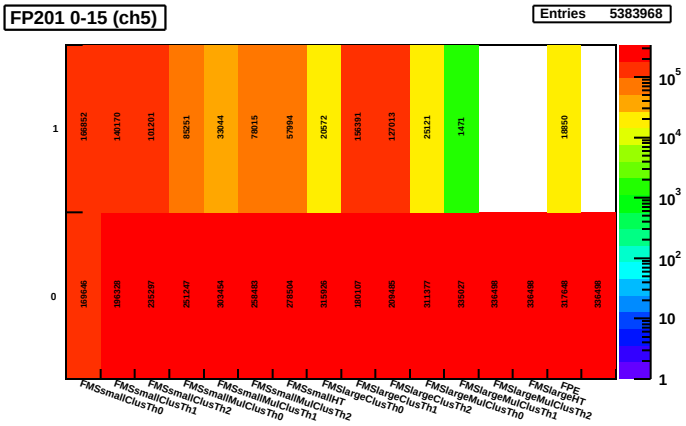

TF201 0-15 (ch0)
Entries 5383968
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

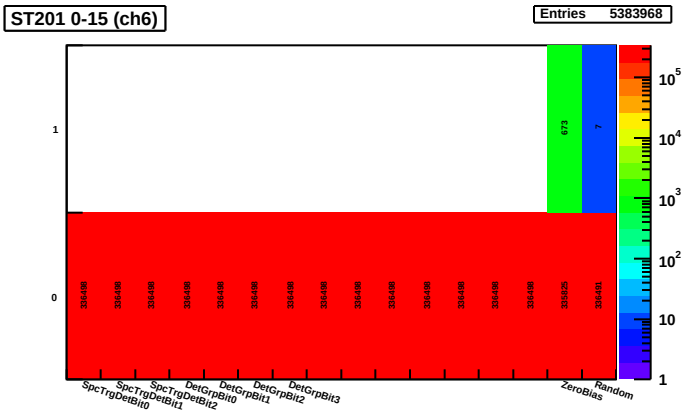

VT201 0-15 (ch1)
Entries 5383968
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

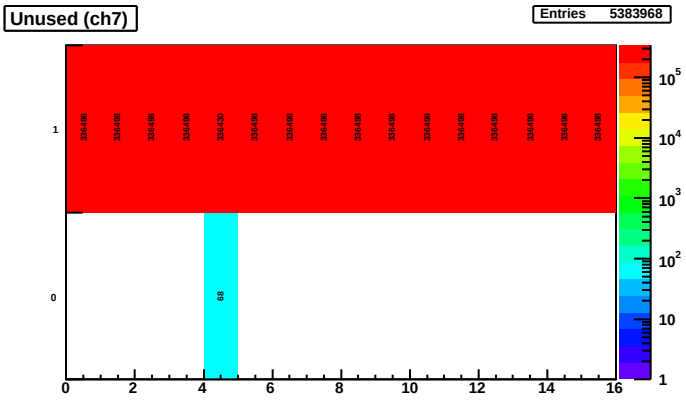

Unused (ch2)
Entries 5383968

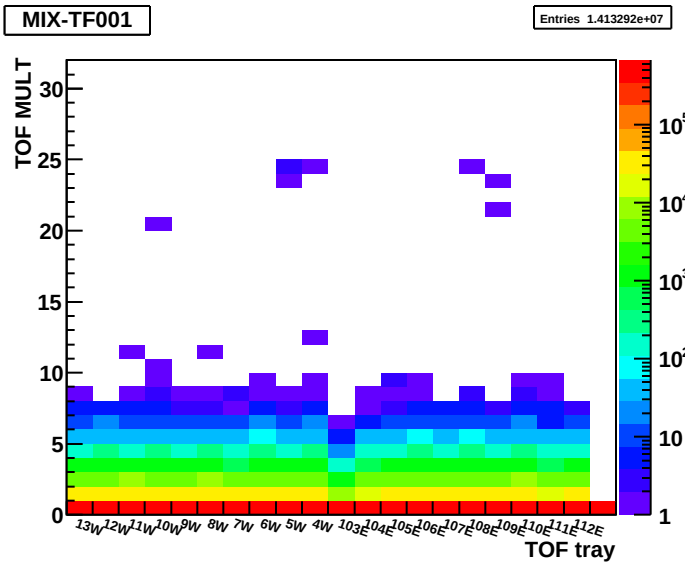

EM201 0-15 (ch3)
Entries 5383968
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

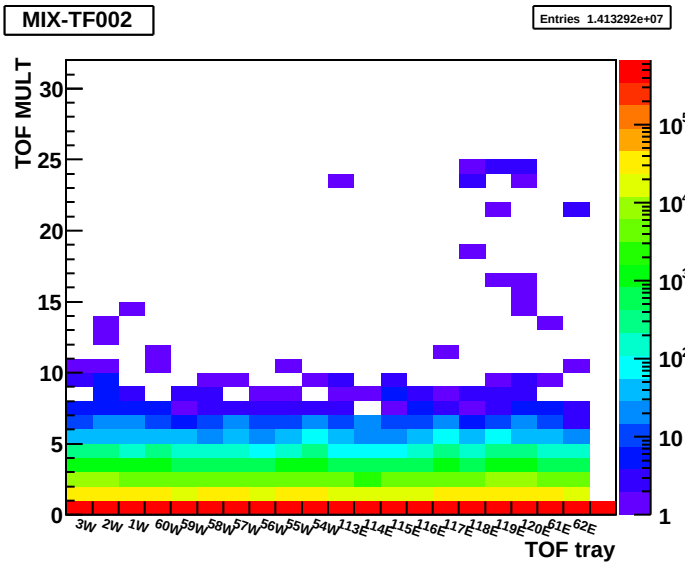

RAT board (ch4)
Entries 5383968
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

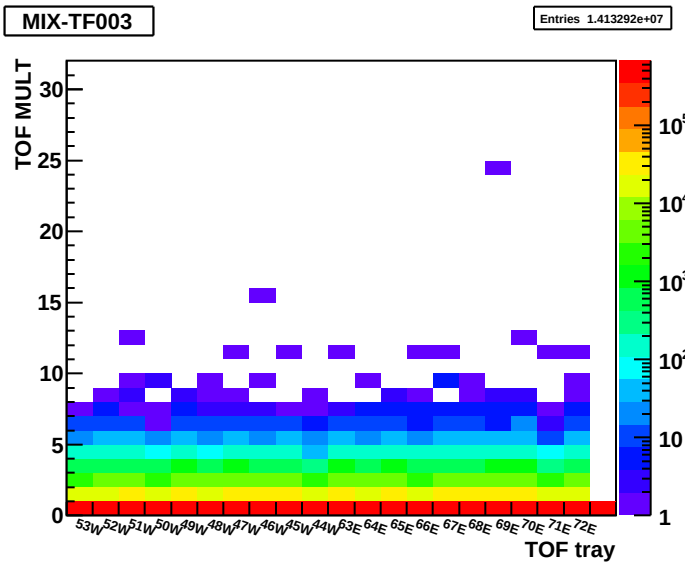

FP201 0-15 (ch5)
Entries 5383968
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

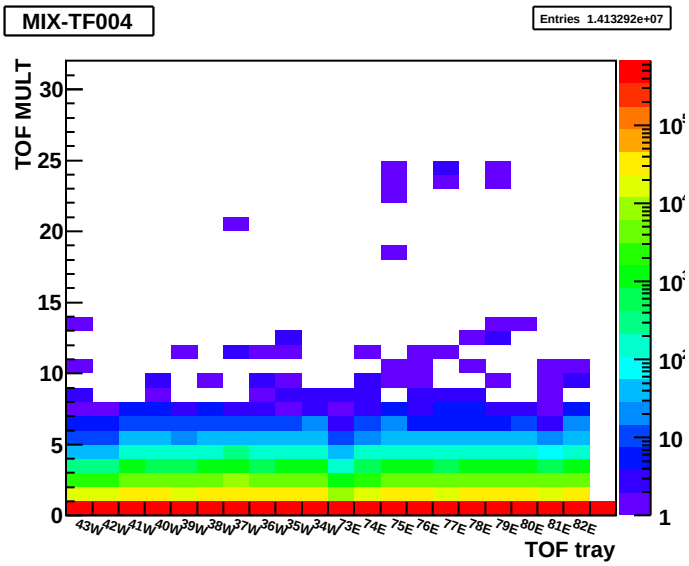

ST201 0-15 (ch6)
Entries 5383968
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

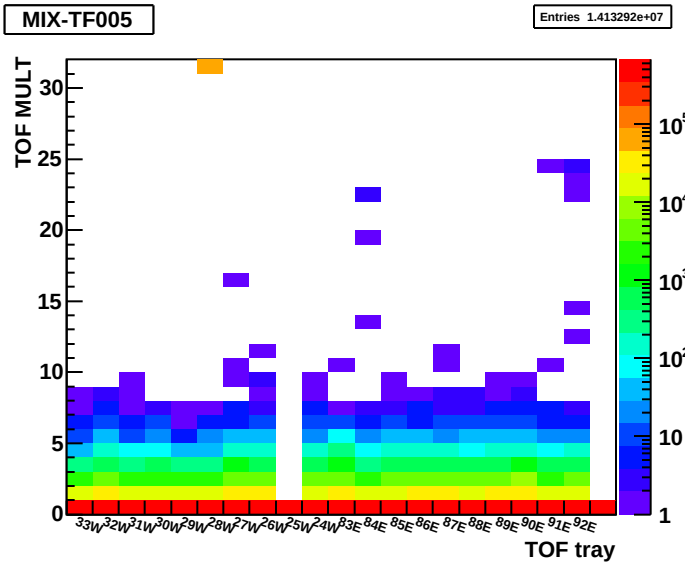

Unused (ch7)
Entries 5383968

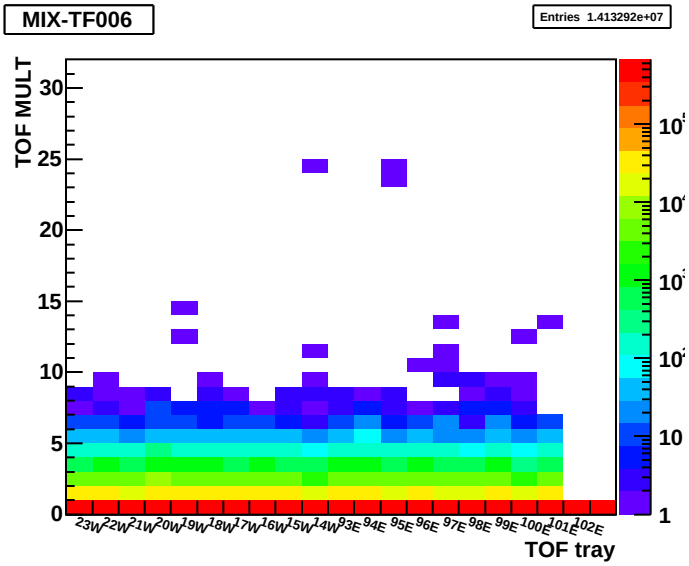

MIX-TF001
Entries 1.413292e+07
TOF MULT
TOF tray

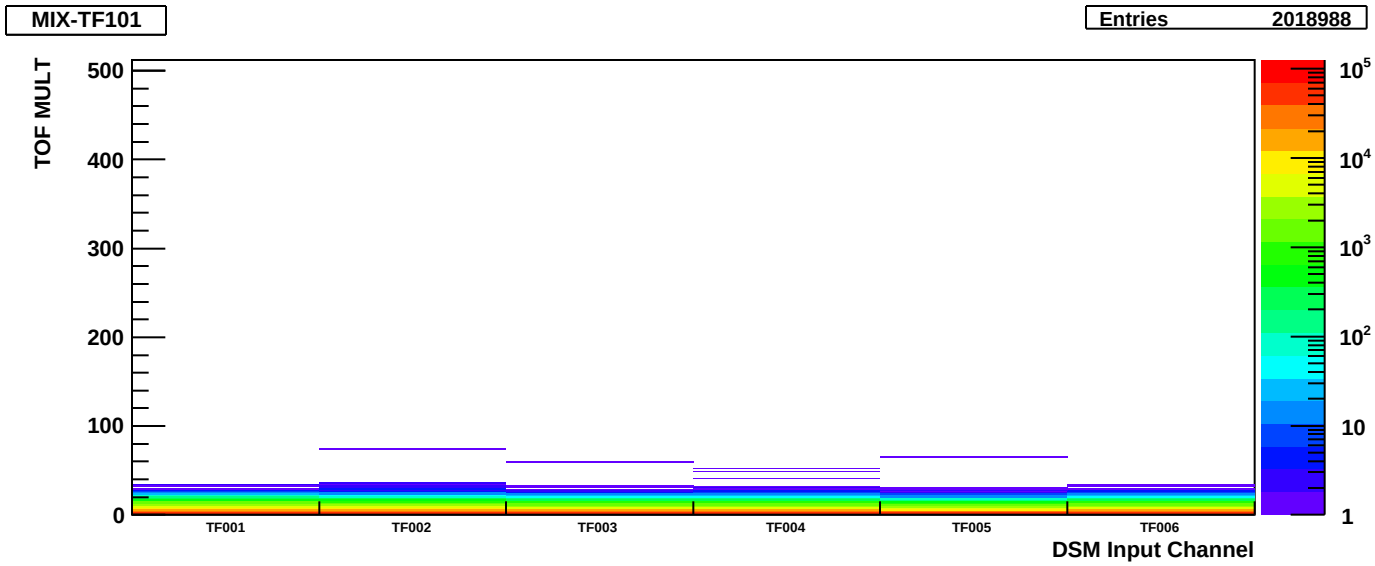

MIX-TF002
Entries 1.413292e+07
TOF MULT
TOF tray

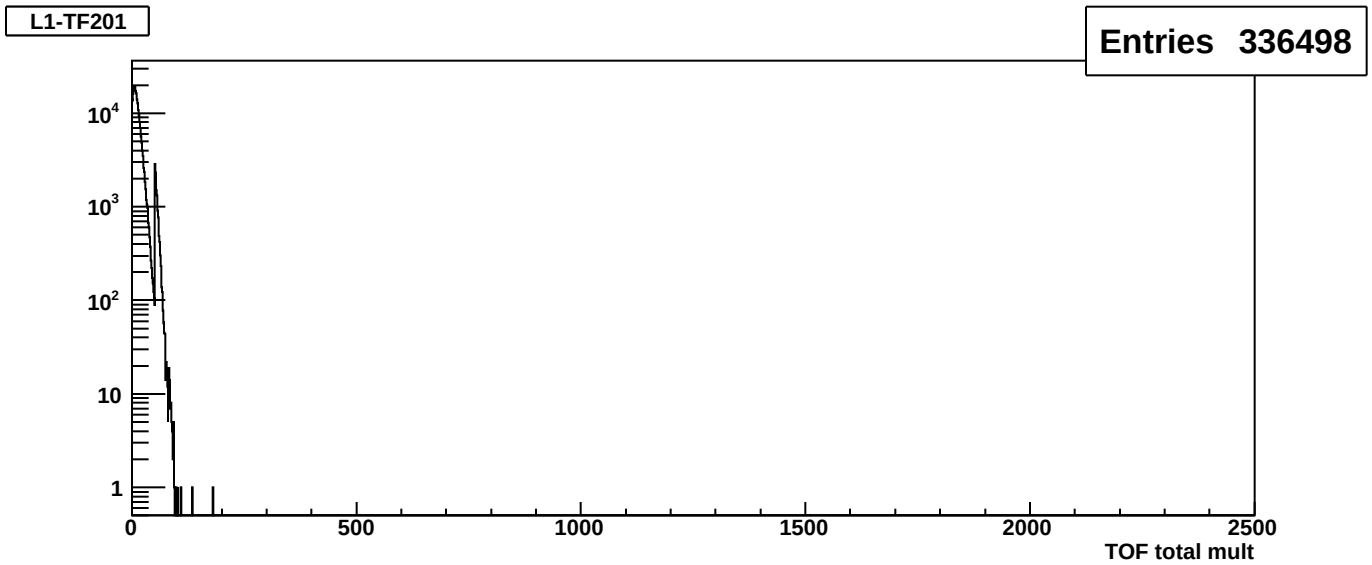

MIX-TF003
Entries 1.413292e+07
TOF MULT
TOF tray

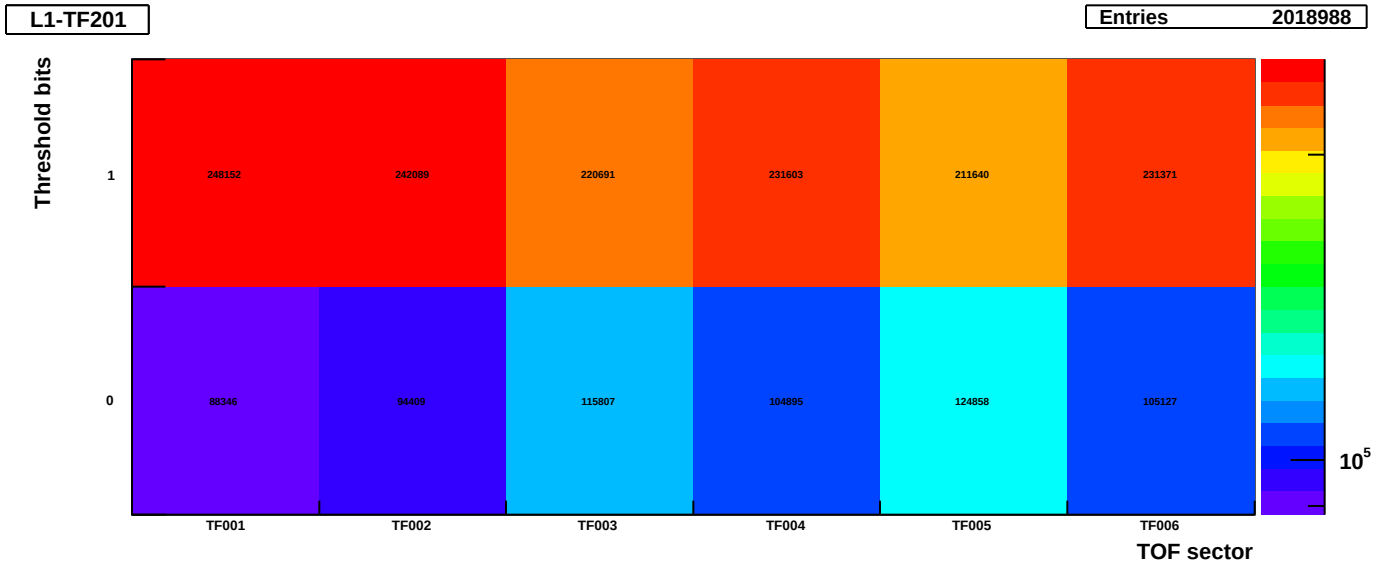

MIX-TF004
Entries 1.413292e+07
TOF MULT
TOF tray

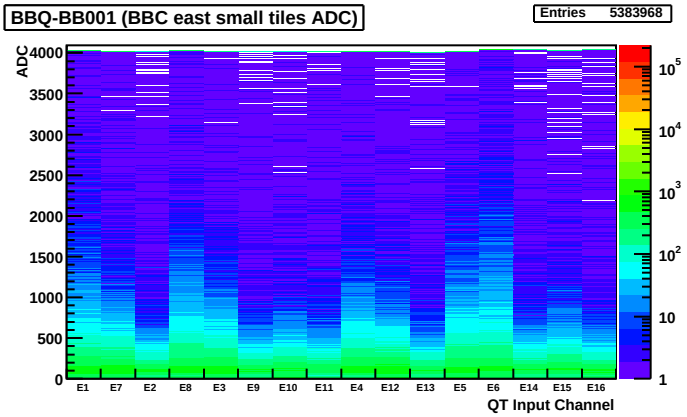

MIX-TF005
Entries 1.413292e+07
TOF MULT
TOF tray

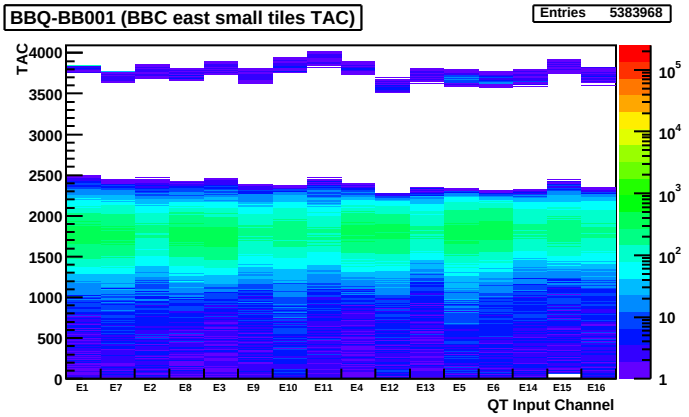

MIX-TF006
Entries 1.413292e+07
TOF MULT
TOF tray

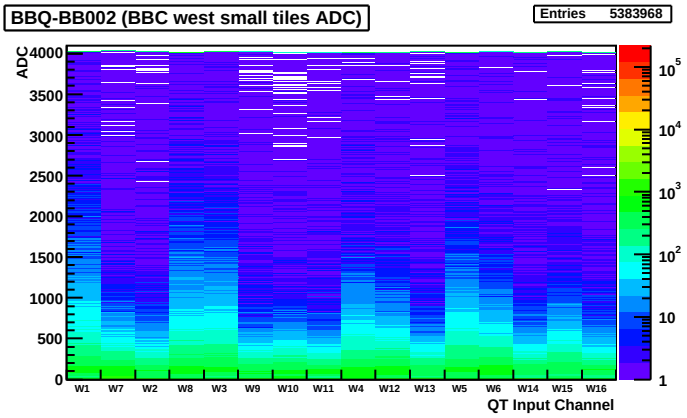

MIX-TF101
Entries 2018988
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

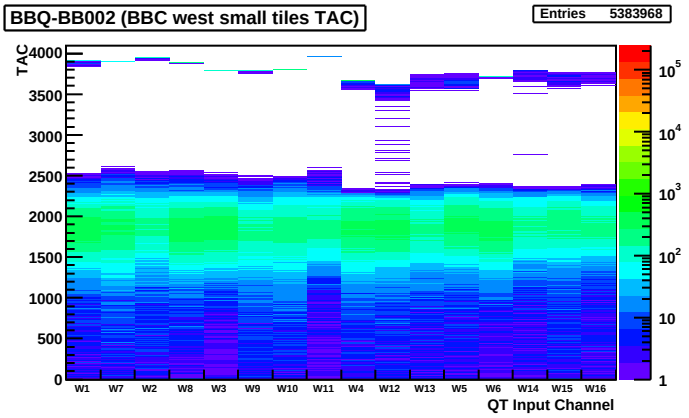

L1-TF201
Entries 336498
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

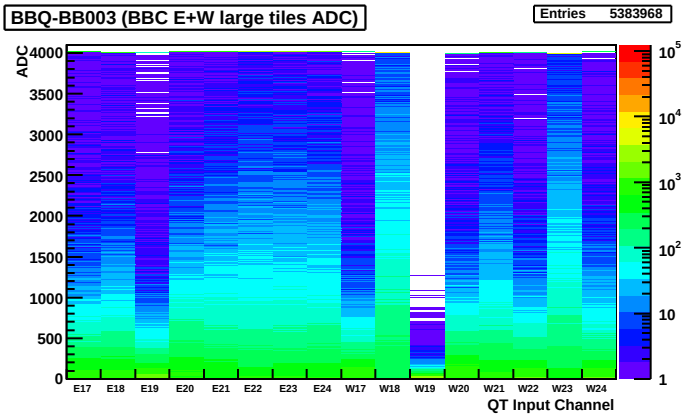

L1-TF201
Entries 2018988
Threshold bits
1
0
248152
242089
220691
231603
211640
231371
88346
94409
115807
104895
124858
105127
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

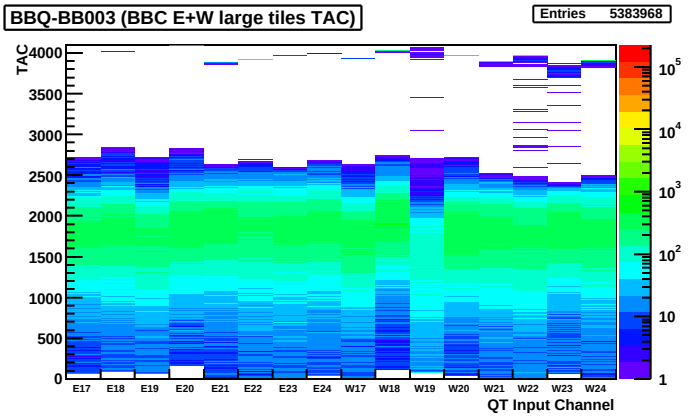

BBQ-BB001 (BBC east small tiles ADC)
Entries 5383968
ADC
QT Input Channel

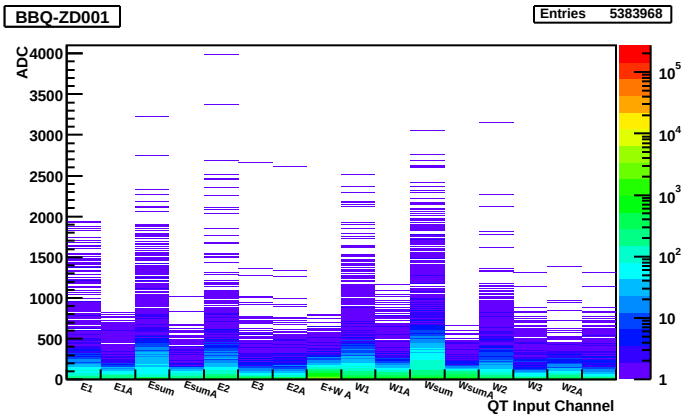

BBQ-BB001 (BBC east small tiles TAC)
Entries 5383968
TAC
QT Input Channel

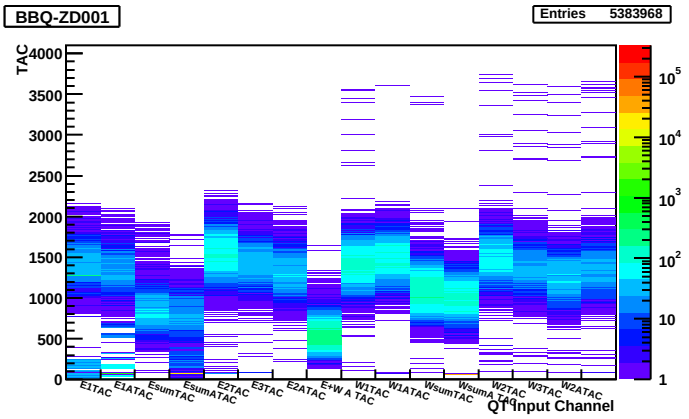

BBQ-BB002 (BBC west small tiles ADC)
Entries 5383968
ADC
QT Input Channel

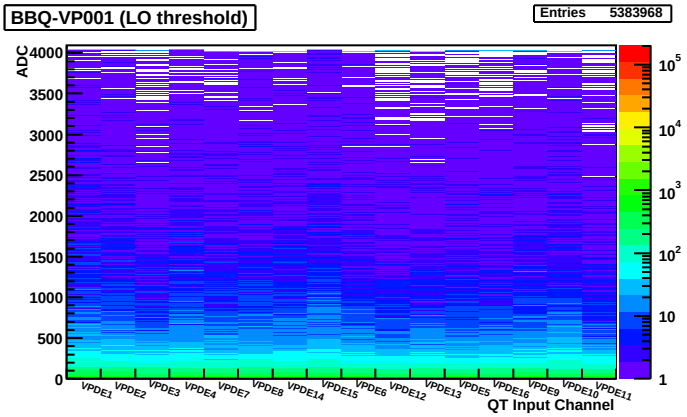

BBQ-BB002 (BBC west small tiles TAC)
Entries 5383968
TAC
QT Input Channel

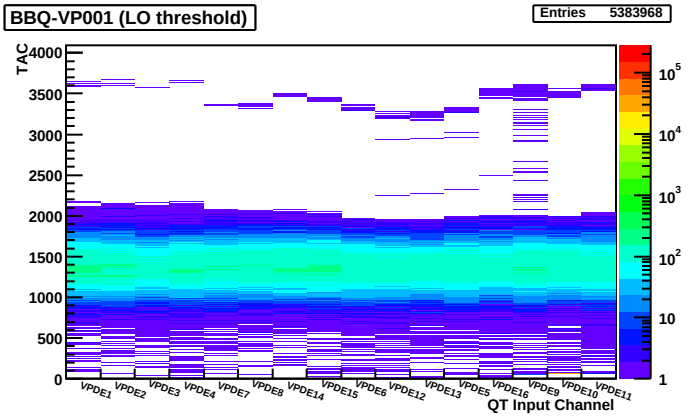

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5383968
ADC
QT Input Channel

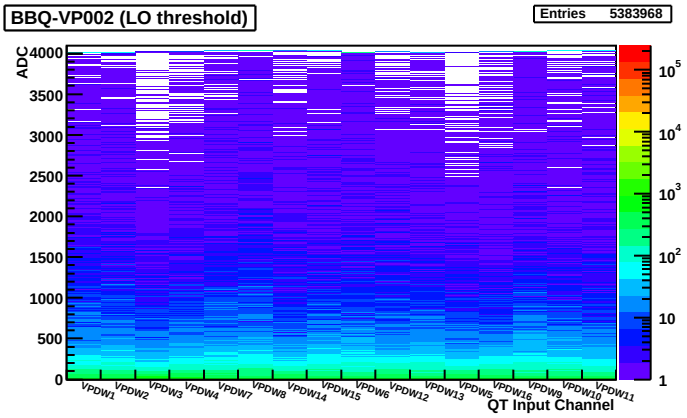

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5383968
TAC
QT Input Channel

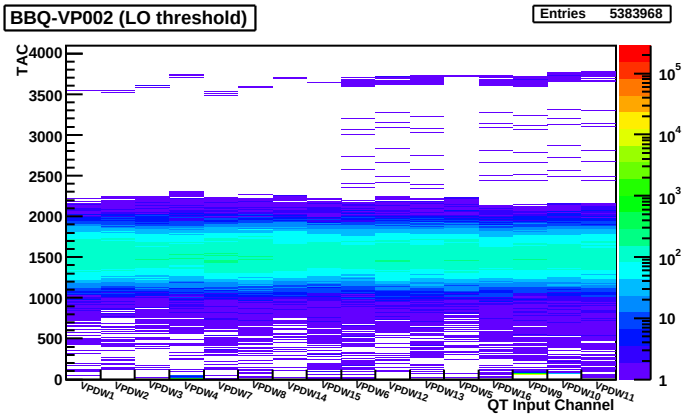

BBQ-ZD001
Entries 5383968
ADC
QT Input Channel

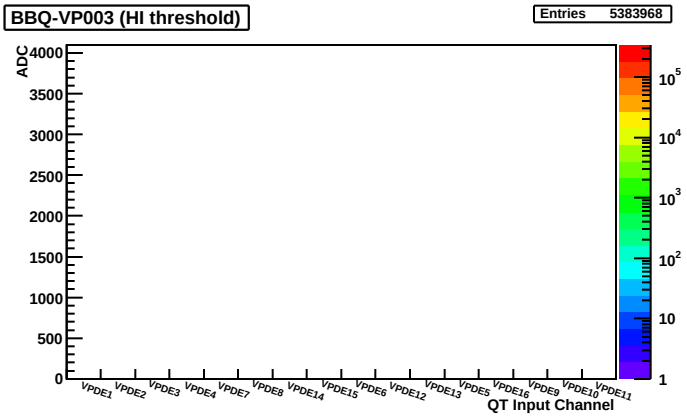

BBQ-ZD001
Entries 5383968
TAC
QT Input Channel

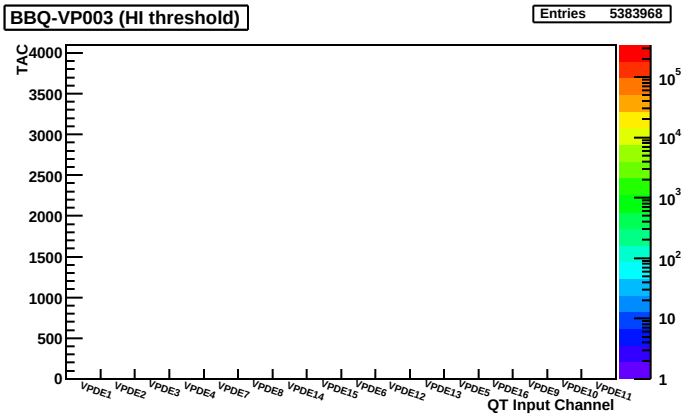

BBQ-VP001 (LO threshold)
Entries 5383968
ADC
QT Input Channel

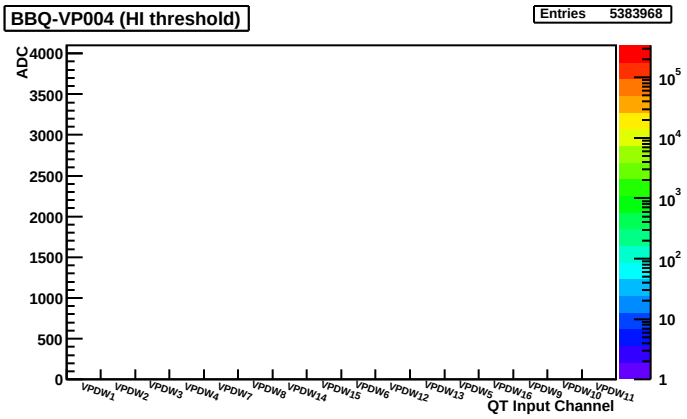

BBQ-VP001 (LO threshold)
Entries 5383968
TAC
QT Input Channel

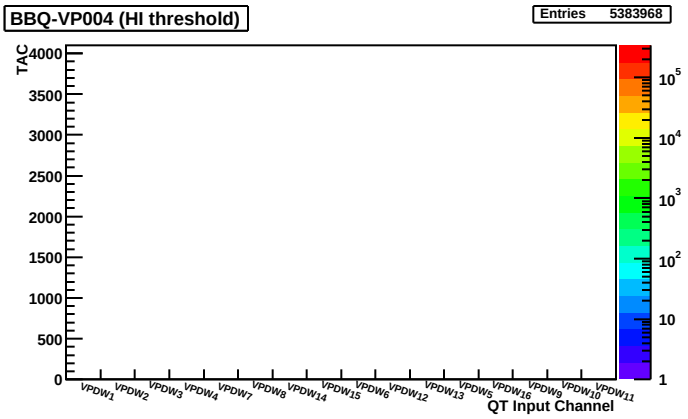

BBQ-VP002 (LO threshold)
Entries 5383968
ADC
QT Input Channel

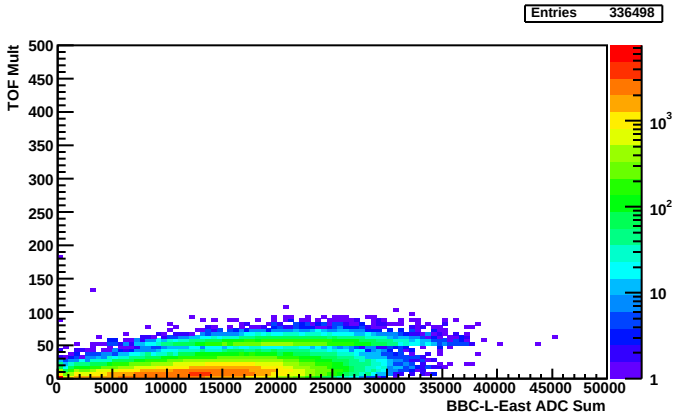

BBQ-VP002 (LO threshold)
Entries 5383968
TAC
QT Input Channel

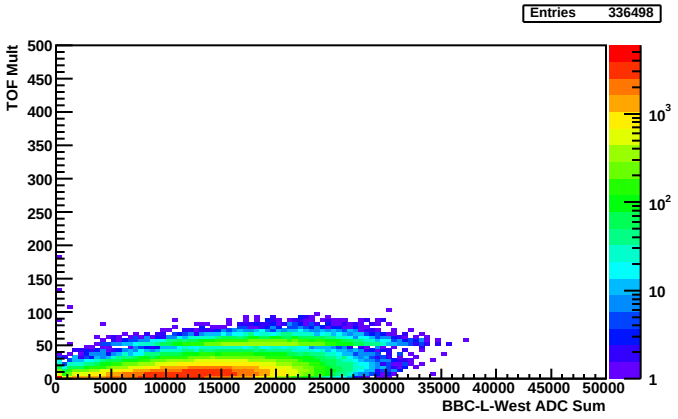

BBQ-VP003 (HI threshold)
Entries 5383968
ADC
QT Input Channel

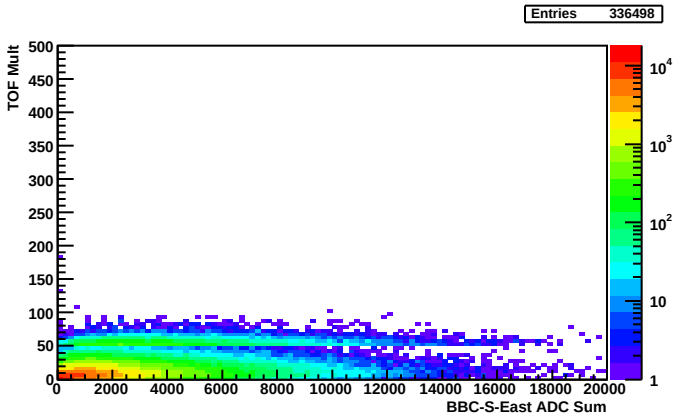

BBQ-VP003 (HI threshold)
Entries 5383968
TAC
QT Input Channel

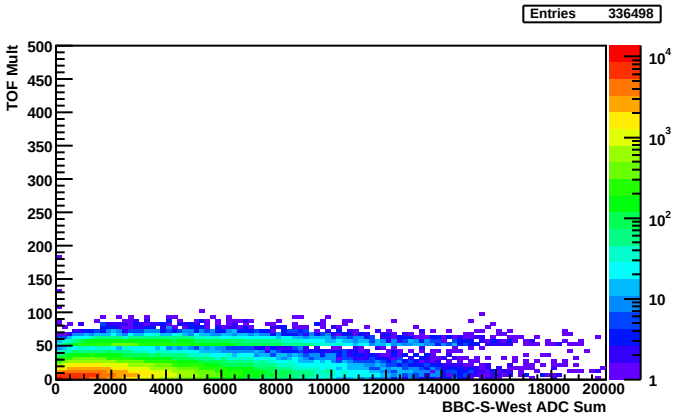

BBQ-VP004 (HI threshold)
Entries 5383968
ADC
QT Input Channel

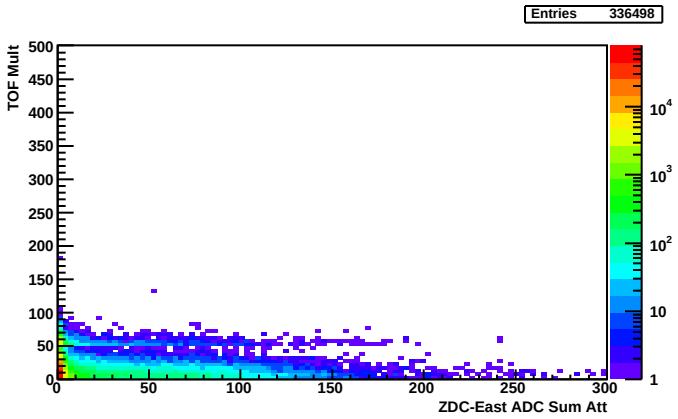

BBQ-VP004 (HI threshold)
Entries 5383968
TAC
QT Input Channel

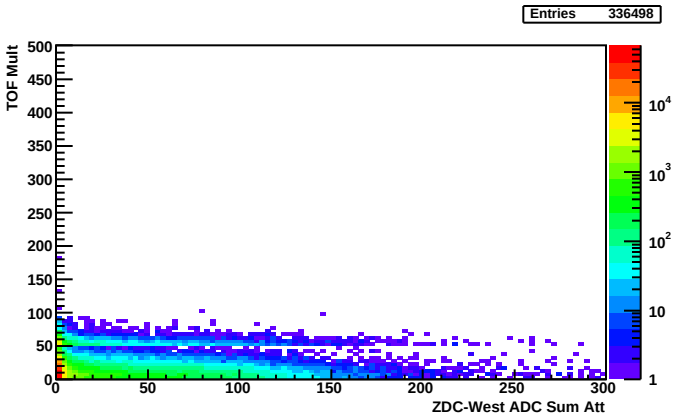

Entries 336498
TOF Mult
BBC-L-East ADC Sum

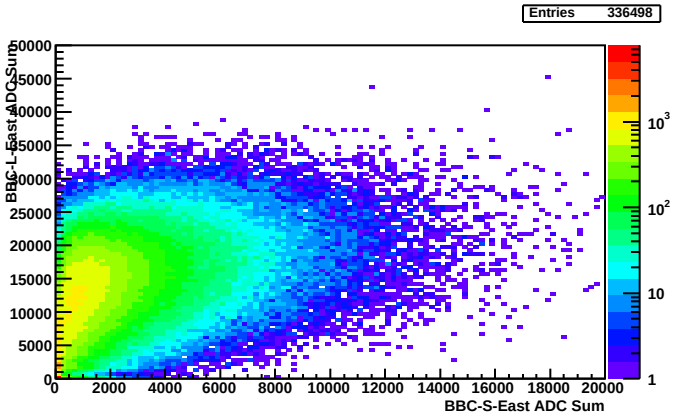

Entries 336498
TOF Mult
BBC-L-West ADC Sum

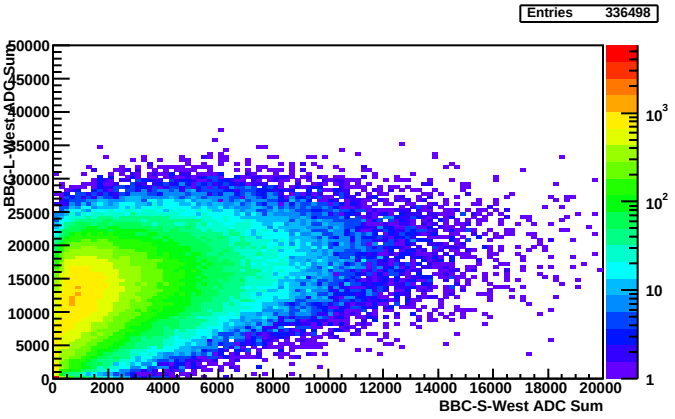

Entries 336498
TOF Mult
BBC-S-East ADC Sum

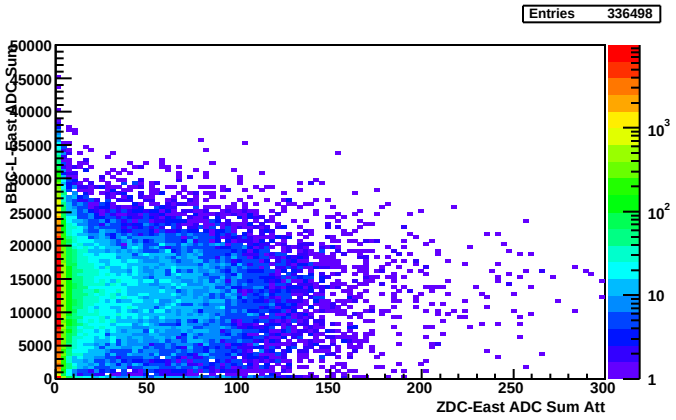

Entries 336498
TOF Mult
BBC-S-West ADC Sum

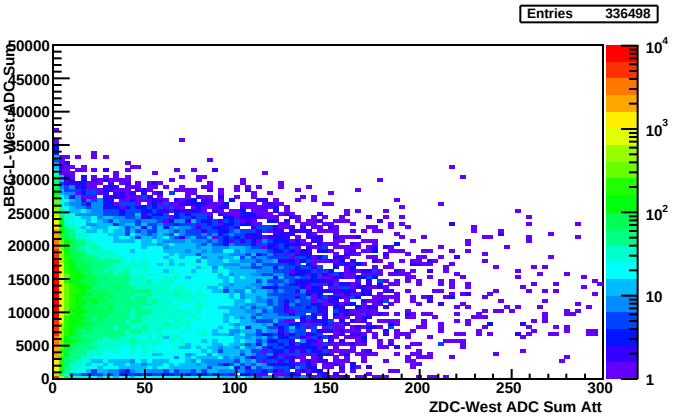

Entries 336498
TOF Mult
ZDC-East ADC Sum Att

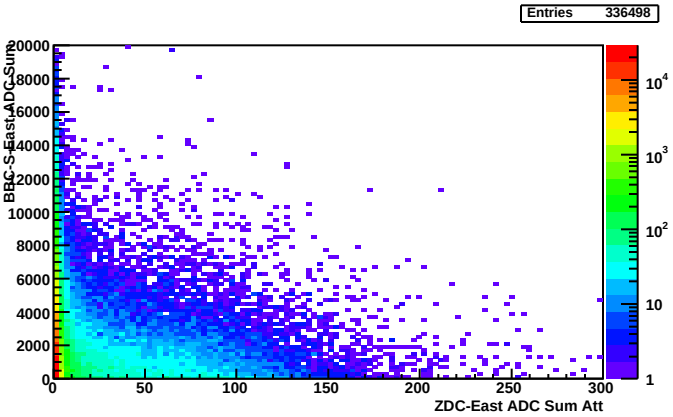

Entries 336498
TOF Mult
ZDC-West ADC Sum Att

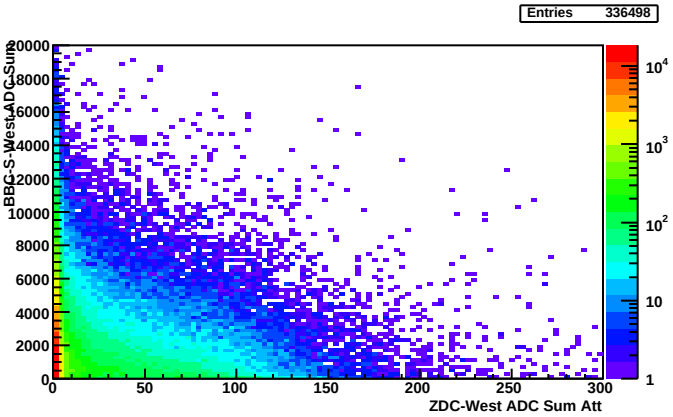

Entries 336498
BBC-L-East ADC Sum
BBC-S-East ADC Sum

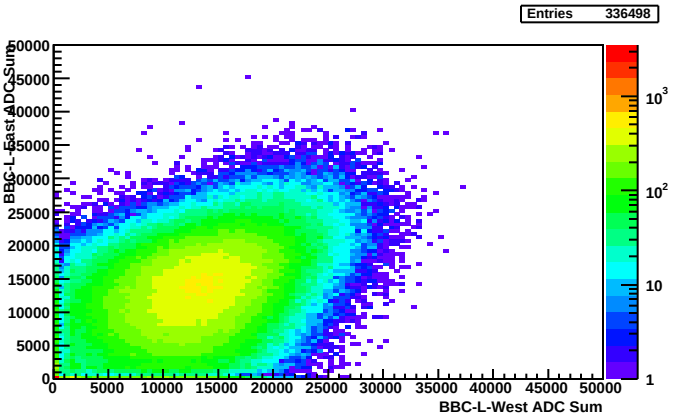

Entries 336498
BBC-L-West ADC Sum
BBC-S-West ADC Sum

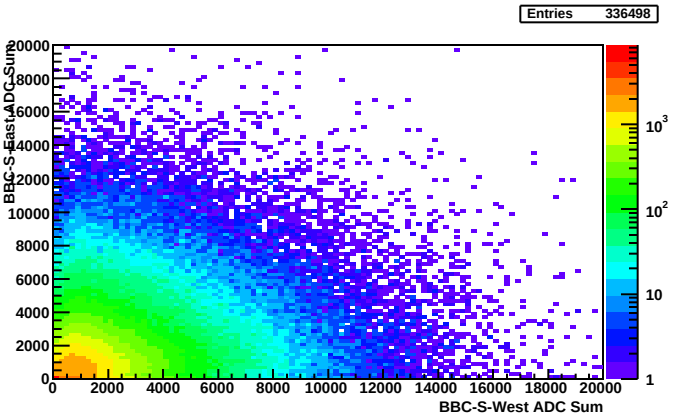

Entries 336498
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

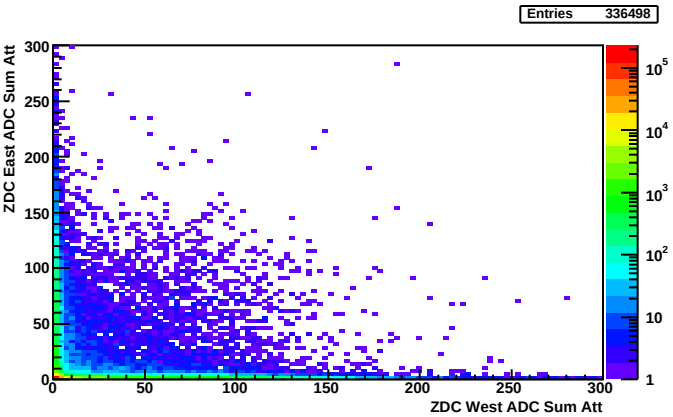

Entries 336498
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

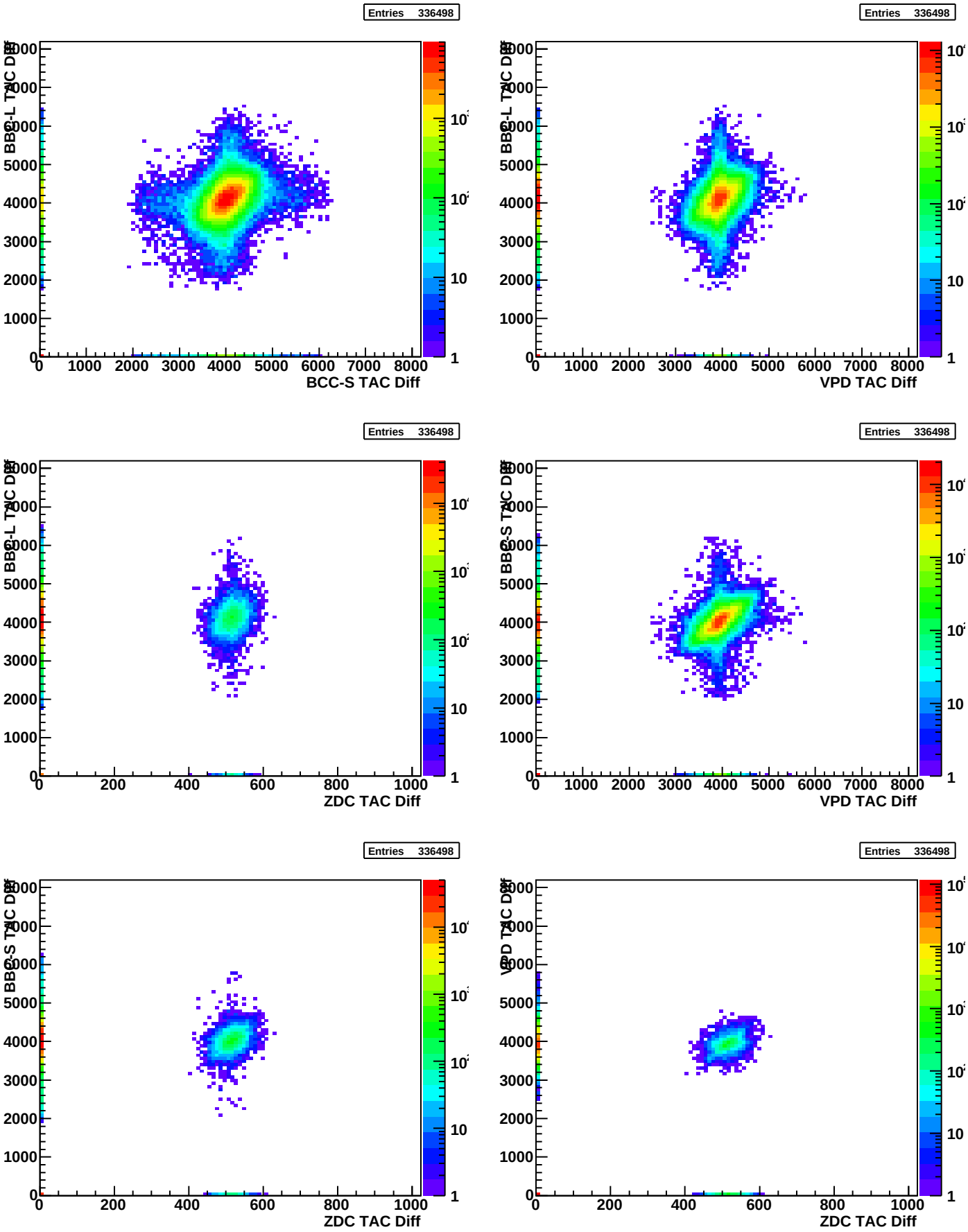

Entries 336498
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

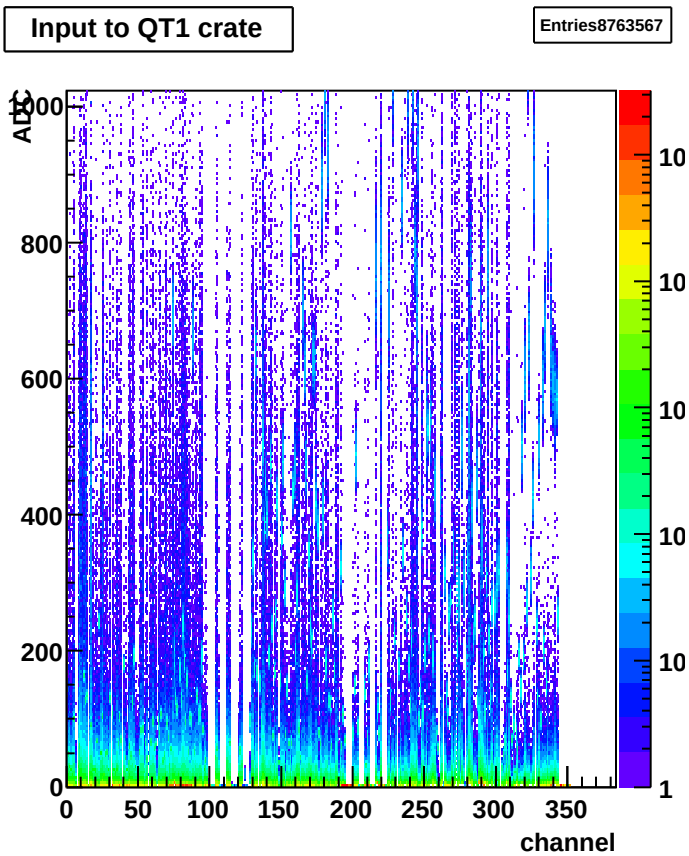

Entries 336498
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

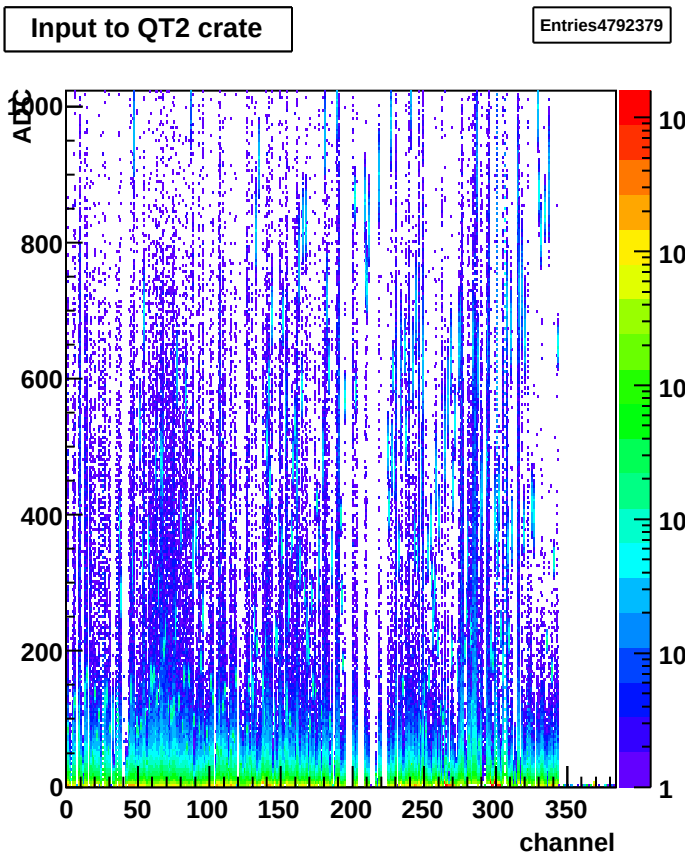

Entries 336498
BBC-L-East ADC Sum
BBC-L-West ADC Sum

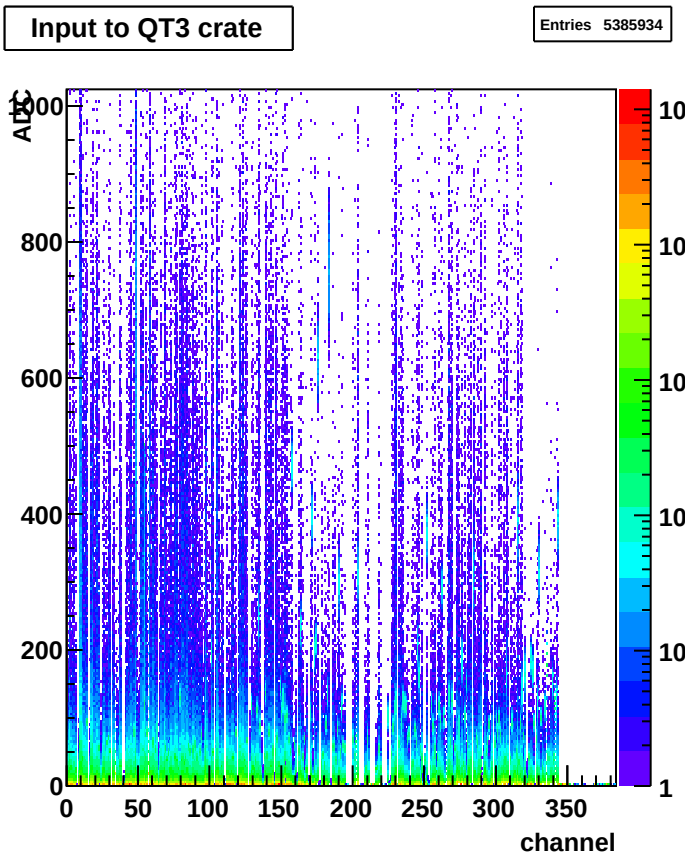

Entries 336498
BBC-S-East ADC Sum
BBC-S-West ADC Sum

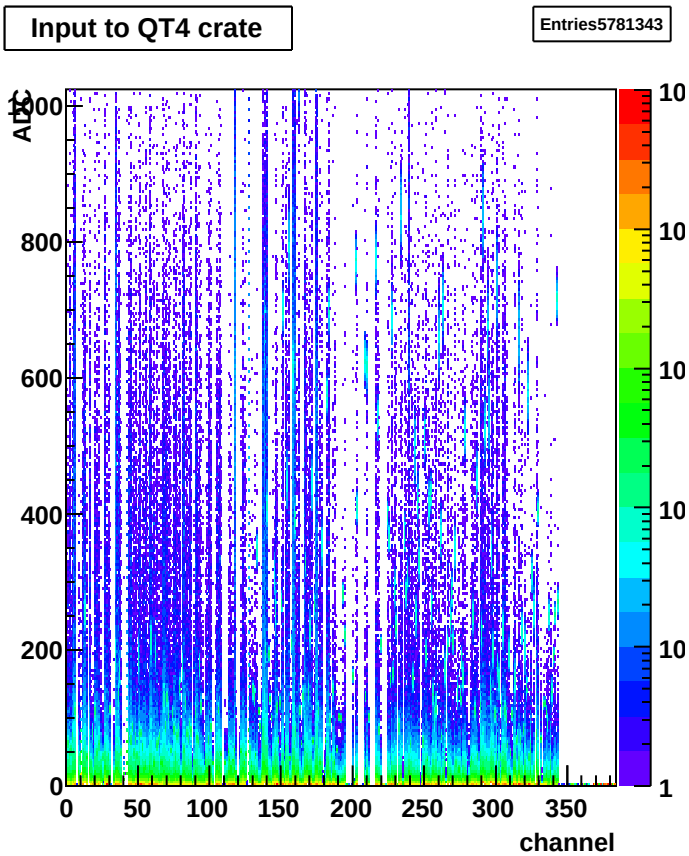

Entries 336498
ZDC East ADC Sum Att
ZDC West ADC Sum Att

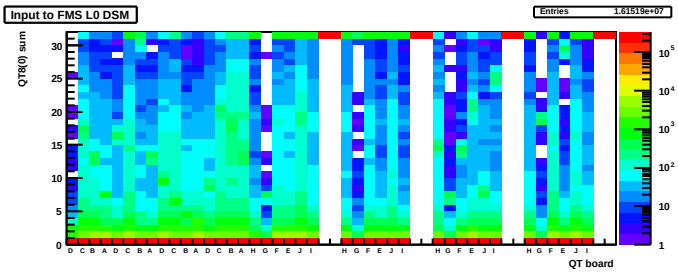

Entries 336498
BBC-L TAC Diff
BCC-S TAC Diff
Entries 336498
BBC-L TAC Diff
VPD TAC Diff
Entries 336498
BBC-L TAC Diff
ZDC TAC Diff
Entries 336498
BBC-S TAC Diff
VPD TAC Diff
Entries 336498
BBC-S TAC Diff
ZDC TAC Diff
Entries 336498
VPD TAC Diff
ZDC TAC Diff

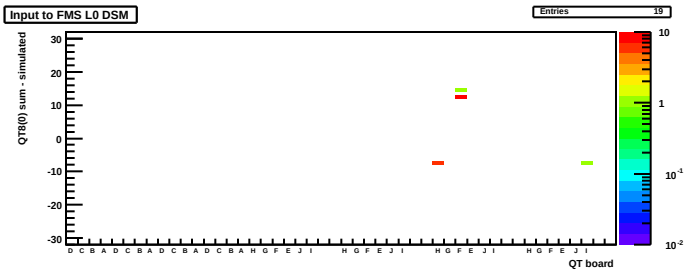

Input to QT1 crate
Entries8763567
ADC
channel

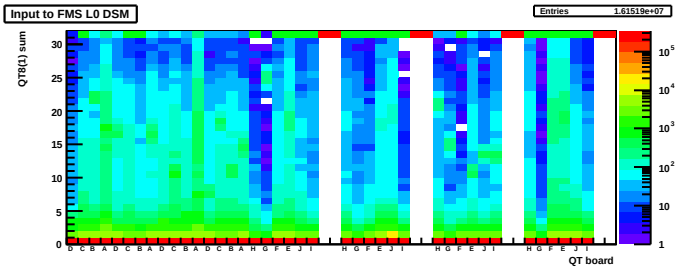

Input to QT2 crate
Entries4792379
ADC
channel

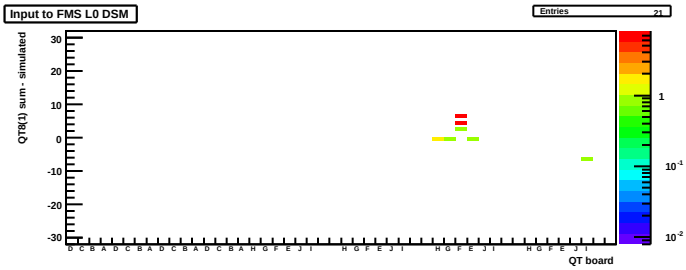

Input to QT3 crate
Entries 5385934
ADC
channel

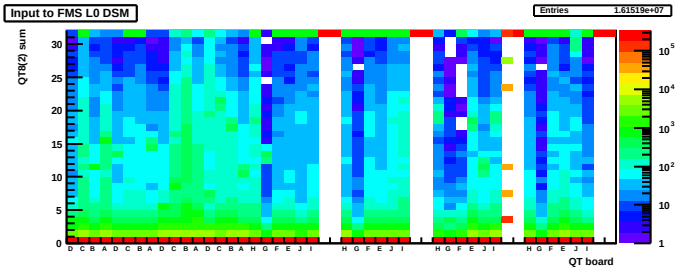

Input to QT4 crate
Entries5781343
ADC
channel

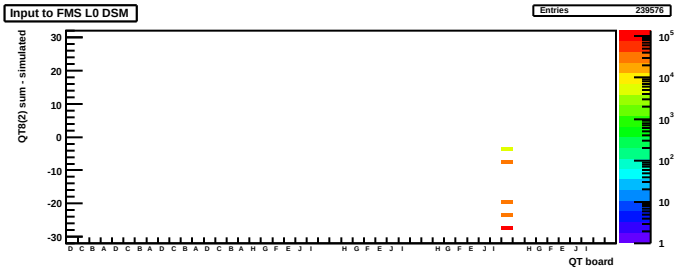

Input to FMS L0 DSM
Entries 1.61519e+07
QT8(0) sum
QT board

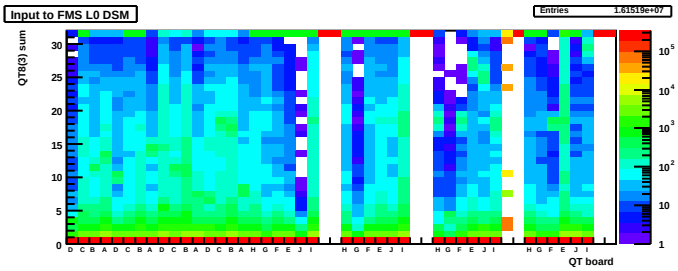

Input to FMS L0 DSM
Entries 19
QT8(0) sum - simulated
QT board

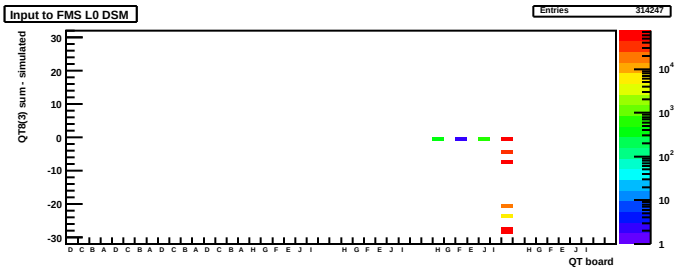

Input to FMS L0 DSM
Entries 1.61519e+07
QT8(1) sum
QT board

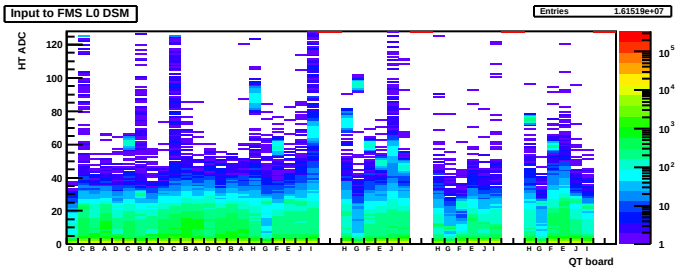

Input to FMS L0 DSM
Entries 21
QT8(1) sum - simulated
QT board

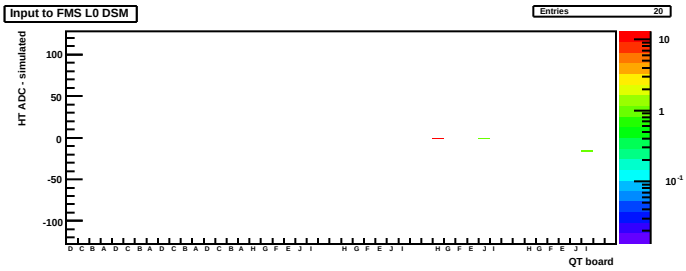

Input to FMS L0 DSM
Entries 1.61519e+07
QT8(2) sum
QT board

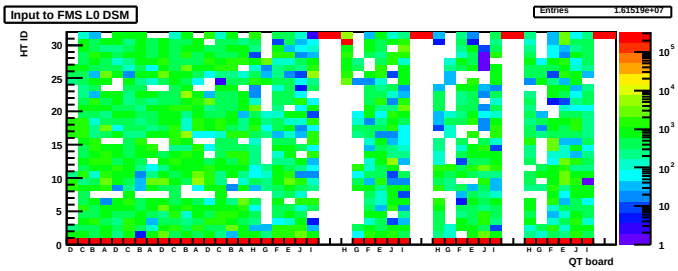

Input to FMS L0 DSM
Entries 239576
QT8(2) sum - simulated
QT board

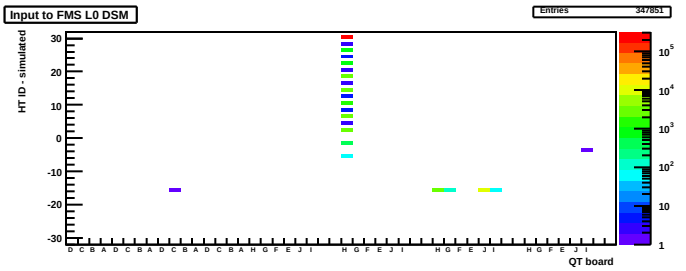

Input to FMS L0 DSM
Entries 1.61519e+07
QT8(3) sum
QT board

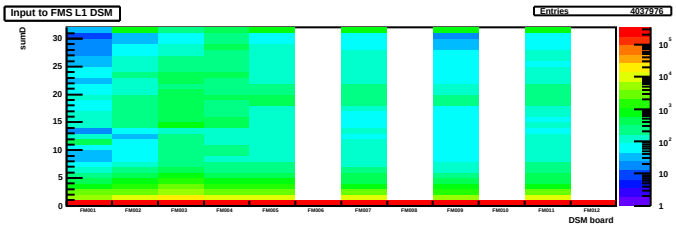

Input to FMS L0 DSM
Entries 314247
QT8(3) sum - simulated
QT board

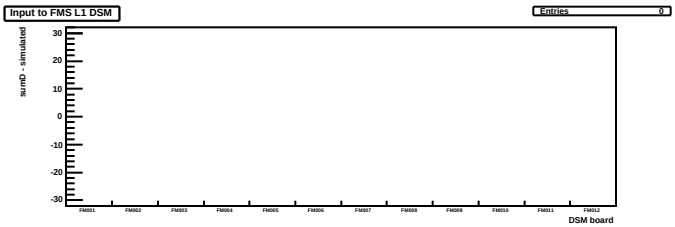

Input to FMS L0 DSM
Entries 1.61519e+07
HT ADC
QT board

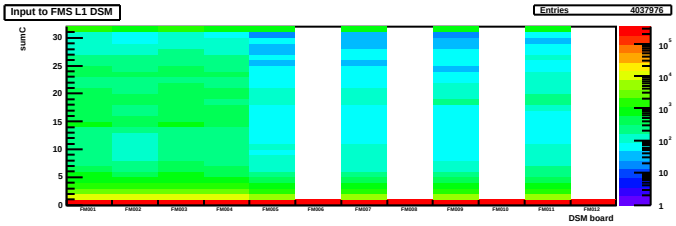

Input to FMS L0 DSM
Entries 20
HT ADC - simulated
QT board

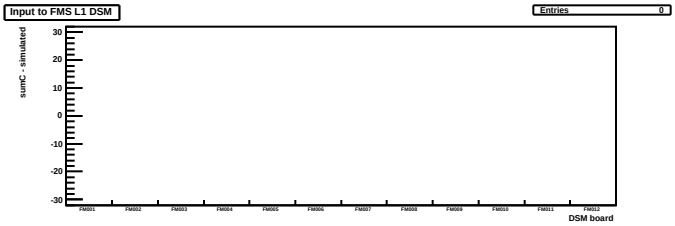

Input to FMS L0 DSM
Entries 1.61519e+07
HT ID
QT board

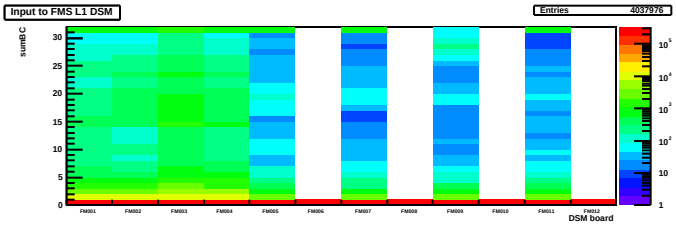

Input to FMS L0 DSM
Entries 347851
HT ID - simulated
QT board

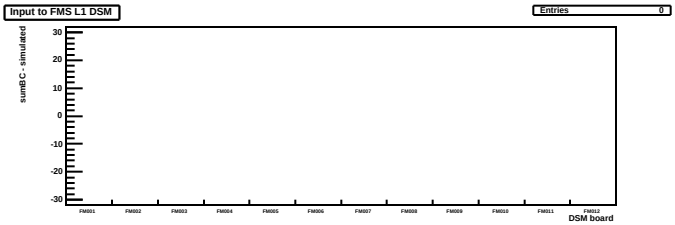

Input to FMS L1 DSM
Entries 4037976
sumD
DSM board

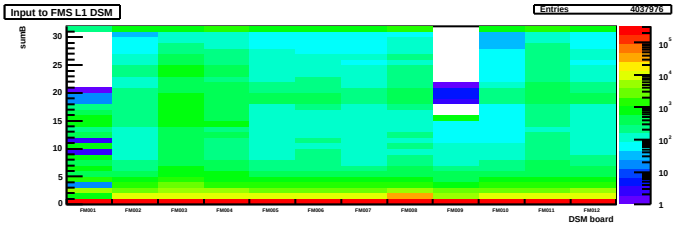

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

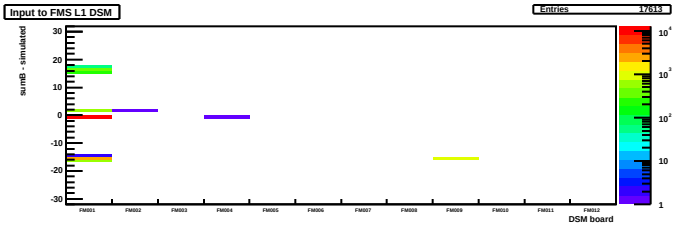

Input to FMS L1 DSM
Entries 4037976
sumC
DSM board

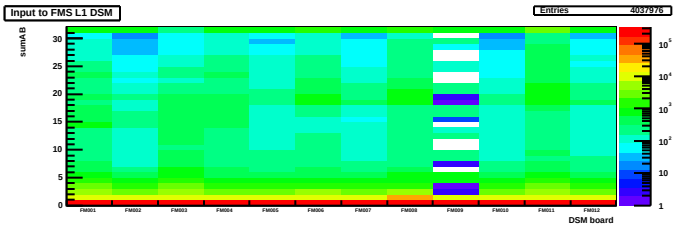

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

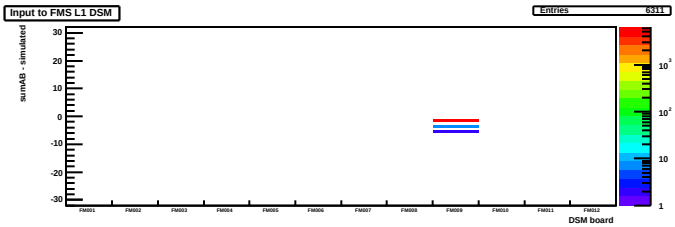

Input to FMS L1 DSM
Entries 4037976
sumBC
DSM board

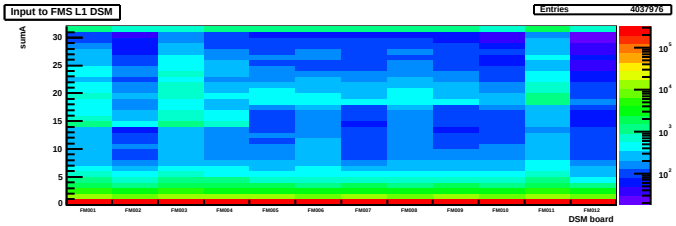

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

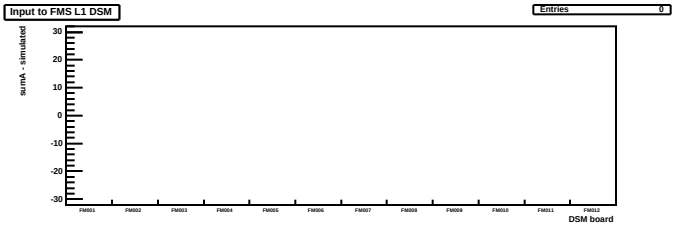

Input to FMS L1 DSM
Entries 4037976
sumB
DSM board

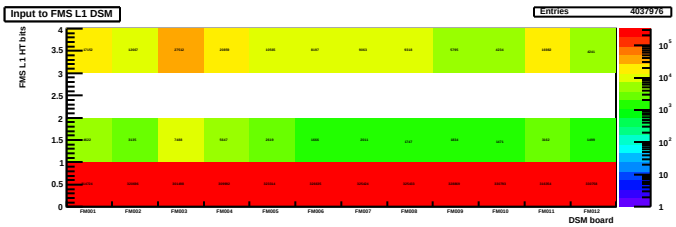

Input to FMS L1 DSM
Entries 17613
sumB - simulated
DSM board

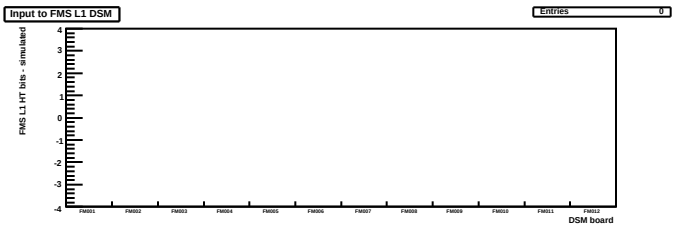

Input to FMS L1 DSM
Entries 4037976
sumAB
DSM board

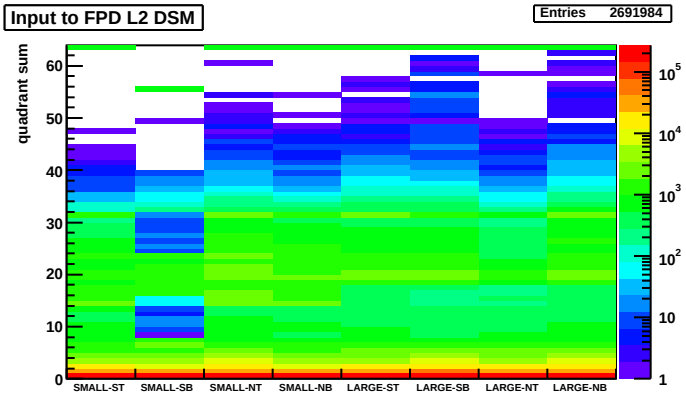

Input to FMS L1 DSM
Entries 6311
sumAB - simulated
DSM board

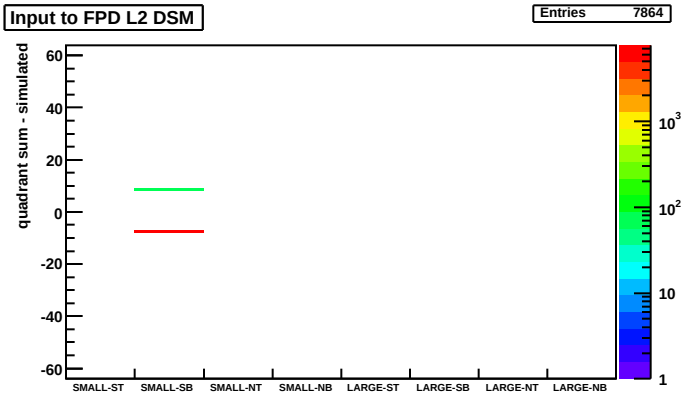

Input to FMS L1 DSM
Entries 4037976
sumA
DSM board

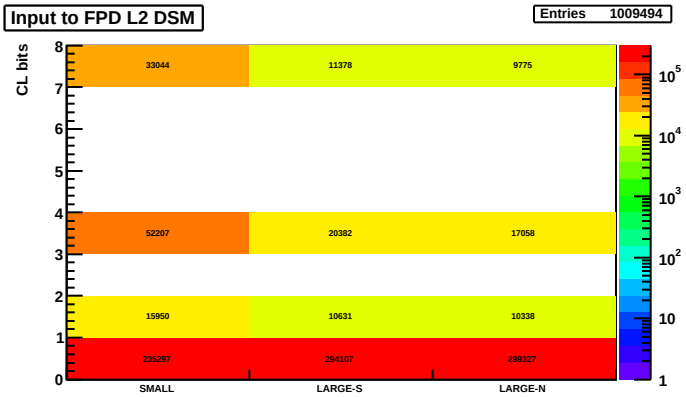

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

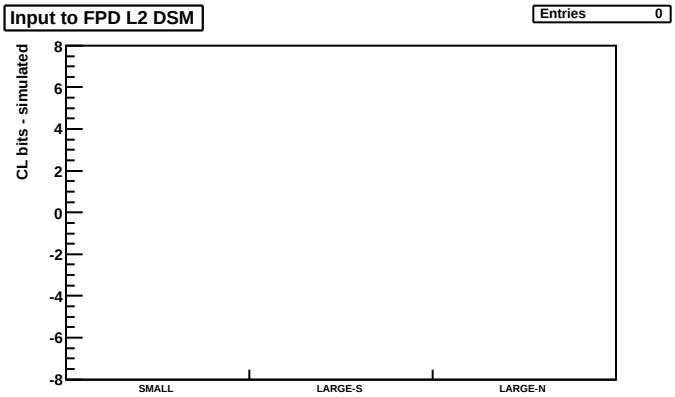

Input to FMS L1 DSM
Entries 4037976
FMS L1 HT bits
DSM board

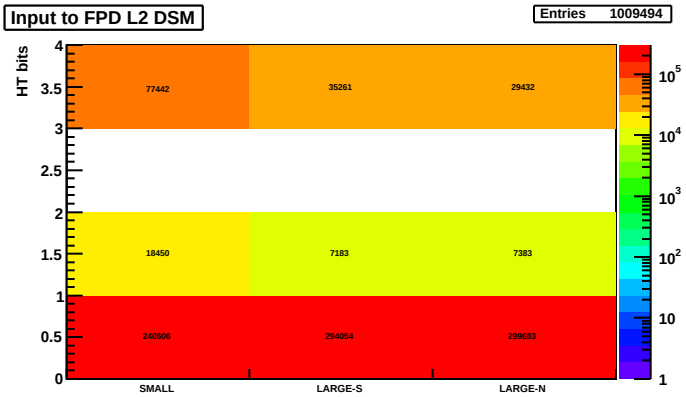

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

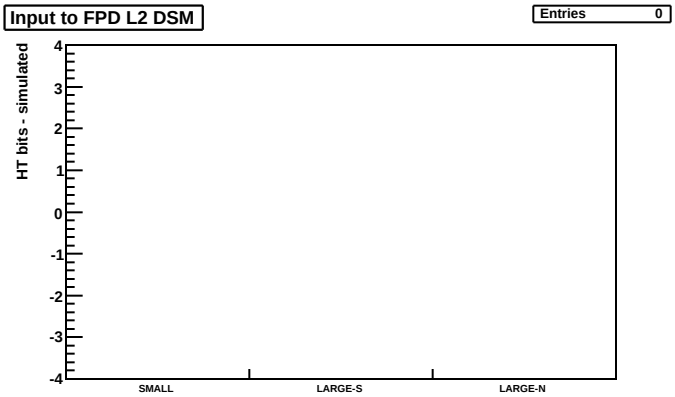

Input to FPD L2 DSM
Entries 2691984
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

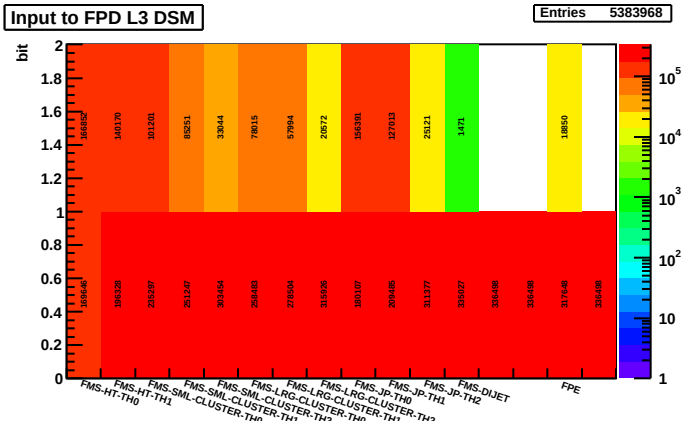

Input to FPD L2 DSM
Entries 7864
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

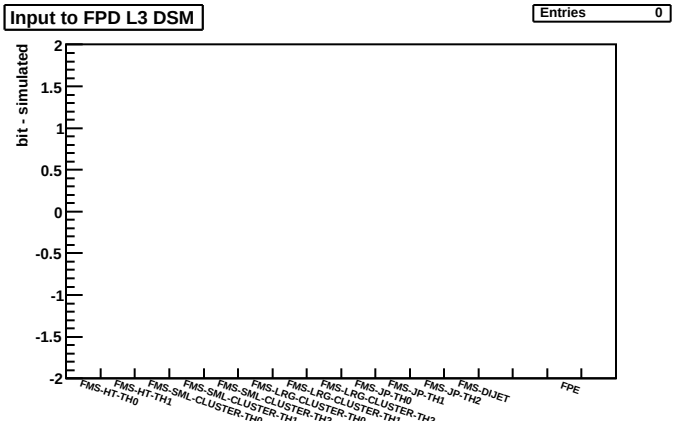

Input to FPD L2 DSM
Entries 1009494
CL bits
33044
11378
9775
52207
20382
17058
15950
10631
10338
235297
294107
299327
SMALL
LARGE-S
LARGE-N

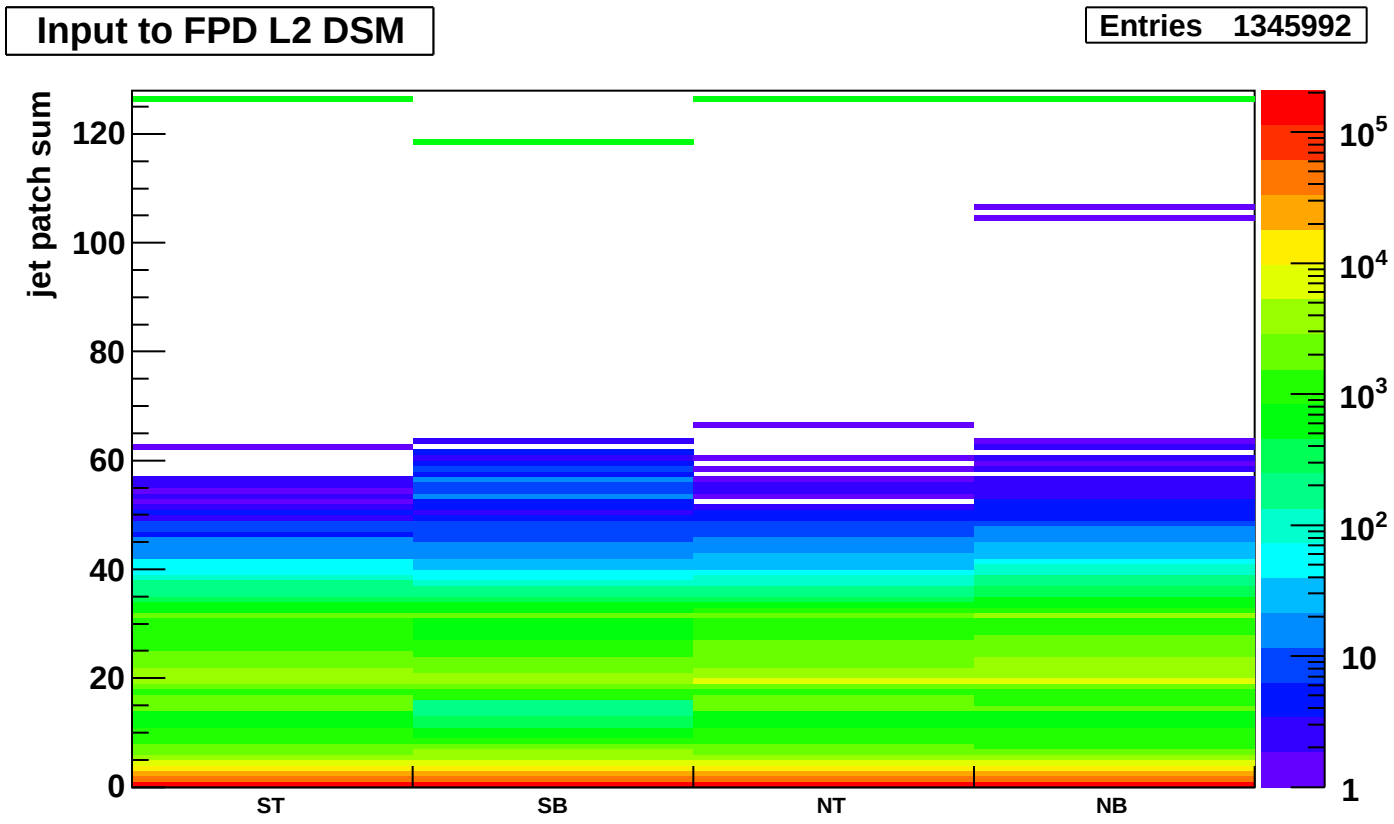

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

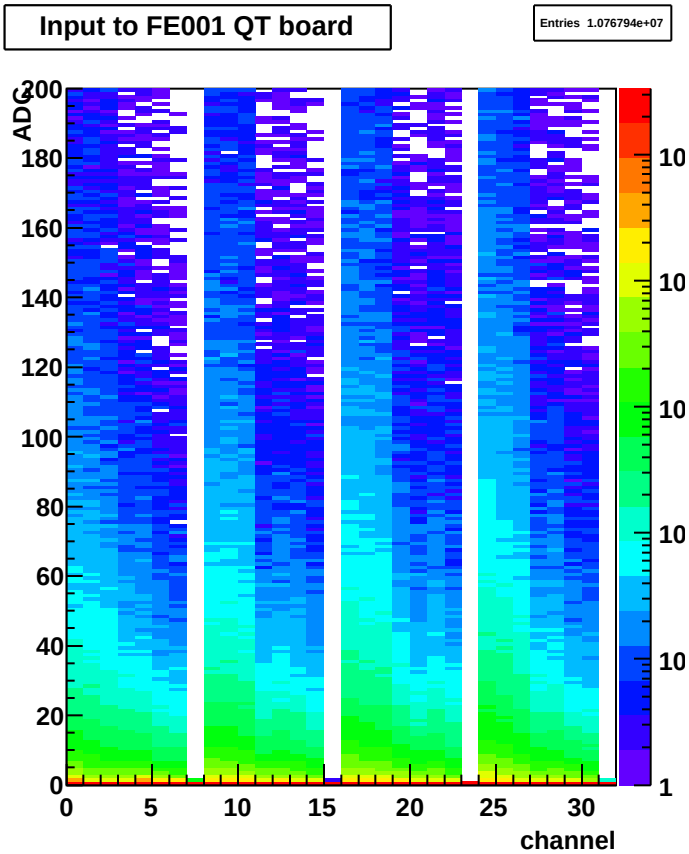

Input to FPD L2 DSM
Entries 1009494
HT bits
77442
35261
29432
18450
7183
7383
240606
294054
299683
SMALL
LARGE-S
LARGE-N

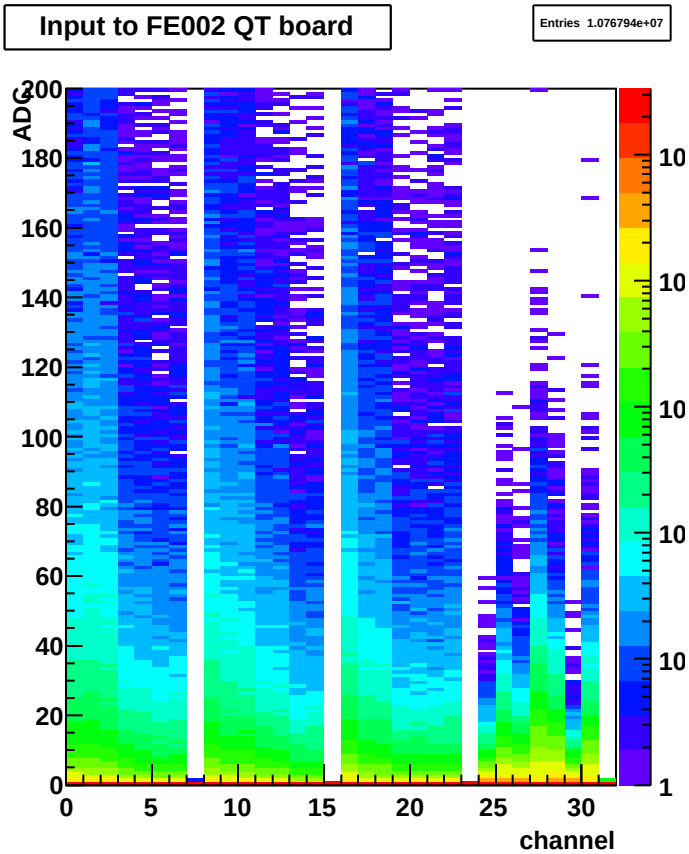

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

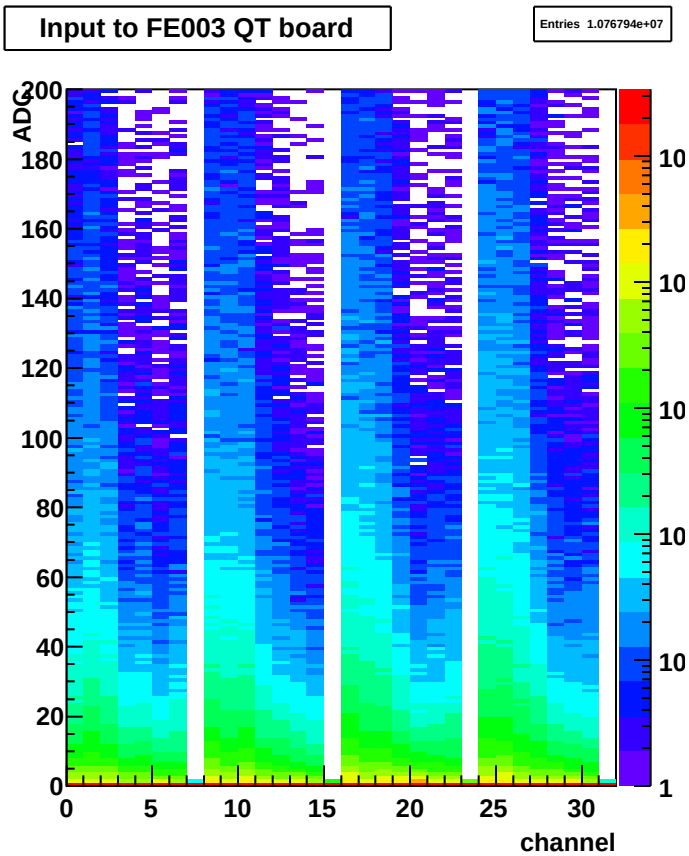

Input to FPD L3 DSM
Entries 5383968
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

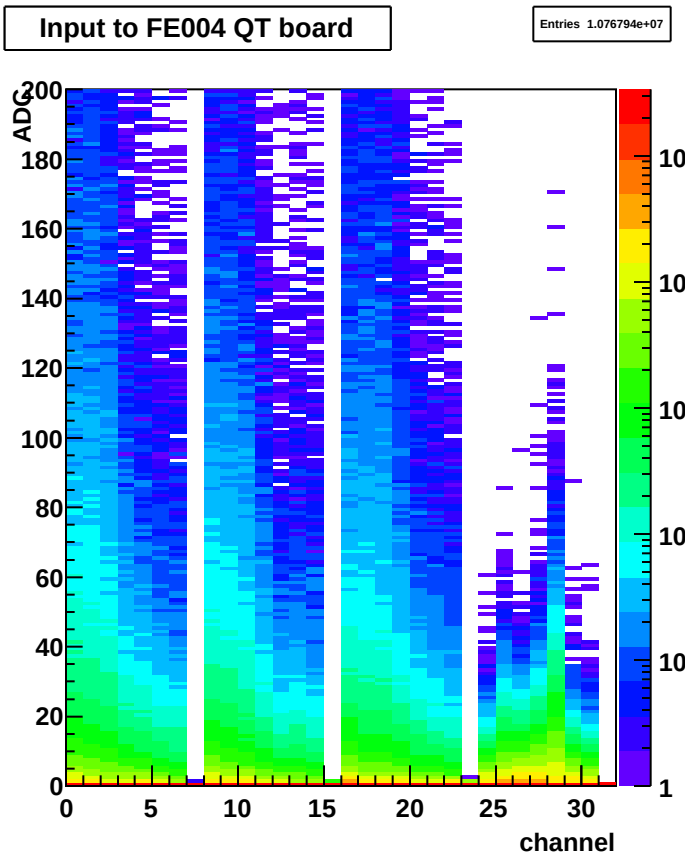

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

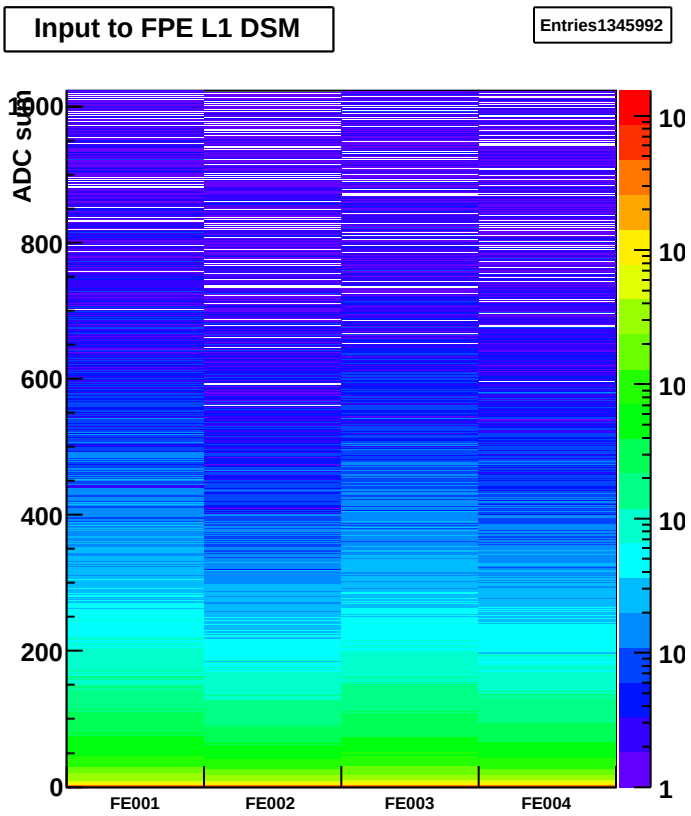

Input to FPD L2 DSM
Entries 1345992
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

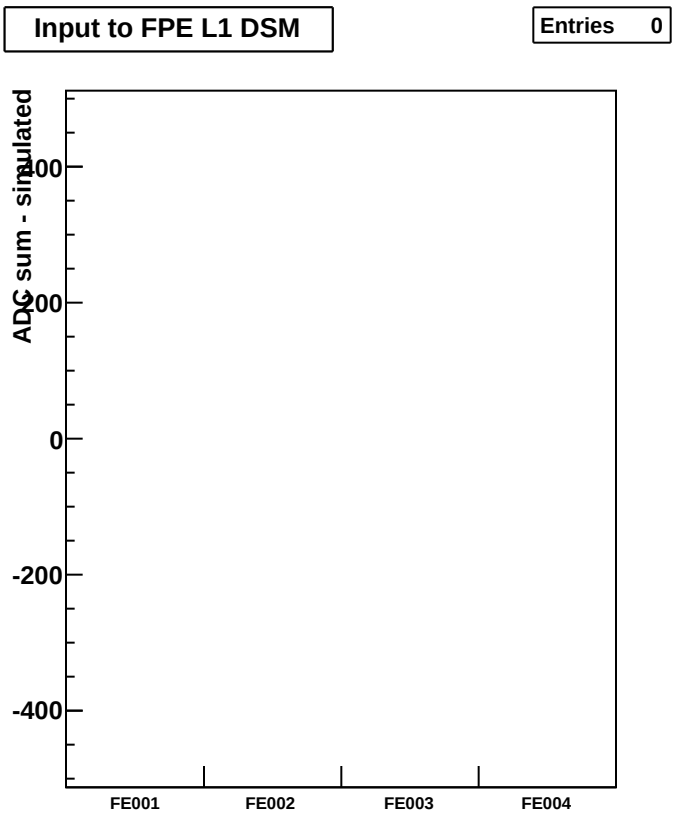

Input to FE001 QT board
Entries 1.076794e+07
ADC
channel

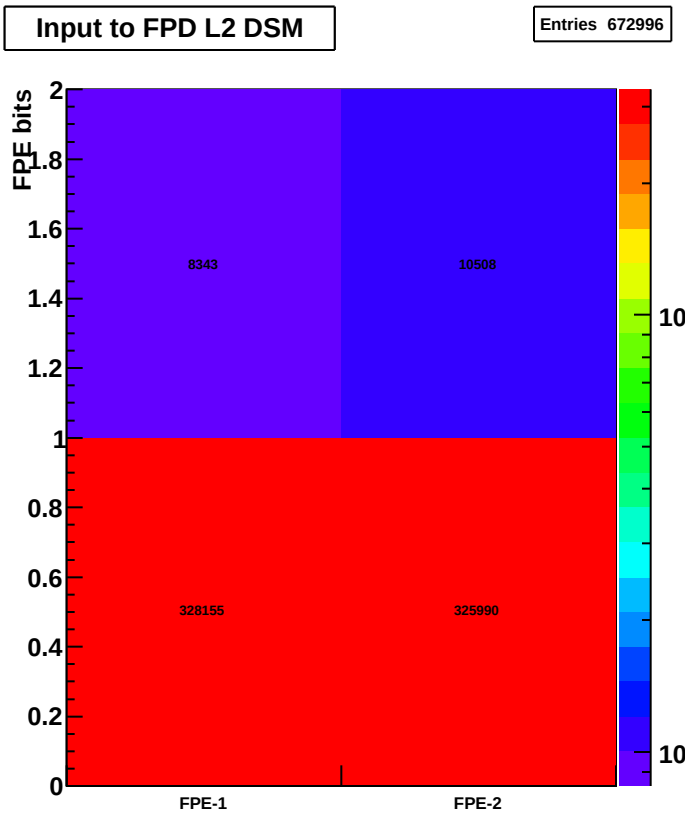

Input to FE002 QT board
Entries 1.076794e+07
ADC
channel

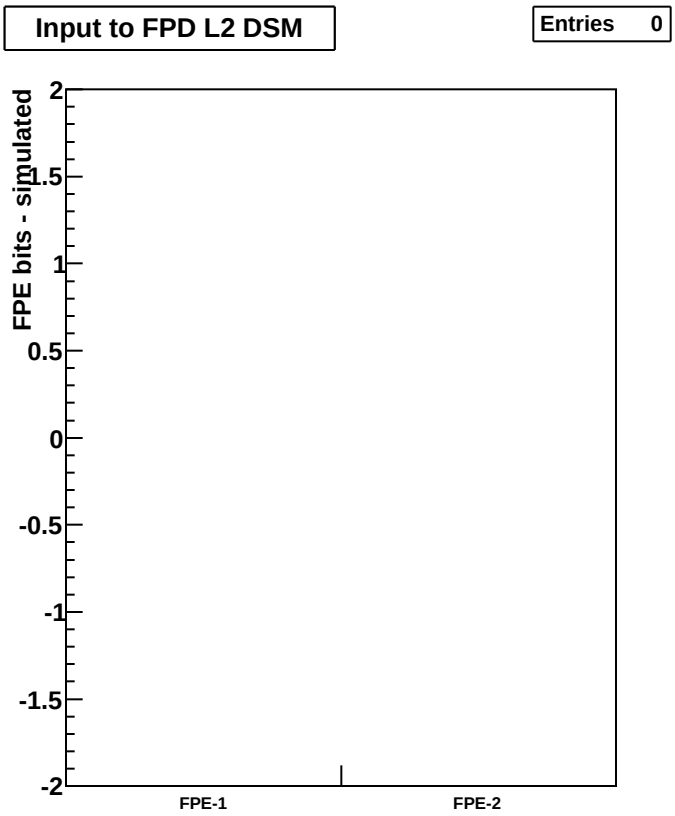

Input to FE003 QT board
Entries 1.076794e+07
ADC
channel


Input to FE004 QT board
Entries 1.076794e+07
ADC
channel


Input to FPE L1 DSM
Entries1345992
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 672996
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
8343
10508
328155
325990
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2