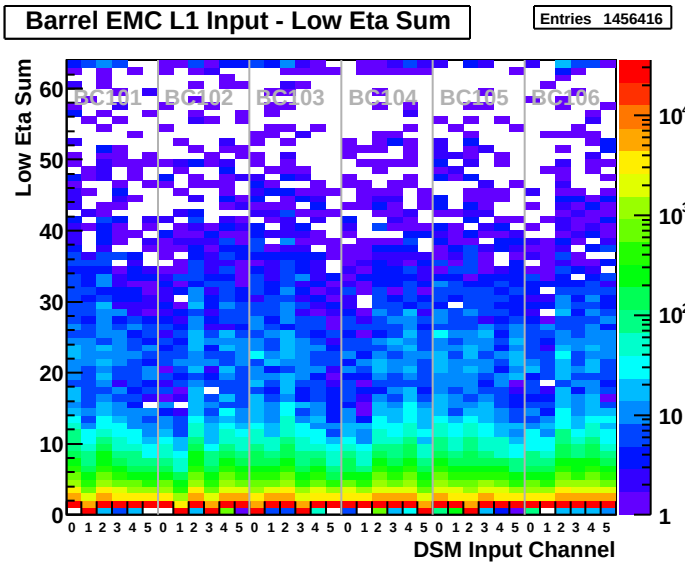
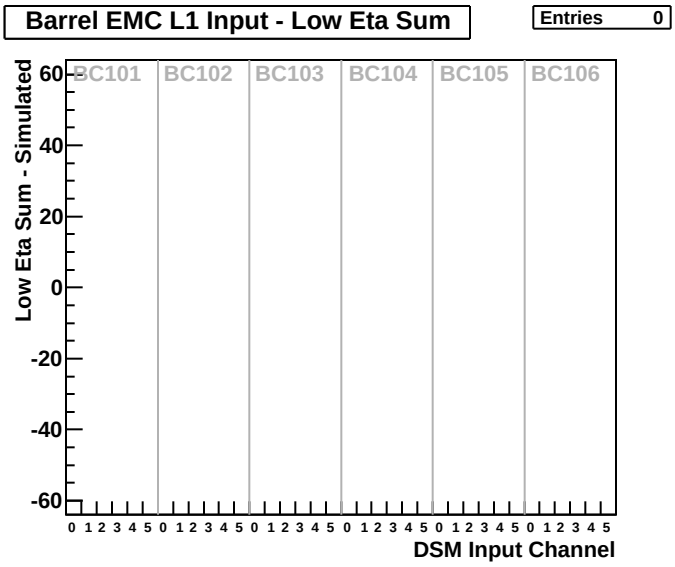
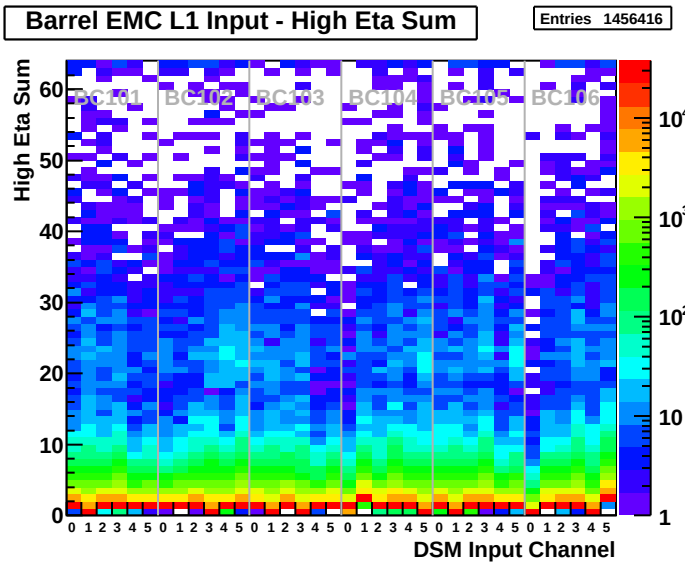
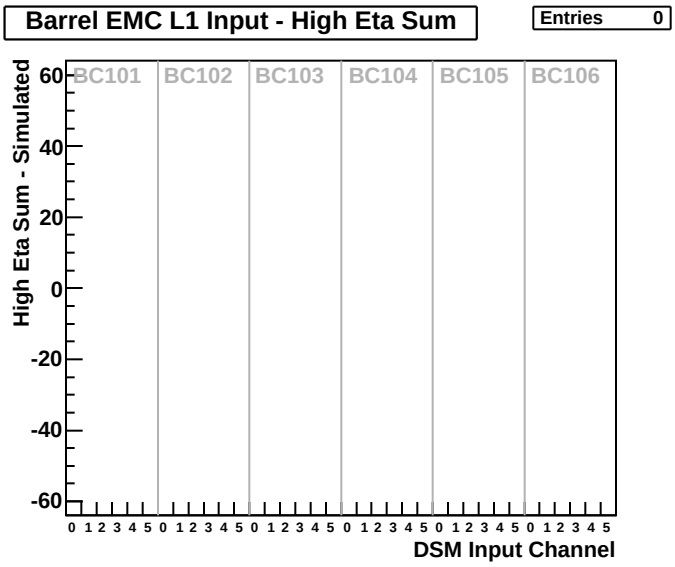
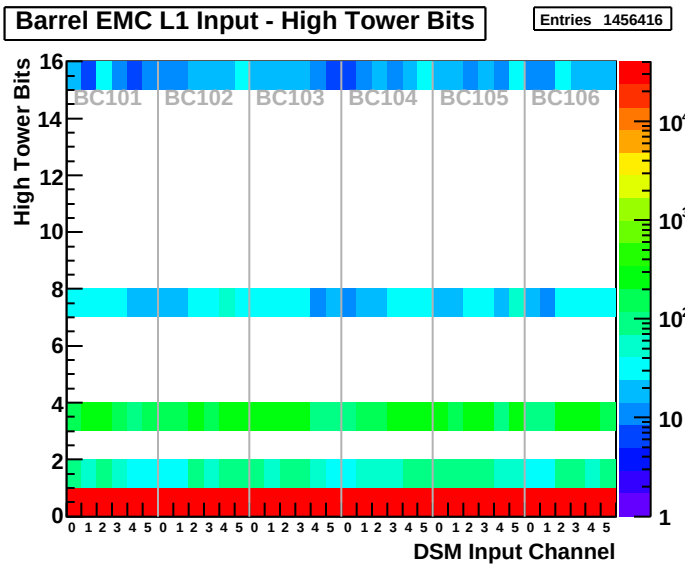
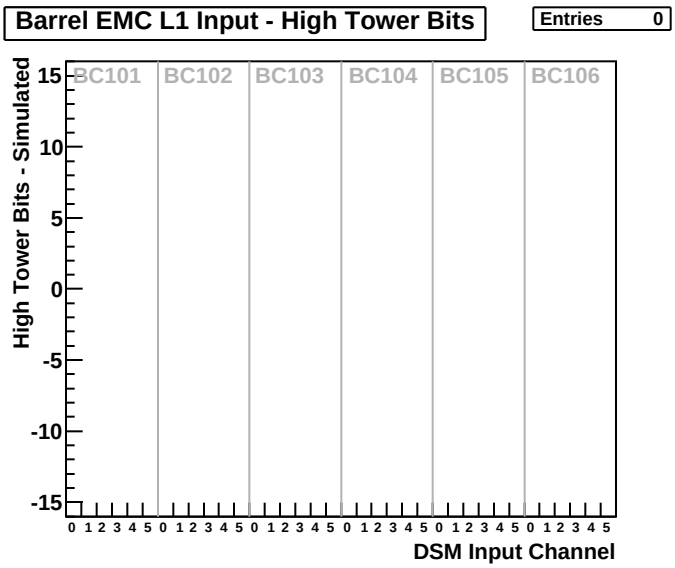

Barrel EMC L1 Input - Low Eta Sum
Entries 1456416
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

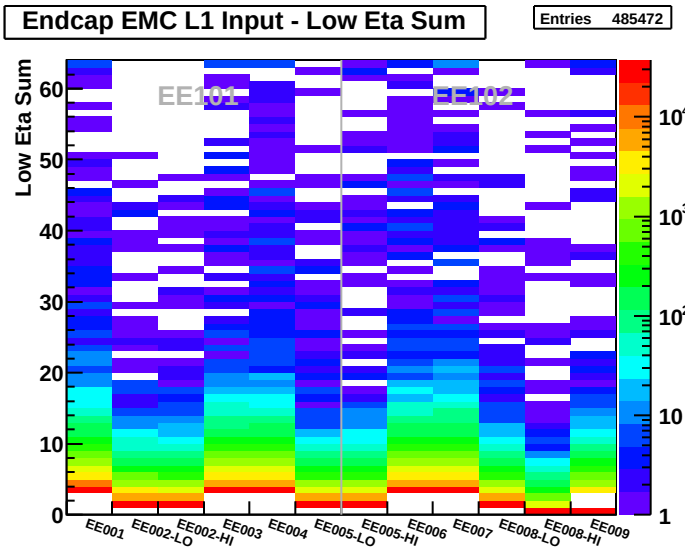

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

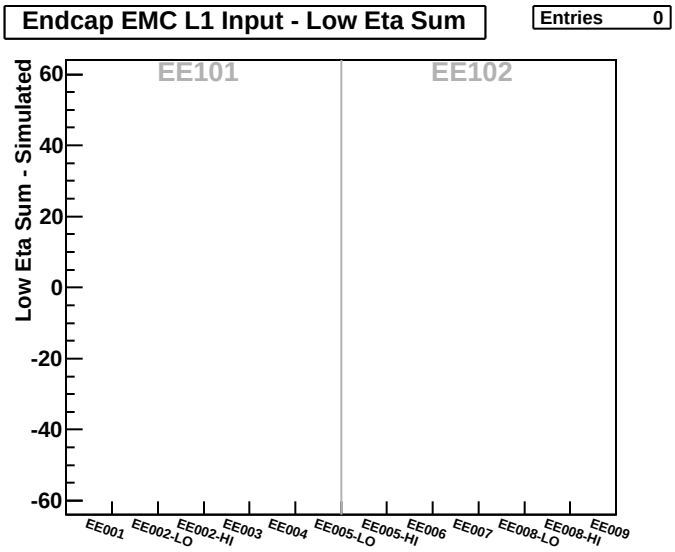

Barrel EMC L1 Input - High Eta Sum
Entries 1456416
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

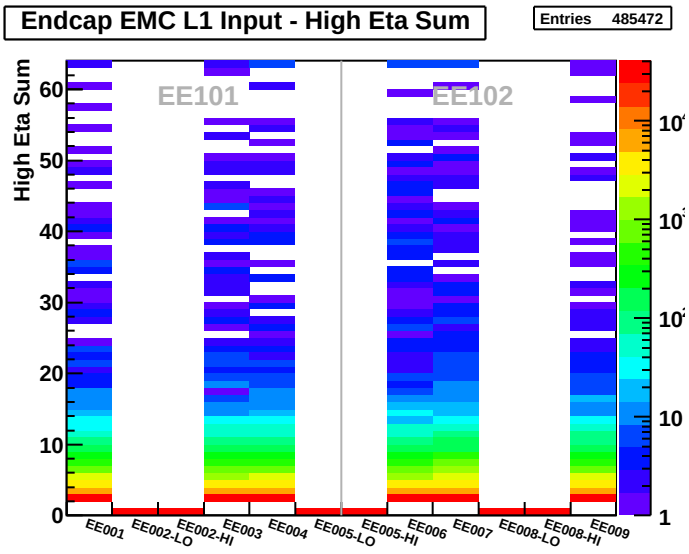

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

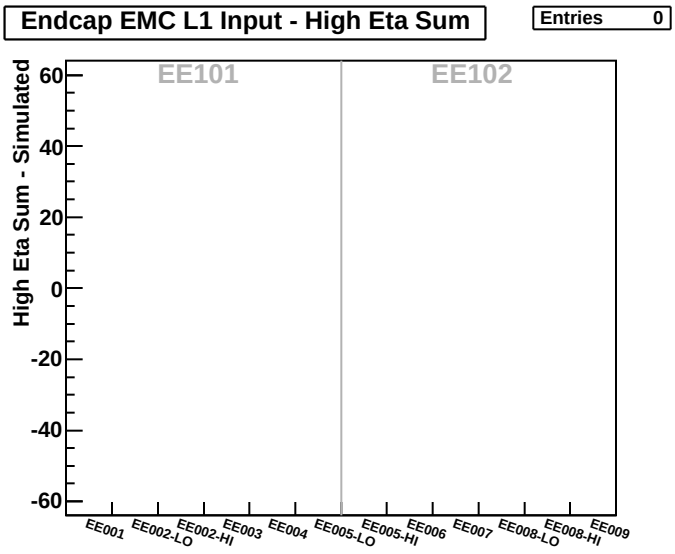

Barrel EMC L1 Input - High Tower Bits
Entries 1456416
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

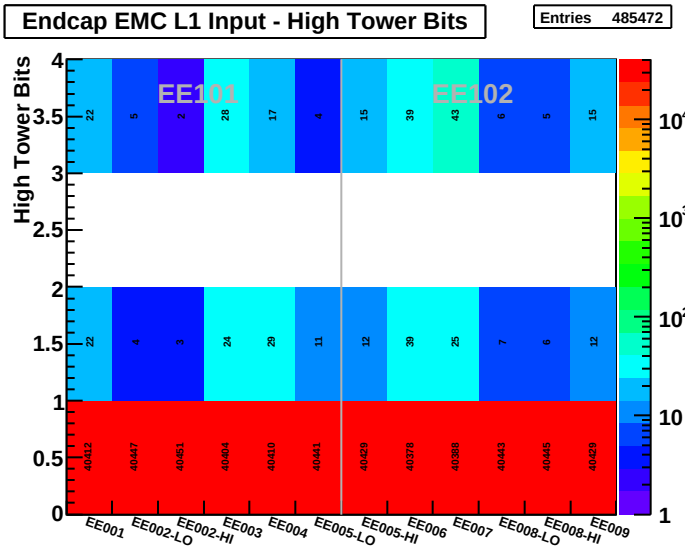

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

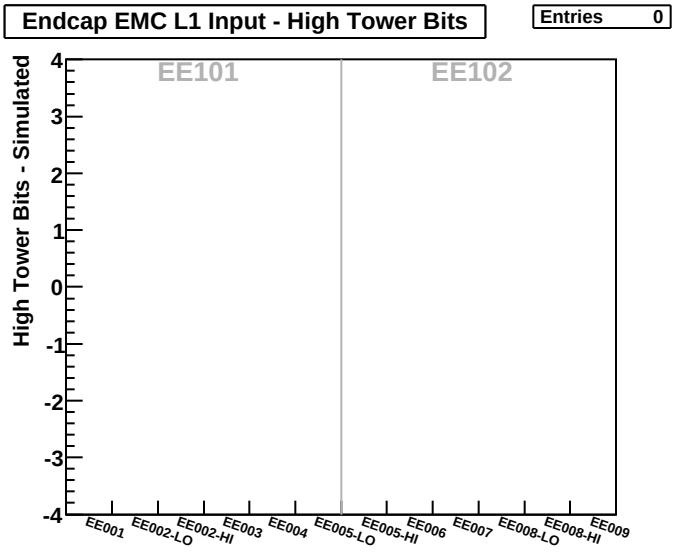

Endcap EMC L1 Input - Low Eta Sum
Entries 485472
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

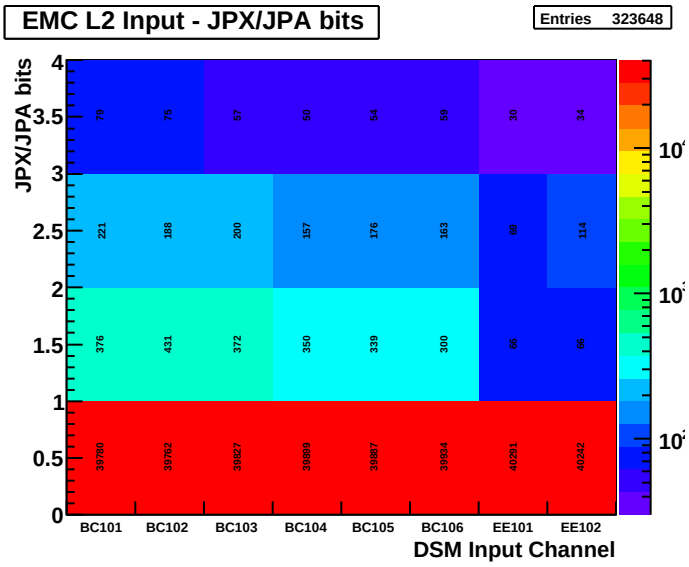

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

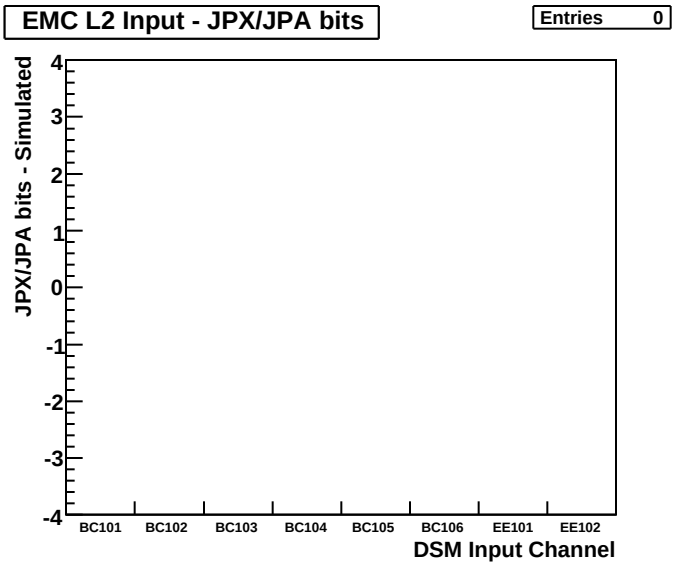

Endcap EMC L1 Input - High Eta Sum
Entries 485472
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

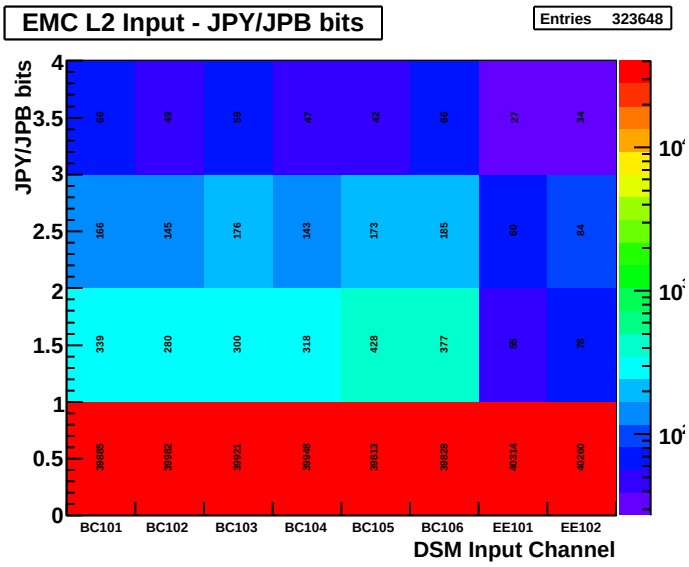

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

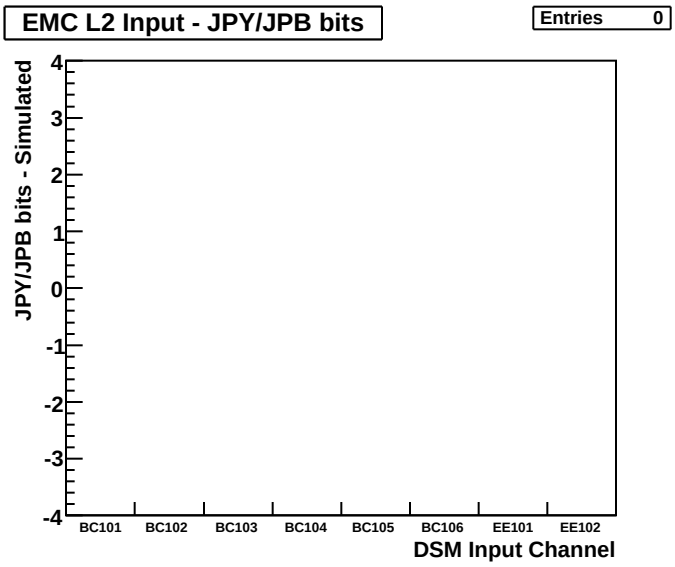

Endcap EMC L1 Input - High Tower Bits
Entries 485472
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

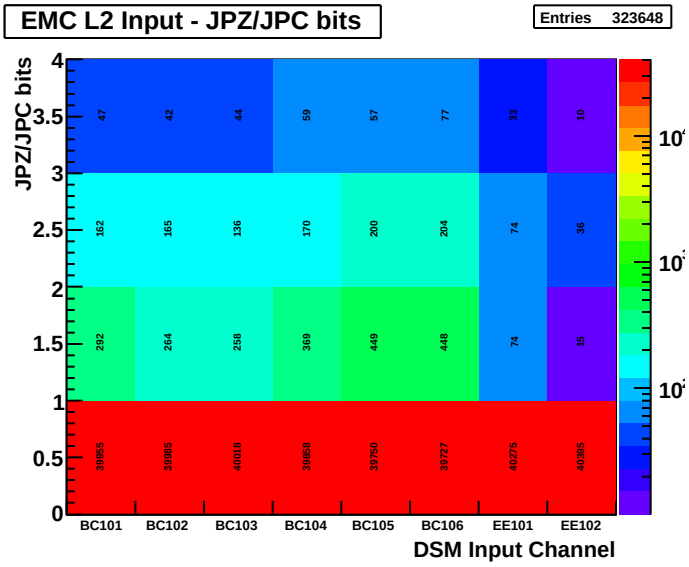

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

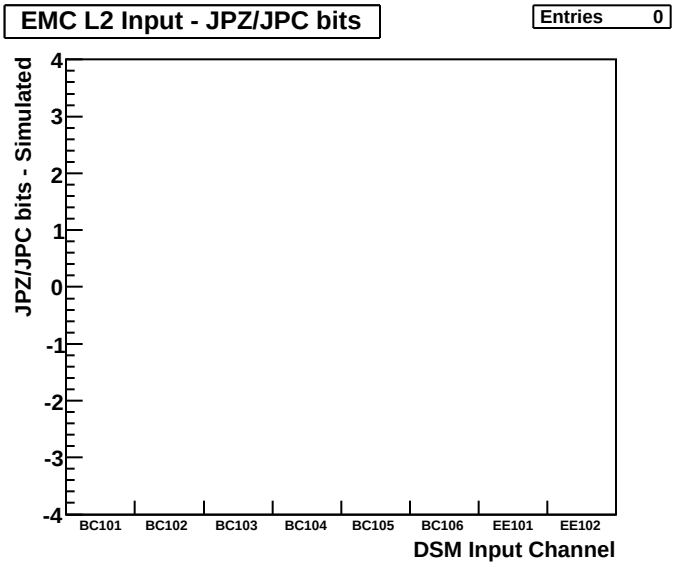

EMC L2 Input - JPX/JPA bits
Entries 323648
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
79 76 57 50 54 59 30 34
221 188 200 157 176 163 69 114
376 431 372 350 339 300 66 66
39780 39762 39827 39899 39887 39934 40291 40242

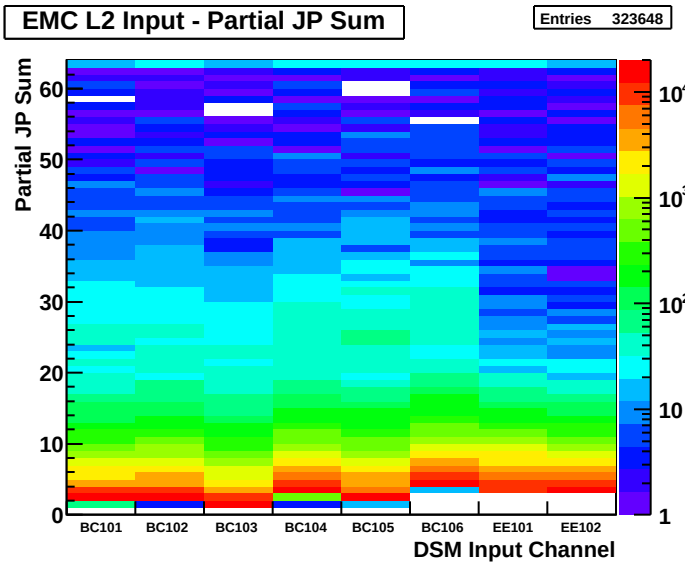

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

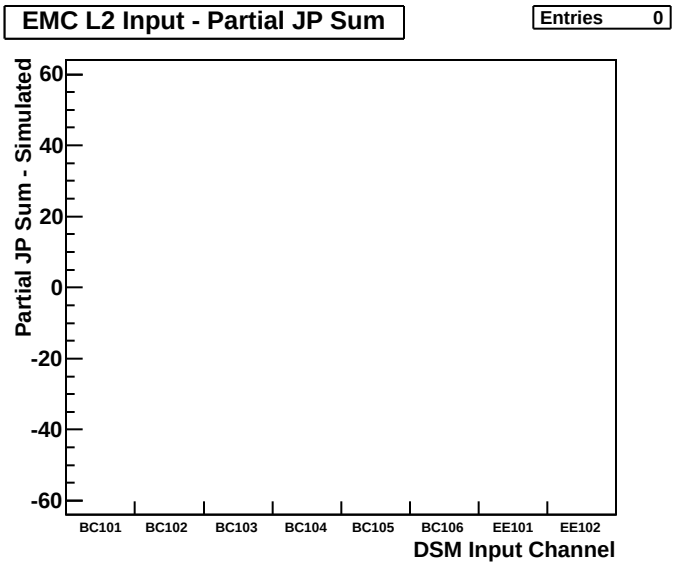

EMC L2 Input - JPY/JPB bits
Entries 323648
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
66 49 59 47 42 66 27 34
166 145 176 143 173 185 60 84
339 280 300 318 428 377 55 78
39885 39882 39921 39948 39813 39828 40314 40260

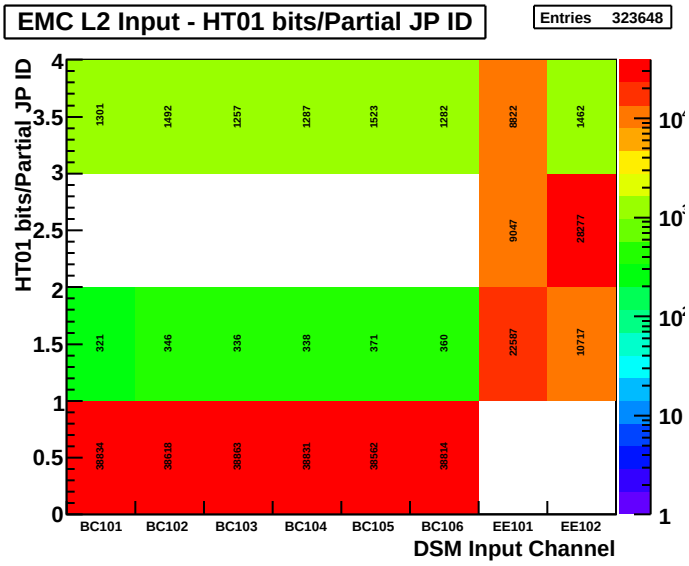

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

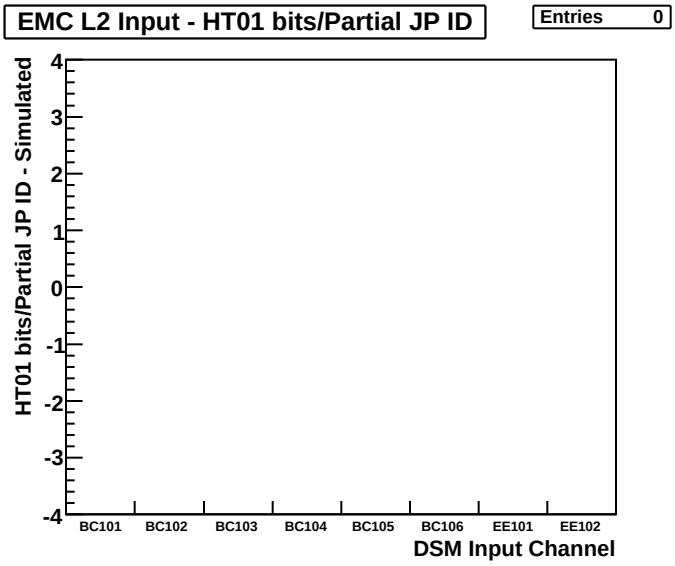

EMC L2 Input - JPZ/JPC bits
Entries 323648
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
47 42 44 59 57 77 33 10
162 165 136 170 200 204 74 36
292 264 258 369 449 448 74 15
39955 39985 40018 39958 39750 39727 40275 40395

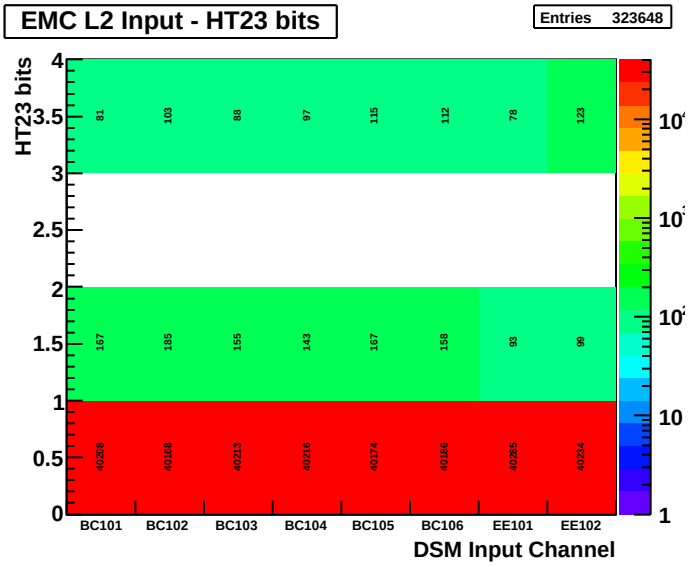

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

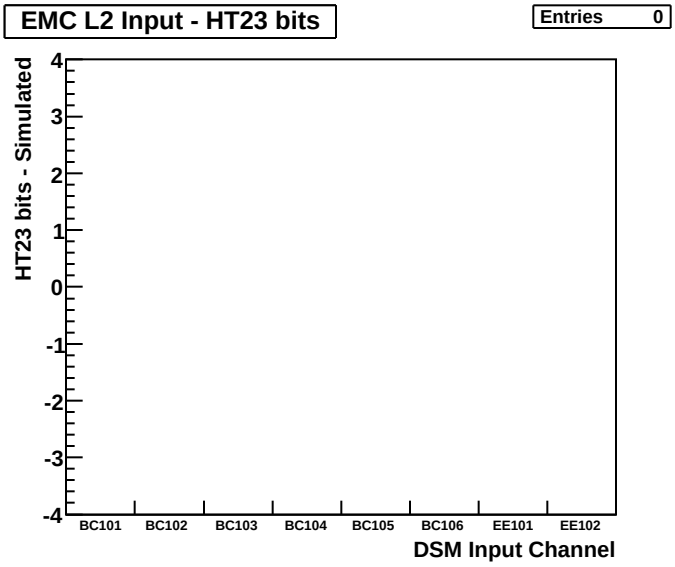

EMC L2 Input - Partial JP Sum
Entries 323648
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

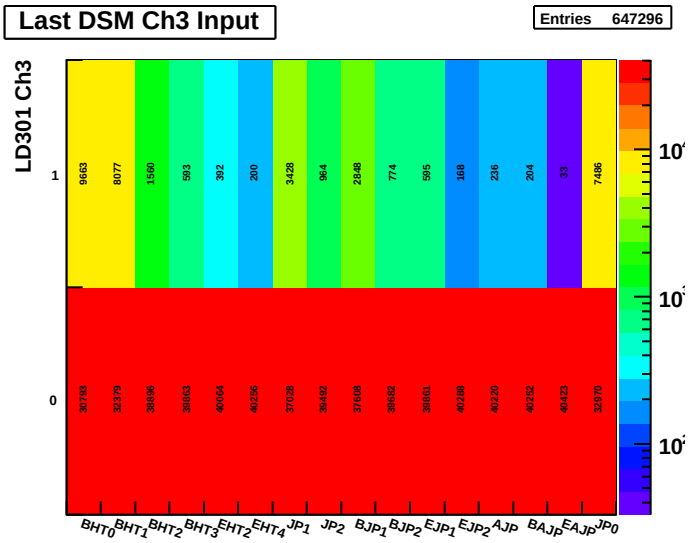

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

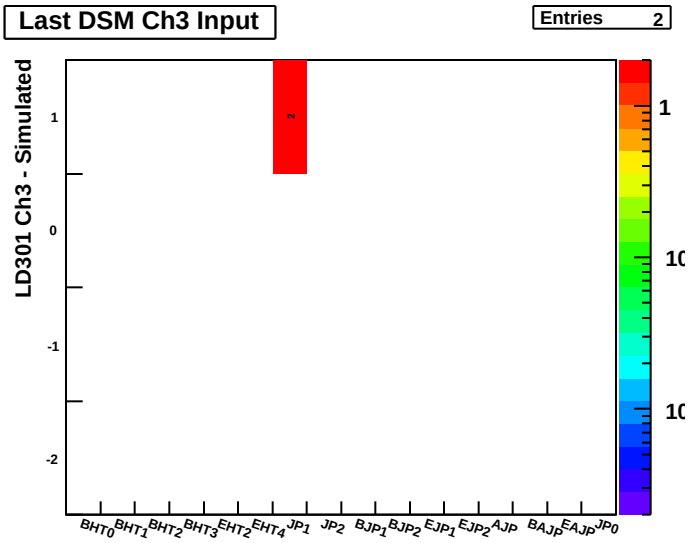

EMC L2 Input - HT01 bits/Partial JP ID
Entries 323648
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

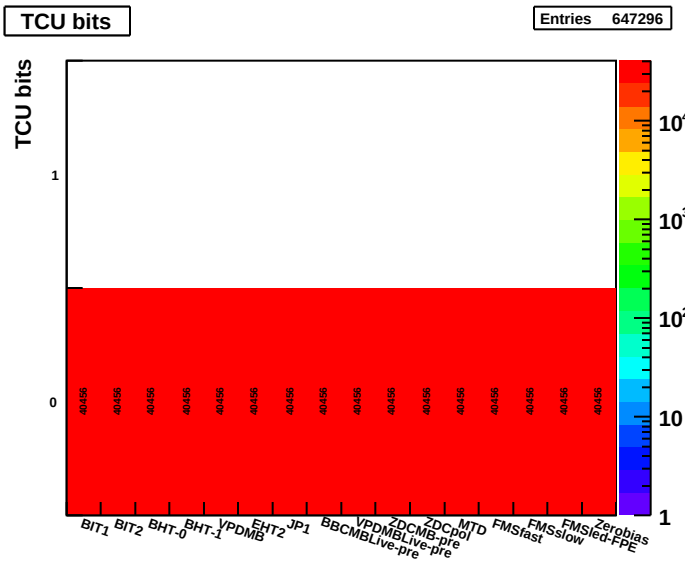

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

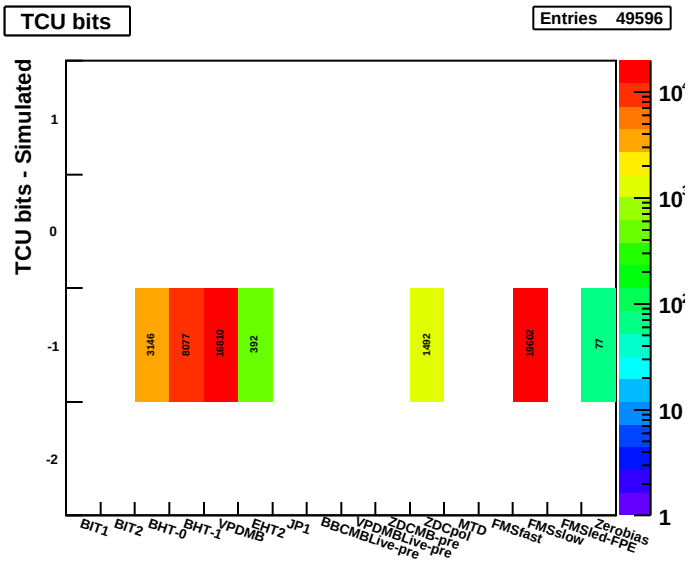

EMC L2 Input - HT23 bits
Entries 323648
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

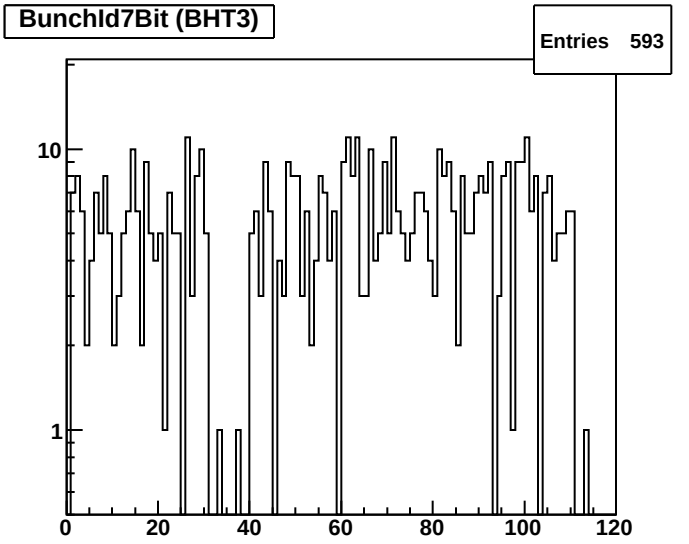

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

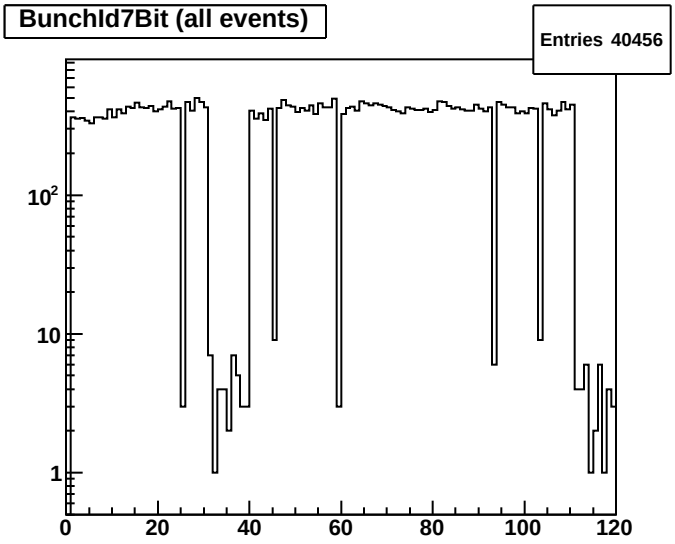

Last DSM Ch3 Input
Entries 647296
LD301 Ch3
9663 8077 1560 593 392 200 3428 964 2848 774 595 168 236 204 33 7486
30793 32379 38896 39863 40064 40256 37028 39492 37608 39682 39861 40288 40220 40252 40423 32970
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

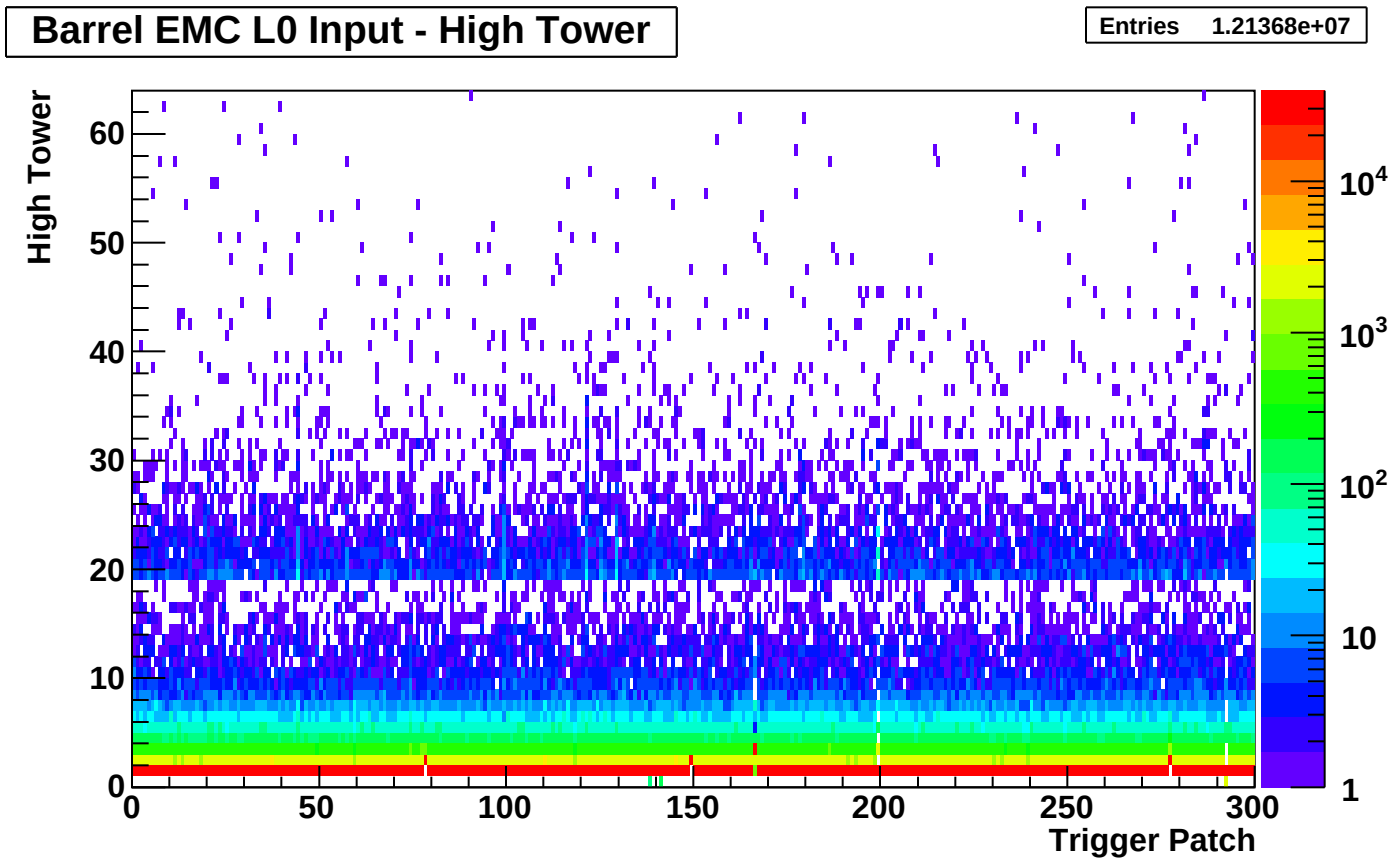

Last DSM Ch3 Input
Entries 2
LD301 Ch3 - Simulated
2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

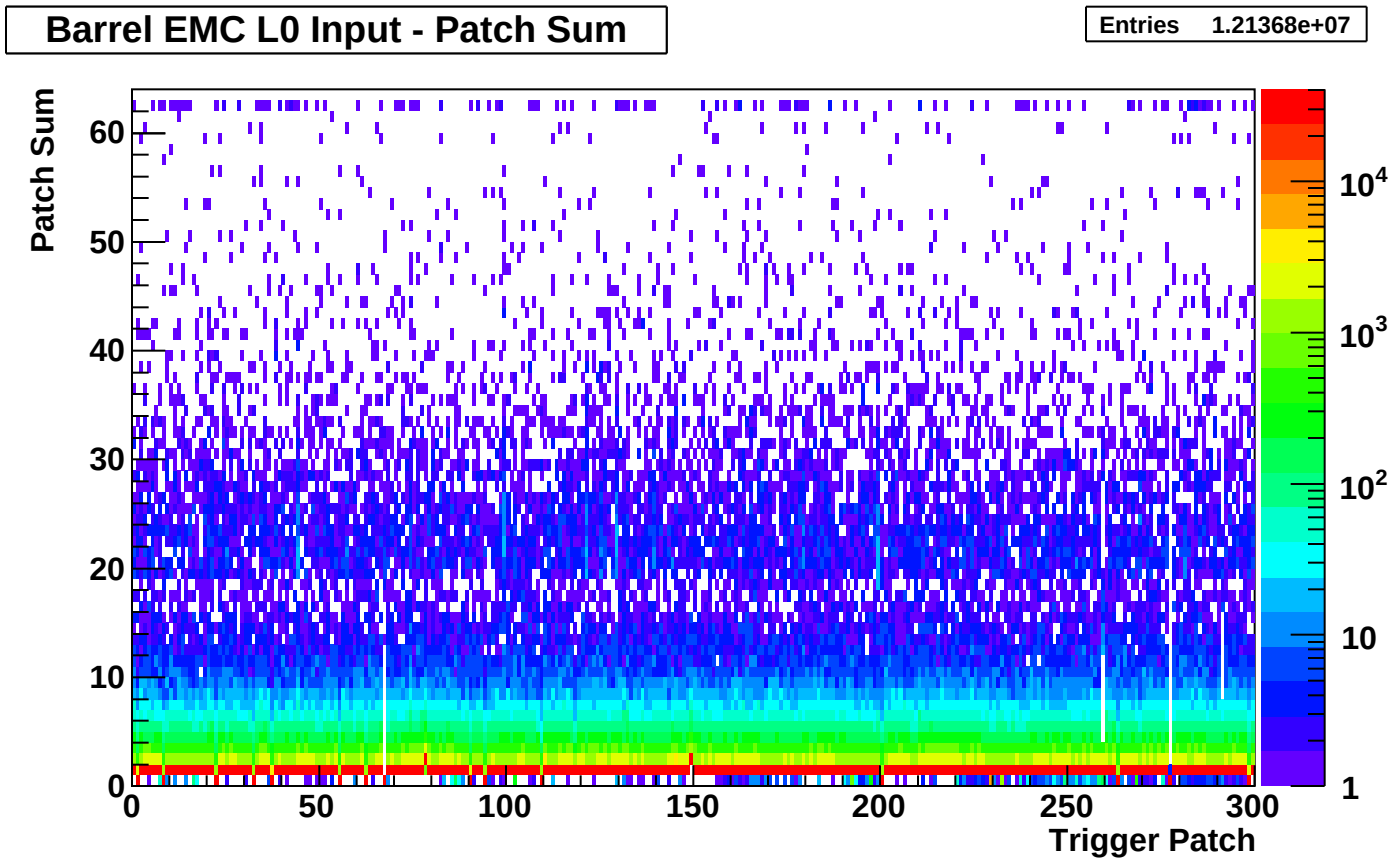

TCU bits
Entries 647296
TCU bits
40456 40456 40456 40456 40456 40456 40456 40456 40456 40456 40456 40456 40456 40456 40456 40456
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

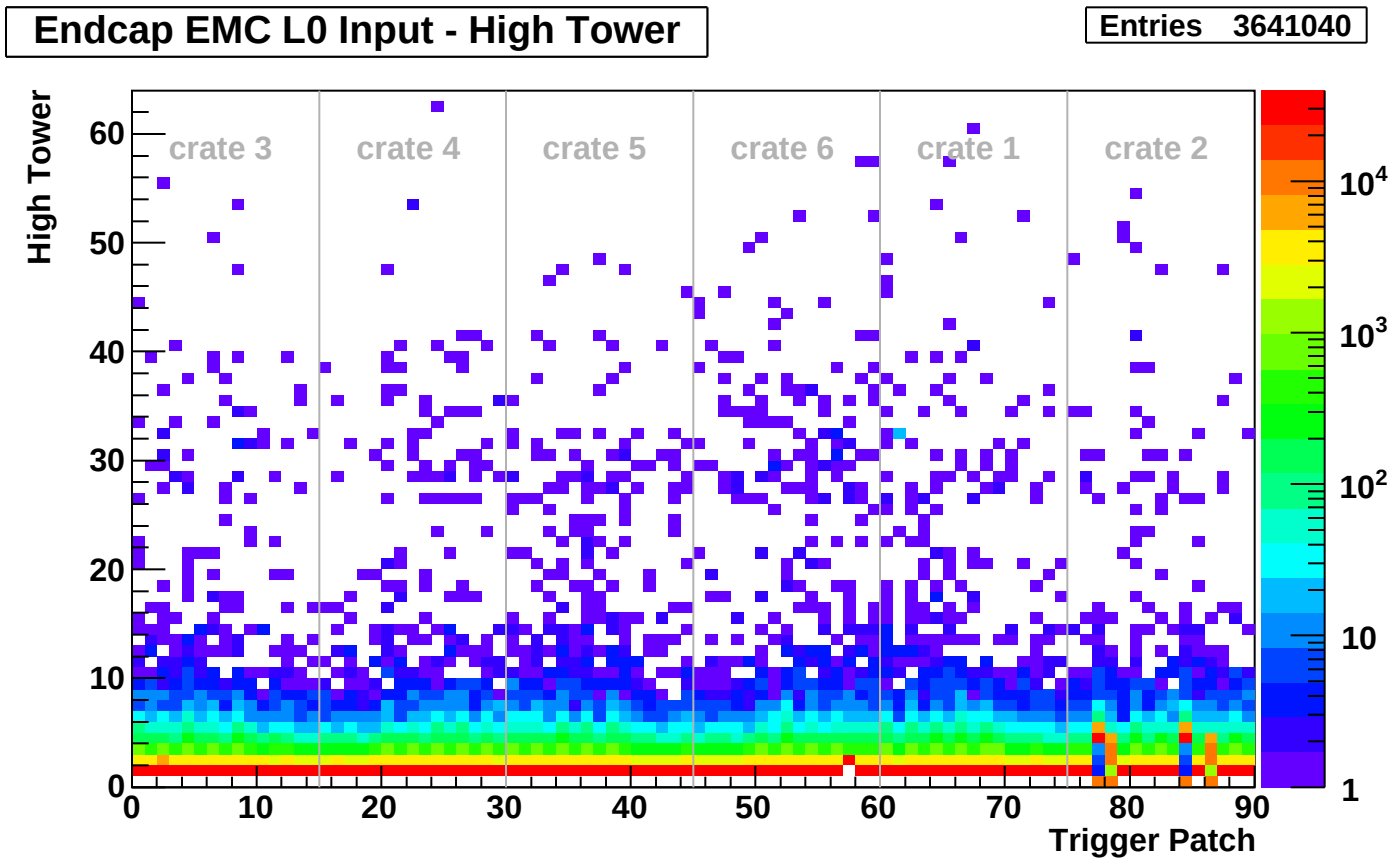

TCU bits
Entries 49596
TCU bits - Simulated
3146 8077 16810 392 1482 19602 77
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

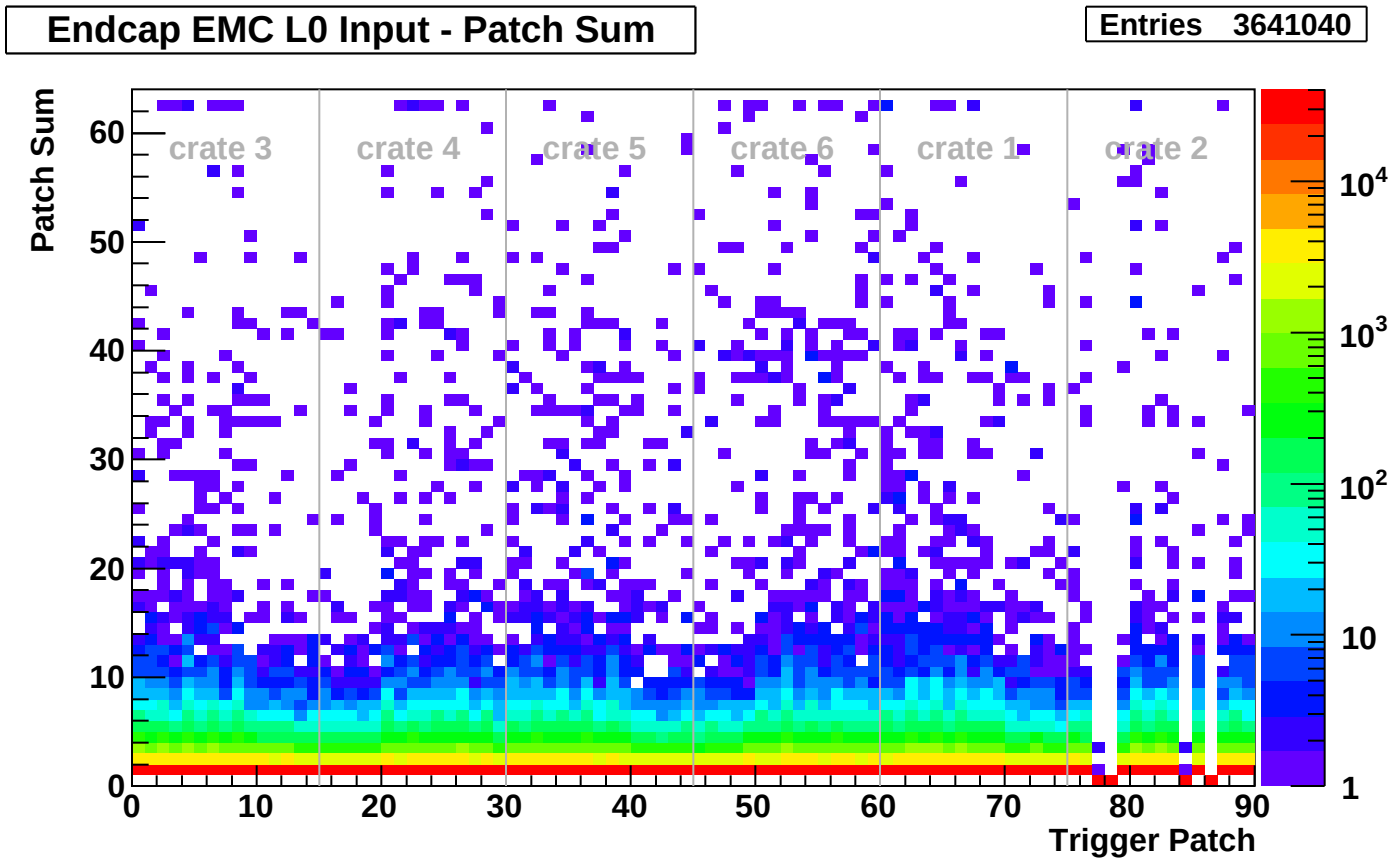

BunchId7Bit (BHT3)
Entries 593

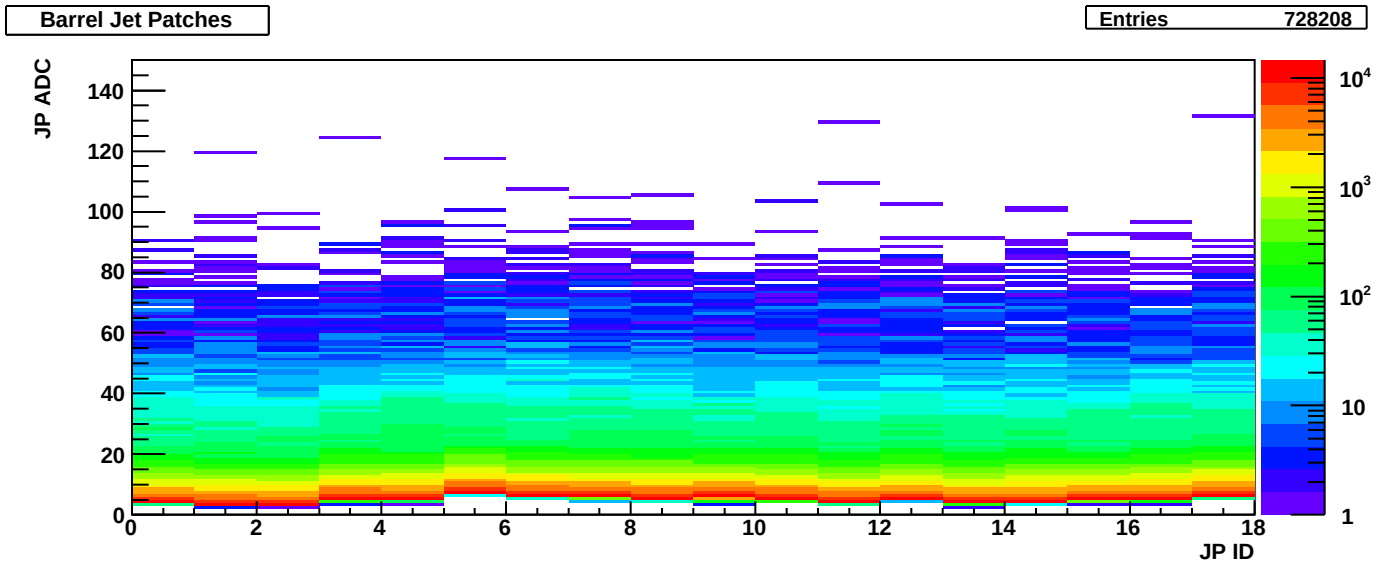

BunchId7Bit (all events)
Entries 40456

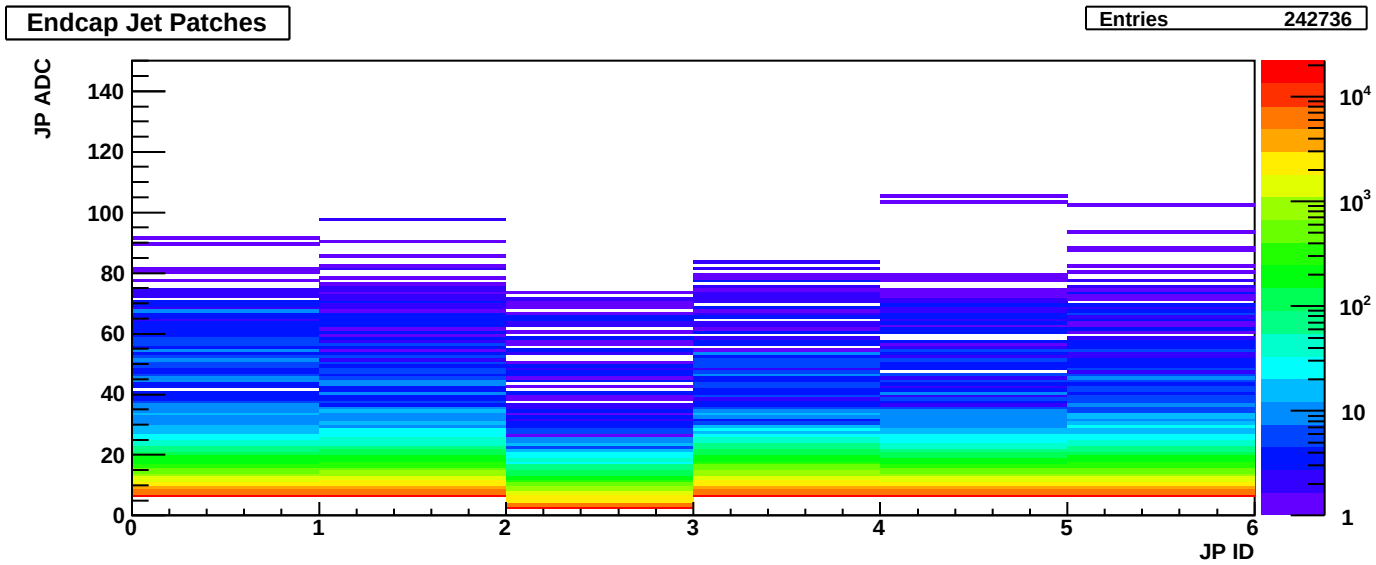

Barrel EMC L0 Input - High Tower
Entries 1.21368e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

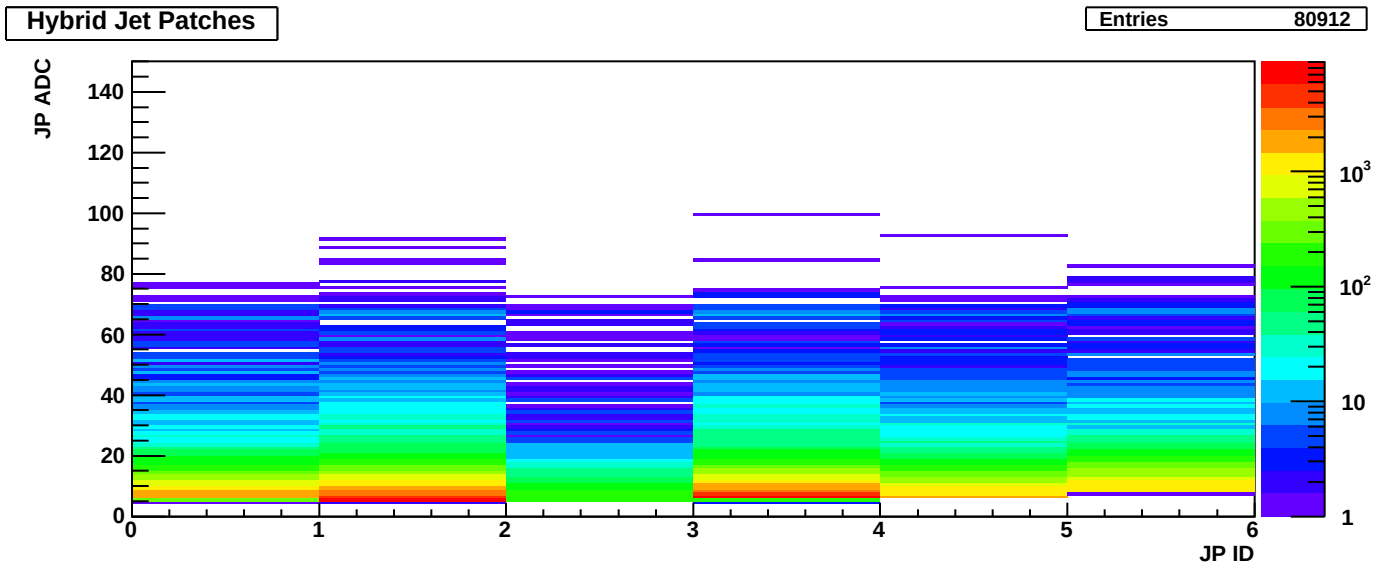

Barrel EMC L0 Input - Patch Sum
Entries 1.21368e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

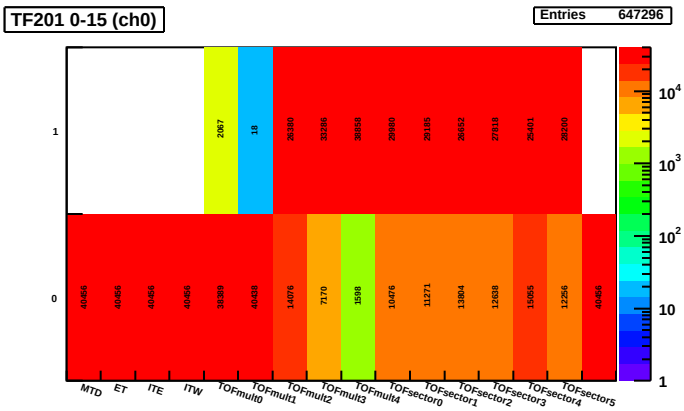

Endcap EMC L0 Input - High Tower
Entries 3641040
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

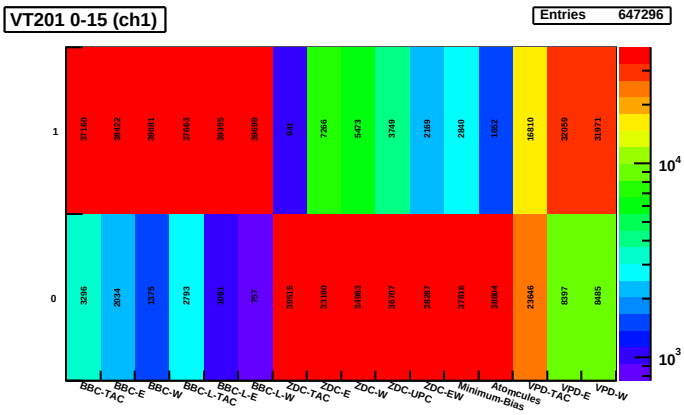

Endcap EMC L0 Input - Patch Sum
Entries 3641040
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

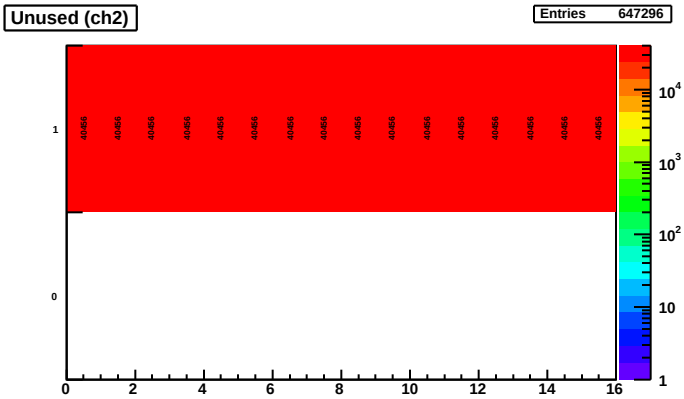

Barrel Jet Patches
Entries 728208
JP ADC
JP ID

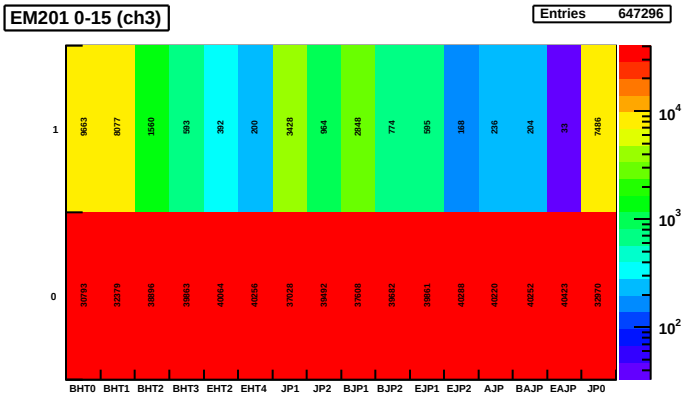

Endcap Jet Patches
Entries 242736
JP ADC
JP ID

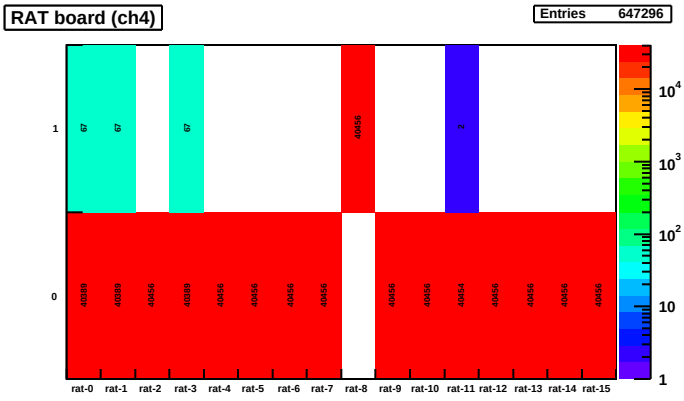

Hybrid Jet Patches
Entries 80912
JP ADC
JP ID

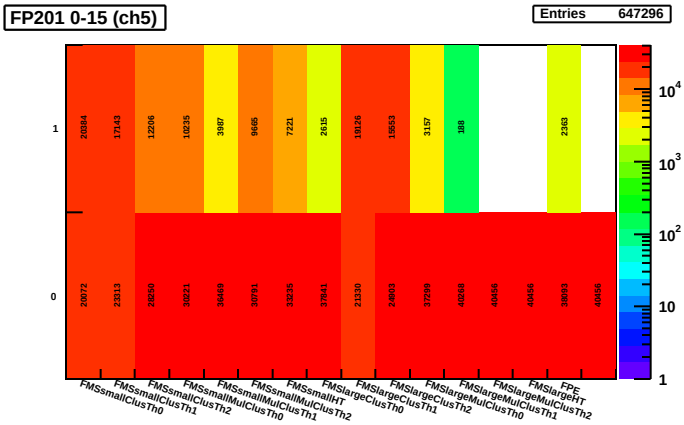

TF201 0-15 (ch0)
Entries 647296
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

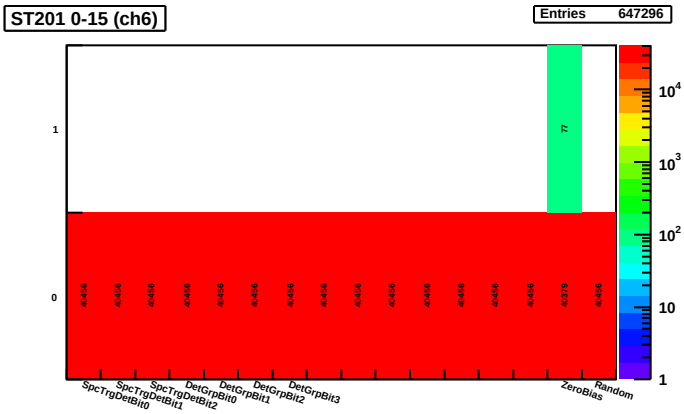

VT201 0-15 (ch1)
Entries 647296
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

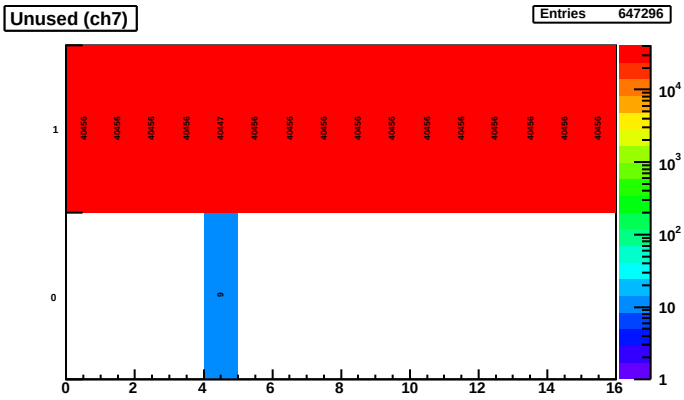

Unused (ch2)
Entries 647296

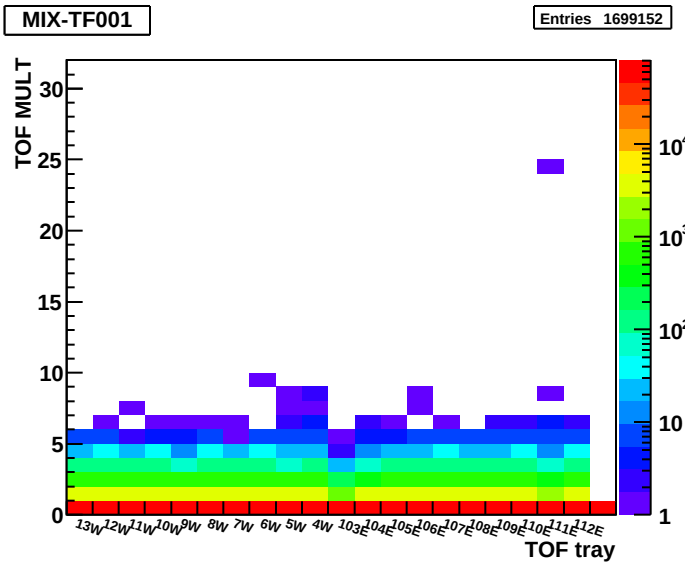

EM201 0-15 (ch3)
Entries 647296
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

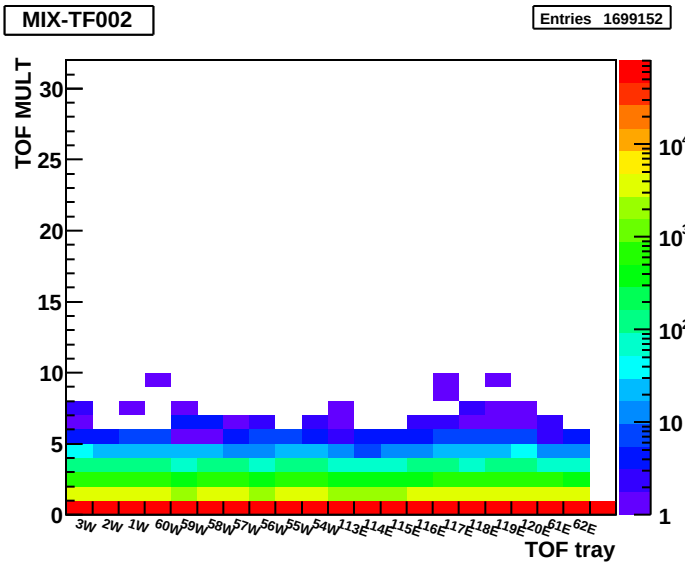

RAT board (ch4)
Entries 647296
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

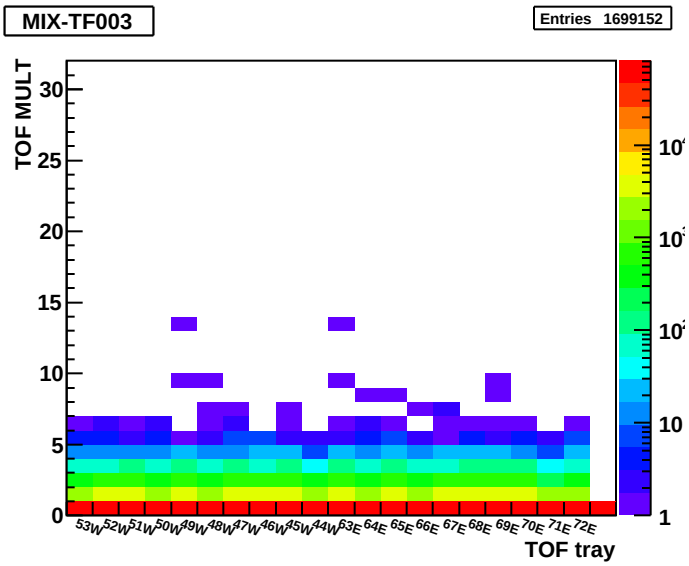

FP201 0-15 (ch5)
Entries 647296
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

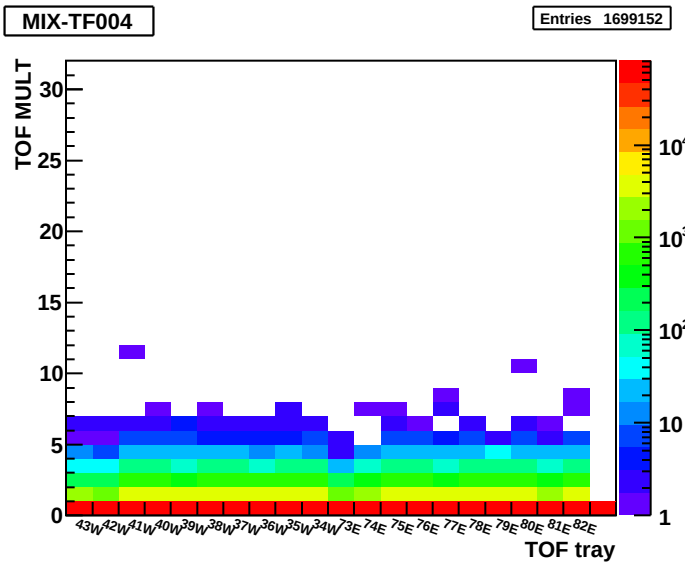

ST201 0-15 (ch6)
Entries 647296
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

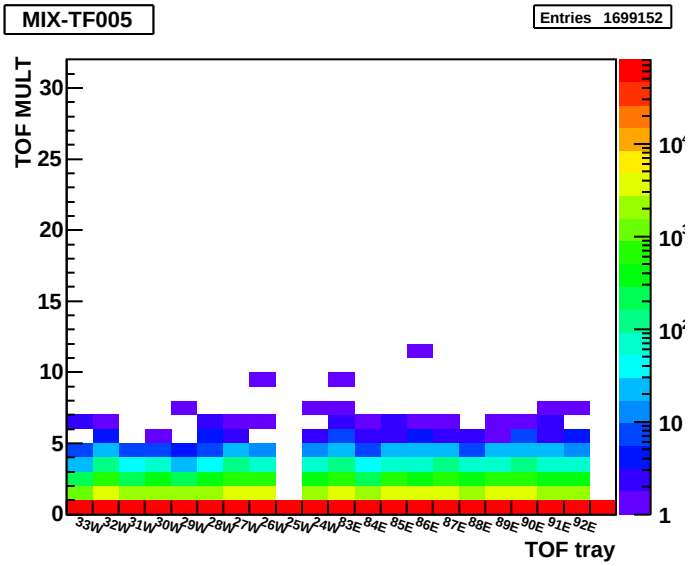

Unused (ch7)
Entries 647296

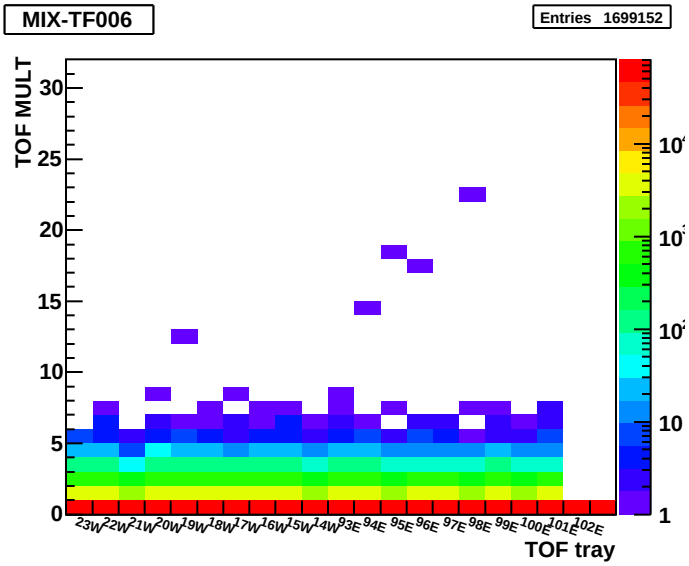

MIX-TF001
Entries 1699152
TOF MULT
TOF tray

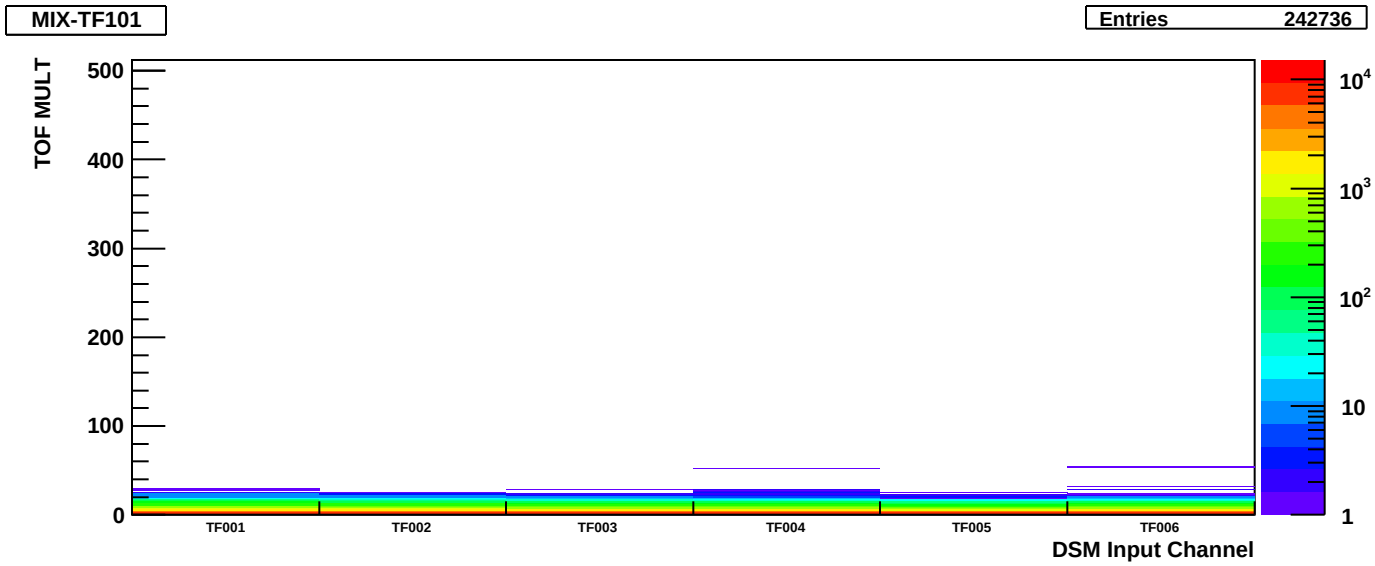

MIX-TF002
Entries 1699152
TOF MULT
TOF tray

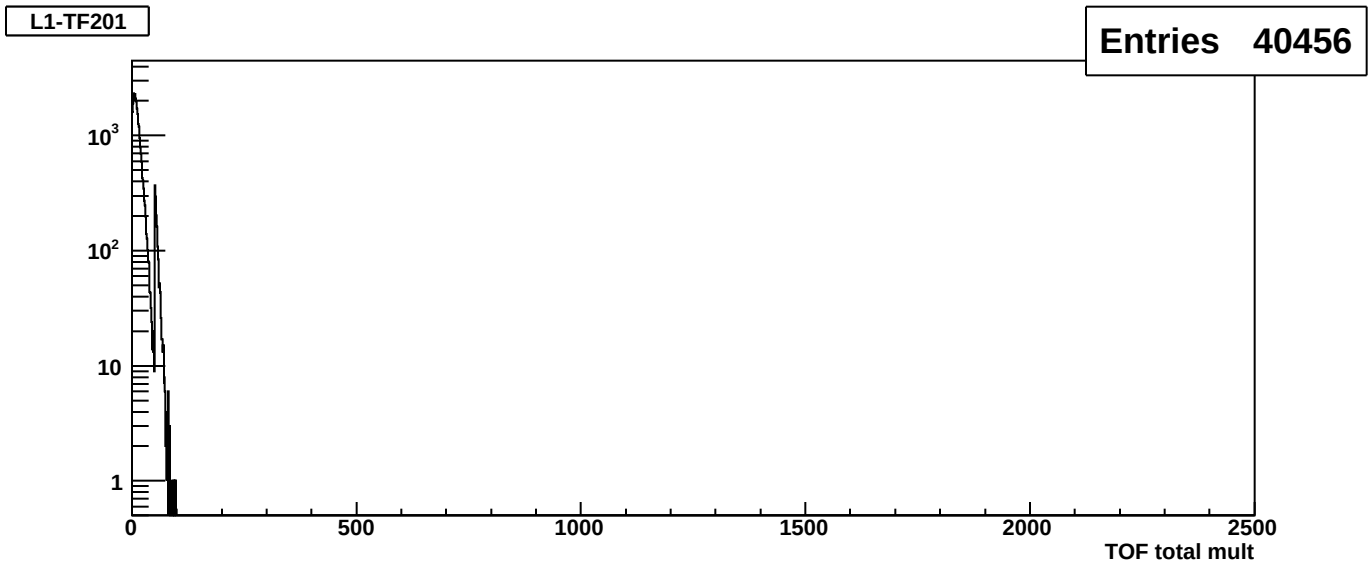

MIX-TF003
Entries 1699152
TOF MULT
TOF tray

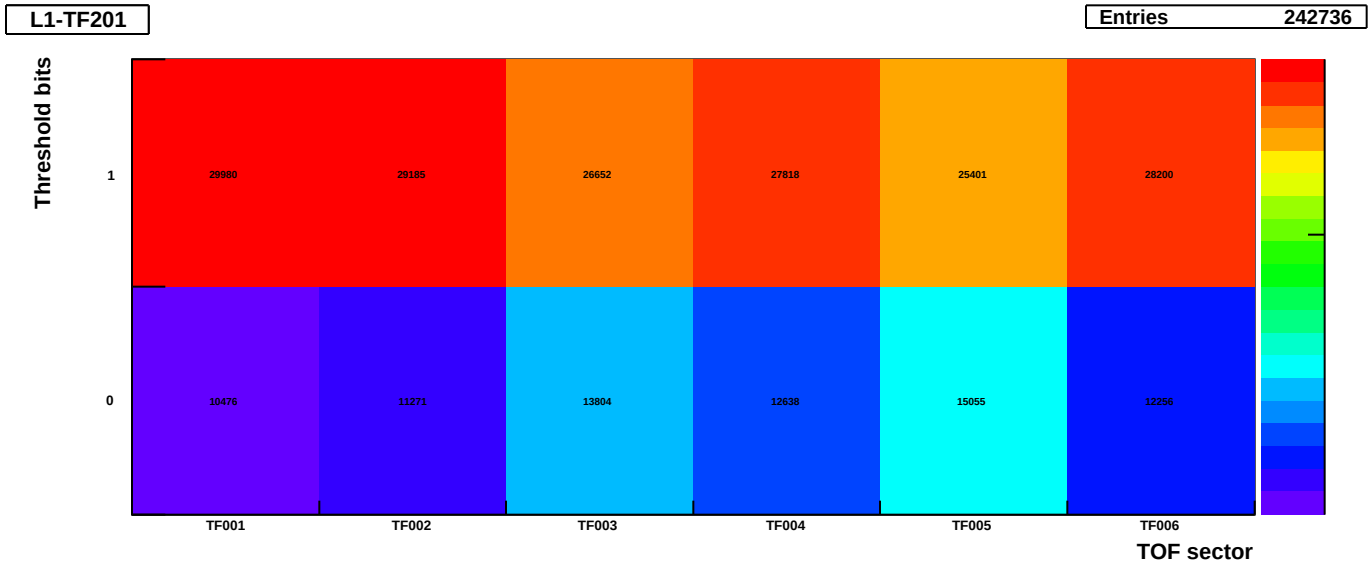

MIX-TF004
Entries 1699152
TOF MULT
TOF tray

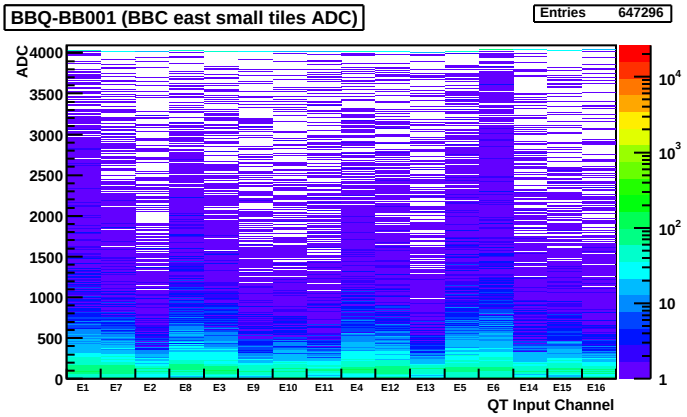

MIX-TF005
Entries 1699152
TOF MULT
TOF tray

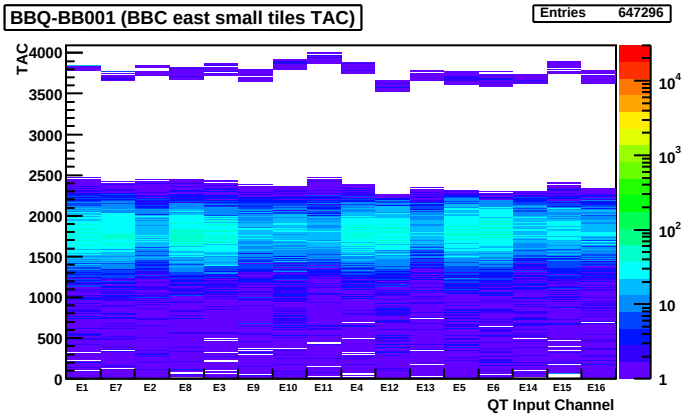

MIX-TF006
Entries 1699152
TOF MULT
TOF tray

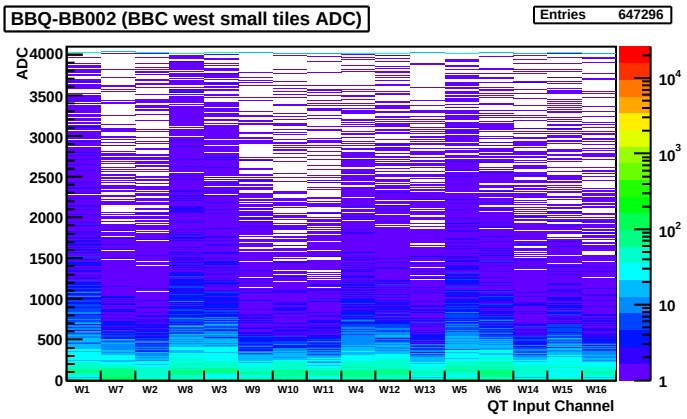

MIX-TF101
Entries 242736
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

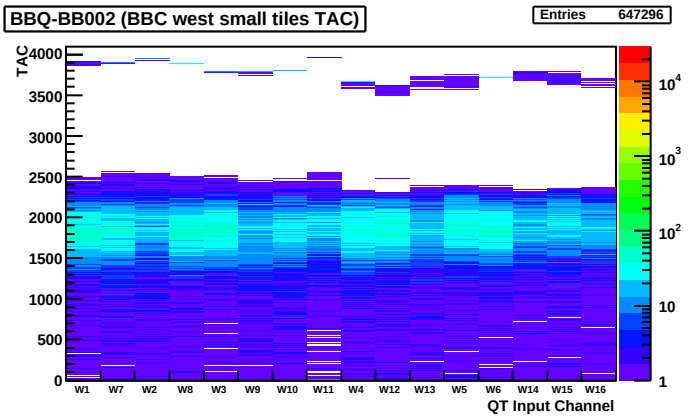

L1-TF201
Entries 40456
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

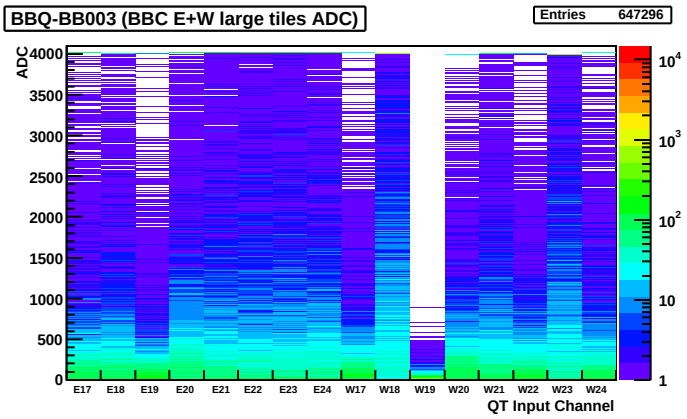

L1-TF201
Entries 242736
Threshold bits
1
0
29980
29185
26652
27818
25401
28200
10476
11271
13804
12638
15055
12256
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

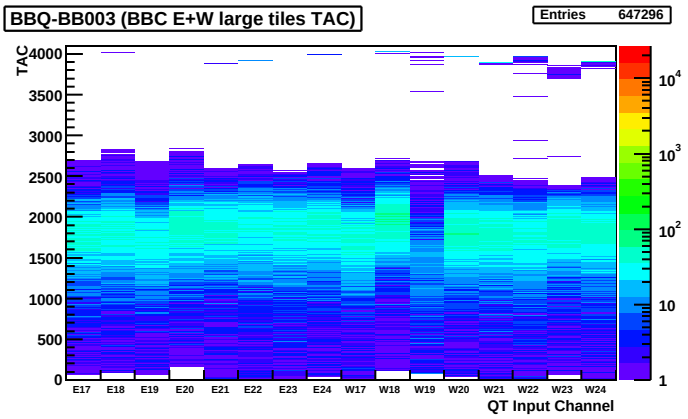

BBQ-BB001 (BBC east small tiles ADC)
Entries 647296
ADC
QT Input Channel

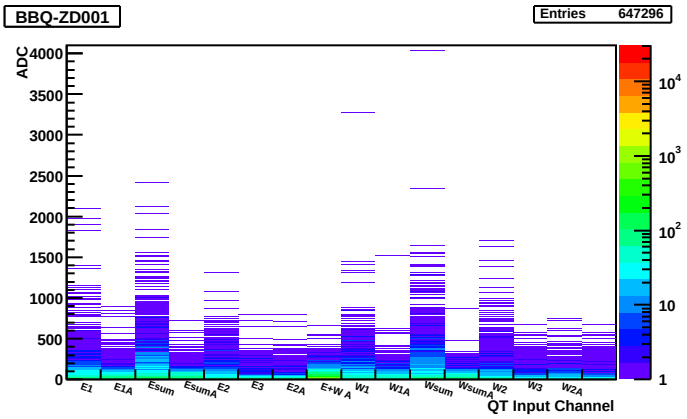

BBQ-BB001 (BBC east small tiles TAC)
Entries 647296
TAC
QT Input Channel

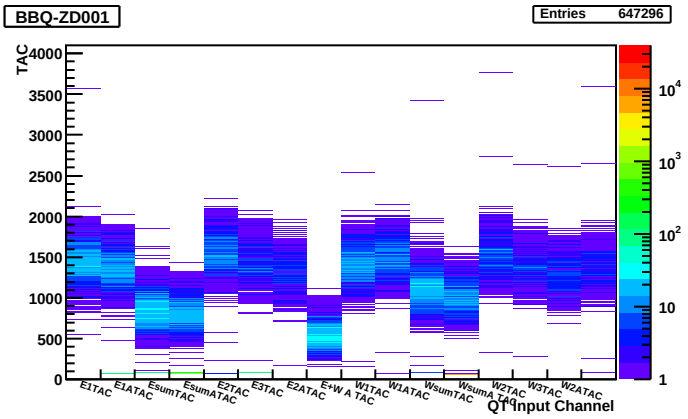

BBQ-BB002 (BBC west small tiles ADC)
Entries 647296
ADC
QT Input Channel

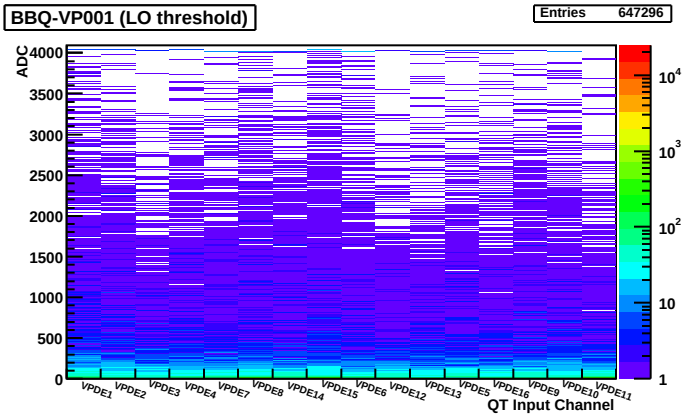

BBQ-BB002 (BBC west small tiles TAC)
Entries 647296
TAC
QT Input Channel

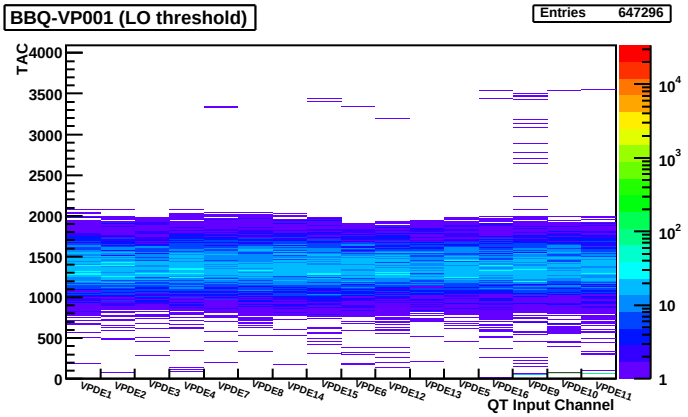

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 647296
ADC
QT Input Channel

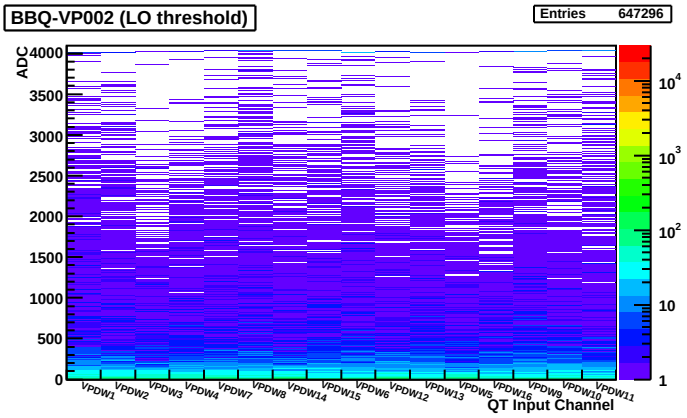

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 647296
TAC
QT Input Channel

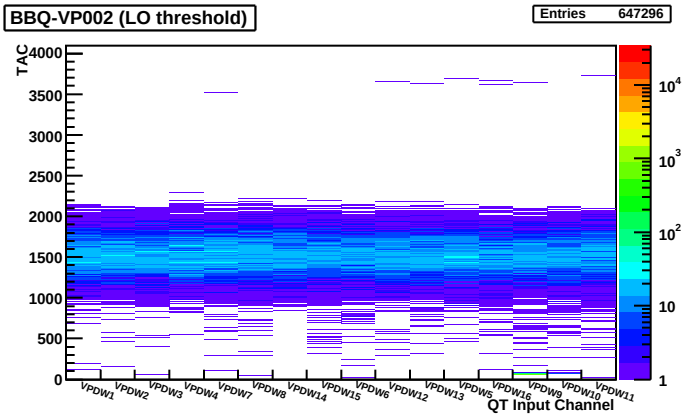

BBQ-ZD001
Entries 647296
ADC
QT Input Channel

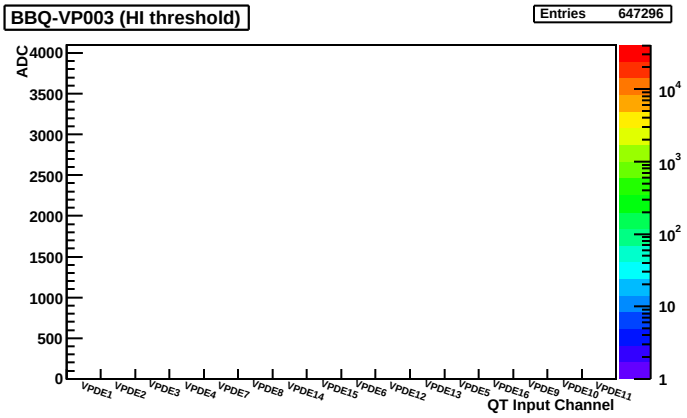

BBQ-ZD001
Entries 647296
TAC
QT Input Channel

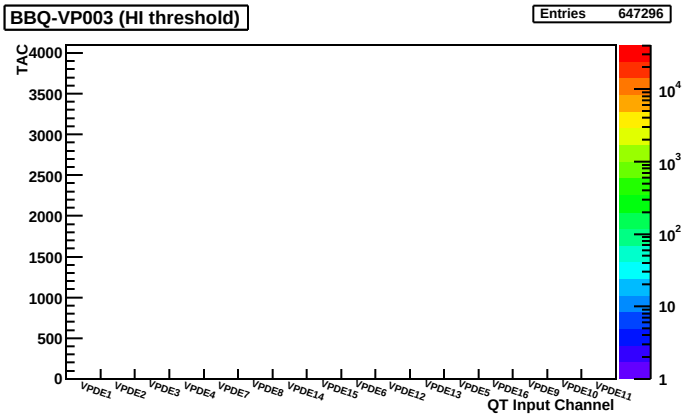

BBQ-VP001 (LO threshold)
Entries 647296
ADC
QT Input Channel

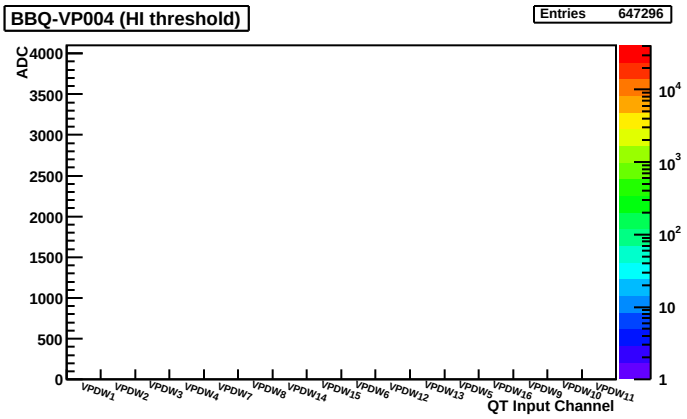

BBQ-VP001 (LO threshold)
Entries 647296
TAC
QT Input Channel

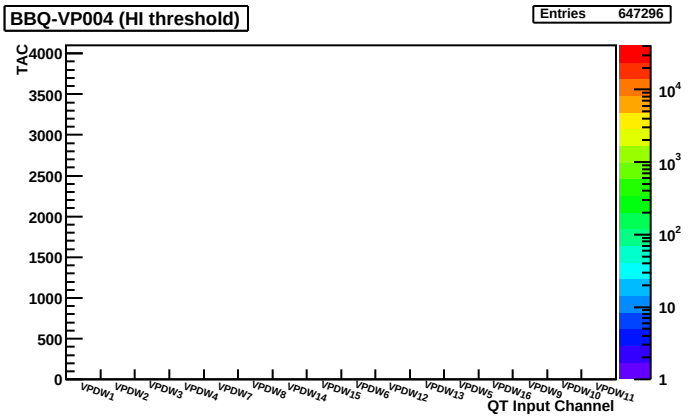

BBQ-VP002 (LO threshold)
Entries 647296
ADC
QT Input Channel

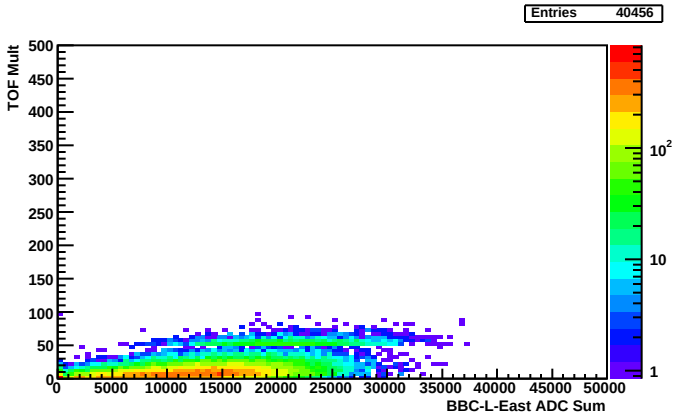

BBQ-VP002 (LO threshold)
Entries 647296
TAC
QT Input Channel

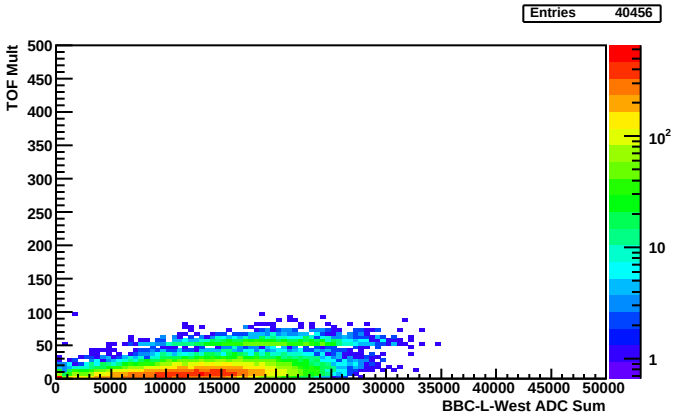

BBQ-VP003 (HI threshold)
Entries 647296
ADC
QT Input Channel

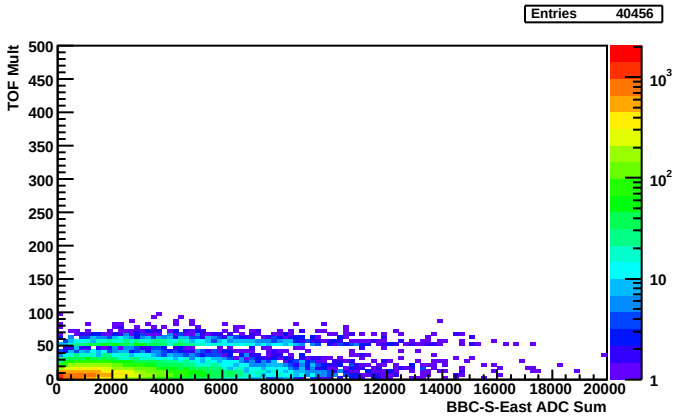

BBQ-VP003 (HI threshold)
Entries 647296
TAC
QT Input Channel

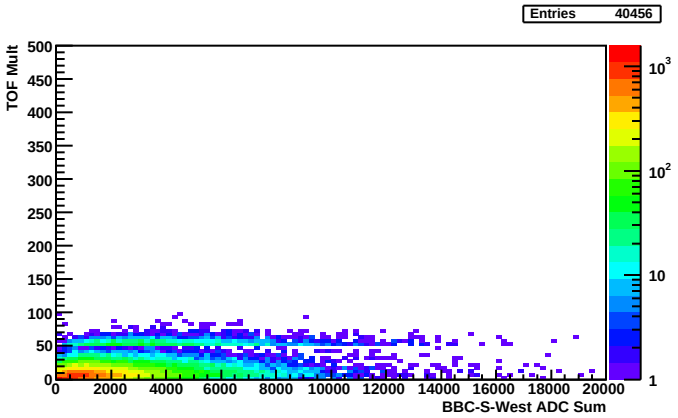

BBQ-VP004 (HI threshold)
Entries 647296
ADC
QT Input Channel

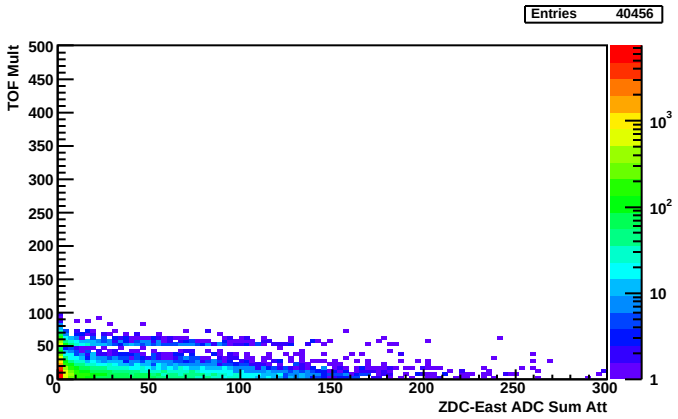

BBQ-VP004 (HI threshold)
Entries 647296
TAC
QT Input Channel

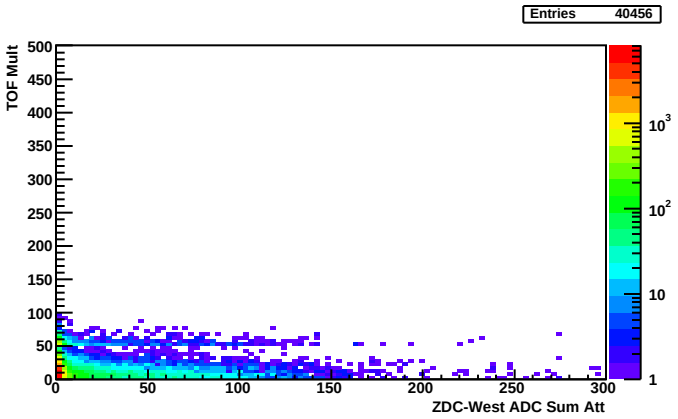

Entries 40456
TOF Mult
BBC-L-East ADC Sum

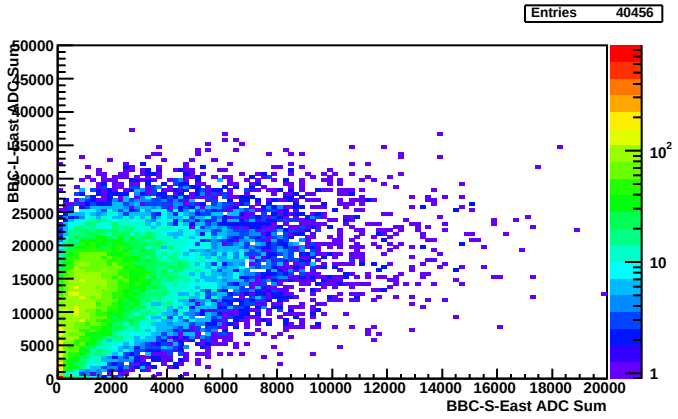

Entries 40456
TOF Mult
BBC-L-West ADC Sum

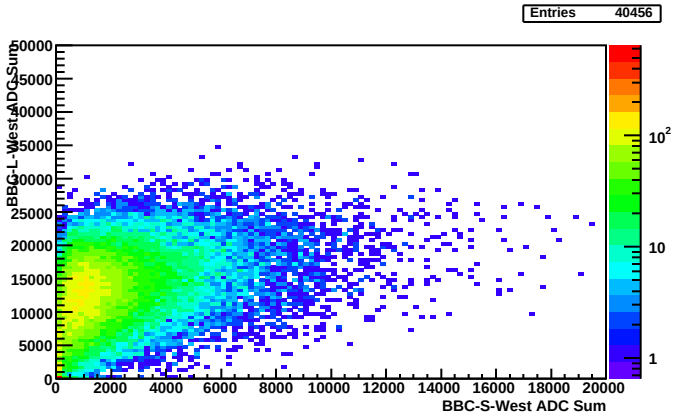

Entries 40456
TOF Mult
BBC-S-East ADC Sum

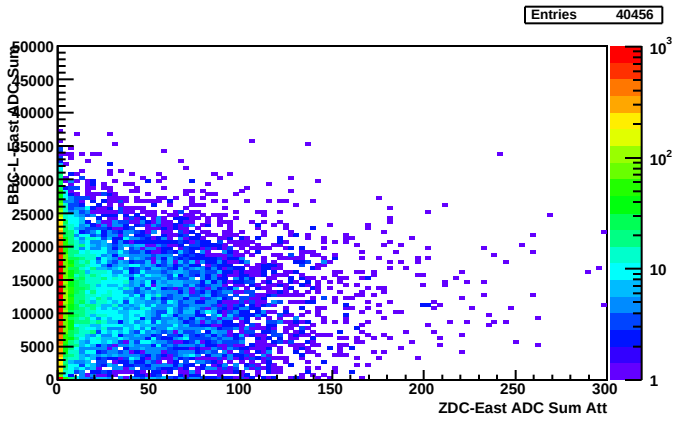

Entries 40456
TOF Mult
BBC-S-West ADC Sum

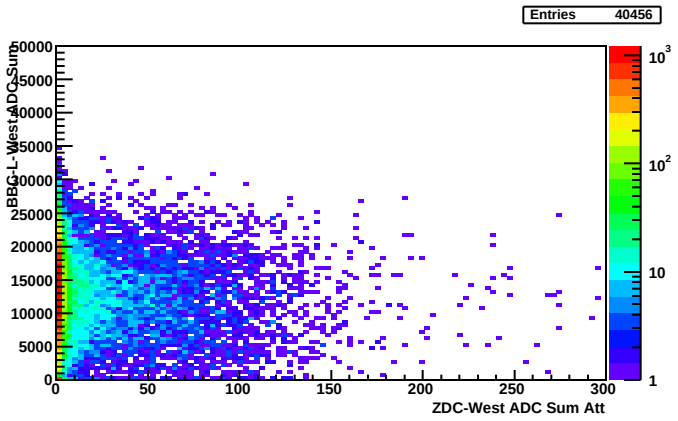

Entries 40456
TOF Mult
ZDC-East ADC Sum Att

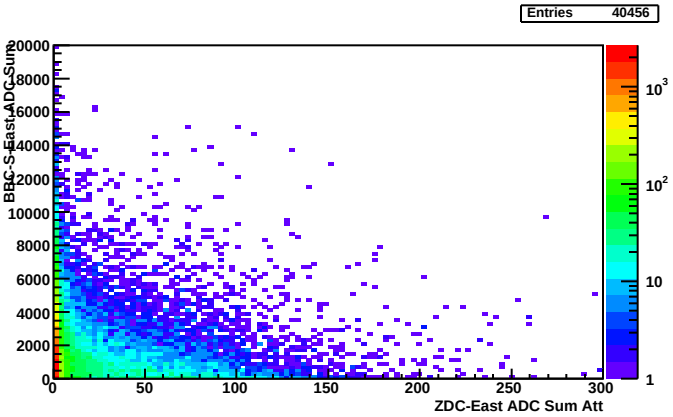

Entries 40456
TOF Mult
ZDC-West ADC Sum Att

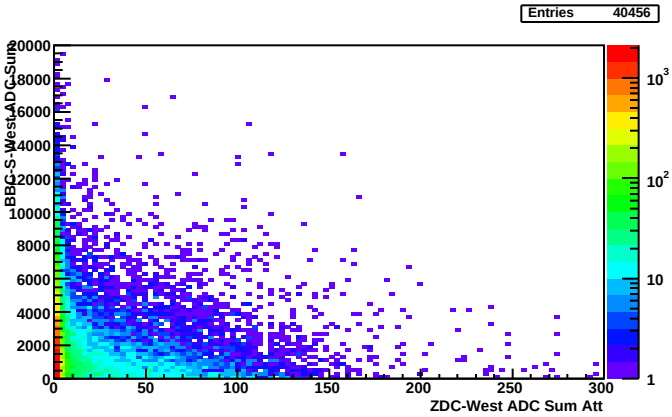

Entries 40456
BBC-L-East ADC Sum
BBC-S-East ADC Sum

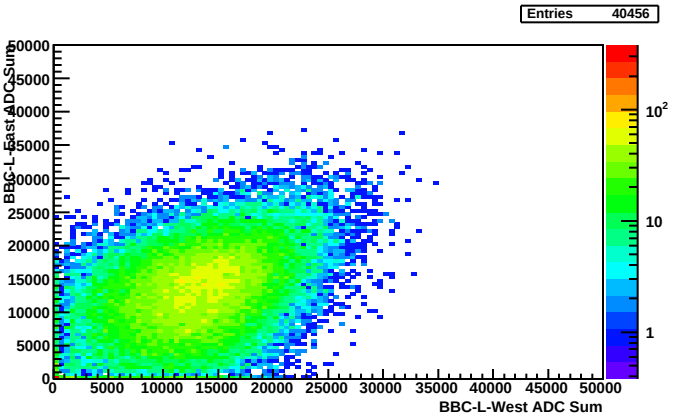

Entries 40456
BBC-L-West ADC Sum
BBC-S-West ADC Sum

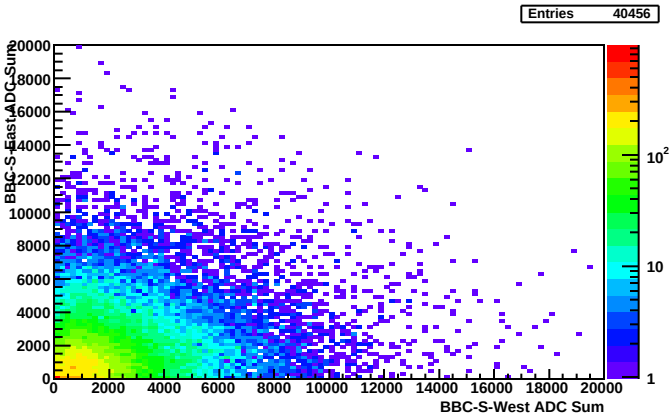

Entries 40456
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

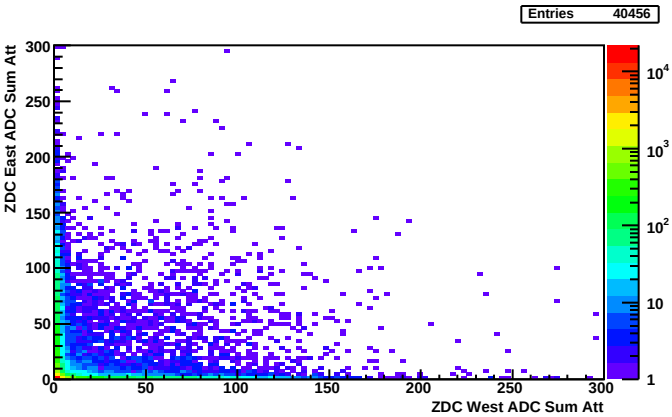

Entries 40456
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

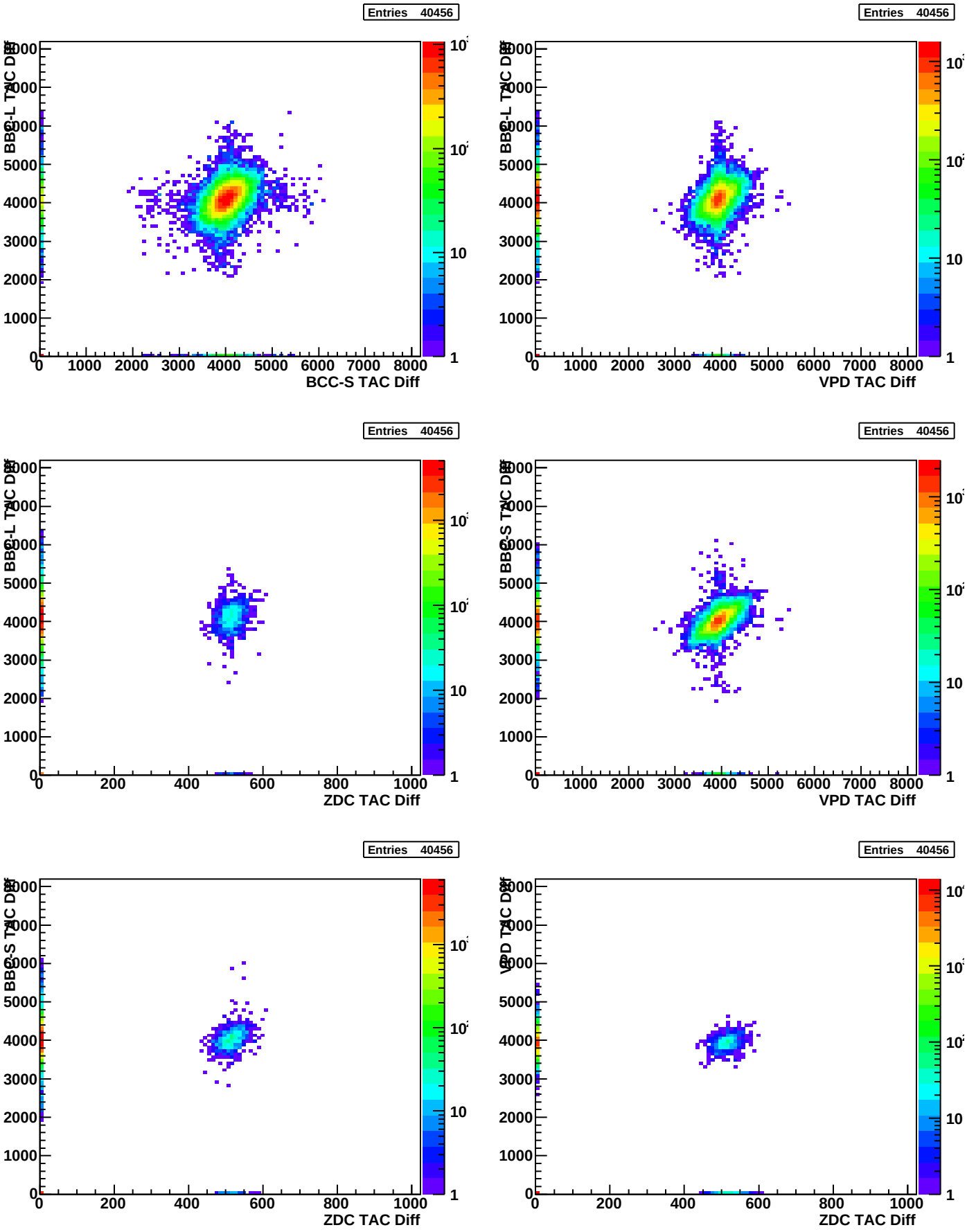

Entries 40456
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

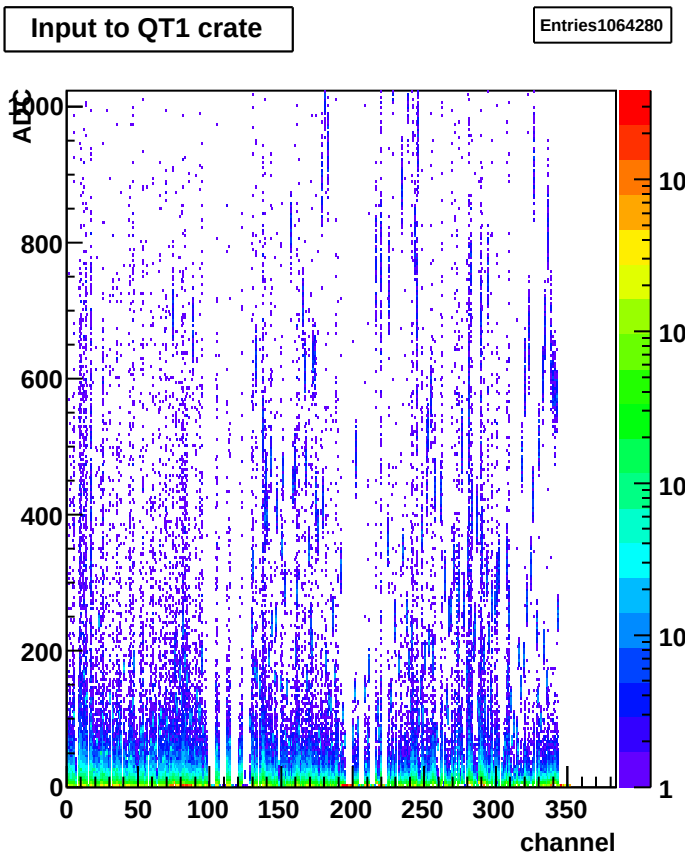

Entries 40456
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

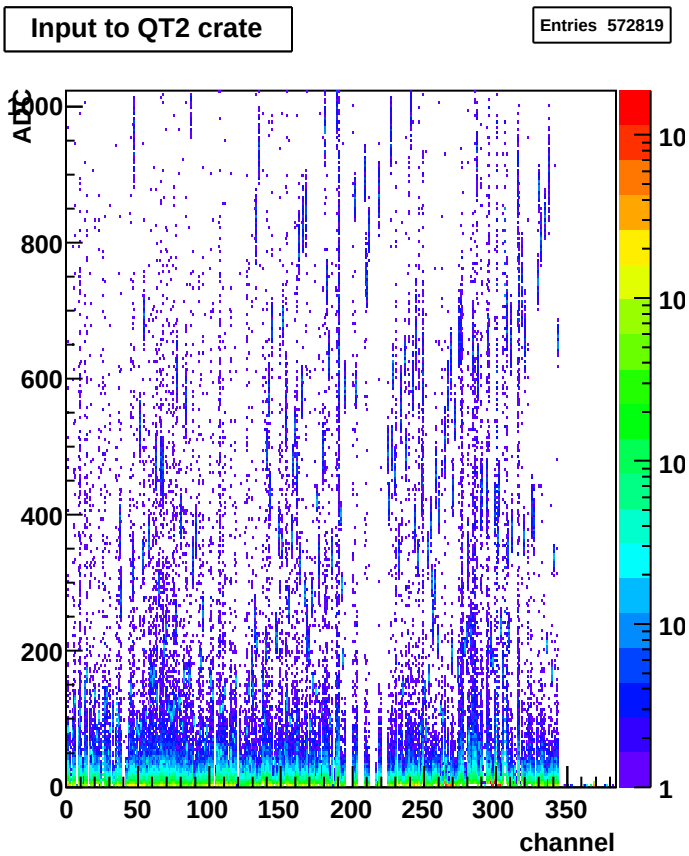

Entries 40456
BBC-L-East ADC Sum
BBC-L-West ADC Sum

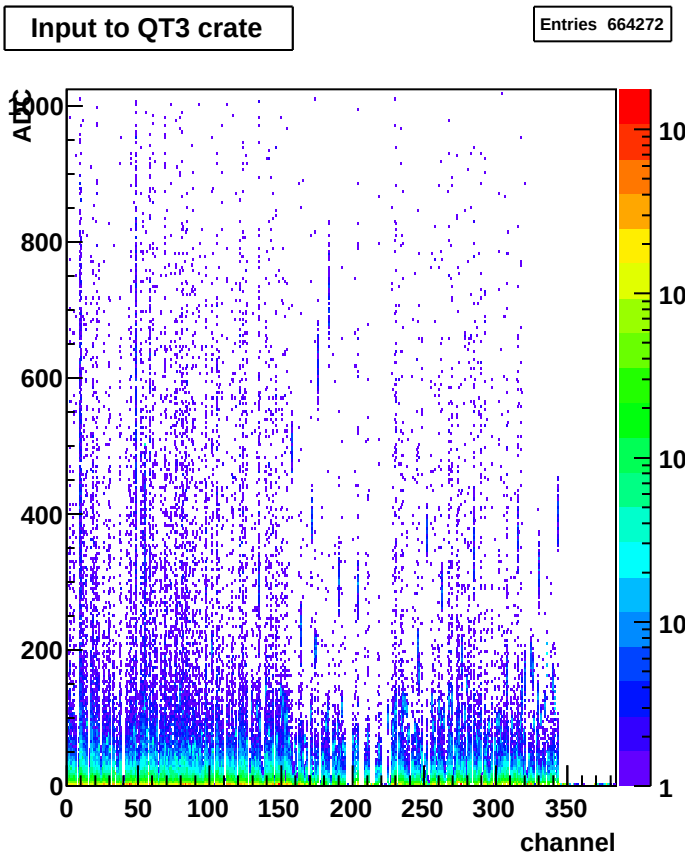

Entries 40456
BBC-S-East ADC Sum
BBC-S-West ADC Sum

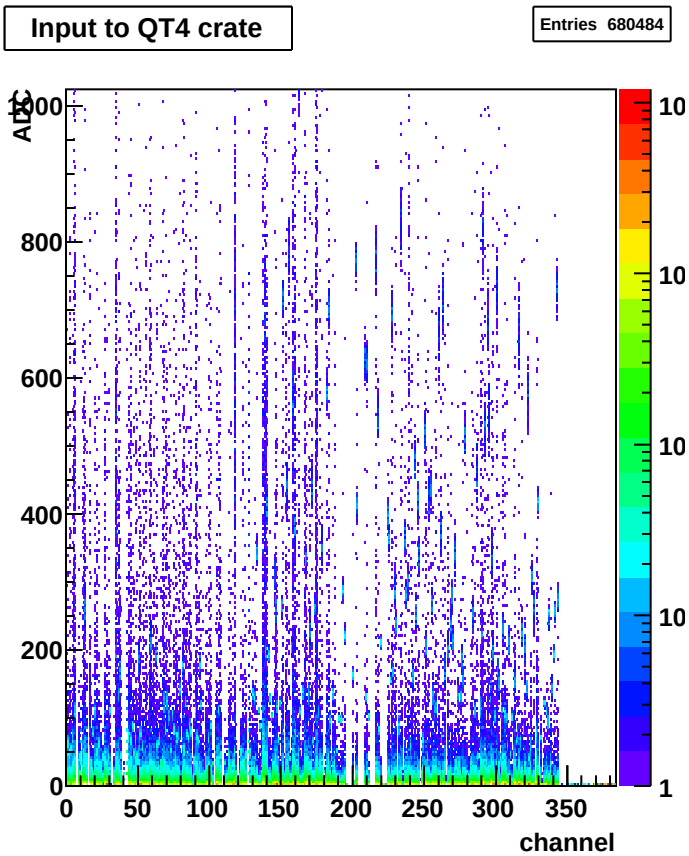

Entries 40456
ZDC East ADC Sum Att
ZDC West ADC Sum Att

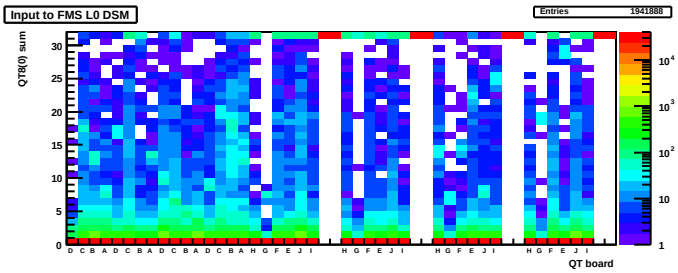

Entries 40456
BBC-L TAC Diff
BCC-S TAC Diff
Entries 40456
BBC-L TAC Diff
VPD TAC Diff
Entries 40456
BBC-L TAC Diff
ZDC TAC Diff
Entries 40456
BBC-S TAC Diff
VPD TAC Diff
Entries 40456
BBC-S TAC Diff
ZDC TAC Diff
Entries 40456
VPD TAC Diff
ZDC TAC Diff

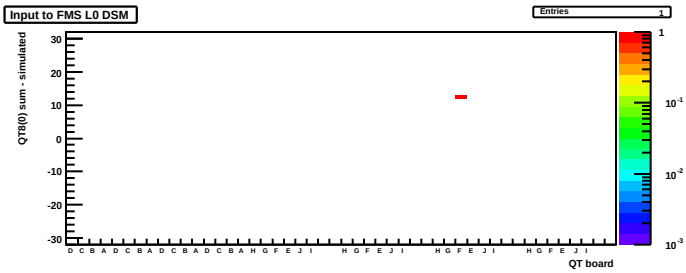

Input to QT1 crate
Entries1064280
ADC
channel

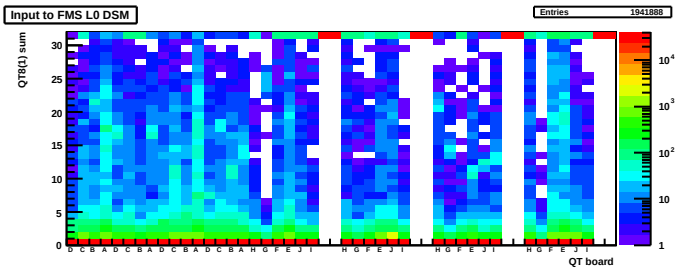

Input to QT2 crate
Entries 572819
ADC
channel

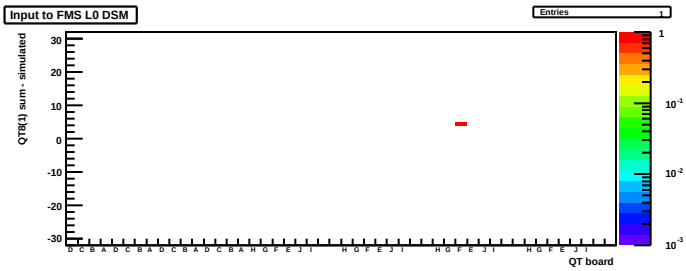

Input to QT3 crate
Entries 664272
ADC
channel

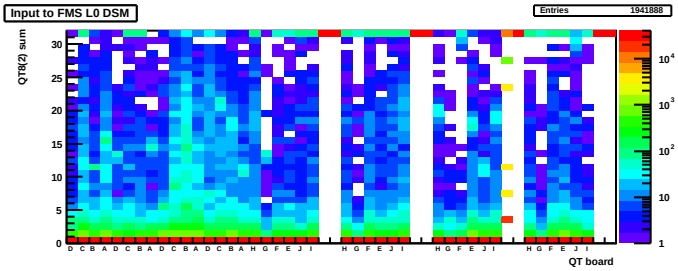

Input to QT4 crate
Entries 680484
ADC
channel

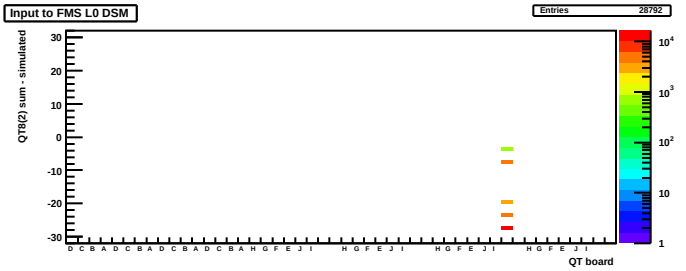

Input to FMS L0 DSM
Entries 1941888
QT8(0) sum
QT board

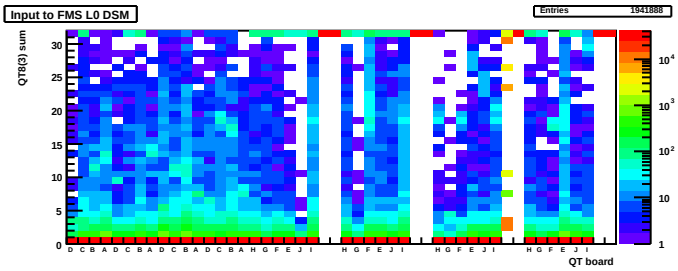

Input to FMS L0 DSM
Entries 1
QT8(0) sum - simulated
QT board

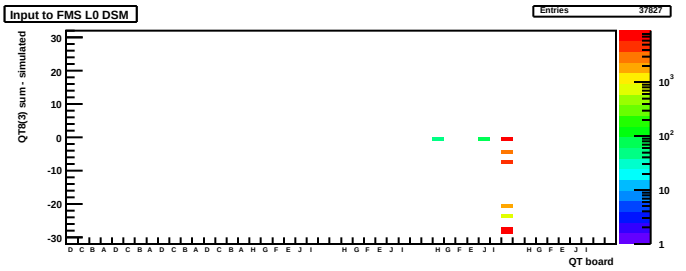

Input to FMS L0 DSM
Entries 1941888
QT8(1) sum
QT board

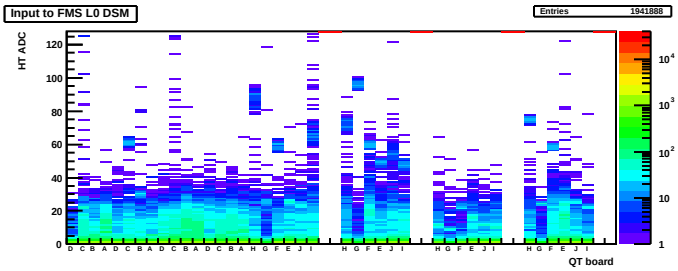

Input to FMS L0 DSM
Entries 1
QT8(1) sum - simulated
QT board

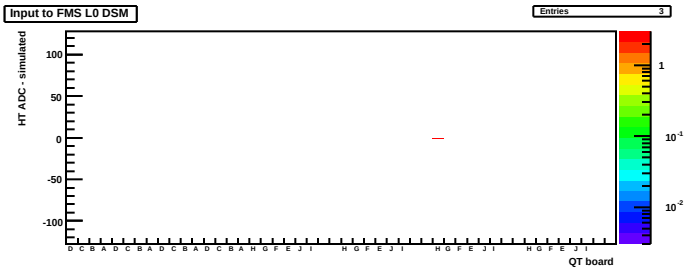

Input to FMS L0 DSM
Entries 1941888
QT8(2) sum
QT board

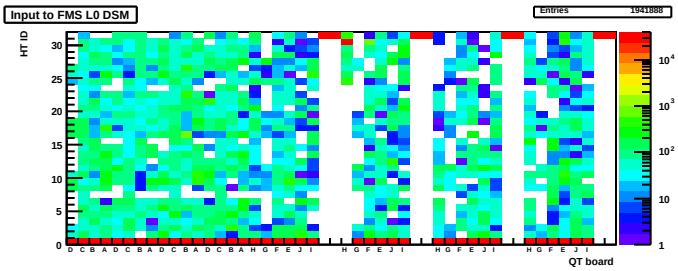

Input to FMS L0 DSM
Entries 28792
QT8(2) sum - simulated
QT board

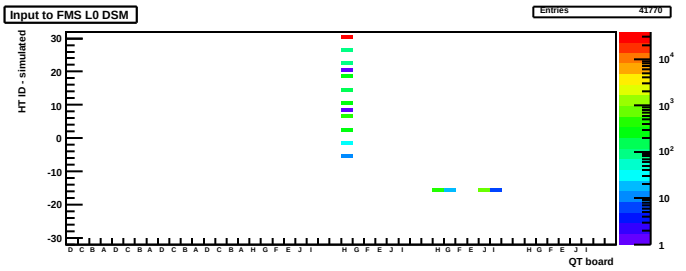

Input to FMS L0 DSM
Entries 1941888
QT8(3) sum
QT board

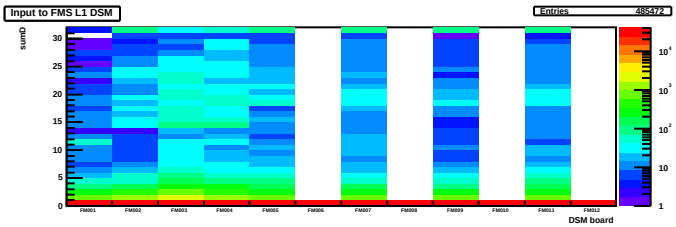

Input to FMS L0 DSM
Entries 37827
QT8(3) sum - simulated
QT board

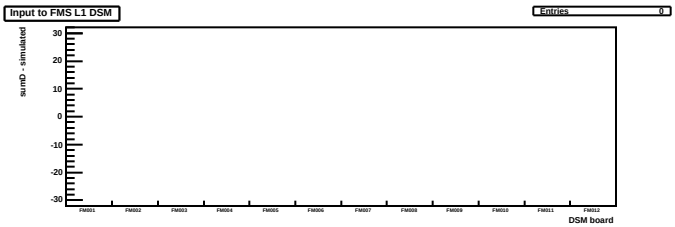

Input to FMS L0 DSM
Entries 1941888
HT ADC
QT board

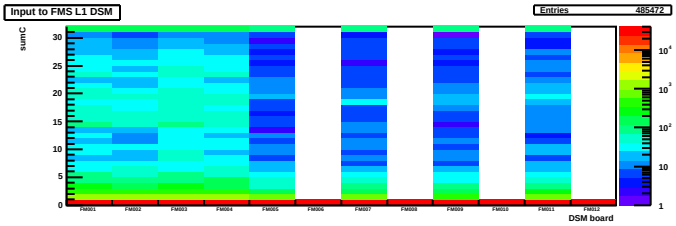

Input to FMS L0 DSM
Entries 3
HT ADC - simulated
QT board

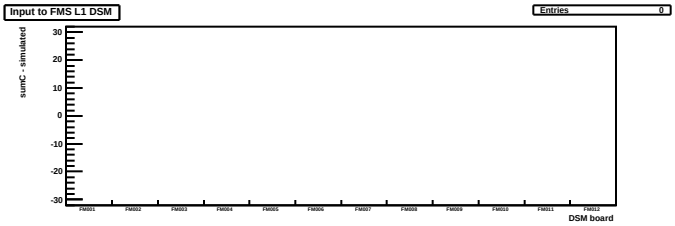

Input to FMS L0 DSM
Entries 1941888
HT ID
QT board

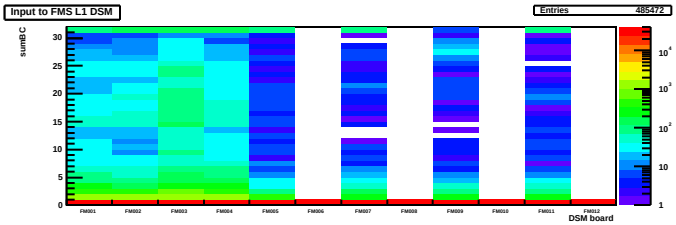

Input to FMS L0 DSM
Entries 41770
HT ID - simulated
QT board

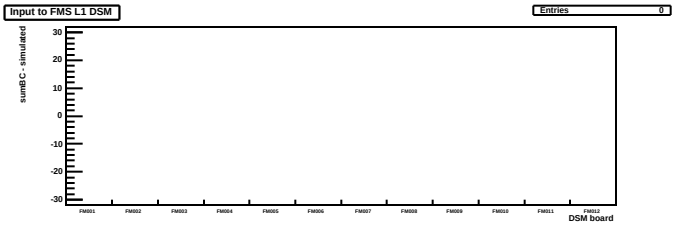

Input to FMS L1 DSM
Entries 485472
sumD
DSM board

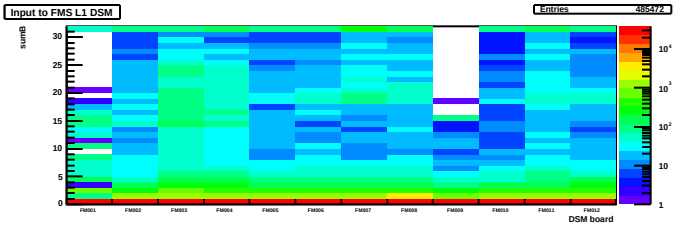

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

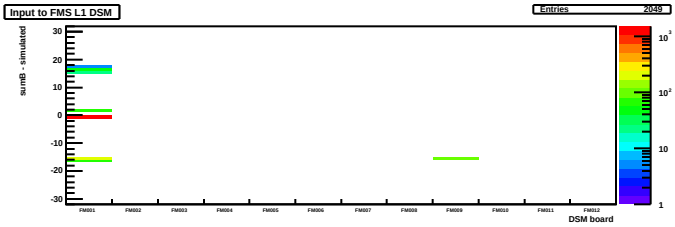

Input to FMS L1 DSM
Entries 485472
sumC
DSM board

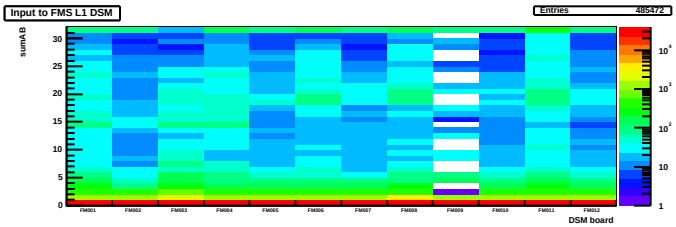

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

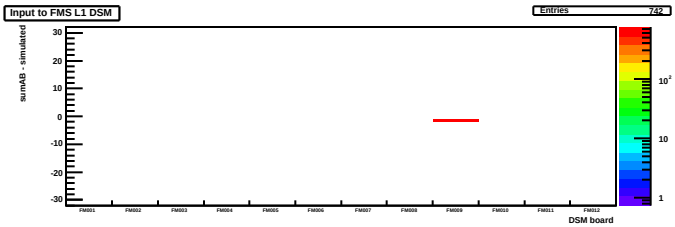

Input to FMS L1 DSM
Entries 485472
sumBC
DSM board

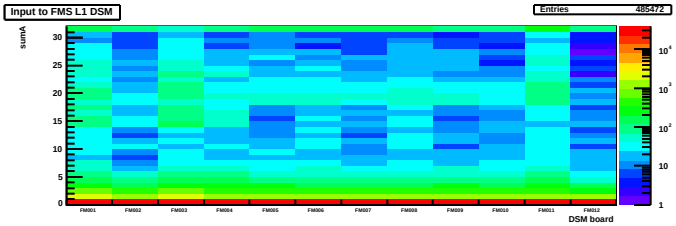

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

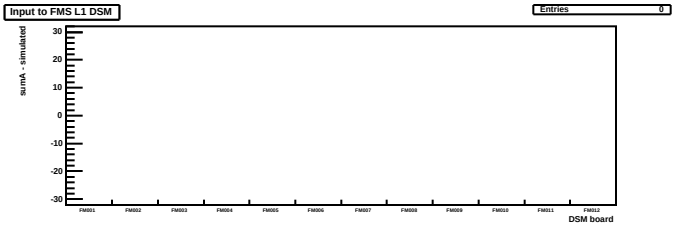

Input to FMS L1 DSM
Entries 485472
sumB
DSM board

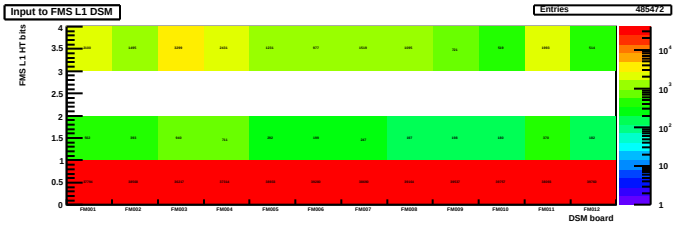

Input to FMS L1 DSM
Entries 2049
sumB - simulated
DSM board

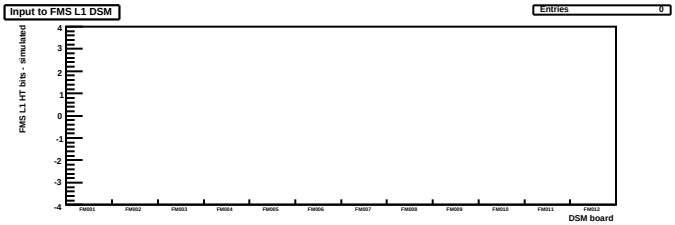

Input to FMS L1 DSM
Entries 485472
sumAB
DSM board

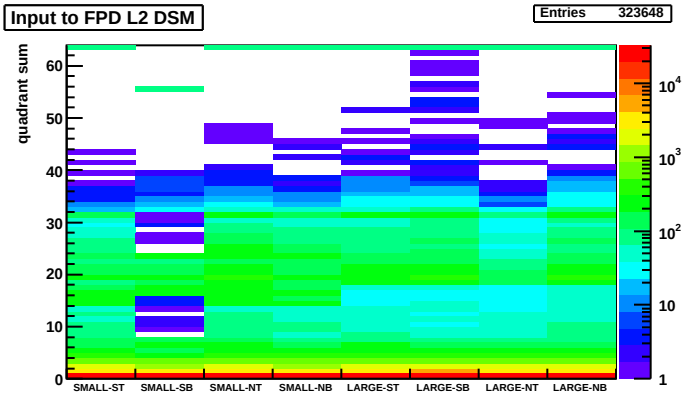

Input to FMS L1 DSM
Entries 742
sumAB - simulated
DSM board

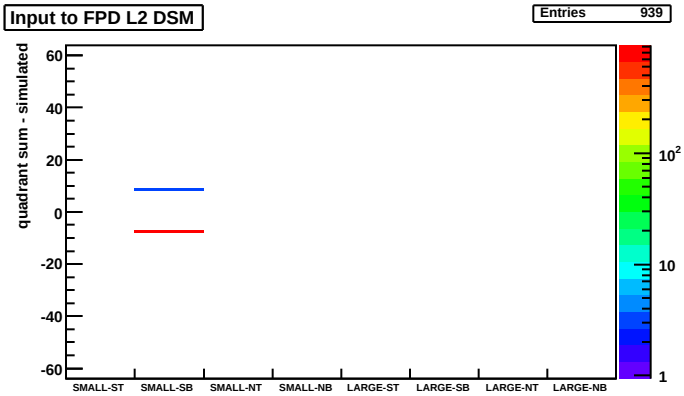

Input to FMS L1 DSM
Entries 485472
sumA
DSM board

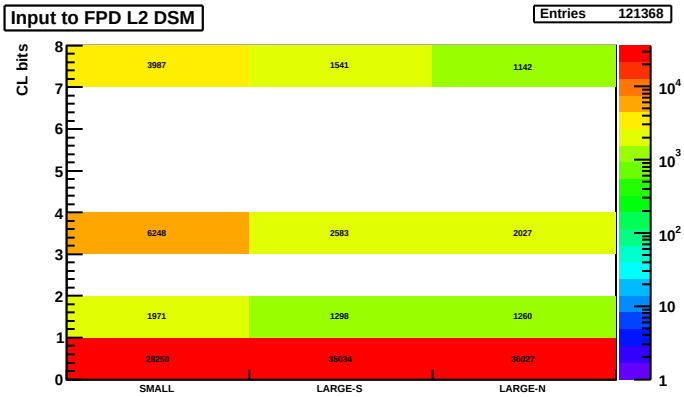

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

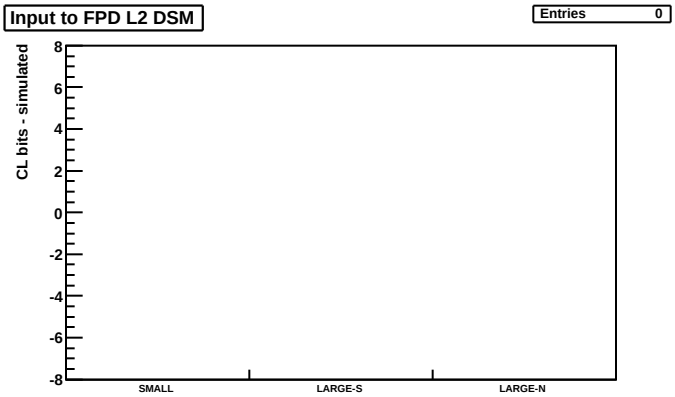

Input to FMS L1 DSM
Entries 485472
FMS L1 HT bits
DSM board

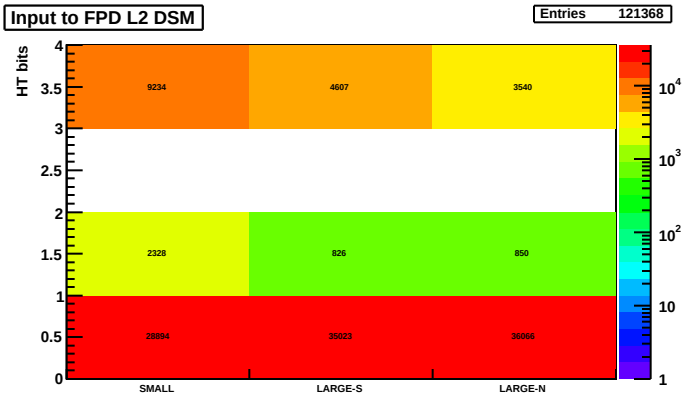

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

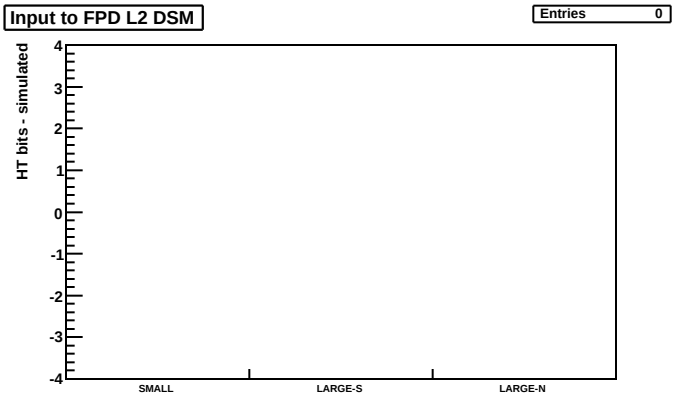

Input to FPD L2 DSM
Entries 323648
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

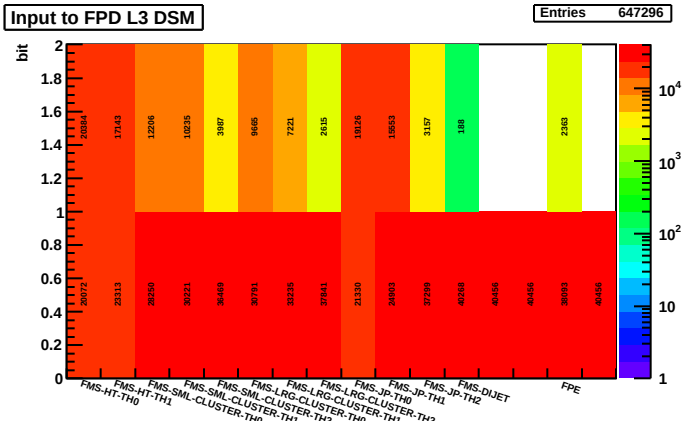

Input to FPD L2 DSM
Entries 939
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

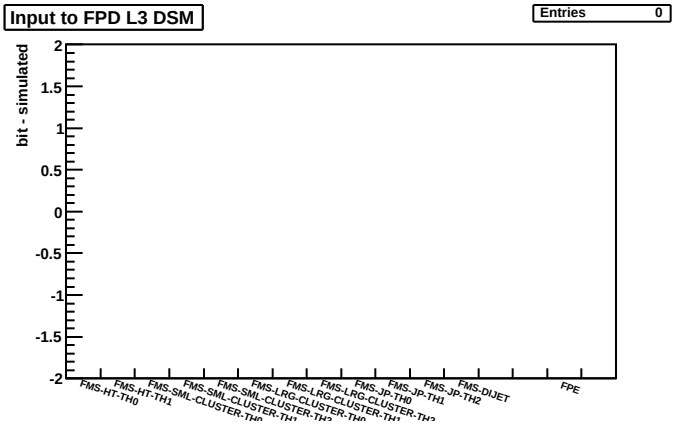

Input to FPD L2 DSM
Entries 121368
CL bits
3987
1541
1142
6248
2583
2027
1971
1298
1260
28250
35034
36027
SMALL
LARGE-S
LARGE-N

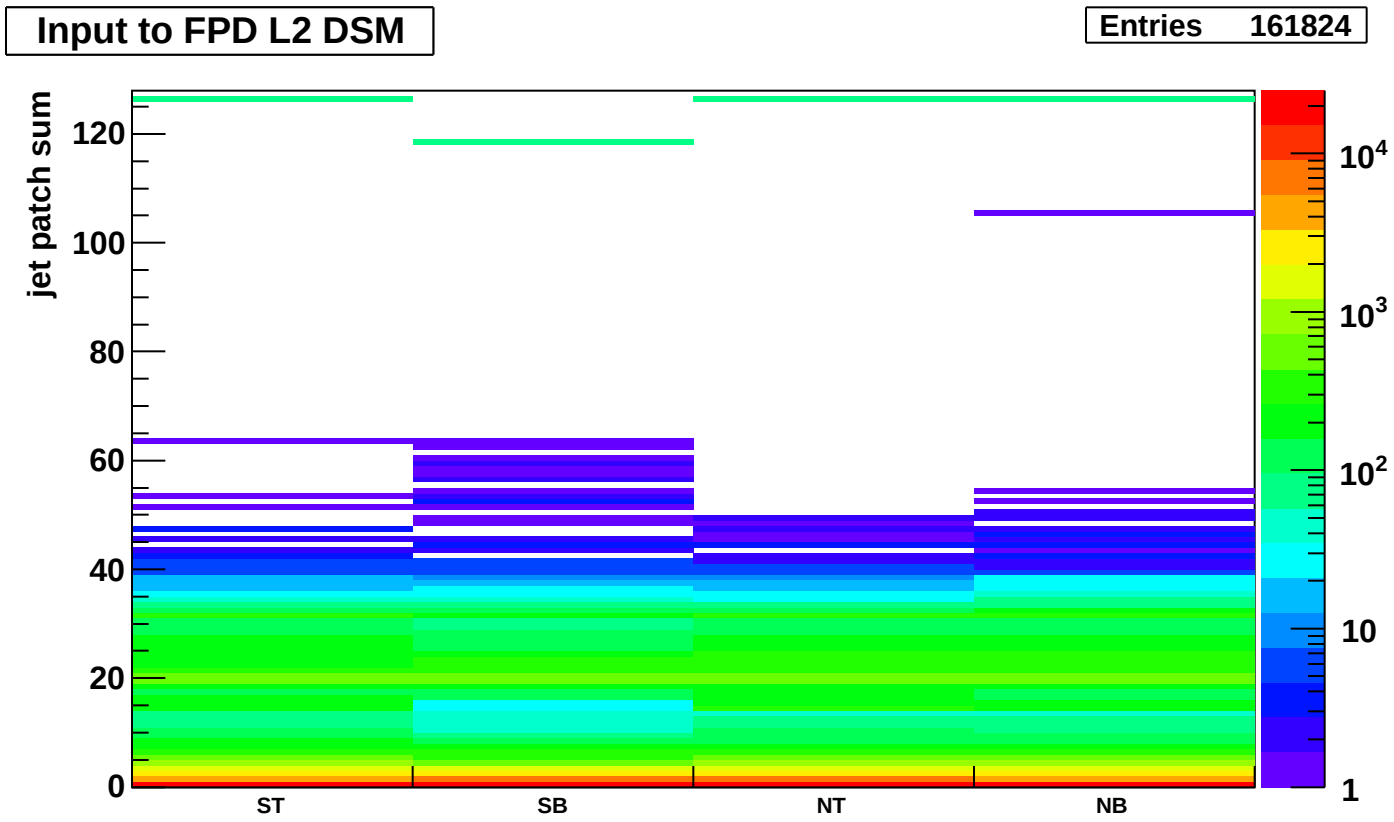

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

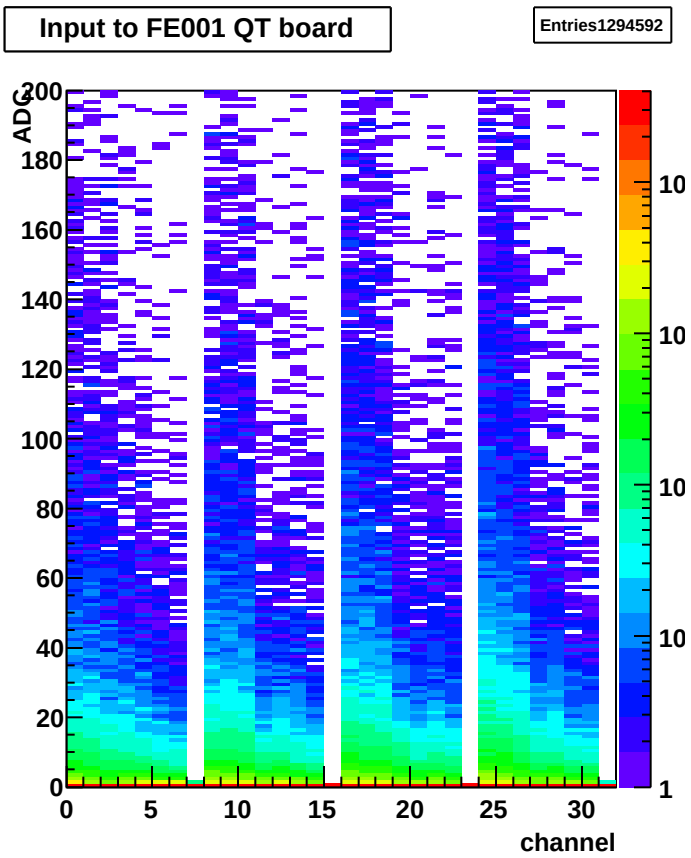

Input to FPD L2 DSM
Entries 121368
HT bits
9234
4607
3540
2328
826
850
28894
35023
36066
SMALL
LARGE-S
LARGE-N

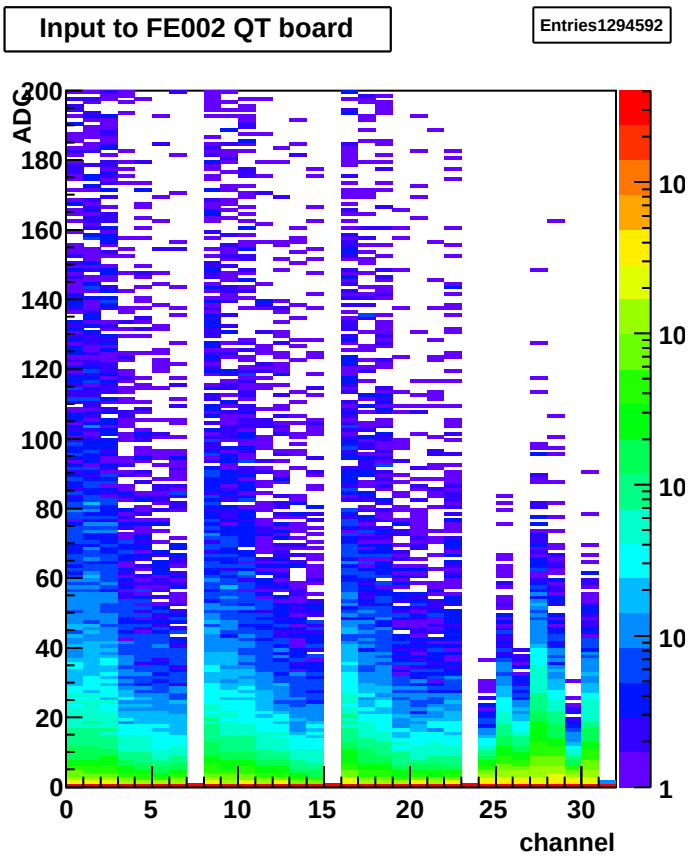

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

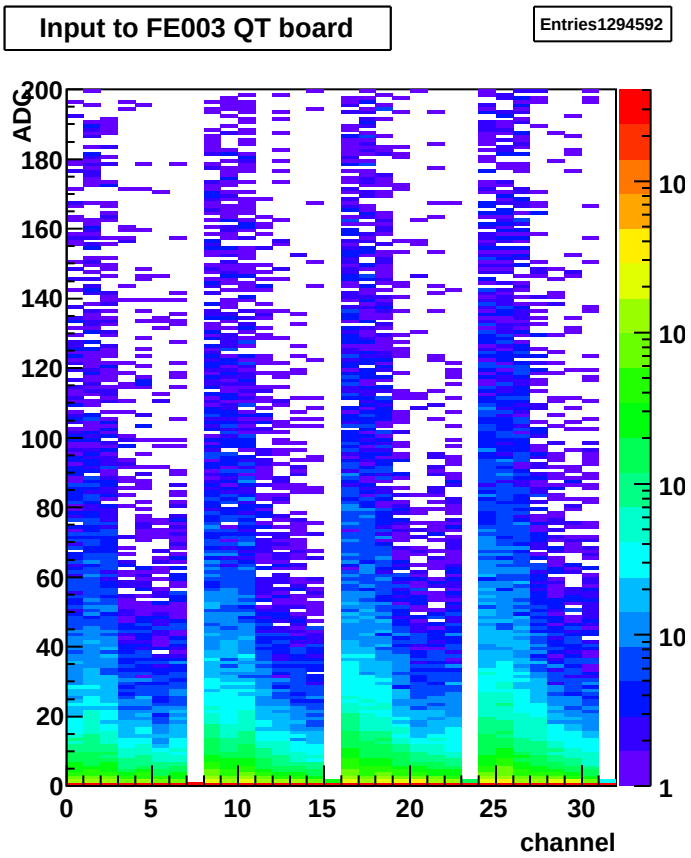

Input to FPD L3 DSM
Entries 647296
bit
23304
17143
12206
10235
3997
9685
7221
2615
10126
15553
3157
188
2383
20072
22313
28250
30221
36469
30791
33235
37841
21330
24903
37299
40268
40456
40456
38093
40456
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

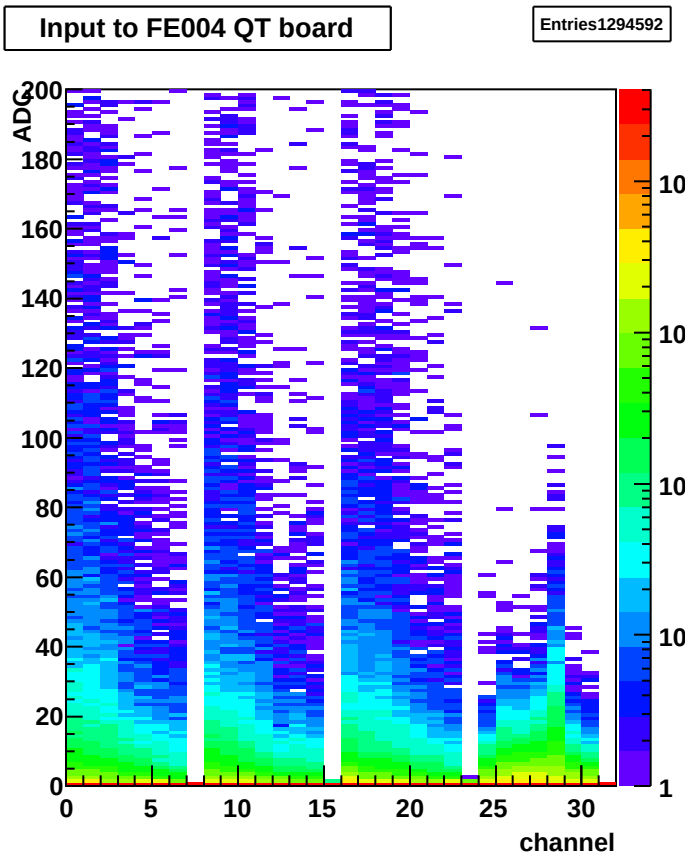

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

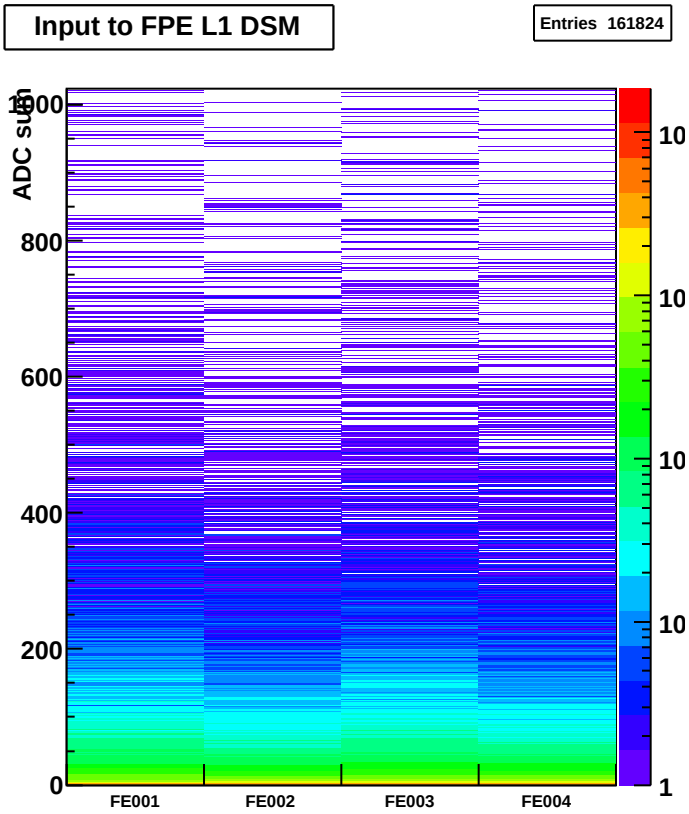

Input to FPD L2 DSM
Entries 161824
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

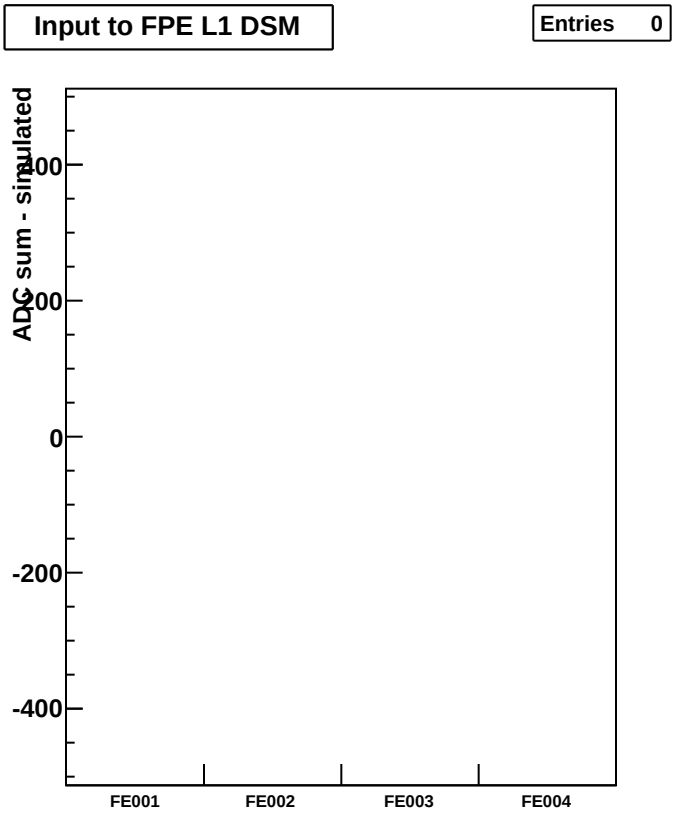

Input to FE001 QT board
Entries1294592
ADC
channel

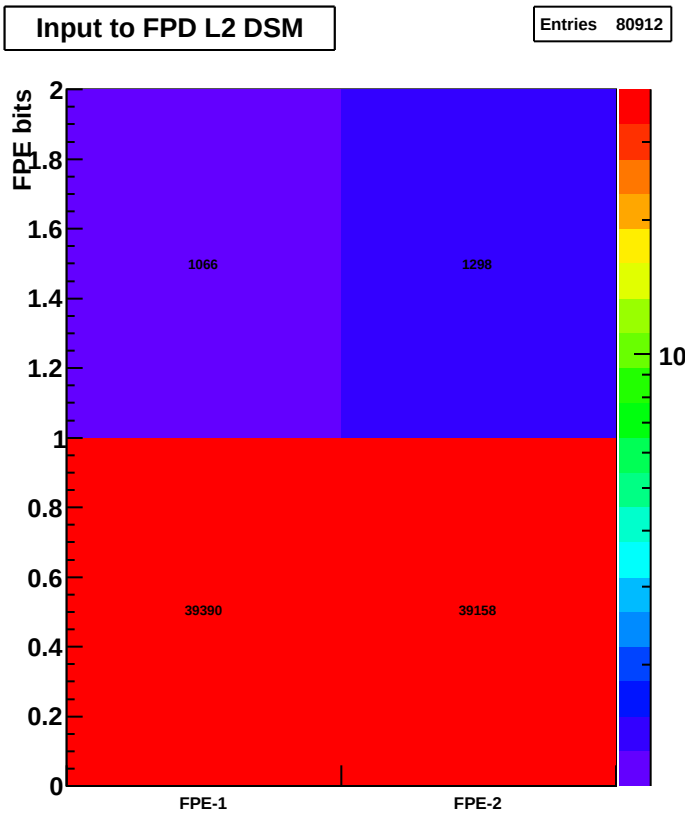

Input to FE002 QT board
Entries1294592
ADC
channel

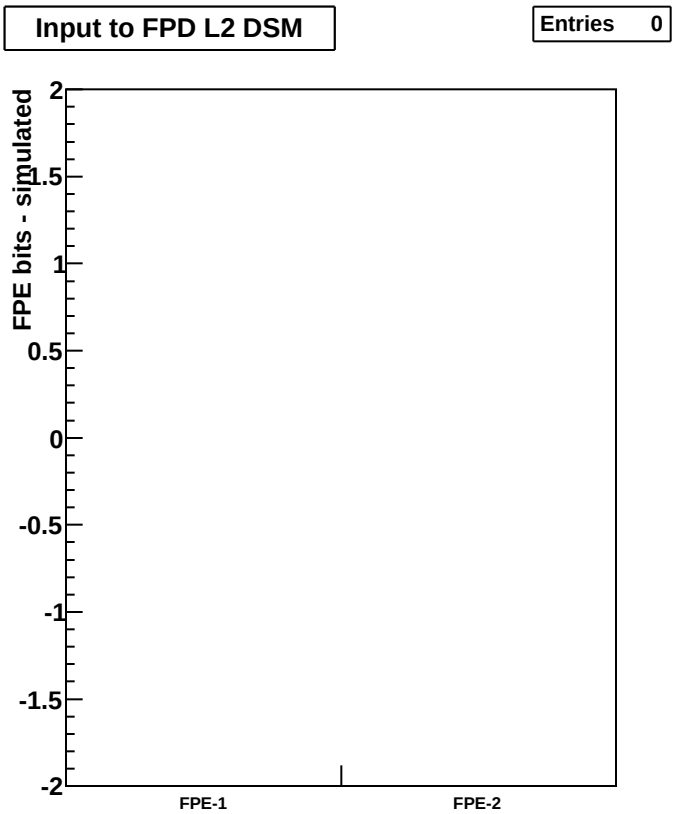

Input to FE003 QT board
Entries1294592
ADC
channel


Input to FE004 QT board
Entries1294592
ADC
channel


Input to FPE L1 DSM
Entries 161824
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 80912
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
1066
1298
39390
39158
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2